

Dynamic Reconfigurable  
Network-on-Chip Design:  
Innovations for Computational  
Processing and Communication

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# Dynamic Reconfigurable Network-on-Chip Design Innovations For Computational Processing And Communication

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**Summary** : Free dynamic reconfigurable network-on-chip design innovations for computational processing and communication pdf download - reconfigurable computing brings immense flexibility to on-chip processing while network-on-chip has improved flexibility in on-chip communication integrating these two areas of research reaps the benefits of both and represents the promising future of multiprocessor systems-on-chip this book is the one of the first compilations written to demonstrate this future for network-on-chip design through dynamic and creative research into questions ranging from integrating reconfigurable computing techniques to task assigning scheduling and arrival to designing an operating system to take advantage of the computing and communication flexibilities brought about by run-time reconfiguration and network-on-chip it represents a complete source of the techniques and applications for reconfigurable network-on-chip necessary for understanding of future of this field

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