Digital Systems Design Using VHDL

Author : Charles H Roth / **Category :**Computers / **Total Pages** : 470 pages

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Summary : Free digital systems design using vhdl pdf download - written for an advanced-level course in digital systems design digital systems design using vhdl integrates the use of the industry-standard hardware description language vhdl into the digital design process following a review of basic concepts of logic design in chapter 1 the author introduces the basics of vhdl in chapter 2 and then incorporates more coverage of vhdl topics as needed with advanced topics covered in chapter 8 rather than simply teach vhdl as a programming language this book emphasizes the practical use of vhdl in the digital design process for example in chapter 9 the author develops vhdl models for a ram memory and a microprocessor bus interface he then uses a vhdl simulation to verify that timing specifications for the interface between the memory and microprocessor bus are satisfied the book also covers the use of cad tools to synthesize digital logic from a vhdl description in chapter 8 and stresses the use of programmable logic devices including programmable gate arrays chapter 10 introduces methods for testing digital systems including boundary scan and a built-in self-test

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