



# High Performance Integer Arithmetic Circuit Design On FPGA

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**Category** :Technology & Engineering / **Total Pages** : 114 pages

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**Summary** : Free high performance integer arithmetic circuit design on fpga pdf download - this book describes the optimized implementations of several arithmetic datapath controlpath and pseudorandom sequence generator circuits for realization of high performance arithmetic circuits targeted towards a specific family of the high-end field programmable gate arrays fpgas it explores regular modular cascadable and bit-sliced architectures of these circuits by directly instantiating the target fpga-specific primitives in the hdl every proposed architecture is justified with detailed mathematical analyses simultaneously constrained placement of the circuit building blocks is performed by placing the logically related hardware primitives in close proximity to one another by supplying relevant placement constraints in the xilinx proprietary user constraints file the book covers the implementation of a gui-based cad tool named flexicore integrated with the xilinx integrated software environment ise for design automation of platform-specific high-performance arithmetic circuits from user-level specifications this tool has been used to implement the proposed circuits as well as hardware implementations of integer arithmetic algorithms where several of the proposed circuits are used as building blocks implementation results demonstrate higher performance and superior operand-width scalability for the proposed circuits with respect to implementations derived through other existing approaches this book will prove useful to researchers students and professionals engaged in the domain of fpga circuit optimization and implementation

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