#### mmWAVE SENSOR EVALUATION SOLUTION

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Joybien Batman BM601 mmWave EVM Kit is a Texas Instruments (TI) IWR1843 ASIC based millimeter-wave (mmWave) Kit with Frequency-Modulated Continuous Wave (FMCW) radar technology capable of operation in the 76GHz to 81GHz band with up to 4 GHz continuous chirp, using 3 Transmission Antennas and 4 Receiving Antennas, for sensing target object's range, velocity, and angle parameters.

Batman BM601 mmWave EVM Kit is an extremely light and compact mmWave Module with low-power, self-monitored, ultra-accurate, and lighting condition independent versatilities for various applications including: Education, Engineering, Science, Industrial, Medical, and Business & Consumer.

#### **Applications**

- Education's Practical Radar Introduction
- Engineering & Science's Motion Detection, Displacement, etc.
- Industrial sensor for Displacement & Safe Guard, Factory Automation, Robotics, etc.
- Building Automation sensor for Occupancy Detection, Proximity & Position sensing, People Counting, People Density, Security and Surveillance,
- Healthcare's Vital Signs Detection, People Fall Detection, etc.
- Business' Traffic Monitoring, Parking Space occupancy and Proximity Advertisement
- Consumer's Gesture Recognition, Obstacle Avoidance, etc.

#### **Features**

Operating Frequency: 76GHz ~ 81GHz coverage

with 4GHz continuous bandwidth

• Antenna: 3 Tx and 4 Rx with:

TX Power: 12 dBm

RX Noise Figure: 14 dB(76GHz ~ 77GHz) / 15 dB(77GHz ~ 81GHz) Phase noise at 1MHz:-95 (76GHz ~ 77GHz) / -93 (77GHz ~ 81GHz)

Processors: ARM R4F based MCU, and C674x DSP

for FMCW signal processing

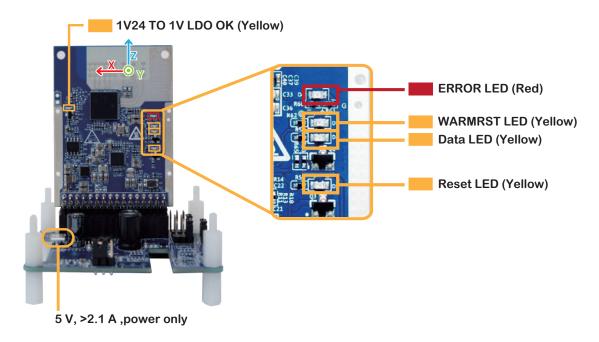
- On-Chip Memory: 1.75MB
- Internal Memories With ECC
- Integrated Peripherals
- Extremely light and compact Module design.
- Supplied Voltage: 5VDC & 2.1A

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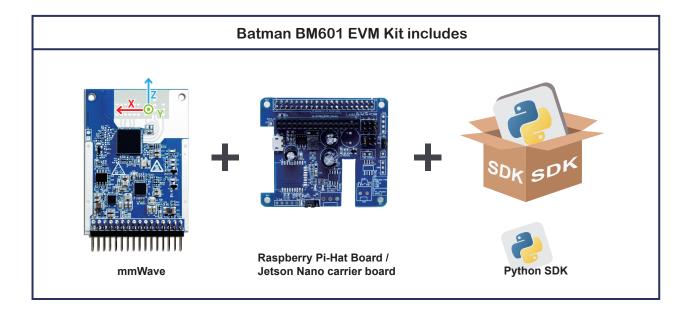
#### mmWAVE SENSOR EVALUATION SOLUTION

#### Packing List: mmWave Module, Raspberry Pi-Hat Board, Python SDK

• Make sure you are using the correct power supply of 5 V, >2.1 A with a Micro USB connection



Note: Raspberry Pi, Jetson Nano, or Linux/Mac/Windows computer not included.

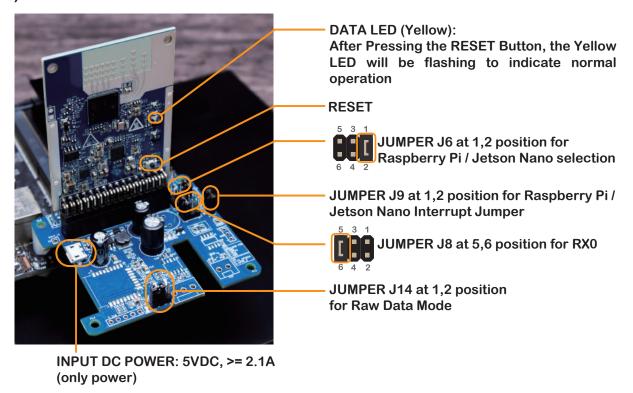


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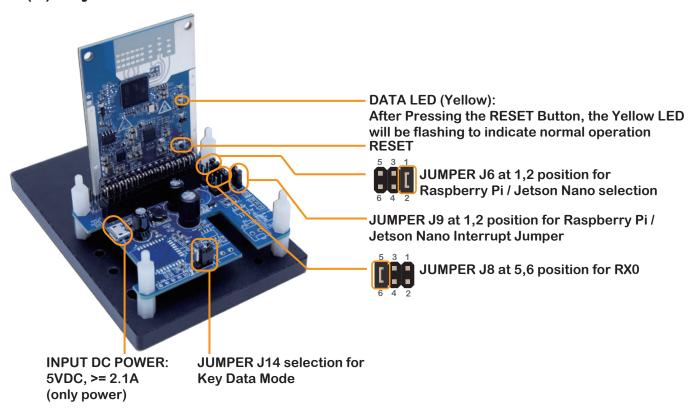
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#### Selection: Key Data Mode or Raw Data Mode Application

#### (A) Raw Data Mode



#### (B) Key Data Mode

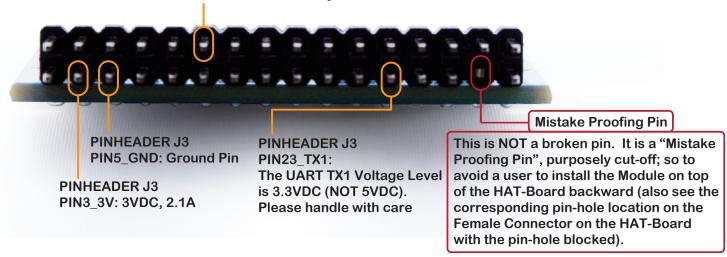


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#### Batman BM601 Module J3 Pin Assignment Note

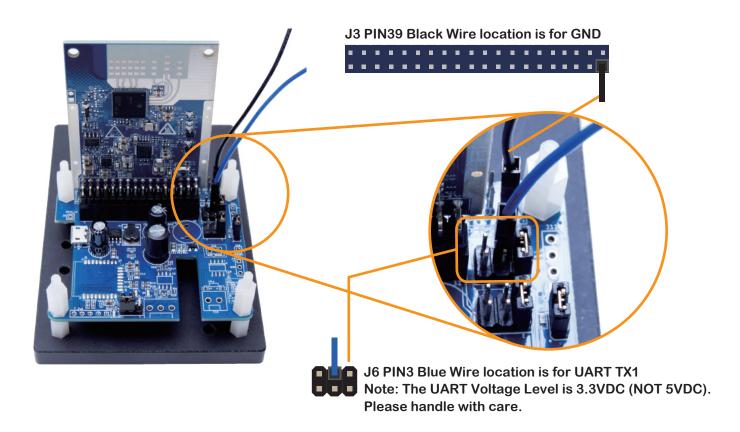
PINHEADER J3 PIN12\_GPIO\_0 High: Raw Data Baud Rate 921600/8/n/1 selection for PIN23\_TX1 PINHEADER J3 PIN12\_GPIO\_0 Low: Key Data Baud Rate 115200/8/n/1 selection for PIN23\_TX1



Alert: All GPIO Pins base on 3.3V System. Pin23\_TX1 is DC 3.3V system.

#### Batman BM601 EVM Kit + External Microprocessor

Wire connections for external microprocessor access on the HAT-Board



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#### **Batman Kit + EM110 Emulator for PC Computer Connection**

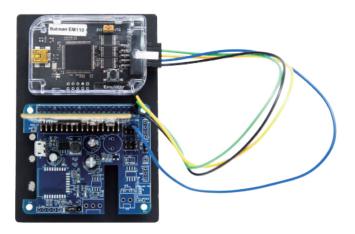
#### Batman BM601 EVM Kit + EM110 Emulator+PC

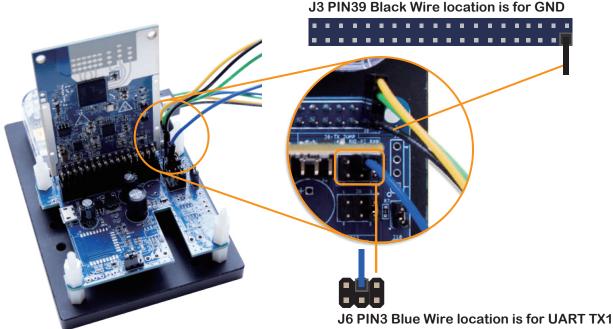
Wire connections for external EM110 Emulator on the HAT-Board

Please visit TI website for UNIFLASH Driver download.









Note: EM110 Emulator not included within this EVM Kit. Please contact Joybien for purchasing info.

Note: The UART Voltage Level is 3.3VDC (NOT 5VDC). Please handle with care.

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#### BM601 EVM Kit Installation on Desktop Computer

On Software side, please download & install Silicon Labs CP210x USB to UART Bridge Virtual COM Port (VCP) drivers for your Computer (Windows, Mac, or Linux) at:

https://www.silabs.com/developers/usb-to-uart-bridge-vcp-drivers

to enable the UART communication between BM601 EVM Kit and Computer.

Please make sure that you have installed Python on your Computer at:

https://www.python.org/downloads/

Note: You must enable "Add Python to PATH" upon installation.

You may download GEANY as your Python code editor at:

https://www.geany.org/download/releases/

At this point, you may download and execute the corresponding BM601 EVM Kit's Python SDK examples at: https://github.com/bigheadG/mmWave

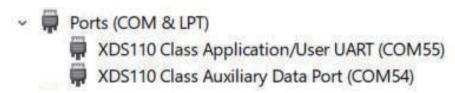
Note: Please follow the Python example to install relevant Libraries for proper execution.

To enable UART port on Computer, you will need to enable proper PORT setting within the Python Code. As an example, for Window PC having UART running at 921600 bps, please enable:

port = serial.Serial("COM#",baudrate = 921600, timeout = 0.5)

where the "#" of the COM# should correspond to the XDS110 Class Auxiliary Data Port dynamically assigned by Windows Device Manager's Ports (COM & LPT) after the USB cable is properly connected on the both ends. As an example, in the picture below, the COM port used is the EM110's XDS110 Class Auxiliary Data Port assigned, and in this case, it is COM54; so you will need to enable your Python Code to include:

port = serial.Serial("COM54",baudrate = 921600, timeout = 0.5)



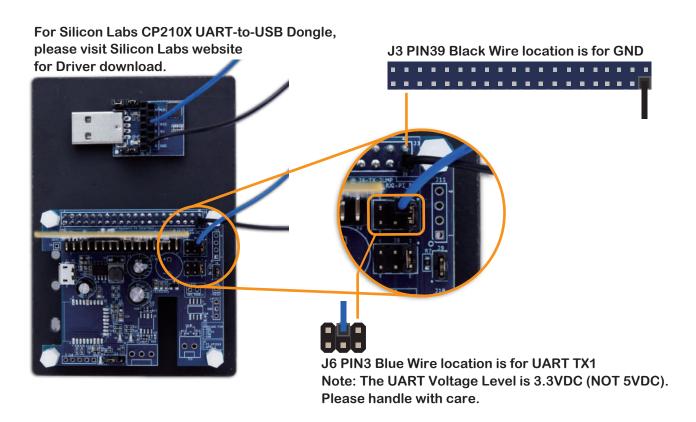
Please follow similar process for Mac or Linux Computer for the UART communication port used.

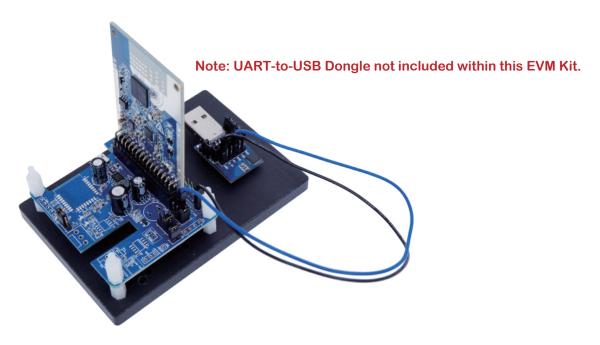
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#### **MMWAVE SENSOR EVALUATION SOLUTION**

#### Batman BM601 EVM Kit + UART USB for PC Computer Connection

#### Batman BM601 EVM Kit + UART





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Batman Kit + NVIDIA Jetson Nano / Batman Kit + Raspberry Pi Please make sure that the JUMPER SETTING is for Raw Data Mode

Batman BM601 EVM Kit + Jetson Nano



### Batman BM601 EVM Kit + Raspberry Pi



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#### **Specifications**

#### mmWave Sensor Evaluation Module



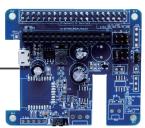
	***************************************			
mmWave ASIC	TI IWR1843 Single Chip mmWave Sensor			
FMCW Transceiver	<ul> <li>Integrated PLL, Transmitter, Receiver, Baseband, and A2D</li> <li>76GHz to 81GHz Coverage With 4GHz Continuous Bandwidth</li> <li>Four Receive Channels</li> <li>Three Transmit Channels</li> <li>Ultra-Accurate Chirp Engine Based on Fractional-N PLL</li> <li>TX Power: 12 dBm</li> <li>RX Noise Figure: 14 dB(76GHz ~ 77GHz) / 15 dB(77GHz ~ 81GHz)</li> <li>Phase Noise at 1 MHz: -95 (76GHz ~ 77GHz) / -93 (77GHz ~ 81GHz)</li> <li>Antenna Type: ISK Antenna</li> <li>Max real sampling rate: 25 Msps</li> </ul>			
	Max complex sampling rate :12.5 Msps			
Built-in Calibration and Self-Test (Monitoring)	<ul> <li>ARM® Cortex® -R4F-Based Radio Control System</li> <li>Built-in Firmware (ROM)</li> <li>Self-calibrating System Across Frequency and Temperature</li> </ul>			
DSP	C674x DSP for Advanced Signal Processing			
On-Chip Memory	● 2MB			
MCU	ARM R4F Microcontroller for Object Detection, and Interface Control     Joybien mmWave Protocol (Per configuration)			
I/O	● UART x 2 ● GPIO x 2(GPIO_31,GPIO_32)			
Power Management	<ul> <li>Built-in LDO Network for Enhanced PSRR</li> <li>I/Os Support Dual Voltage 3.3 V</li> </ul>			
Clock Source	40MHz			
Antenna Orientation	4 receive(RX) 3 transmit (TX) antenna with 120° azimuth field of view (FoV) and 40° elevation FoV			
Input Power	3.3VDC, 2.1A source			
Operating Temperature & Humidity	0°C ~ 40°C 10% ~ 85% Non-Condensing			
Dimensions & Weight 70.2mm x 45.9mm x 9mm ; 16 grams net				

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#### Raspberry Pi-Hat Board /

Jetson Nano carrier board



Connector	<ul> <li>Matching mmWave Module Female Connector</li> <li>Matching Raspberry Pi GPIO Female Connector</li> <li>Micro USB Power Connector</li> <li>Jumpers for Bluetooth Tx/Rx or Raspberry Pi Tx/Rx Selection</li> <li>Jumper for mmWave Raw Data or Key Data Selection</li> </ul>		
Bluetooth (optional)	Joybien JBT24M Bluetooth Low Energy Module		
Micro USB Input Power	5VDC, 2.1Amp. (Note: Power Adapter and Micro USB Cable NOT included)		
Operating Temperature Operating Humidity	<ul> <li>0° to 40° degree Celsius</li> <li>10 ~ 85% Non-Condensing</li> </ul>		
Dimensions & Weight  • 65.3mm x 56.3mm 23 grams			

#### **Python SDK**



Available on GitHub

Note: Please refer to README.md file first for proper configuration

**Python SDK** 



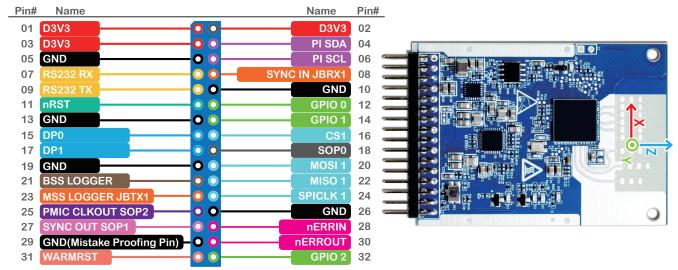


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#### mmWAVE SENSOR EVALUATION SOLUTION

# mmWave Pin Assignment





mmWAVE SENSOR EVALUATION SOLUTION

#### **BATMAN BM601 mmWAVE SENSOR MODULE**

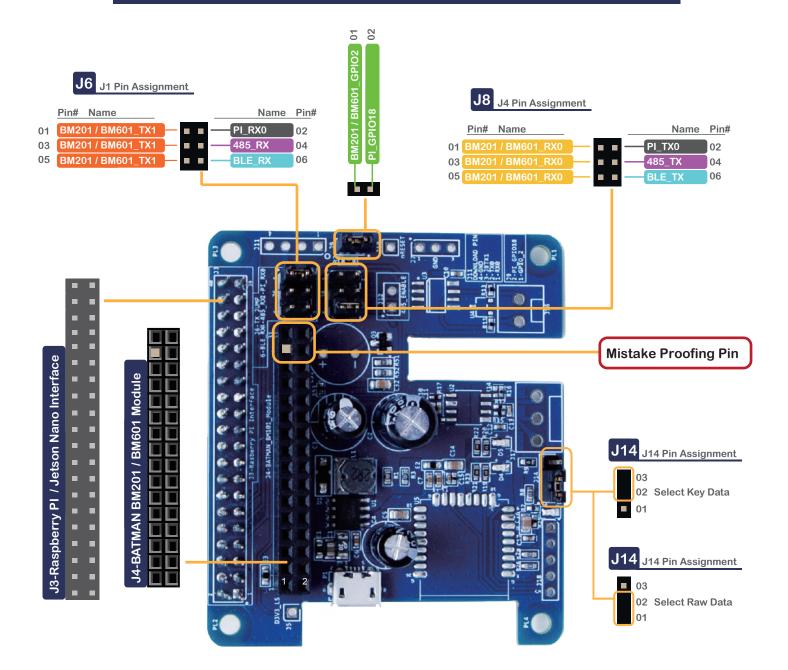
### **J3 Pin Assignment**

O9	Jo Pili Assigninent				
D3V3	Pin No	Name	Pin Type	Function Description	
D3V3	01	D3V3	1	POWER DC 3V3 Input	
O4   SDA	02	D3V3	ı	POWER DC 3V3 Input	
OS	03	D3V3	ı	POWER DC 3V3 Input	
06         SCL         IO         I2C Pin           07         RS232 RX0         I         UART A Receive           08         SYNC IN JBRX1         I         Low frequency Synchronization signal input, UART B Receive           09         RS232 TX0         O         UART A Transmit           10         GND         GROUND         Digital ground           11         nRST         I         Power on reset for chip. Active low           12         GPIO 0         I         Select KeyData or RawData           13         GND         GROUND         Digital ground           14         GPIO 1         I         Reserved           15         DP0         IO         GPIO Pin           16         CS1         IO         SPI Channel A - chip Select           17         DP1         IO         GPIO Pin           18         SOP0         O         SOP0           19         GND         GROUND         Digital ground           20         MOSI 1         IO         SPI Channel A - Master Out Slave In           21         BSS LOGGER         IO         BSS LOGGER           22         MISO 1         IO         SPI Channel A - Master In Slave Out	04	SDA	10	I2C Pin	
O7 RS232 RX0	05	GND	GROUND	Digital ground	
SYNC IN JBRX1	06	SCL	10	I2C Pin	
O9	07	RS232 RX0	ı	UART A Receive	
10 GND GROUND Digital ground   11 nRST   1 Power on reset for chip. Active low   12 GPIO 0	08	SYNC IN JBRX1	ı	Low frequency Synchronization signal input, UART B Receive	
11 nRST I Power on reset for chip. Active low 12 GPIO 0 I Select KeyData or RawData 13 GND GROUND Digital ground 14 GPIO 1 I Reserved 15 DPO IO GPIO Pin 16 CS1 IO SPI Channel A - chip Select 17 DP1 IO GPIO Pin 18 SOPO O SOPO 19 GND GROUND Digital ground 20 MOSI 1 IO SPI Channel A - Master Out Slave In 21 BSS LOGGER IO BSS LOGGER 22 MISO 1 IO SPI Channel A - Master In Slave Out 23 MSS LOGGER JBTX1 O UART B Transmit 24 SPICLK 1 IO SPI Channel A - Clock 25 SOP2 I SOP2 26 GND GROUND Digital ground 27 SOP1 I SOP1 28 nERRIN I Failsafe input to the device. Nerror output from any other device can be concentrated in the error signaling monitor module inside the device and appropriate action can be taken by Firmware. 29 GND GROUND Mistake Proofing Pin O Open drain fail safe output signal. Connected to PMIC/Processor/MCU to indicate that some severe criticatlity fault has happened. Recovery would be through reset.	09	RS232 TX0	0	UART A Transmit	
12   GPIO 0	10	GND	GROUND	Digital ground	
13 GND GROUND Digital ground  14 GPIO 1 I Reserved  15 DPO IO GPIO Pin  16 CS1 IO SPI Channel A - chip Select  17 DP1 IO GPIO Pin  18 SOPO O SOPO  19 GND GROUND Digital ground  20 MOSI 1 IO SPI Channel A - Master Out Slave In  21 BSS LOGGER IO BSS LOGGER  22 MISO 1 IO SPI Channel A - Master In Slave Out  23 MSS LOGGER JBTX1 O UART B Transmit  24 SPICLK 1 IO SPI Channel A - Clock  25 SOP2 I SOP2  26 GND GROUND Digital ground  27 SOP1 I SOP1  28 nERRIN I Failsafe input to the device. Nerror output from any other device can be concentrated in the error signaling monitor module inside the device and appropriate action can be taken by Firmware.  29 GND GROUND Mistake Proofing Pin  O Open drain fail safe output signal. Connected to PMIC/Processor/MCU to indicate that some severe criticallity fault has happened. Recovery would be through reset.	11	nRST	ı	Power on reset for chip. Active low	
14 GPIO 1 I Reserved 15 DP0 IO GPIO Pin 16 CS1 IO SPI Channel A - chip Select 17 DP1 IO GPIO Pin 18 SOP0 O SOP0 19 GND GROUND Digital ground 20 MOSI 1 IO SPI Channel A - Master Out Slave In 21 BSS LOGGER IO BSS LOGGER 22 MISO 1 IO SPI Channel A - Master In Slave Out 23 MSS LOGGER JBTX1 O UART B Transmit 24 SPICLK 1 IO SPI Channel A - Clock 25 SOP2 I SOP2 26 GND GROUND Digital ground 27 SOP1 I SOP1 28 nERRIN I Failsafe input to the device. Nerror output from any other device can be concentrated in the error signaling monitor module inside the device and appropriate action can be taken by Firmware. 29 GND GROUND Mistake Proofing Pin 30 nERROUT O Open drain fail safe output signal. Connected to PMIC/Processor/MCU to indicate that some severe criticallity fault has happened. Recovery would be through reset.	12	GPIO 0	1	Select KeyData or RawData	
15 DP0 IO GPIO Pin  16 CS1 IO SPI Channel A - chip Select  17 DP1 IO GPIO Pin  18 SOP0 O SOP0  19 GND GROUND Digital ground  20 MOSI 1 IO SPI Channel A - Master Out Slave In  21 BSS LOGGER IO BSS LOGGER  22 MISO 1 IO SPI Channel A - Master In Slave Out  23 MSS LOGGER JBTX1 O UART B Transmit  24 SPICLK 1 IO SPI Channel A - Clock  25 SOP2 I SOP2  26 GND GROUND Digital ground  27 SOP1 I SOP1  28 NERRIN I Failsafe input to the device. Nerror output from any other device can be concentrated in the error signaling monitor module inside the device and appropriate action can be taken by Firmware.  29 GND GROUND Mistake Proofing Pin  O Open drain fail safe output signal. Connected to PMIC/Processor/MCU to indicate that some severe criticatlity fault has happened. Recovery would be through reset.	13	GND	GROUND	Digital ground	
16 CS1 IO SPI Channel A - chip Select  17 DP1 IO GPIO Pin  18 SOP0 O SOP0  19 GND GROUND Digital ground  20 MOSI 1 IO SPI Channel A - Master Out Slave In  21 BSS LOGGER IO BSS LOGGER  22 MISO 1 IO SPI Channel A - Master In Slave Out  23 MSS LOGGER JBTX1 O UART B Transmit  24 SPICLK 1 IO SPI Channel A - Clock  25 SOP2 I SOP2  26 GND GROUND Digital ground  27 SOP1 I SOP1  28 nERRIN I Failsafe input to the device. Nerror output from any other device can be concentrated in the error signaling monitor module inside the device and appropriate action can be taken by Firmware.  29 GND GROUND Mistake Proofing Pin  O Open drain fail safe output signal. Connected to PMIC/Processor/MCU to indicate that some severe criticatlity fault has happened. Recovery would be through reset.	14	GPIO 1	ı	Reserved	
17 DP1 IO GPIO Pin  18 SOP0 O SOP0  19 GND GROUND Digital ground  20 MOSI 1 IO SPI Channel A - Master Out Slave In  21 BSS LOGGER IO BSS LOGGER  22 MISO 1 IO SPI Channel A - Master In Slave Out  23 MSS LOGGER JBTX1 O UART B Transmit  24 SPICLK 1 IO SPI Channel A - Clock  25 SOP2 I SOP2  26 GND GROUND Digital ground  27 SOP1 I SOP1  28 NERRIN I Failsafe input to the device. Nerror output from any other device can be concentrated in the error signaling monitor module inside the device and appropriate action can be taken by Firmware.  29 GND GROUND Mistake Proofing Pin  30 NERROUT O OPEN drain fail safe output signal. Connected to PMIC/Processor/MCU to indicate that some severe criticatlity fault has happened. Recovery would be through reset.	15	DP0	10	GPIO Pin	
18 SOP0 O SOP0  19 GND GROUND Digital ground  20 MOSI 1 IO SPI Channel A - Master Out Slave In  21 BSS LOGGER IO BSS LOGGER  22 MISO 1 IO SPI Channel A - Master In Slave Out  23 MSS LOGGER JBTX1 O UART B Transmit  24 SPICLK 1 IO SPI Channel A - Clock  25 SOP2 I SOP2  26 GND GROUND Digital ground  27 SOP1 I SOP1  28 NERRIN I Failsafe input to the device. Nerror output from any other device can be concentrated in the error signaling monitor module inside the device and appropriate action can be taken by Firmware.  29 GND GROUND Mistake Proofing Pin  30 NERROUT O Open drain fail safe output signal. Connected to PMIC/Processor/MCU to indicate that some severe criticatlity fault has happened. Recovery would be through reset.	16	CS1	10	SPI Channel A - chip Select	
19 GND GROUND Digital ground 20 MOSI 1 IO SPI Channel A - Master Out Slave In 21 BSS LOGGER IO BSS LOGGER 22 MISO 1 IO SPI Channel A - Master In Slave Out 23 MSS LOGGER JBTX1 O UART B Transmit 24 SPICLK 1 IO SPI Channel A - Clock 25 SOP2 I SOP2 26 GND GROUND Digital ground 27 SOP1 I SOP1 28 nERRIN I Failsafe input to the device. Nerror output from any other device can be concentrated in the error signaling monitor module inside the device and appropriate action can be taken by Firmware. 29 GND GROUND Mistake Proofing Pin 30 nERROUT O Open drain fail safe output signal. Connected to PMIC/Processor/MCU to indicate that some severe criticatlity fault has happened. Recovery would be through reset.	17	DP1	10	GPIO Pin	
20 MOSI 1 IO SPI Channel A - Master Out Slave In 21 BSS LOGGER IO BSS LOGGER  22 MISO 1 IO SPI Channel A - Master In Slave Out  23 MSS LOGGER JBTX1 O UART B Transmit  24 SPICLK 1 IO SPI Channel A - Clock  25 SOP2 I SOP2  26 GND GROUND Digital ground  27 SOP1 I SOP1  28 NERRIN I Failsafe input to the device. Nerror output from any other device can be concentrated in the error signaling monitor module inside the device and appropriate action can be taken by Firmware.  29 GND GROUND Mistake Proofing Pin  30 NERROUT O Open drain fail safe output signal. Connected to PMIC/Processor/MCU to indicate that some severe criticatlity fault has happened. Recovery would be through reset.	18	SOP0	0	SOP0	
21 BSS LOGGER   IO BSS LOGGER	19	GND	GROUND	Digital ground	
22 MISO 1	20	MOSI 1	10	SPI Channel A - Master Out Slave In	
23 MSS LOGGER JBTX1 O UART B Transmit  24 SPICLK 1 IO SPI Channel A - Clock  25 SOP2 I SOP2  26 GND GROUND Digital ground  27 SOP1 I SOP1  28 nERRIN I Failsafe input to the device. Nerror output from any other device can be concentrated in the error signaling monitor module inside the device and appropriate action can be taken by Firmware.  29 GND GROUND Mistake Proofing Pin  30 nERROUT O Open drain fail safe output signal. Connected to PMIC/Processor/MCU to indicate that some severe criticatlity fault has happened. Recovery would be through reset.	21	BSS LOGGER	10	BSS LOGGER	
24   SPICLK 1   IO   SPI Channel A - Clock     25   SOP2	22	MISO 1	10	SPI Channel A - Master In Slave Out	
25 SOP2 I SOP2  26 GND GROUND Digital ground  27 SOP1 I SOP1  28 nERRIN I Failsafe input to the device. Nerror output from any other device can be concentrated in the error signaling monitor module inside the device and appropriate action can be taken by Firmware.  29 GND GROUND Mistake Proofing Pin  30 nERROUT O Open drain fail safe output signal. Connected to PMIC/Processor/MCU to indicate that some severe criticatlity fault has happened. Recovery would be through reset.	23	MSS LOGGER JBTX1	0	UART B Transmit	
26 GND GROUND Digital ground  27 SOP1 I SOP1  28 nERRIN I Failsafe input to the device. Nerror output from any other device can be concentrated in the error signaling monitor module inside the device and appropriate action can be taken by Firmware.  29 GND GROUND Mistake Proofing Pin  30 nERROUT O Open drain fail safe output signal. Connected to PMIC/Processor/MCU to indicate that some severe criticatlity fault has happened. Recovery would be through reset.	24	SPICLK 1	10	SPI Channel A - Clock	
27 SOP1  1 SOP1  28 nERRIN  I Failsafe input to the device. Nerror output from any other device can be concentrated in the error signaling monitor module inside the device and appropriate action can be taken by Firmware.  29 GND  GROUND Mistake Proofing Pin  30 nERROUT  O Open drain fail safe output signal. Connected to PMIC/Processor/MCU to indicate that some severe criticatlity fault has happened. Recovery would be through reset.	25	SOP2	1	SOP2	
Pailsafe input to the device. Nerror output from any other device can be concentrated in the error signaling monitor module inside the device and appropriate action can be taken by Firmware.  29 GND GROUND Mistake Proofing Pin  Open drain fail safe output signal. Connected to PMIC/Processor/MCU to indicate that some severe criticatlity fault has happened. Recovery would be through reset.	26	GND	GROUND	Digital ground	
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Open drain fail safe output signal. Connected to PMIC/Processor/MCU to indicate that some severe criticatlity fault has happened. Recovery would be through reset.	28	nERRIN	I	device can be concentrated in the error signaling monitor module inside the device and appropriate action can be	
PMIC/Processor/MCU to indicate that some severe criticatlity fault has happened. Recovery would be through reset.	29	GND	GROUND	Mistake Proofing Pin	
	30	nERROUT	0	PMIC/Processor/MCU to indicate that some severe criticatlity	
31 WARMRST IO Open drain fail safe warm reset signal. Can be driven from PMIC for diagnostic or can be used as status signal that the device is going through reset.	31	WARMRST	10		
32 GPIO2 O LED Indicator	32	GPIO2	0	LED Indicator	

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## mmWave Raspberry Pi Hat Pin Assignment



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#### **Product Dimensions**



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