**RAJASHEKHAR**  [kingr6647@gmail.com](mailto:kingr6647@gmail.com)

✆+91-7259551912

**OBJECTIVE**

Seeking employment that allows me to grow professionally, while being able to utilize my skills for the betterment of the organization with the best use of my dedication, determination

**Technical Summary:**

**Core Java**:

* Good practical knowledge of oops concept like Abstraction,Polymaphisim,Inheritance etc.
* Good understanding of Serialization.
* Excellent in Collection Framework.
* Good in Thread and Exception Handling

**SQL:**

* Good understanding of RDBMS Concept
* Comfortable in writing SQL statements like DDL,

DML,DCL and TCL.

* Good knowledge of Joins and Normalization.

**EDUCATIONAL PROFILE:**

**Educational Qualification**: Bachelor of engineering in Electronics and Communication Engineering.

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| --- | --- | --- | --- | --- |
| **Qualification** | **Institution** | **University** | **Year of**  **Passing** | **Percentage** |
| B.E(ECE) | Poojya Doddappa Appa College of Engineering Gulbarga | Autonomous | 2015 | 7.44 CGPA |
| PUC | Shree guru vidhya peetha pu college | Pre-university Karnataka | 2011 | 77.5% |
| SSLC | Government High School hipparaga (bag) | Karnataka Secondary Education Examination Board | 2009 | 86.08% |

**Technical Skills**:

* Programming Language: Core Java,SQL,
* IDE:Eclipse,Edit plus,Oracle(11g).

**Personal Skills:**

* Good grasping power.
* Sincere and hard worker.
* Good adaptability and Flexible.
* Quick learner.
* Believe in teamwork and time management.

**Academic Project:**

**Title : “Arithmetic Logic Unit DESIGN”**

In this project there are two approaches

**1 Software Approach**

First we generated the code of ALU using by VHDL. This software not only compiles the given VHDL code but also produces waveform results. .

**2 Hardware Approach**

The VHDL code which implies the hardware part of ALU is downloaded on FPGA processor using JTAG cable interfacing PC and the hardware element for verifying.

**Personal details:**

Name :Rajashekhar

Gender : Male

Date of Birth :20-07-1992

Languages : Kannada, English, Hindi

Marital status     : single

Address : S/O Nagidrappa hipparagi At:Ratkal HNO 110,

Tq:Chicholi Dist:Gulbarga Pin:583234, State:Karnsataka

**DECLARATION:**

I hereby declare that the above information is true to the best of my knowledge. I take full responsibility if any of the above facts are later proved to be inaccurate.

Current location: Bangalore Thanking you

Date : 2015 Rajashekhar