



Sahi Prep Hai Toh Life Set Hai

Topic Title







MFT - Done Part ...

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Content :-

1. Multiprogramming with variable partition 🛹 •

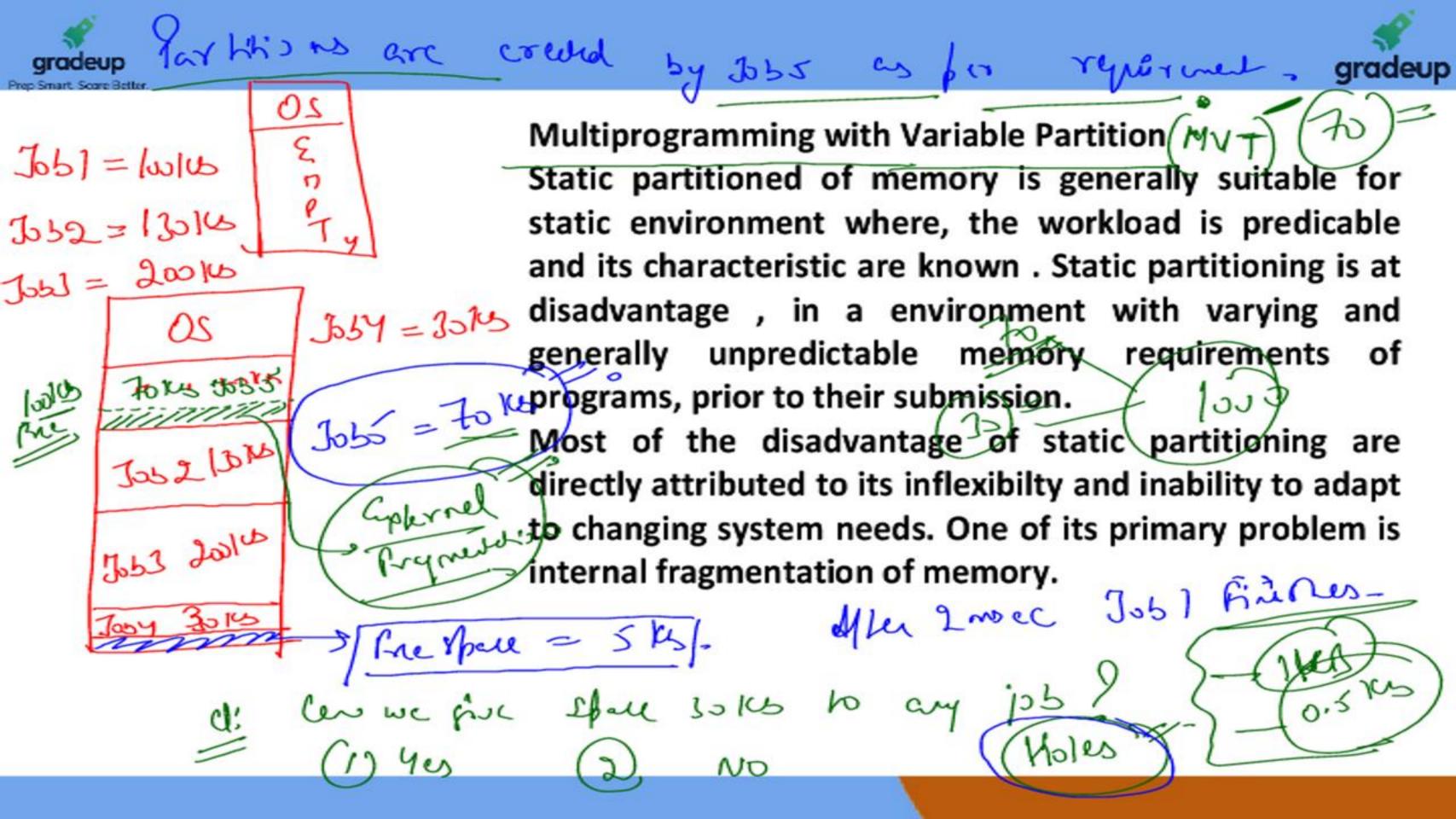
2. Paging

3. Segmentation

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(Today) 6:30 lm Jan mornig 8'.00 an.







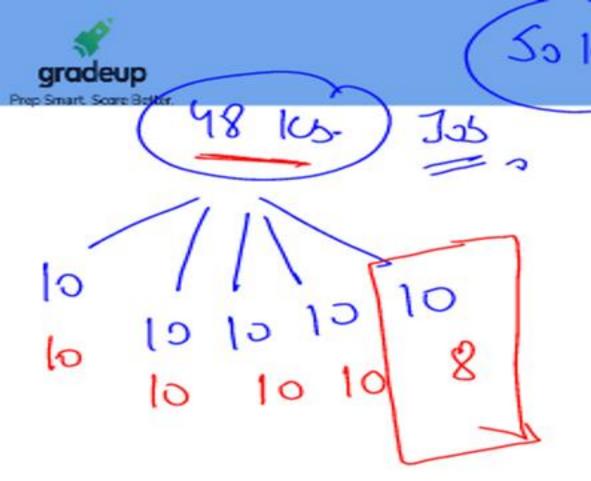
One technique for overcoming external fragmentation is compaction, which means to move the processes in the memory in such a way that the scattered pieces of unused memory (holes) can be placed together, so that any other process demanding contiguous memory for it can be use.





Depending upon the situation either of the following three algorithms are used for selection of a free area of memory to create partition:-

- a. First fit and its variant, next fit
- b. Best fit
- c. Worst fit





a. In first fit, we allocate the first hole that is big enough. Searching can be started either at the beginning of the set of holes(first fit) or where the previous first fit search ended (Next fit). We stop searching as we obtain a free hole that is large enough to accommodate the coming process.

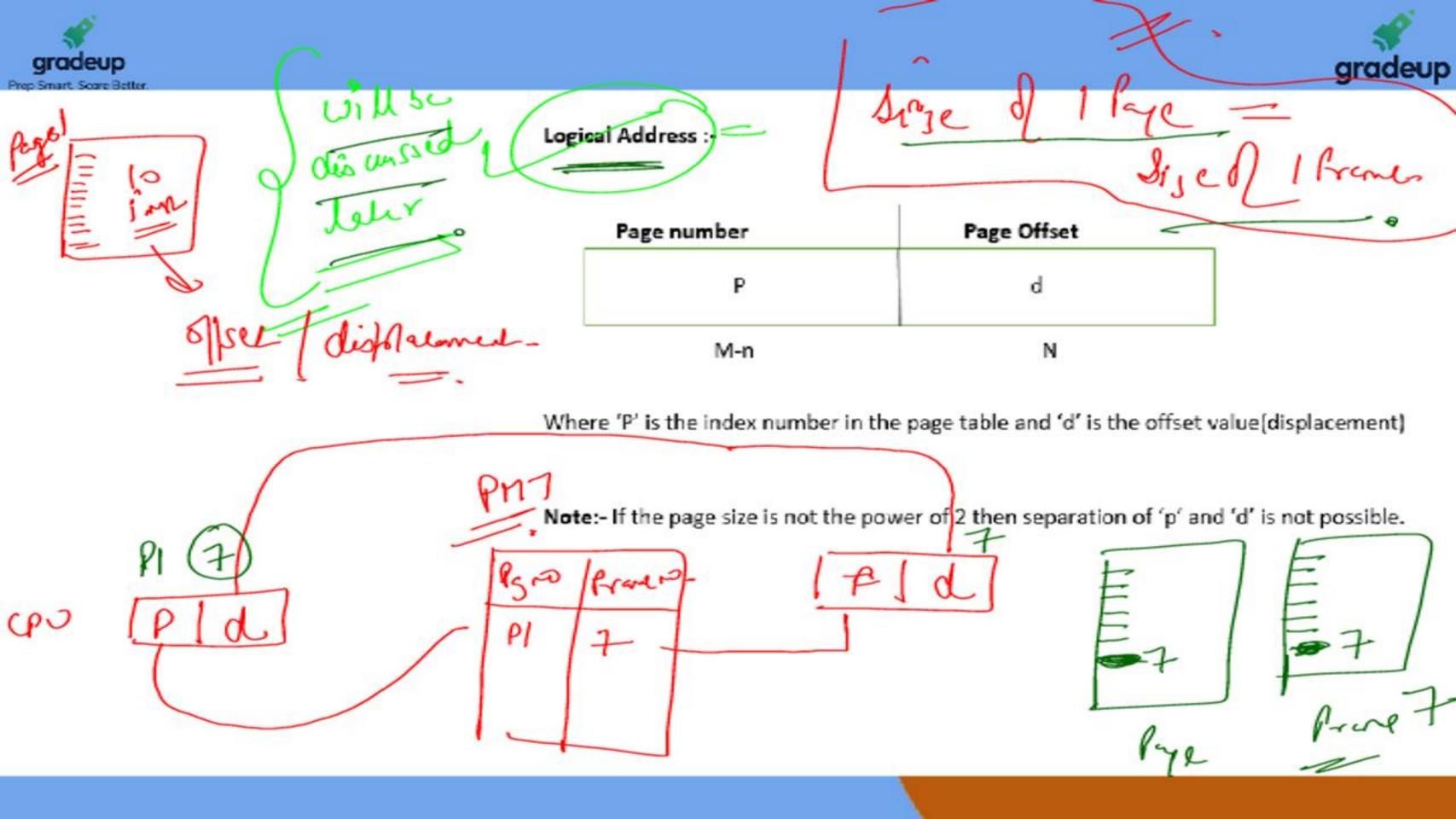
b. In best fit, we allocate the smallest hole that is large enough. Searching of entire list is done unless it is sorted by size. This strategy produces least size of unused memory space (fragments).

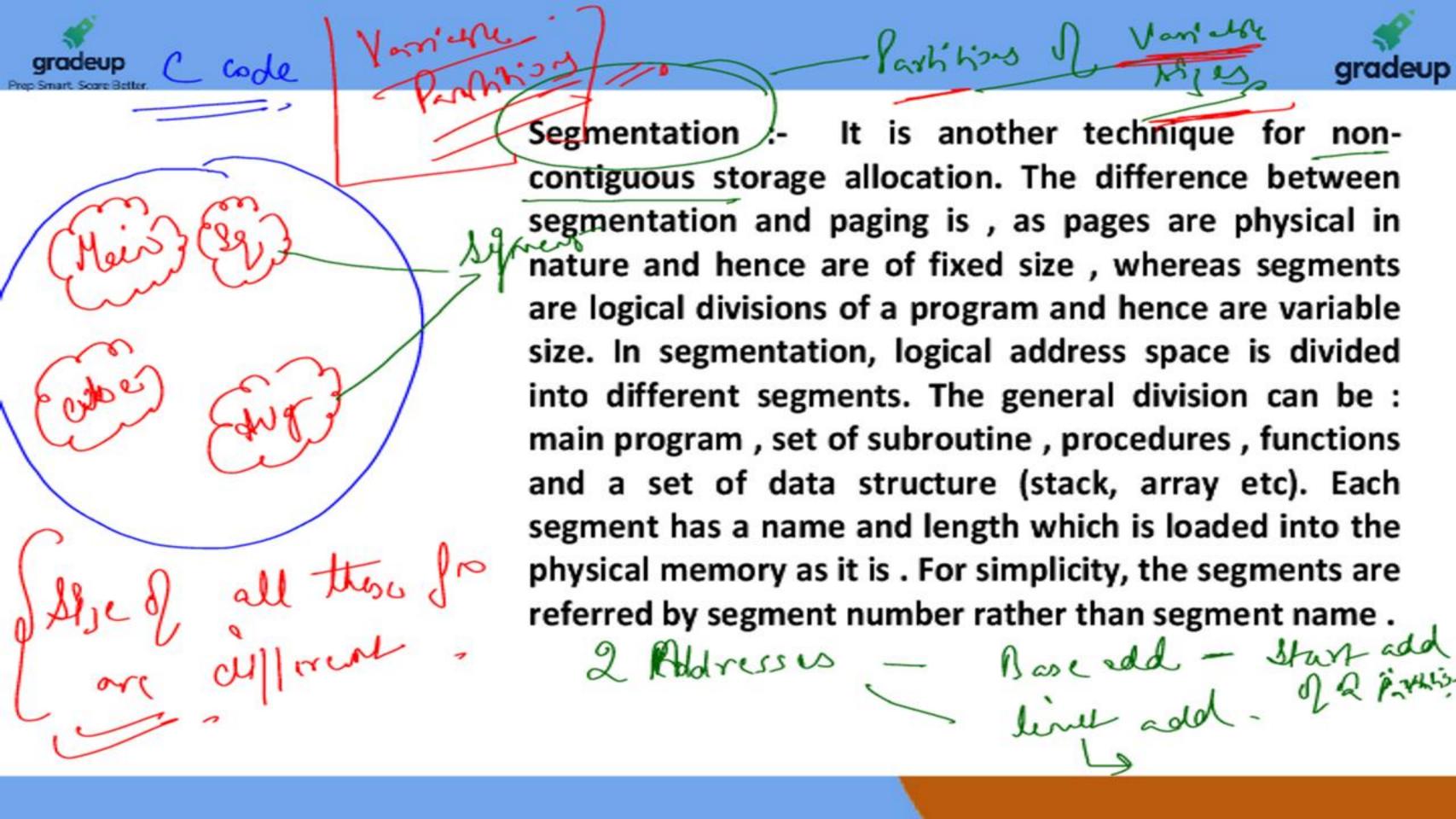
c. In worst case, we allocate the largest hole. Again we search the entire list, unless it is sorted by size. This strategy procures largest size of unused memory space. (Ris is fixe

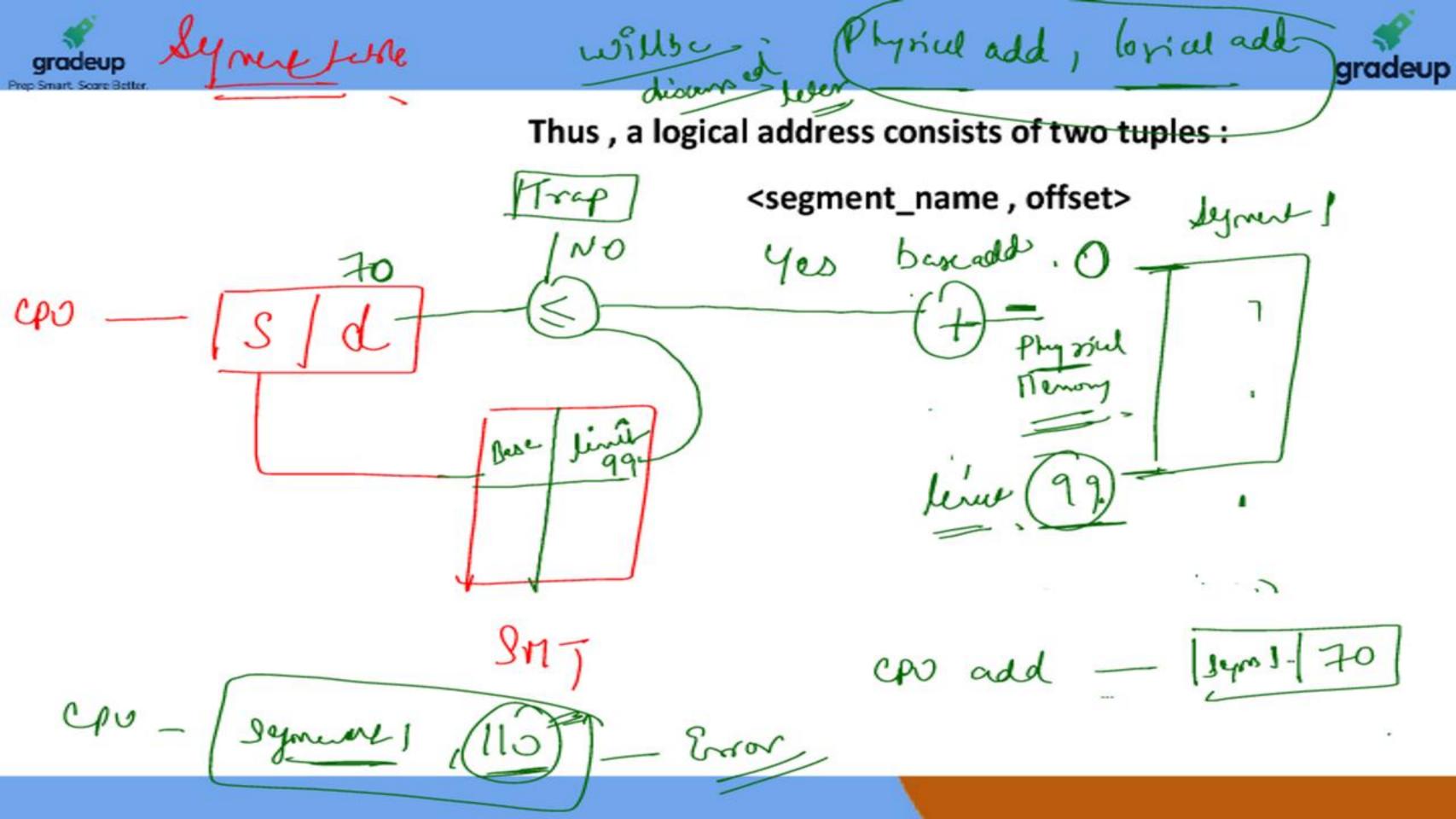
Note: - first fit and Best fit is better that worst fit in terms of deceasing both time and storage utilization . Neither first fit nor best fit can be said better in terms of storage utilization as they again results to external fragmentation. One of the solution is compaction but it is time consuming approach another possible solution is to permit the logical address space of a process to be noncontiguous. Thus allowing a process to be allocated in physical memory . 1

Entire ps is not shred Continuing radeup Paging :- Paging is a memory management schemes that removes the requirement of contiguous allocation of physical memory. Physical address space of a process to be non-contiguous is permitted by this scheme The physical memory is conceptually divided into a number of fixed-size blocks called frames, and the logical address space (virtual address space) is also splited into fixed size blocks called pages. P3 305 25

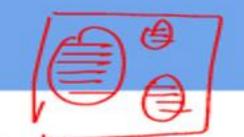
gradeup Paring - Euch liged Parrisons of a Job celled as (1ges) gradeup
In paging system, address translation is performed with
the help of a mapping table called page-map table (PMT).
PMT is constructed at process loading time in order to
establish the correspondence between the virtual and
Part Mephysical address . In PMT, there is one entry for each
2 PS page of a process indexed with the page number. The
γρη γη value of each entry is the number or frames (Composed
$\frac{1}{2}$ $\frac{\rho_3}{\rho_1}$ $\frac{\rho_3}{\rho_2}$ of high order, page level bits) in the physical memory
when the corresponding page is placed.
1) 1 10 Created In every process.
Partitions of menony (Frames) Pages are shored on Frames.













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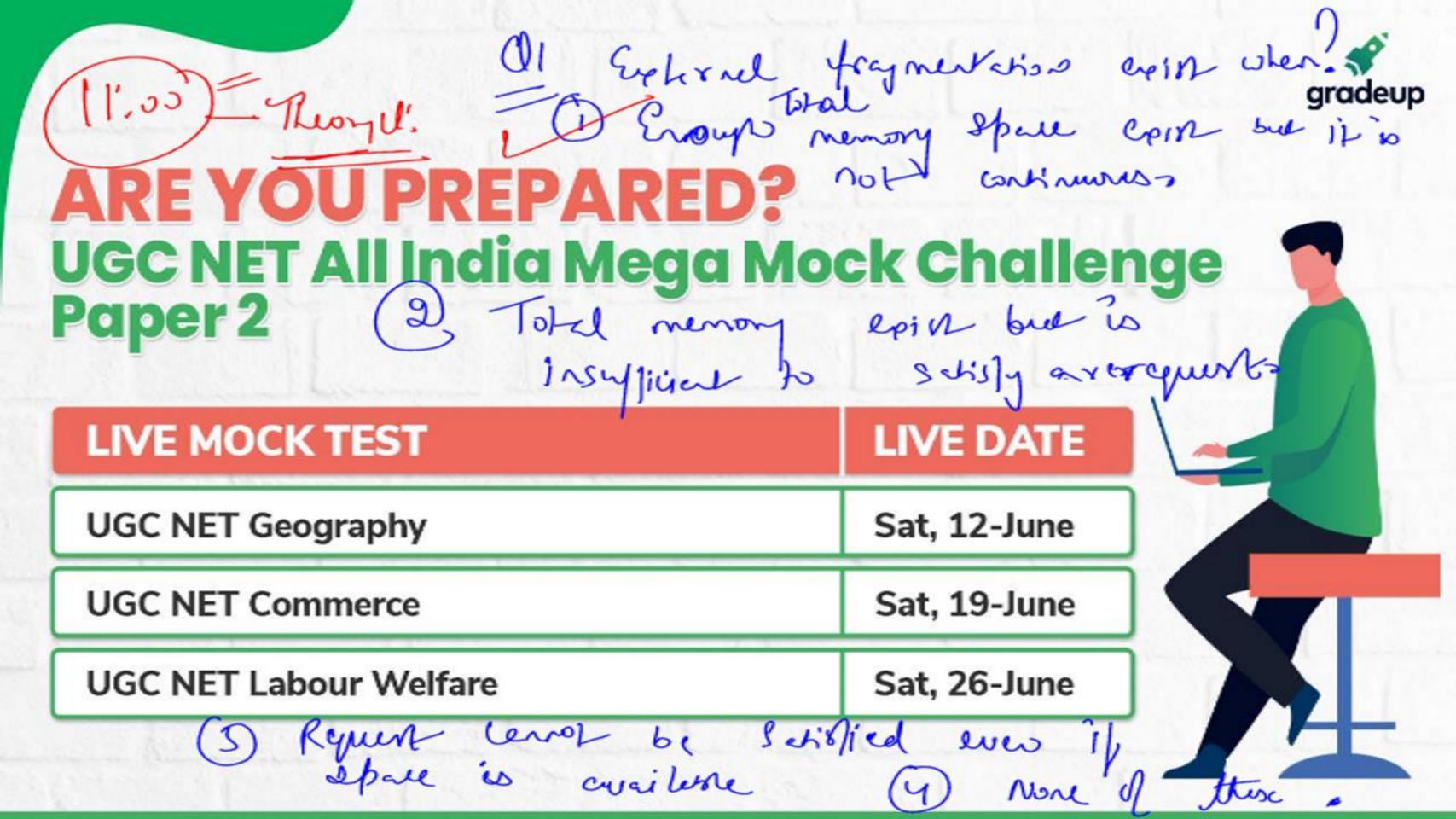
memory eliminated by using paging.

Paring applied

Segmentation with paging (Combined Approach): // e Both approaches paging and segmentation have their respective advantages and disadvantages and none is superior to other over all characteristics. Some computer systems combine the two approaches to avail the advantages of both approaches in a single system. This approach facilitates to use the segmentation from the user's point of view also to divided each segment into pages of fixed size for allocation purposes. In this way, the combined system retains not only the advantages of segmentation but also the problems of complex segment placement and management of secondary

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Paper 01

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