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TI third parties are making other emulation interfaces available, including PCMCIA, USB, and Ethernet connections. With these connection methods, scan management is performed by the CPU of the host computer and an external processor, respectively. SPRA439E- January 2011 Emulation Fundamentals for TI's DSP Solutions 3 Submit Documentation Feedback

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between the target and the emulation HW. The protocol used within the emulation logic of TI processors is the Joint Test Action Group (JTAG). This section covers the basics of ensuring proper communication setup between the emulation hardware and the target processor. 2 Emulation Fundamentals for TI's DSP Solutions SPRA439C- October 2005

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Web-Based Simulators for DSPs and Microcontrollers

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how the emulation logic and emulation tools work together with the TI digital signal processors. By understanding the fundamentals of emulation, you will be able to accelerate the process of setting

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