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The purpose of this book is to bring together the device fundamentals that govern the behavior of CMOS and bipolar transistors into a single text, with emphasis on those parameters and performance factors that are particularly important for VLSI (very-large scale-integration) devices of deep-submicron dimensions.

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Target Audience: Graduate students and advanced undergraduates in electrical engineering, computer engineering and physics interested in VLSI devices and circuits. Course Description: Study of device phenomena in very small and high-speed devices including effects of scaling, interfaces, and high doping. Control of electrical characteristics (threshold voltage, breakdown voltage, on/off currents, current gain, switching speed) in small MOS structures.

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