

Objective Type Questions In Microprocessor With Answers

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Objective Type Questions In Microprocessor

In an intel 8085A microprocessor, why is READY signal used? a. To indicate to user that the microprocessor is working and is ready for use. b. To provide proper WAIT states when the microprocessor is communicating with a slow peripheral device. c. To slow down a fast peripheral device so as to communicate at the microprocessor's device. d.

Microprocessor Multiple choice Questions and Answers-Intel ...

MICROPROCESSORS Questions :-1. The minimum number of transistors required to implement a two input AND gate is. A. 2 B. 4 C. 6 D. 8. Answer: C. 2. Using DeMorgan's Theorem we can convert any AND-OR structure into. A. NAND-NAND B. OR-NAND C. NAND-NOR D. NOR-NAND. Answer: A. 3. For a memory with a 16-bit address space, the address ability is A. 16 bits B. 8 bits

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Microprocessor and Microcontroller Multiple Choice ...

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Microprocessor Basics Objective test

Paper : Objective Questions for 8085 Microprocessor. 1. In Synchronous data Transfer type both Transmitter and Receiver will operate in a) Same Clock pulse b) Different Clock pulse c) None of the above 2. The term PSW Program Status word refers a) Accumulator & Flag register b) H and L register c) Accumulator & Instruction register d) B and C register 3.

(Paper) Objective Questions for 8085 Microprocessor ...

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The appropriate modes of operation of the 8255 for (i) and (ii) would be (A) Mode 0 for (i) and Mode 1 for (ii) (B) Mode 1 for (i) and Mode 2 for (ii) (C) Mode 2 for (i) and Mode 0 for (ii) (D) Mode 2 for (i) and Mode 1 for (ii) 13. The microprocessor 8085 has ____ basic instructions and ____ opcodes.

Microprocessor Multiple Choice Ques | Central Processing ...

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C-DAC,C-CAT Microprocessor objective type questions with ...

a. Ability to store one of the operands before the execution of an instruction b. Ability to store the result after the execution of an instruction c. Both a & b d. None of the above.

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8085 MICROPROCESSOR OBJECTIVE TYPE QUESTIONS WITH ANSWERS

Explanation: 8086 microprocessor is a 16-bit microprocessor and all the registers of 8086 are 16-bit registers. So, Instruction pointer is a 16 bit register.

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