

## *Objective Questions In Microprocessor 8085 With Answers*

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### **Objective Questions In Microprocessor 8085**

Paper : Objective Questions for 8085 Microprocessor. 1. In Synchronous data Transfer type both Transmitter and Receiver will operate in a) Same Clock pulse b) Different Clock pulse c) None of the above 2. The term PSW Program Status word refers a) Accumulator & Flag register b) H and L register c) Accumulator & Instruction register d) B and C register 3.

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Multiple choice questions on 8085 microprocessor pdf 1 Which is the microprocessor comprises: a. 3 Accumulator based microprocessor example are: a. Motorola.A microprocessor is a chip integrating all the functions of a motorola pdf gp CPU of a computer.

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MICROPROCESSORS Questions :-1. The minimum number of transistors required to implement a two input AND gate is. A. 2 B. 4 C. 6 D. 8. Answer: C. 2. Using DeMorgan's Theorem we can convert any AND-OR structure into. A. NAND-NAND B. OR-NAND C. NAND-NOR D. NOR-NAND. Answer: A. 3. For a memory with a 16-bit address space, the address ability is A. 16 bits B. 8 bits

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The data bus in 8080A/8085 microprocessor is a group of: a. eight bidirectional lines that are used to transfer 8 bits between the microprocessor and its I/O and memory: b. sixteen bidirectional lines that are used for data transfer between the microprocessor and memory: c. eight unidirectional lines that are used for I/O devices: d.

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26. The Intel 8086 microprocessor is a \_\_\_\_ processor. a) 8 bits. b) 4 bits. c) 16 bits. d) 32 bits. Answer: c . 27. What is /are the improvement is in the architecture of 8086 over 8085 architecture. a) A 16-bit ALU, a set of 16-bit registers. b) Segmented memory addressing. c) Fetched instruction queue for overlapped fetching and execution. d) All of the mentioned

### **8086 Microprocessor Multiple choice Questions & Answers**

6. The program counter in 8085 microprocessor is a 16-bit register, because. It counts 16 bits at a

time; There are 16 address times; It facilitates the users storing 16-bit data temporarily; It has to fetch two 8-bit data at a time.

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BHE of 8086 microprocessor signal is used to interface the a) Even bank memory b) Odd bank memory c) I/O d) DMA 21. In 8086 microprocessor the following has the highest priority among all type interrupts. a) NMI b) DIV 0 c) TYPE 255 d) OVER FLOW 22.

### **Architecture of Microprocessors - NPTEL**

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### **Microprocessors Objective Questions - Set 7 ...**

Multiple Choice Questions on 8086 Microprocessor 1. A microprocessor is a \_\_\_\_ chip integrating all the functions of a CPU of a computer. A. multiple B. single C. double D. triple ANSWER: B 2.

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