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Authors: J. D. Plummer, M. D. Deal, and P. B. Griffin SILICON VLSI TECHNOLOGY

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Chapter 2 Problems 2.1 Sketch a process flow that would result in the structure shown in Figure 1-34 by drawing a series of drawings similar to those in this chapter.

Chapter 2 Problems - GUC

Text: Silicon VLSI Technology: Plummer, Deal and Griffin Prerequisite: EE/MSE486 (Fundamentals of Integrated Circuit Technology) or EE/MSE 502 (Introduction to MEMS) or EE 527 (Solid State Laboratory Techniques) or equivalent exposure to basics of photolithography and diffusion/reaction problems, or consent of instructor.

EE/MSE 528 Physics and Modeling of VLSI Fabrication

Readings Download Course Materials; Wherever possible, the book citations below reflect the specific editions used in the course. Textbooks Recommended. Plummer, James, Michael Deal, and Peter Griffin. Silicon VLSI Technology: Fundamentals, Practice, and Modeling. Upper Saddle River, NJ: Prentice Hall, 2000.

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