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### Analog computer - Wikipedia

Up until this point, our analysis of transistor logic circuits has been limited to the TTL design paradigm, whereby bipolar transistors are used, and the general strategy of floating inputs being equivalent to "high" (connected to V cc) inputs—and correspondingly, the allowance of "open-collector" output stages—is maintained. This, however, is not the only way we can build logic gates.

# CMOS Gate Circuitry | Logic Gates | Electronics Textbook

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# What is CMOS Sensor | CMOS Image Sensors | CCD Vs CMOS

Low Voltage, Low Power CMOS Bandgap References ECE 1352 – Reading Assignment University of Toronto Professor K. Phang Timothy Lo

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