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### **Design of Analog CMOS Integrated Circuits - Engineering**

EECE488: Analog CMOS Integrated Circuit Design Introduction and Background Shahriar Mirabbasi  
Department of Electrical and Computer Engineering University of British Columbia  
shahriar@ece.ubc.ca Technical contributions of Pedram Lajevardi in revising the slides is greatly acknowledged. SM 2 EECE 488 – Set 1: Introduction and Background Marking

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McGraw-Hill First Edition of the Year for the book "Design of Analog CMOS Integrated Circuits," 2001 : ISSCC Beatrice Winner Award for Editorial Excellence J. Savoj and B. Razavi, "Design of Half-Rate Clock and Data Recovery Circuits for Optical Communication Systems," Proc. Design Automation Conference, pp. 121-126, June 2001.

### **Behzad Razavi | EE**

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