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2 APPENDIX F. SELECTED SOLUTIONS (d) Memory Address - Location 5: 0000 0000 0000 0110 Refers to location 6. Value stored in location 6 is 1111 1110 1101 0011 4.7 60 opcodes = 6 bits 32 registers = 5 bits So number of bits required for IMM = $32 - 6 - 5 - 5 = 16$ Since IMM is a 2's complement value, its range is $-2^{15} \dots (2^{15}-1) = -32768 \dots 32767$.

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Selected Solutions F.9 Chapter 9 Solutions 9.1 The most important advantage of doing I/O through a trap routine is the fact that it is not necessary for the programmer to know the gory low-level details of the specific hardware's input/output mechanism. These details include: the hardware data registers for the input and output devices

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