# Laboratory Exercise 0

## Getting Started

There are two parts to this introductory laboratory exercise:

1. Perform the provided tutorial called "Using the ModelSim-Intel FPGA Simulator with Verilog Testbenches". This tutorial is included as part of the *Design Files* for this exercise. You will need to know how to use the ModelSim simulator for Laboratory Exercises 1 and 2.

2. To re-familiarize yourself with the Quartus Prime software tools, which you have used previously in ECE241, read through the provided tutorial called "Quartus Prime Introduction Using Verilog Designs". This tutorial is included as part of the *Design Files* for this exercise. You will use the Quartus Prime software in Laboratory Exercises 1 and 2.