Practice questions about parity bits

- Q1. Compute the even parity bits for the data bits: 11010111 and 10001010
- Q2. Compute the **odd parity** bits for the data bits: 11010111 and 10001010
- Q3. Transform the following bit string (D) into a 3×8 matrix and compute the two-dimensional even parity EDC bits.

Q4. Assume that a sender transmits <D, EDC> as shown and computed in Q3. Next, assume that the receiver receives D' and EDC' as follows:

Show that the receiver can detect the single bit error in D' and correct it.

Q5. Referring to Q3, if a sender transmits <D, EDC>, <u>try</u> to introduce two bit errors in the received <D', EDC'> so that the receiver cannot detect it.

Practice questions about **CRC** computation

Q1. Add the pairs of A and B given below using modulo-2 arithmetic. Let the result be C.

Q2. Using the binary strings from Q1, subtract B from C in part (a) and A from C in part (b) using modulo-2 arithmetic.

Practice questions about **CRC** computation

Q3. Consider the following D and G bit-strings.

- (a) For the given G bit-string, how many bits of CRC code will be computed? In other words, what is the value of r?
- (b) Compute the bit string: D.2^r.
- (c) Divide D.2^r by G using modulo-2 arithmetic to find the CRC bits. Let these CRC bits be denoted by EDC.
- (d) Show the bit string <D, EDC> that is transmitted by the sender.
- (e) Let D' = 1 1 0 1 1 0 1 0 and EDC' = EDC, and let the receiver receive <D',EDC'> after the sender transmits the bits in (d).
 - What is the outcome of the receiver's error detection step: error-free frame or erroneous frame?
- (f) Let D' = 0 0 1 1 1 1 0 1 (the complement of D) and EDC' = EDC, and let the receiver receive <D', EDC'> after the sender transmits the bits in (d).
 - What is the outcome of the receiver's error detection step: error-free frame or erroneous frame?

Practice questions about **CRC** computation

Q4. Consider the following data (D) and G bit-strings.

$$G = 10011$$

Compute the frame to be transmitted by the sender.