AXILite GPIO v1.0

IP User Guide (Alpha Release)



January 25, 2023





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IP Summary

Introduction

GPIO stands for General-Purpose Input/Output. It refers to a type of interface found on microcontrollers and single-board computers that allows them to connect to and interact with a wide variety of external devices. GPIO pins can be configured to function as either inputs or outputs, and they can be used to read digital signals or generate digital signals, respectively. This allows them to be used for a wide range of applications, such as controlling LEDs, reading buttons, and communicating with sensors. This GPIO IP is AXILite compliant and hence can be used in a bunch of AXI based systems. A macro block diagram for the top level of this AXILite GPIO is shown in Figure 1.

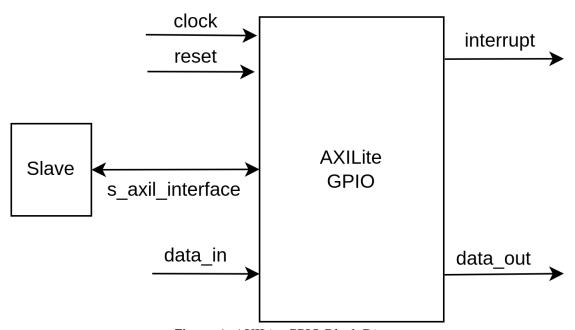


Figure 1. AXILite GPIO Block Diagram



Revision History

Date	Version	Revisions
January 25, 2023	0.01	Initial version AXILite GPIO User Guide Document