

AXIStreaming FIFO v1.0

IP User Guide (Alpha Release)



January 25, 2023

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IP Summary

Introduction

A FIFO is a type of memory buffer that is designed to organize and manipulate the data in such a manner that the oldest element is processed first. It has some control signals too that are used to manage the buffer, such as signals to indicate when the buffer is full or empty. This is an AXIStreaming compliant FIFO for easy integration with other AXI based systems. A macro block diagram of this AXIStreaming FIFO is shown in Figure 1.

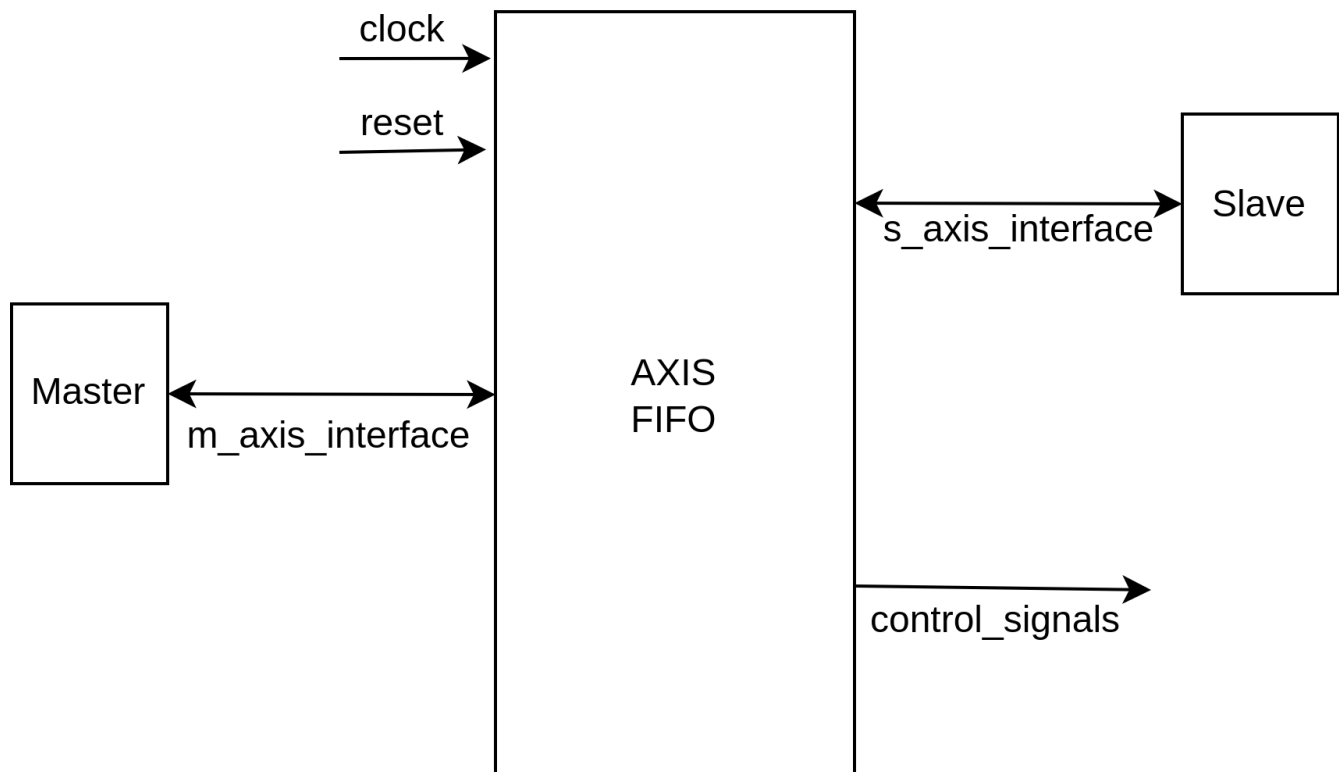


Figure 1. AXIStreaming FIFO Block Diagram

Revision History

Date	Version	Revisions
January 25, 2023	0.01	Initial version AXIStreaming FIFO User Guide Document