

# AXIStreaming UART v1.0

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*IP User Guide (Alpha Release)*



January 25, 2023

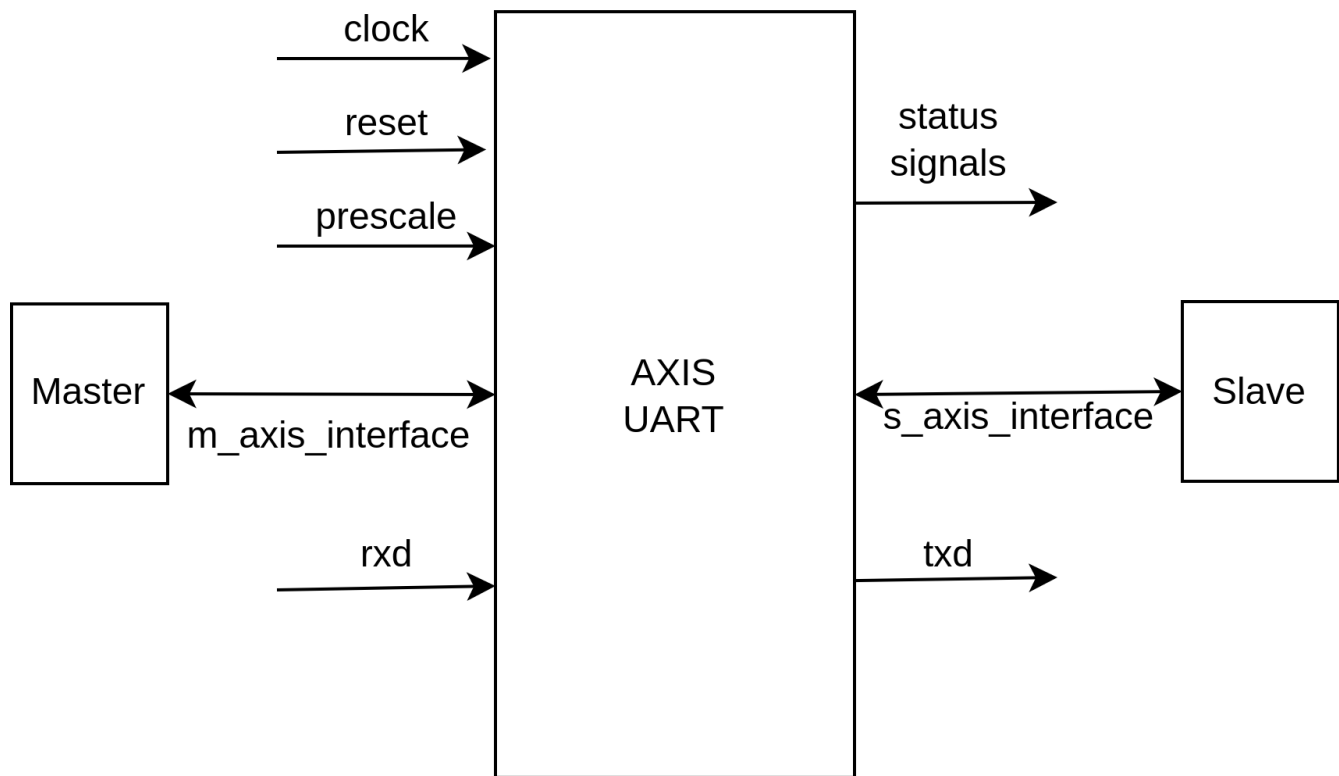
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# IP Summary

## Introduction

The AXIS UART is designed to be used with the AXIS bus, which provides a high-speed, low-latency data path between the UART and other components in the system. The AXIS UART is often used to interface with other peripherals such as memory or other processors, or to provide a serial communication link to external devices such as a PC or another microcontroller. UARTs typically include a transmitter and receiver and can be used for a wide range of applications. This is an AXISstreaming compliant UART IP that can be integrated in a number of systems. A macro block diagram of this AXISstreaming UART is shown in Figure 1.



**Figure 1.** UART Block Diagram

# Revision History

Date	Version	Revisions
January 25, 2023	0.01	Initial version AXIStreaming UART User Guide Document