AXI4 Dual Port RAM v1.0

IP User Guide (Alpha Release)



January 26, 2023





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IP Summary

Introduction

Dual-port RAM is a type of memory that allows multiple devices to access the same memory location simultaneously. This is achieved by having two sets of read and write ports, each with their own address and data buses. This allows for greater flexibility in the design of computer systems, as it allows for multiple processes to access memory at the same time without interfering with each other. Dual-port RAM is commonly used in systems that require high-speed data transfer, such as networking equipment and computer telephony systems. This is an AXI4 compliant Dual Port RAM IP for easy integration with other AXI based systems. A macro block diagram for the top level of this AXI4 Dual Port RAM is shown in Figure 1.

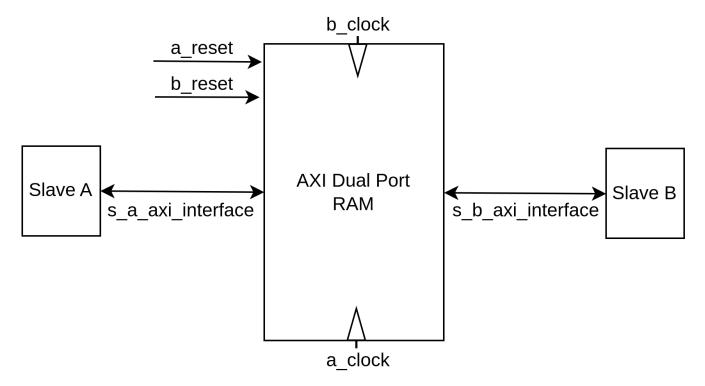


Figure 1. AXI4 Dual Port RAM Block Diagram



Revision History

| Date | Version | Revisions |
|------------------|---------|--|
| January 26, 2023 | 0.01 | Initial version AXI4 Dual Port RAM User Guide Document |