

AXI4 Register v1.0

IP User Guide (Alpha Release)



January 25, 2023

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IP Summary

Introduction

A register is a small amount of memory that is built into a system that is used to temporarily store data that the system frequently accesses. Registers are used to hold data that is being processed, as well as the memory addresses of instructions that are being executed. They are much faster to access than main memory, and are used to speed up the execution of instructions in a computer. This is an AXI4 compliant Register for easy integration with other AXI based systems. A macro block diagram of this AXI4 Register is shown in Figure 1.

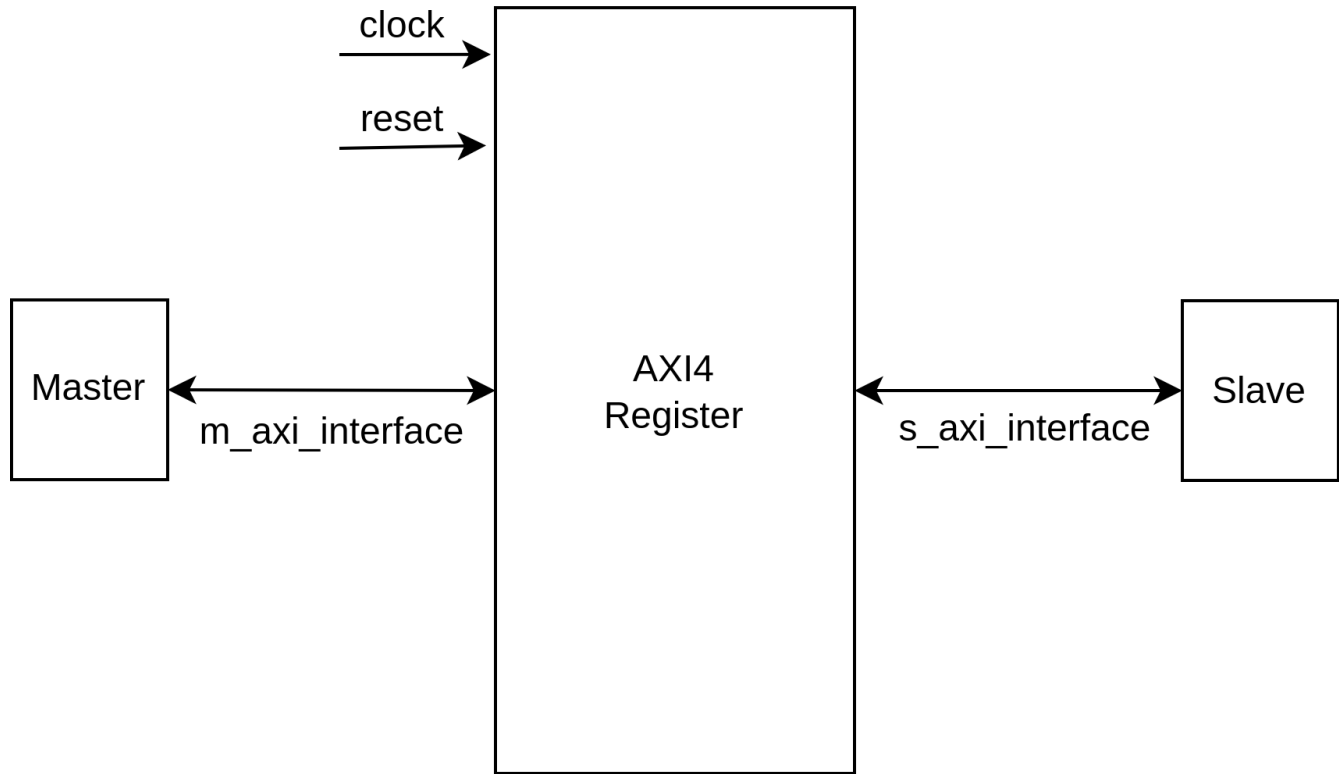


Figure 1. AXI4 Register Block Diagram

Revision History

Date	Version	Revisions
January 25, 2023	0.01	Initial version AXI4 Register User Guide Document