

AXI4 Interconnect v1.0

IP User Guide (Alpha Release)



January 25, 2023

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IP Summary

Introduction

An interconnect is a device that connects two or more components in a system allowing them to communicate and exchange data between them. This Interconnect IP is AXI4 compliant making it easier to integrate into other AXI compliant systems enabling data routing between several masters and slaves. A macro block diagram of this AXI Interconnect is shown in Figure 1.

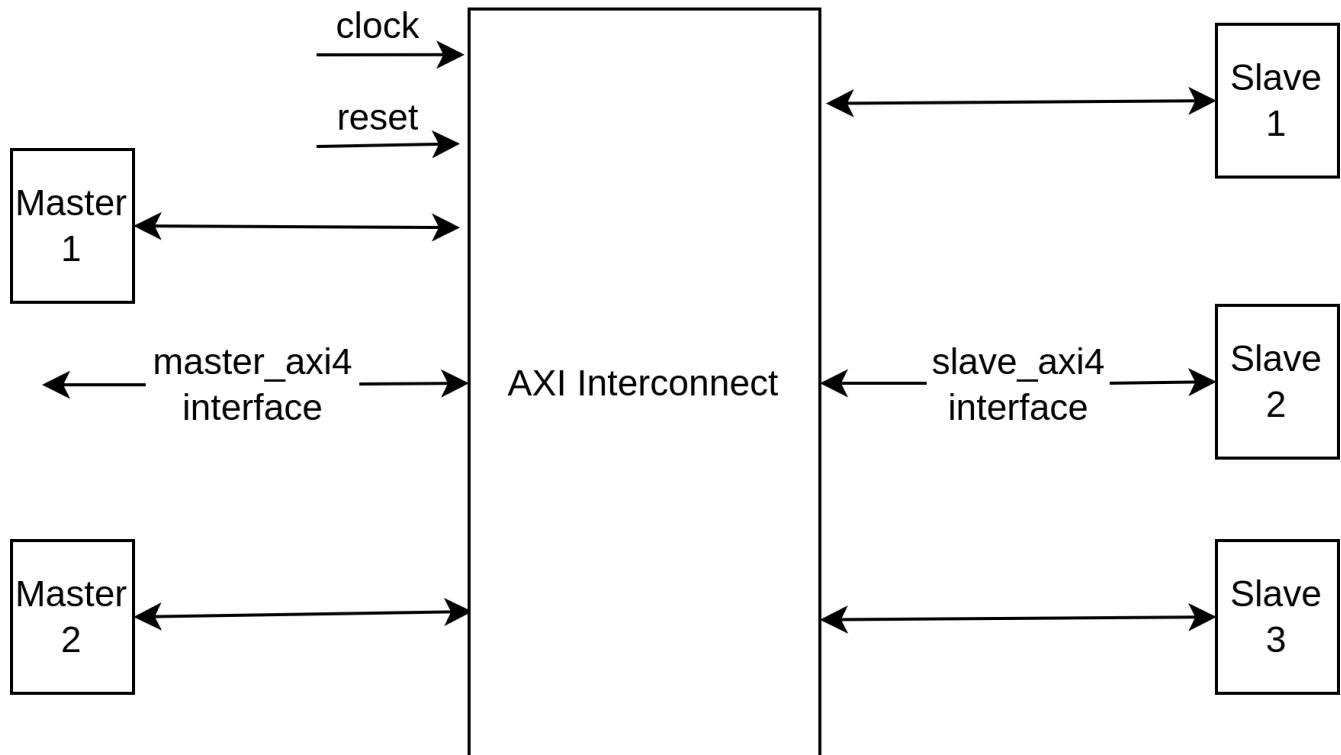


Figure 1. AXI4 Interconnect Block Diagram

Revision History

Date	Version	Revisions
January 25, 2023	0.01	Initial version AXI4 Interconnect User Guide Document