

Timer Using Loop Delays

Course: ENGG*3640 Microcomputer Interfacing

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Contents

1. Introduction	1
1.1. Lab Description	1
1.2. System Requirements	1
2. Background	1
2.1. EQUIPMENTS	2
3. Implementation	2
3.1. Lab Implementation Overview	2
3.1.1. Software Implementation	3
3.1.2. Hardware Implementation	4
3.2. Lab Requirements	4
3.2.1 Lab 1 Requirement	4
3.2.2 Lab 2 Requirement	5
3.3. Simulation Results	7
4. Conclusion	9
References	10
Appendices	11
A. Main.c	11
B. myMain.s – Part 1	12
C. myMain.s – Part 2	14

1. Introduction

The Purpose of lab 1 was to get familiar with the Kiel Uvision program interface and learn the basics of collaborative programming between C language and assembly.

1.1. Lab Description

The first part of the lab was focused on setting up the Kiel Uvision program and create a template that would as a workstation. The second task was focused on creating a short program that loads two values into registers from which a C program calls the assembly code to make a copy of the src string and print the results to the putty screen. The third task of the first lab was to design and implement a delay loop. The delay loop acts as a timer that prints the count every ten seconds.

1.2. System Requirements

During the first lab, the tools and equipment that was used were the following:

- Kiel Uvision Program was used to create instructions to the K60 Microcontroller
- FreeScale TWR-K60D100M Microcontroller was used to implement instructions sent
- Connection cable between PC and board was used to connect the Microcontroller to the PC

2. Background

2.1. EQUIPMENTS

- **Kiel Uvision Program:** The Kiel Uvision program is an IDE that combines project management, source code editing, program debugging, and run-time environment into a single powerful environment. Using this environment, the user is able to easily and efficiently test, verify, debug, and optimize the code developed. During the first lab, the debug functionality helped in understanding how the code is acting.
- **K60 Microcontroller:** The K60 Microcontroller (Figure 2.1) from NXP contains a low power MCU core ARM Cortex-M4 that features an analog integration, serial communication, USB 2.0 full-speed OTG controller and 10/100 Mbps Ethernet MAC. These characteristics makes this Microcontroller suitable to preform task in a very efficient and fast manner. A USB connection was used to sync the microcontrollerwith the Keil uVision software that that was provided by the Teacher Assistant.



Figure 2.1: K60 Microcontroller

3. Implementation

3.1. Lab Implementation Overview

```
COM4 - PuTTY
String source
String destination
String source
String source
█
```

Figure 3.1.1: String printed Properly after troubleshooting

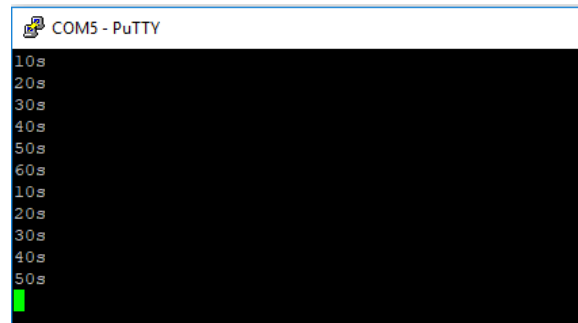


Figure 3.1.2: Counter and Loop Delay output

3.1.1. Software Implementation

The main.c that is used in part one (Figure A.1: Page 1 main.c) of the lab was provided in the lab manual. Simply, the main.c has the definitions and functions that are used in assembly and that are extremely essential to the workings of microcontroller. Lines 35-39 contains the standard C included files, The printing function which displays the output on putty are shown in Lines 42-45. From line 46-51 is the entry function that is needed only for the entry point into the assembly code mymain.s.

The mymain.s (Figure B.1: Page 1 myMain.s) in part one was written using ARM assembly language, Lines 6-11 contains the instructions needed to set the string in the registers and printing it. Lines 14-21, loads the bytes from source(r1) into the carrier(r3) and updates the address, then stores it from the carrier to the destination (r0) and updates the address. It then it checks for a zero terminator (last byte), loads the registers with strings and prints them. Lines 24-28 comprises simple instructions to assign the strings that had to be stored in the registers and displayed on putty.

For part two, the mymain.s (Figure C.1: Page 1 myMain.s) contains the implementation of the delay program that is required. As it is shown in Figure C.1, after zero is loaded into register r5, the “tim” loop,

Timer Using Loop Delay

which is the main loop that the program runs on, begins by loading r1 with a number large enough to aid in delaying the delay timer to approximately 10 seconds. The number used is 0xF2EDE0(15920608) and it was procured after some calculations and an extensive period of trial and error that are better explained in the below section Lab 2 Requirements. Then a 4 instruction delay loop is created “Dloop” between line 15-18 having subtraction by 1 from register r1 and comparing it by zero and a NOP instruction. As we go outside this delay loop we start adding one to r5 register (Line 20) as the “tim” loop is still running again and again it will continue adding r5 by one till it reaches any of the 10,20,30,40,50 or 60. Every time we increment r5 we check if one of these numbers is reached as shown in lines 23-34, if any of these numbers hits the, code branches to load r0 (Line 39-63) and then prompts the correct string that are store between line 65-72, and then “tim” loop starts the same procedure again.

3.1.2. Hardware Implementation

During the first lab, Implementation of hardware was minimal. The TWR-K60D100M Microcomputer was used to demonstrate the code implementation. Putty terminal was used to display the results on screen. Throughout lab 1, it was noticed that the on-board crystal frequency was valued at 50 MHz. The above frequency value was used to calculate the register preload values.

3.2. Lab Requirements

3.2.1 Lab 1 Requirement

The mymain.s program that was provided from the lab manual contained an intentional code error. The second task of lab 1 was to debug the program and fix the error to print the expected result. After carefully debugging the provided program, it was observed that register 0 gets cleared every time the

Timer Using Loop Delay

“myprint” function was called. To solve this error, register 1 had to be reloaded with an immediate offset. Register 1 had to be reloaded with the previous string in order for the compare function to work. The following code line “LDR r0 = dststr” was included before r1 was loaded with the source string.

Through monitoring the disassembly window and using the debugger mode, it was discovered that the “dststr” and the “srcstr” were both located in the data memory at location 0x1FFF002E. It was also found that the Main.c file was located at 0x00023C0 and assmain at 0x0000083C. The screenshots of these locations can be seen below:

```
0x000023C6 F7FEFA39 BL.W      assmain (0x0000083C)
```

Figure 3.2.1.1

Call Stack + Locals		
Name	Location/V...	Type
main	0x000023C0	int f()

Figure 3.2.1.2

Core	
R0	0x1FFF002E
R1	0x1FFF002E

Figure 3.2.1.3

3.2.2 Lab 2 Requirement

As for this program, it's required to develop program that counts by 10 seconds and displays the seconds repeatedly within approximately 10 second apart. To achieve this, a delay loop was implemented with 4 lines of code shown in the figure above including one NOP (No Operation Instruction). The TWR-K60D100M has an innate crystal speed of 50 MHz, this was used for getting the

Timer Using Loop Delay

processor cycles as shown below:

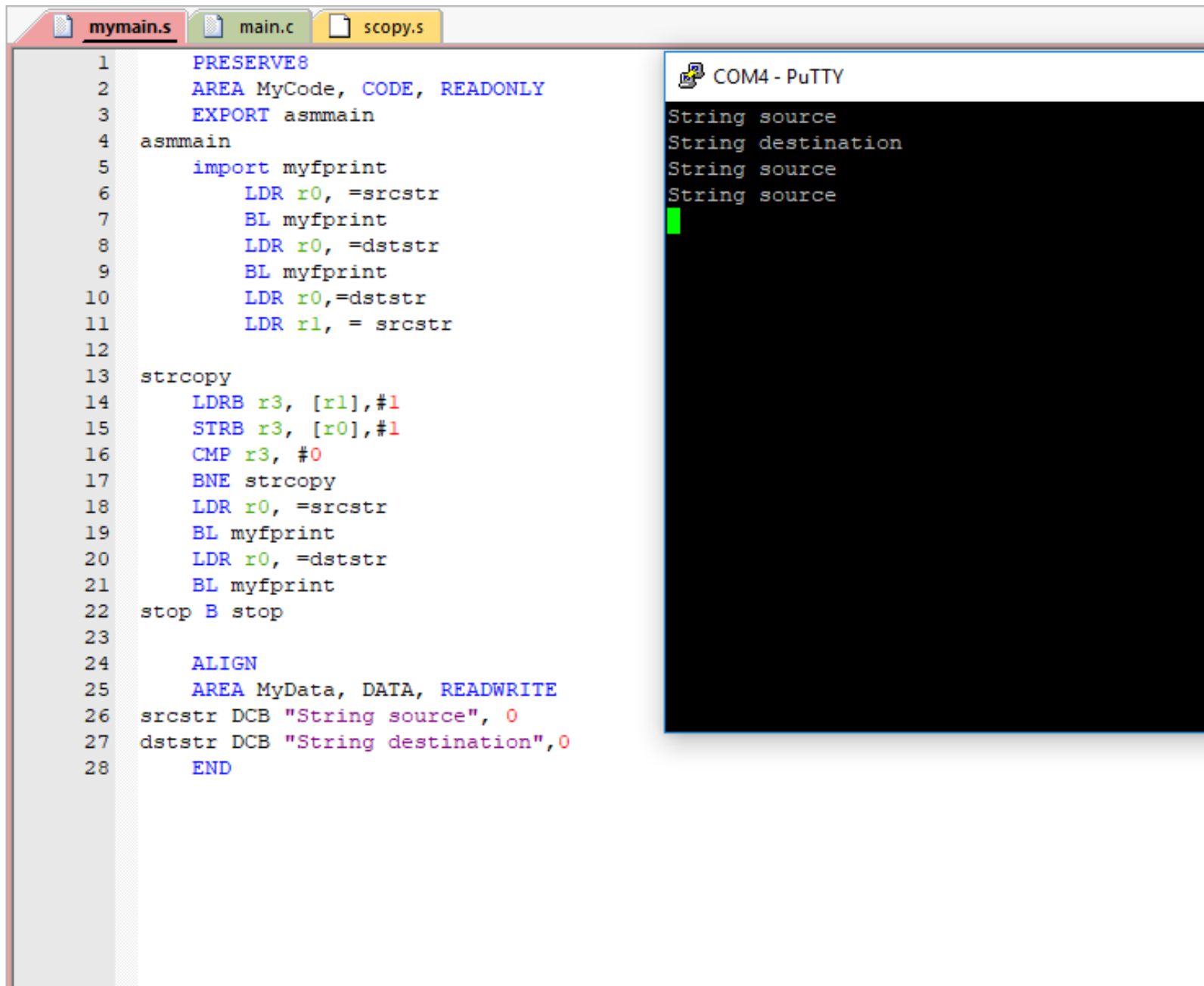
$$\begin{aligned}T &= \frac{1}{50\text{Mhz}} \\T &= 2 \times 10^{-8} \text{ s (per cycle)} \\ \frac{1\text{s}}{2 \times 10^{-8}} &= 50,000,000 \text{ instructions a second}\end{aligned}$$

Figure 3.2.2: Formula used to figure out time per cycle

This number is divided by 4 because of the 4 instructions that we used in our delay loop, then converted to hexadecimal and loaded to r1 (loop register). After each loop exit, the r0 register which is set to 0 initially will increment by 1. The result of r0 in each iteration is compared to 10,20,30,40,50,60 and every time it hits a number within these, it prompts it on the screen. By convention, if r0 is incrementing from 0 it will hit 10 first and finishes with 60 thus displaying the result in a specific sequence and time this code is shown above.

3.3. Simulation Results

3.3.1. Simulation Result Part 1



The screenshot displays a simulation environment with two main components: a code editor on the left and a serial output window on the right.

The code editor shows the following assembly code:

```
1 PRESERVE8
2 AREA MyCode, CODE, READONLY
3 EXPORT asmmain
4 asmmain
5     import myfprint
6     LDR r0, =srcstr
7     BL myfprint
8     LDR r0, =dststr
9     BL myfprint
10    LDR r0,=dststr
11    LDR r1, = srcstr
12
13 strcopy
14    LDRB r3, [r1],#1
15    STRB r3, [r0],#1
16    CMP r3, #0
17    BNE strcopy
18    LDR r0, =srcstr
19    BL myfprint
20    LDR r0, =dststr
21    BL myfprint
22 stop B stop
23
24 ALIGN
25 AREA MyData, DATA, READWRITE
26 srcstr DCB "String source", 0
27 dststr DCB "String destination",0
28 END
```

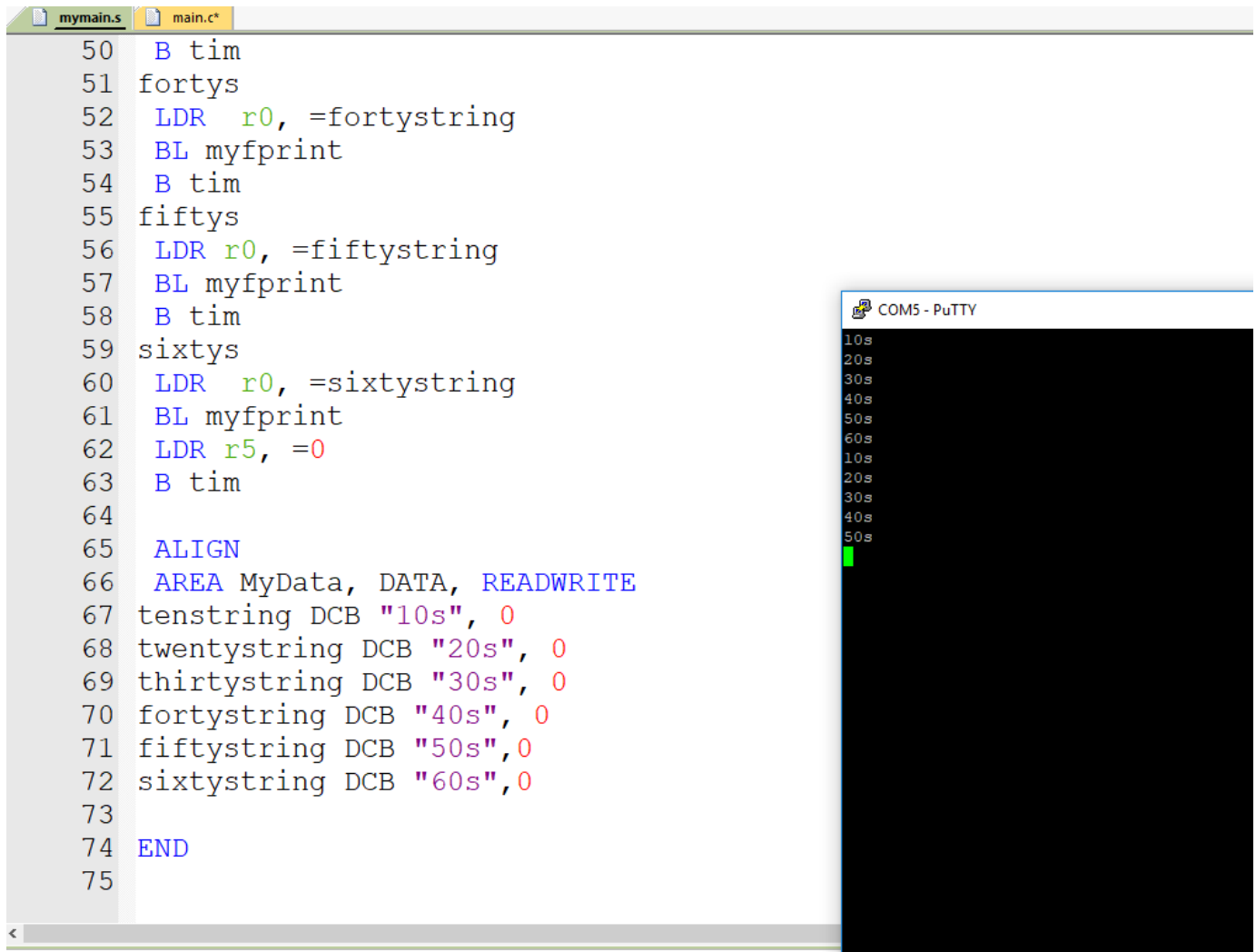
The serial output window, titled "COM4 - PuTTY", shows the following text:

```
String source
String destination
String source
String source
█
```

Figure 3.3.1: Output when the reset button was pressed after troubleshooting

Timer Using Loop Delay

3.3.2. Simulation Result Part 2



The screenshot shows an IDE with two tabs: 'mymain.s' and 'main.c*'. The 'mymain.s' tab is active, displaying assembly code from line 50 to 75. The code includes labels for delays (fortys, fiftys, sixtys), instructions for loading strings into registers (LDR r0, =fortystring, etc.), and instructions for printing (BL myfprint). It also includes a section for data (AREA MyData, DATA, READWRITE) with DCB instructions for strings like '10s', '20s', etc. The 'main.c*' tab is empty. To the right, a terminal window titled 'COM5 - PuTTY' shows a list of time intervals: 10s, 20s, 30s, 40s, 50s, 60s, 10s, 20s, 30s, 40s, 50s. A green cursor is visible at the bottom of the terminal window.

```
50 B tim
51 fortys
52 LDR r0, =fortystring
53 BL myfprint
54 B tim
55 fiftys
56 LDR r0, =fiftystring
57 BL myfprint
58 B tim
59 sixtys
60 LDR r0, =sixtystring
61 BL myfprint
62 LDR r5, =0
63 B tim
64
65 ALIGN
66 AREA MyData, DATA, READWRITE
67 tenstring DCB "10s", 0
68 twentystring DCB "20s", 0
69 thirtystring DCB "30s", 0
70 fortystring DCB "40s", 0
71 fiftystring DCB "50s", 0
72 sixtystring DCB "60s", 0
73
74 END
75
```

3.3.2: Output when the timer and delay loop was running

4. Conclusion

In conclusion, all the objectives for this lab were met. This lab was aimed towards familiarizing us with the Kinetis K60 Microcontroller as well as ARM assembly language. This lab comprised of two parts. The first of which asked us to determine the logic error in a given block of code. This logic error was fixed with an additional line of code and the source string was successfully copied to the destination variable. The main challenge faced in this part of the lab was when using the “myfprint” function as it manipulated the data in the register after printing it to the terminal. Through frequent debugging the workings of the function were figured out and hence we were successfully able to fix the code error.

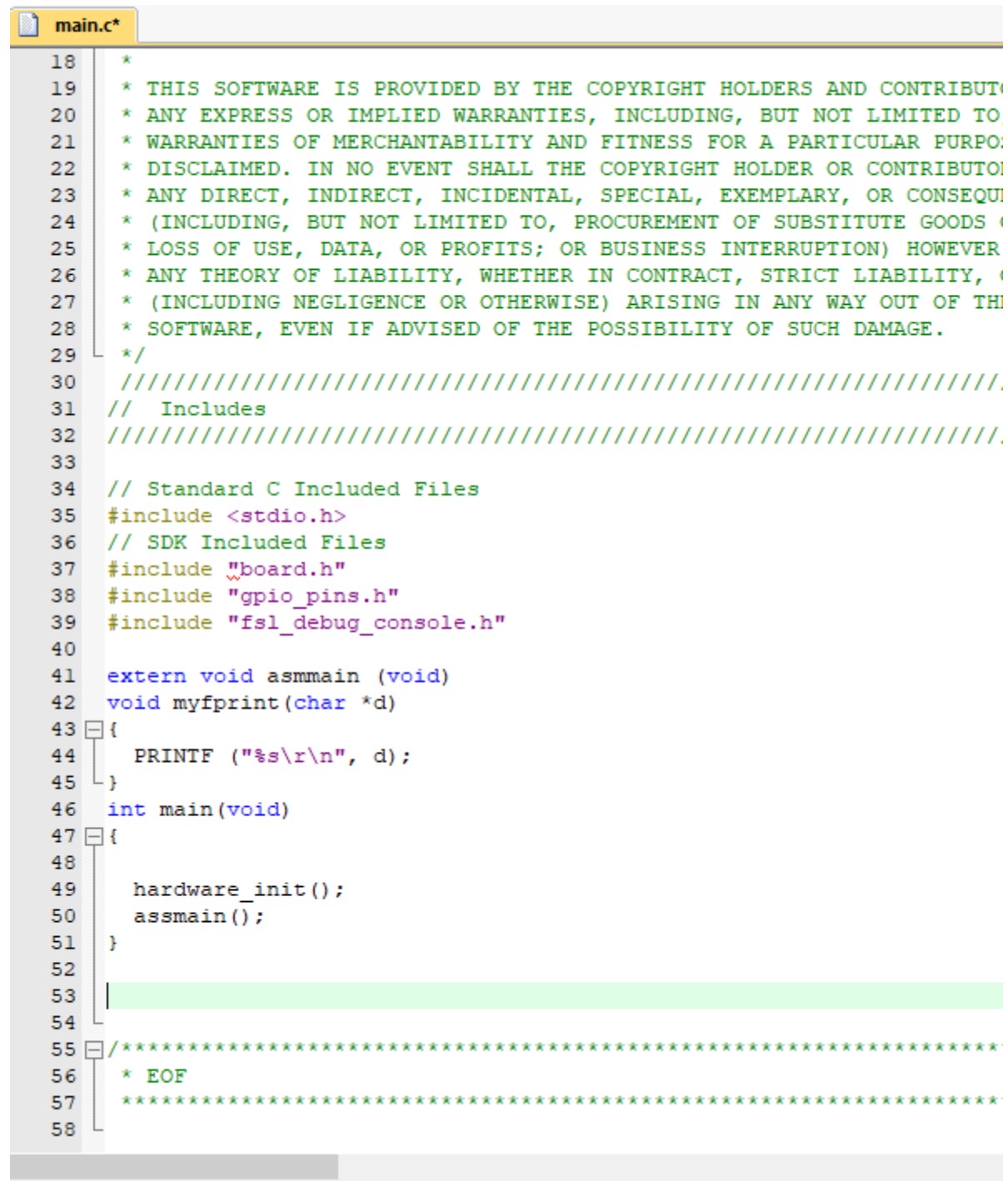
The second and more complex part as it included a timer. This section of the lab was very thorough and exhaustive as we were required to print to the screen after every ten seconds, so the exactness of our timer was of utmost importance. We were successful in our implementation of this as time in-between was simulated by using various delay loops and through trial and error we were able to get it to about 9.95 seconds exactly. The proof of this fruitful execution can be seen under Simulation Results.

References

[1] Radu Muresan, "ENGG3640: Microcomputer Interfacing Laboratory Manual, Version 2", University of Guelph, July 2016.

Appendices

A. Main.c



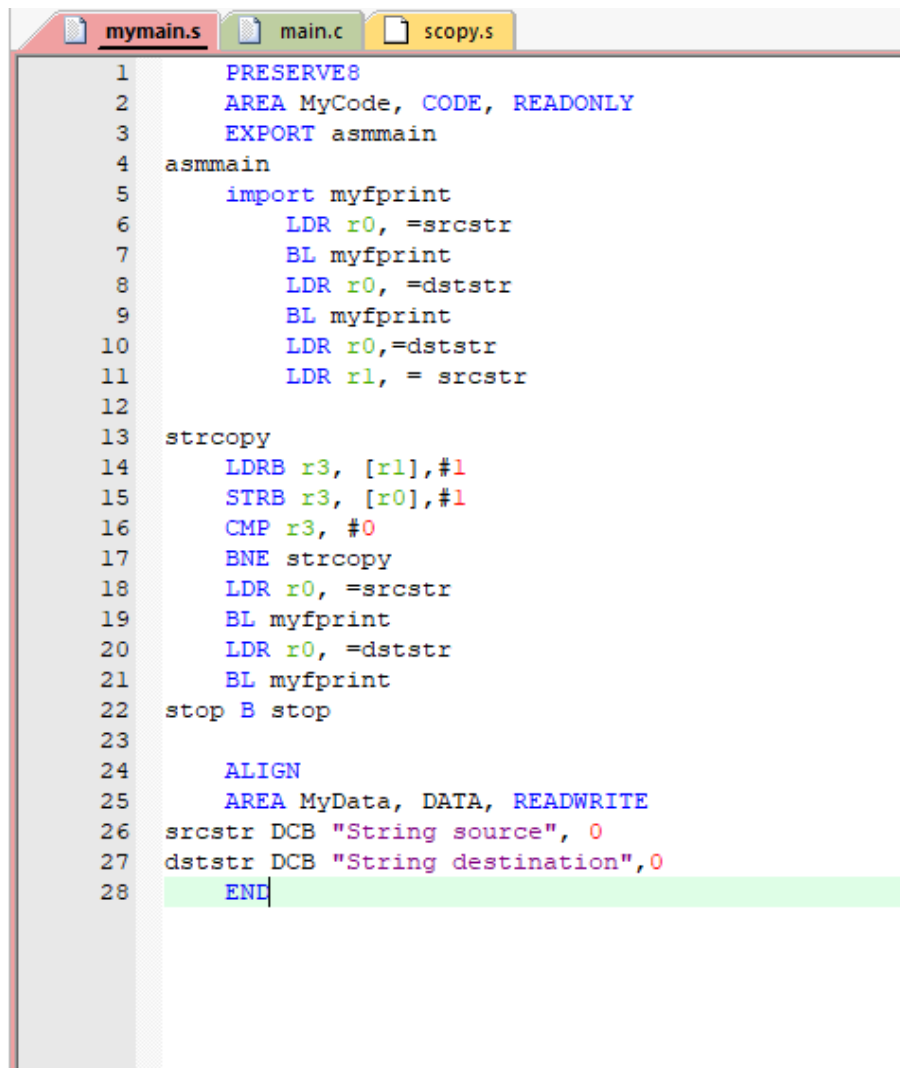
```

18  *
19  * THIS SOFTWARE IS PROVIDED BY THE COPYRIGHT HOLDERS AND CONTRIBUTORS
20  * ANY EXPRESS OR IMPLIED WARRANTIES, INCLUDING, BUT NOT LIMITED TO,
21  * WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE
22  * DISCLAIMED. IN NO EVENT SHALL THE COPYRIGHT HOLDER OR CONTRIBUTORS
23  * ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, OR CONSEQUENTIAL
24  * (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF SUBSTITUTE GOODS OR
25  * LOSS OF USE, DATA, OR PROFITS; OR BUSINESS INTERRUPTION) HOWEVER
26  * ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT
27  * (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE
28  * SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.
29  */
30  ///////////////////////////////////////////////////.
31  // Includes
32  ///////////////////////////////////////////////////.
33
34  // Standard C Included Files
35  #include <stdio.h>
36  // SDK Included Files
37  #include "board.h"
38  #include "gpio_pins.h"
39  #include "fsl_debug_console.h"
40
41  extern void assmain (void)
42  void myfprint(char *d)
43  {
44      PRINTF ("%s\r\n", d);
45  }
46  int main(void)
47  {
48
49      hardware_init();
50      assmain();
51  }
52
53
54
55  /*****
56   * EOF
57   *****/
58

```

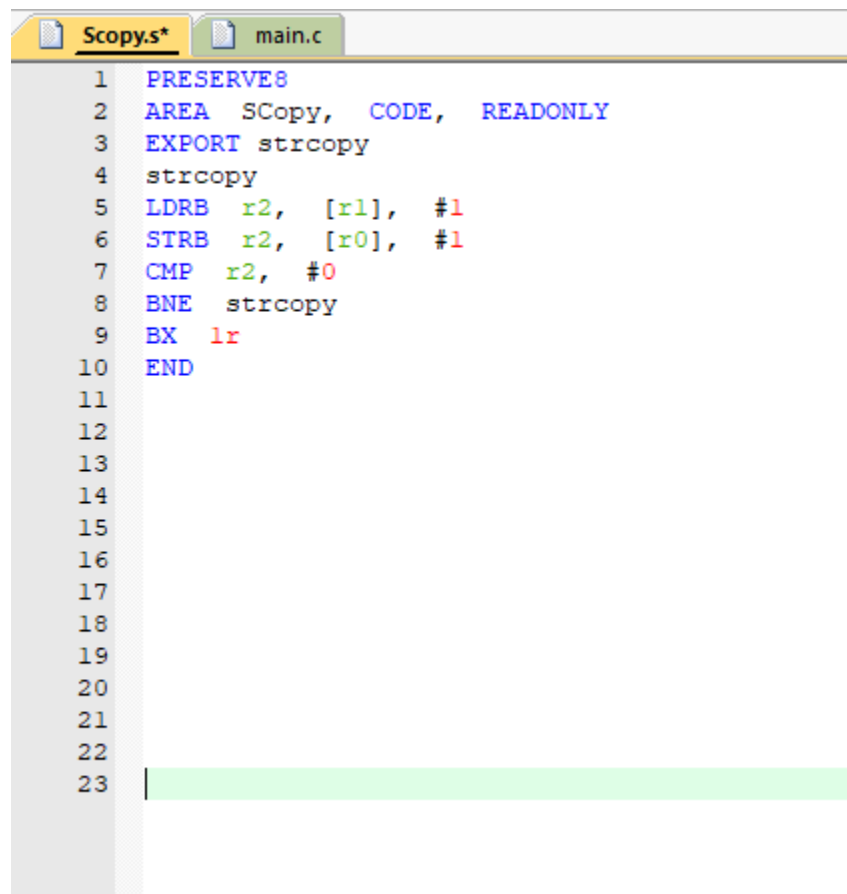
Figure A.1: Page 1 main.c

B. myMain.s – Part 1



```
1      PRESERVE8
2      AREA MyCode, CODE, READONLY
3      EXPORT asmmain
4      asmmain
5          import myfprint
6          LDR r0, =srcstr
7          BL myfprint
8          LDR r0, =dststr
9          BL myfprint
10         LDR r0,=dststr
11         LDR r1, = srcstr
12
13     strcpy
14         LDRB r3, [r1],#1
15         STRB r3, [r0],#1
16         CMP r3, #0
17         BNE strcpy
18         LDR r0, =srcstr
19         BL myfprint
20         LDR r0, =dststr
21         BL myfprint
22     stop B stop
23
24     ALIGN
25     AREA MyData, DATA, READWRITE
26     srcstr DCB "String source", 0
27     dststr DCB "String destination",0
28     END
```

Figure B.1: Page 1 myMain.s



```
1  PRESERVE8
2  AREA SCopy, CODE, READONLY
3  EXPORT strcpy
4  strcpy
5  LDRB r2, [r1], #1
6  STRB r2, [r0], #1
7  CMP r2, #0
8  BNE strcpy
9  BX lr
10 END
11
12
13
14
15
16
17
18
19
20
21
22
23
```

Figure B.2: Page 1 Scopy.s

C. myMain.s – Part 2

```

1  PRESERVE8
2  AREA MyCode, CODE, READONLY
3  EXPORT asmmain
4
5  asmmain
6  import myfprint ;*d pointer is in register r0
7  LDR r5,=0 ;counter register
8
9  tim
10     LDR r1,=0xF2EDE0 ;Roughly one second @ 50Mhz
11     ;previous line sets register 1 to about 1 second. Value in hex represents the time.
12     ;must change value to get accurate time for the code you made yourself
13
14
15 Dloop SUB r1, #1 ;loop function that compares timer value to 0, wasting some time and
16     NOP
17     CMP r1,#0
18     BNE Dloop
19     ;if it not equal subtract 1 from register 5
20     ADD r5, #1
21
22 ;These are all your compares for sets of ten seconds
23     CMP r5, #10
24     BEQ tens
25     CMP r5, #20
26     BEQ twentys
27     CMP r5, #30
28     BEQ thirtys
29     CMP r5, #40
30     BEQ fortys
31     CMP r5, #50
32     BEQ fiftys
33     CMP r5, #60
34     BEQ sixtys
35
36     B tim
37
38 ;these are your labels to what wyou want for each set of ten seconds
39 tens
40     LDR r0, =tenstring

```

Figure C.1: Page 1 myMain.s


```

36  B tim
37
38  ;these are your labels to what wyou want for each set of ten seconds
39  tens
40  LDR r0, =tenstring
41  BL myfprint
42  B tim
43  twentys
44  LDR r0, =twentystring
45  BL myfprint
46  B tim
47  thirtys
48  LDR r0, =thirtystring
49  BL myfprint
50  B tim
51  fortys
52  LDR r0, =fortystring
53  BL myfprint
54  B tim
55  fiftys
56  LDR r0, =fiftystring
57  BL myfprint
58  B tim
59  sixtys
60  LDR r0, =sixtystring
61  BL myfprint
62  LDR r5, =0
63  B tim
64
65  ALIGN
66  AREA MyData, DATA, READWRITE
67  tenstring DCB "10s", 0
68  twentystring DCB "20s", 0
69  thirtystring DCB "30s", 0
70  fortystring DCB "40s", 0
71  fiftystring DCB "50s", 0
72  sixtystring DCB "60s", 0
73
74  END
75

```

Figure C.1: Page 2 myMain.c