

HW4.1. RISC-V Warmup

Q1.1: Which of the following is true of the RISC philosophy? Select all that apply.

☐ (a) RISC stands for Reduced Instruction Set Computing

☐ (b) RISC encourages the instruction set to be large and expansive

☐ (c) In RISC, a simple instruction set allows for simpler hardware

☐ (d) In RISC, complicated operations can be performed by decomposing them into simple instructions

☐ (e) None of the above

Select all possible options that apply. ?

Q1.2: Which of the following is true about a processor register?

☐ (a) A register is a small location of RAM memory.

☐ (b) A register has access speed comparable to a hard drive.

☐ (c) Registers comprise most of a computer's storage space.

☐ (d) A computer typically has thousands of registers.

☐ (e) A register is a small amount of storage directly on the CPU.

☐ (f) None of the above

Select all possible options that apply. ?

Q1.3: How many integer registers are available in RISC-V (not including the Program Counter)?

integer

?

Q1.4: For the RISC-V we learn in this class, how many bytes does each register store?

integer

?

Save & Grade 20 attempts left

Save only

Additional attempts available with new variants ?

Homework 4

Assessment overview

Total points:

0/100

Score:

0%

Question

Value:

8

History:

Awarded points:

0/8

Report an error in this question ?

Previous question

Next question

Attached files

No attached files

Attach a file ?

Attach text ?