

HW4.1. RISC-V Warmup

Q1.1: Which of the following is true of the RISC philosophy? Select all that apply.

☒ (a) RISC stands for Reduced Instruction Set Computing

☐ (b) RISC encourages the instruction set to be large and expansive

☒ (c) In RISC, a simple instruction set allows for simpler hardware

☒ (d) In RISC, complicated operations can be performed by decomposing them into simple instructions

☐ (e) None of the above

Select all possible options that apply.

✓ 100%

✓

Q1.2: Which of the following is true about a processor register?

☐ (a) A register is a small location of RAM memory.

☐ (b) A register has access speed comparable to a hard drive.

☐ (c) Registers comprise most of a computer's storage space.

☐ (d) A computer typically has thousands of registers.

☒ (e) A register is a small amount of storage directly on the CPU.

☐ (f) None of the above

Select all possible options that apply.

✓ 100%

✓

Q1.3: How many integer registers are available in RISC-V (not including the Program Counter)?

32

?

✓ 100%

Q1.4: For the RISC-V we learn in this class, how many bytes does each register store?

4

?

✓ 100%

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Homework 4

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Total points:

8/100

Score:

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Question

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History:

8

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Correct answer

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4

Submitted answer 3

correct: 100%

Submitted at 2022-09-20 09:11:29 (PDT)

i

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Submitted answer 2

partially correct: 75%

i

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▼

Submitted at 2022-09-20 09:11:08 (PDT)

Submitted answer 1

partially correct: 75%

i

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Submitted at 2022-09-20 09:10:54 (PDT)