

HW4.7. RISC-V-Binary Translation (Randomized)

Feel free to check out the [guide](#) that we have prepared to help you in this problem.

For the following problems, you would need to refer to the [Reference Card](#) to help you with the conversion.

Translate the following instructions from RISC-V to hexadecimal. Note that we use integer values for branches and jumps, instead of labels (this is permitted by the ISA, but normally discouraged since it makes debugging incredibly hard).

Q1.1: `auipc s6 11`

0x

0000BB17

?

✓ 100%

Q1.2: `slt s9 a0 a6`

0x

01052CB3

?

✓ 100%

Q1.3: `jal s1 40`

0x

028004EF

?

✓ 100%

Translate the following instructions from hexadecimal to RISC-V. Follow these conventions when writing your answer:

Do not include commas in your answer.

For immediates, submit a number in decimal. We have guaranteed that all immediates have absolute value 100 or less.

As with the above, use integer values for branches and jumps.

Use the register names (s0, a4, etc.) for all registers except register x0 (which should use x0).

Q2.1: `0x041B5703`

lhu a4 65(s6)

?

✓ 100%

Q2.2: `0x05D59C23`

sh t4 88(a1)

?

✓ 100%

Q2.3: `0xFD9ADBE3`

bge s5 s9 -42

?

✓ 100%

Try a new variant

Homework 4

Assessment overview

Total points: 80/100

Score: 80%

Question

Value:

18

History:

18

Awarded points:

18/18

Report an error in this question

Previous question

Next question

Attached files

No attached files

Attach a file

Attach text

Correct answer

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Q1.1: `auipc s6 11`

0x

Q1.2: `slt s9 a0 a6`

0x

Q1.3: `jal s1 40`

0x

Translate the following instructions from hexadecimal to RISC-V. Follow these conventions when writing your answer:

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As with the above, use integer values for branches and jumps.

Use the register names (s0, a4, etc.) for all registers *except* register x0 (which should use x0).

Q2.1: `0x041B5703`

Q2.2: `0x05D59C23`

Q2.3: `0xFD9ADBE3`

Submitted answer 5 **correct: 100%**

Submitted at 2022-09-21 00:59:17 (PDT)

 

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Translate the following instructions from RISC-V to hexadecimal. Note that we use integer values for branches and jumps, instead of labels (this is permitted by the ISA, but normally discouraged since it makes debugging incredibly hard).

Q1.1: `auipc s6 11`

0x **✓100%**

Q1.2: `slt s9 a0 a6`

0x **✓100%**

Q1.3: `jal s1 40`

0x **✓100%**

Translate the following instructions from hexadecimal to RISC-V. Follow these conventions when writing your answer:

Do not include commas in your answer.

For immediates, submit a number in decimal. We have guaranteed that all immediates have absolute value 100 or less.

As with the above, use integer values for branches and jumps.

Use the register names (s0, a4, etc.) for all registers *except* register x0 (which should use x0).

Q2.1: 0x041B5703

lhu a4 65(s6)

✓ 100%

Q2.2: 0x05D59C23

sh t4 88(a1)

✓ 100%

Q2.3: 0xFD9ADBE3

bge s5 s9 -42

✓ 100%

Submitted answer 4 **partially correct: 83%**

Submitted at 2022-09-21 00:55:58 (PDT)



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Submitted answer 3 **partially correct: 66%**

Submitted at 2022-09-21 00:55:17 (PDT)



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