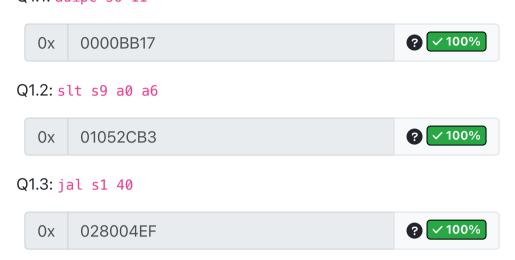
HW4.7. RISC-V-Binary Translation (Randomized)

Feel free to check out the guide that we have prepared to help you in this problem.

For the following problems, you would need to refer to the <u>Reference Card</u> to help you with the conversion.

Translate the following instructions from RISC-V to hexadecimal. Note that we use integer values for branches and jumps, instead of labels (this is permitted by the ISA, but normally discouraged since it makes debugging incredibly hard).

Q1.1: auipc s6 11



Translate the following instructions from hexadecimal to RISC-V. Follow these conventions when writing your answer:

Do not include commas in your answer.

For immediates, submit a number in decimal. We have guaranteed that all immediates have absolute value 100 or less.

As with the above, use integer values for branches and jumps.

Use the register names (s0, a4, etc.) for all registers except register x0 (which should use x0).

Q2.1: 0x041B5703



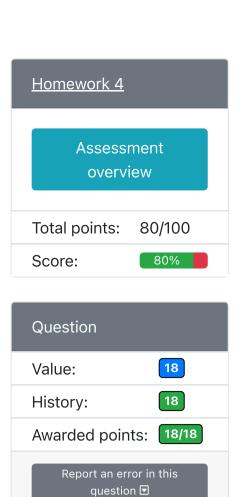
Try a new variant

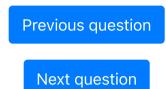
Correct answer

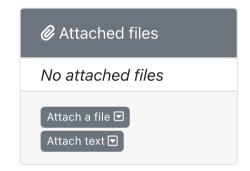
Feel free to check out the guide that we have prepared to help you in this problem.

For the following problems, you would need to refer to the <u>Reference Card</u> to help you with the conversion.

Translate the following instructions from RISC-V to hexadecimal. Note that we use integer values for branches and jumps, instead of labels (this is permitted by the ISA, but normally discouraged since it makes debugging incredibly hard).







Q1.1: auipc s6 11

Ox 0000BB17

Q1.2: slt s9 a0 a6

Ox 01052CB3

Q1.3: jal s1 40

Ox 028004EF

Translate the following instructions from hexadecimal to RISC-V. Follow these conventions when writing your answer:

Do not include commas in your answer.

For immediates, submit a number in decimal. We have guaranteed that all immediates have absolute value 100 or less.

As with the above, use integer values for branches and jumps.

Use the register names (s0, a4, etc.) for all registers except register x0 (which should use x0).

Q2.1: 0x041B5703

lhu a4 65(s6)

Q2.2: 0x05D59C23

sh t4 88(a1)

Q2.3: 0xFD9ADBE3

bge s5 s9 -42

Submitted answer 5 correct: 100%
Submitted at 2022-09-21 00:59:17 (PDT)

Feel free to check out the guide that we have prepared to help you in this problem.

For the following problems, you would need to refer to the <u>Reference Card</u> to help you with the conversion.

Translate the following instructions from RISC-V to hexadecimal. Note that we use integer values for branches and jumps, instead of labels (this is permitted by the ISA, but normally discouraged since it makes debugging incredibly hard).

Translate the following instructions from hexadecimal to RISC-V. Follow these conventions when writing your answer:

Do not include commas in your answer.

For immediates, submit a number in decimal. We have guaranteed that all immediates have absolute value 100 or less.

