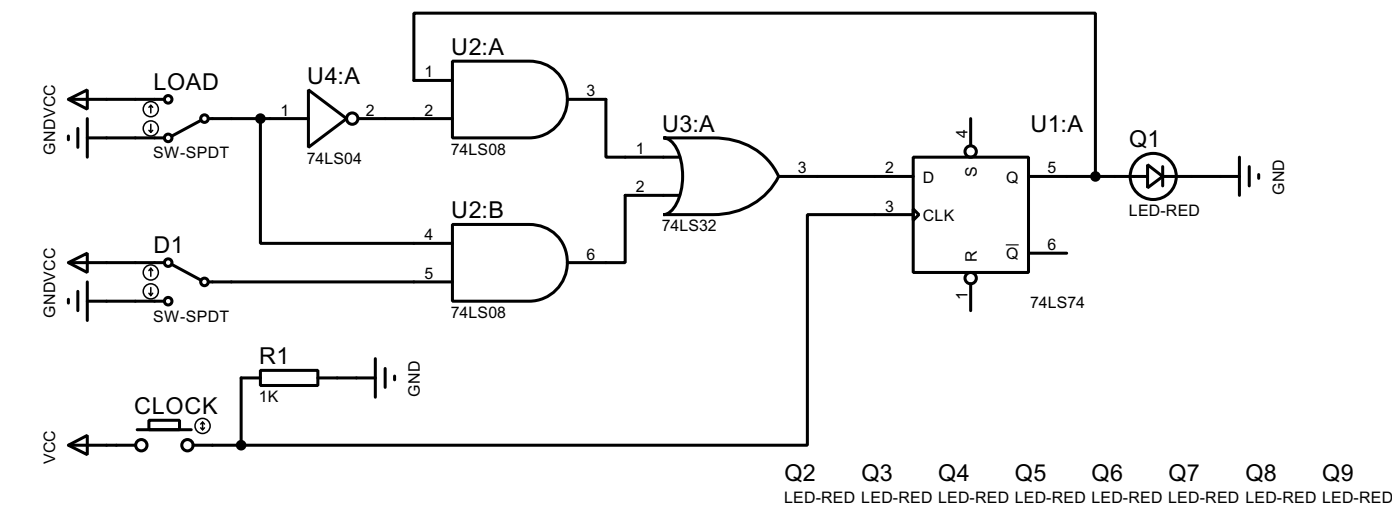


1 BiT Register



8 BiT Register

