

Piezo-sounder Driver with Multi-mode charge pump

■GENERAL DESCRIPTION

The NJU72501 is a switching driver with multi mode charge pump for piezo-sounder. It can drive outputs up to 18Vpp from 3V supply. For adjusting the piezoelectric sounder sound volume, the charge pump can operate in either of a 1x, 2x or 3x mode.

Because NJU72501 has the shutdown function, it is suitable for the battery application.

■PACKAGE OUTLINE





NJU72501MJE

NJU72501MG2



NJU72501V

■ APPLICATION

Healthcare, Wrist Watches, Alarm Clocks, Handheld GPS devices, PDAs

■FEATURES

●Operating Voltage : 2.3 to 5.0V(1x/ 2x Mode), 2.3 to 3.4V(3x Mode)

●Consumption current (Active) : I_{DS}=0.3mA typ. (VIN=3V, DIN=4kHz, C_{PIEZO}=15nF, 1x Mode)

(Shutdown): I_{DS}=1uA max. (VIN=3V, DIN=0V)

●Multi-Mode Charge Pump (1x/2x/3x)

●Input Signal Detector & Shutdown Control

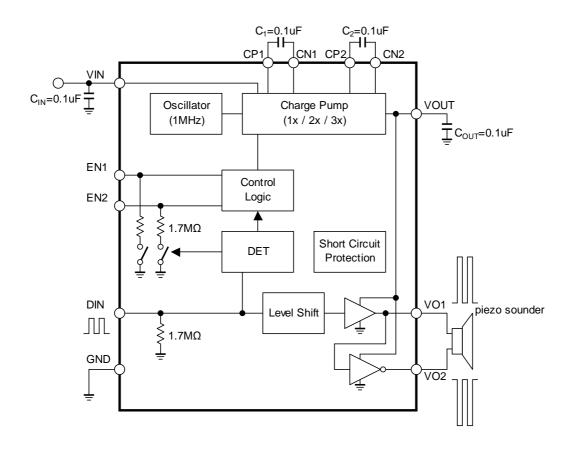
Output Short-circuit Protection Circuit

C-MOS Technology

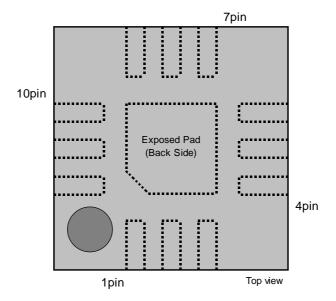
● Package Outline : EQFN12-JE, EQFN16-G2, SSOP14

● Direct replacement to MAS6240

■BLOCK DIAGRAM



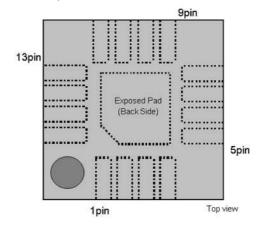
■TERMINAL CONFIGURATION (EQFN12-JE)



■PIN DESCRIPTION

Pin No.	SYMBOL	FUNCTION
1	EN1	Step-up Mode Switch Terminal 1
2	EN2	Step-up Mode Switch Terminal 2
3	DIN	Input Terminal
4	CN1	Capacitor Connection Terminal
5	GND	GND Terminal
6	VO2	Output Terminal 2
7	VO1	Output Terminal 1
8	CN2	Capacitor Connection Terminal
9	CP1	Capacitor Connection Terminal
10	VOUT	Charge Pump Output Terminal
11	CP2	Capacitor Connection Terminal
12	VIN	Power Supply Terminal
Exp Pad	Exposed Pad	Exposed Pad connected to GND or left floating

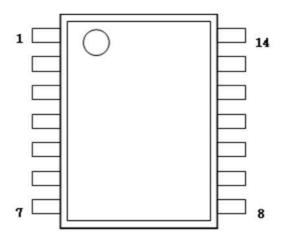
■TERMINAL CONFIGURATION (EQFN16-G2)



■PIN DESCRIPTION

Pin No.	SYMBOL	FUNCTION
1	NC	
2	EN1	Step-up Mode Switch Terminal 1
3	EN2	Step-up Mode Switch Terminal 2
4	DIN	Input Terminal
5	NC	
6	CN1	Capacitor Connection Terminal
7	GND	GND Terminal
8	VO2	Output Terminal 2
9	VO1	Output Terminal 1
10	CN2	Capacitor Connection Terminal
11	CP1	Capacitor Connection Terminal
12	NC	
13	VOUT	Charge Pump Output Terminal
14	CP2	Capacitor Connection Terminal
15	VIN	Power Supply Terminal
16	NC	
Exp Pad	Exposed Pad	Exposed Pad connected to GND or left floating

■TERMINAL CONFIGURATION (SSOP14)



Pin No.	SYMBOL	FUNCTION
1	EN2	Step-up Mode Switch Terminal 2
2	DIN	Input Terminal
3	CN1	Capacitor Connection Terminal
4	GND	GND Terminal
5	VO2	Output Terminal 2
6	VO1	Output Terminal 1
7	CN2	Capacitor Connection Terminal
8	CP1	Capacitor Connection Terminal
9	NC	
10	VOUT	Charge Pump Output Terminal
11	CP2	Capacitor Connection Terminal
12	VIN	Power Supply Terminal
13	NC	
14	EN1	Step-up Mode Switch Terminal 1

■ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATING	UNIT
Supply Voltage	V _{IN}	5.5V 1xMode,2xMode 4.0V 3xMode	V
Input Voltage	V_{DIN}	-0.3 to VIN+0.3	V
Power Dissipation	P _D	480 * ¹⁾ / 1300 * ²⁾ (EQFN12) 400 * ¹⁾ / 1000 * ²⁾ (EQFN16) 440 * ³⁾ /550 * ⁴⁾ (SSOP14)	mW
Operating Temperature	Topr	-40 to +85	°C
Storage Temperature	Tstg	-40 to +125	°C

⁽Note) *10 EIA/JEDEC STANDARD Test board (76.2 x 114.3 x 1.6mm, 2layers, FR-4) mounting. The PAD connecting to GND in the center part

■RECOMMENDED OPEARATING CONDITION (Ta=25°C)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Operating Voltage	VANI	1x Mode, 2x Mode	2.3	3.0	5.0	V
Operating Voltage	VIN	1x Mode, 2x Mode, 3x Mode	2.3	3.0	3.4	

■ELECTRICAL CHARACTERISTICS

 $(Ta=25^{\circ}C,V_{IN}=3V, C_1=100nF, C_2=100nF, C_{OUT}=100nF, C_{PIEZO}=15nF, DIN=4kHz)$

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
	V _{OUT1}	1x Mode	2.8	-	3	V
Output Voltage	V _{OUT2}	2x Mode	5.2	-	6	V
	V _{OUT3}	3x Mode	7.2	-	9	V
	I _{DD11}	1x Mode C _{PIEZO} =no load	-	140	240	μА
Operating Current 1	I _{DD12}	2x Mode C _{PIEZO} =no load	-	720	1200	μА
	I _{DD13}	3x Mode C _{PIEZO} =no load	-	2500	4500	μА
	I _{DD21}	1x Mode Single ended application	-	0.3	-	mA
Operating Current 2	I _{DD22}	2x Mode Single ended application	-	1.4	-	mA
	I _{DD23}	3x Mode Single ended application	-	3.9	-	mA
	I _{DD31}	1x Mode Differential application	-	0.9	-	mA
Operating Current 3	I _{DD32}	2x Mode Differential application	-	3.6	-	mA
	I _{DD33}	3x Mode Differential application	-	7.9	-	mA
Consumption Current at Shutdown	I _{SD}	DIN=0V , (*1	-	-	1	μΑ
Input Frequency	F _{IN}	Rectangular pulse 0.2		4	8	kHz
Oscillating Frequency	F _{osc}			1	1.8	MHz

(*1: When 50msec or more maintains DIN, EN1, EN2 in 0

on the back. *2 EIA/JEDEC STANDARD Test board (76.2 x 114.3 x 1.6mm, 4layers, FR-4) mounting. The PAD connecting to GND in the center part *3) EIA/JEDEC STANDARD Test board (76.2 x 114.3 x 1.6mm, 2layers, FR-4) mounting.

^{*&}lt;sup>4)</sup> EIA/JEDEC STANDARD Test board (76.2 x 114.3 x 1.6mm, 4layers, FR-4) mounting.

NJU72501

■ELECTRICAL CHARACTERISTICS

 $(Ta=25^{\circ}C, V_{IN}=3V, C_1=100nF, C_2=100nF, C_{OUT}=100nF, C_{PIEZO}=15nF, DIN=4kHz)$

1	, 00	, , , , , , ,				
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
	T _{ON1}	1x Mode From DIN signal High to 90% VOUT steady state	1	30	100	μS
VOUT Start Delay Time	T _{ON2}	2x Mode From DIN signal High to 90% VOUT steady state	1	90	200	μS
	T _{ON3}	3x Mode From DIN signal High to 90% VOUT steady state	ı	180	350	μS
Shutdown Delay Time	T _{OFF}	DIN=H -> L	21	42	84	Ms
Output Short-circuit Protection Limitation Current	I _{SC}		20	40	60	mA

■CONTROL PART CHARACTERISTICS

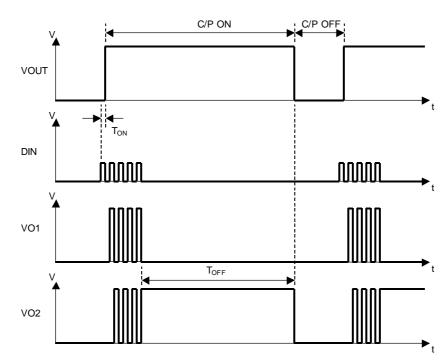
Ta=25°C, V_{IN}=3V

14-20 0, VIN-0V						
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Control Terminal Voltage H	V _{IH}	T _{IH} EN1 , EN2 , DIN pins		-	VIN	V
Control Terminal Voltage L	V _{IL}	EN1 , EN2 , DIN pins	0	-	0.2*VIN	V
Control Terminal Current 1	I _{IH1}	DIN=3V	-	1.7	3.4	μА
Control Terminal Current 2	I _{IH2}	V _{EN1} , V _{EN2} =3V , DIN=3V	-	1.7	3.4	μА
Control Terminal Current 3	I _{IH3}	V _{EN1} , V _{EN2} =3V , DIN=0V	-	-	1	μА

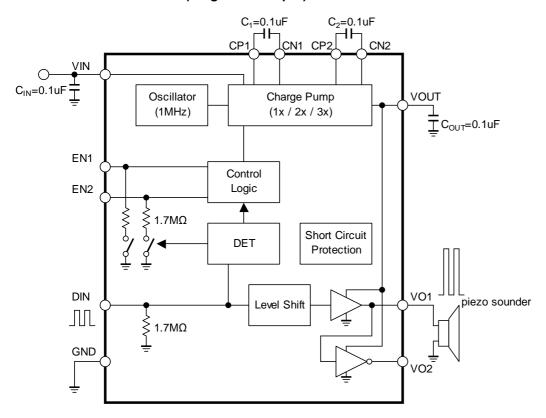
■CHARGE PUMP MODE SETTING

DIN	EN1	EN2	Charge Pump Mode
0	-	-	Shut Down Mode
1	0	0	Shut Down Mode
1	0	1	1x Mode
1	1	0	2x Mode
1	1	1	3x Mode

■TIMING CHART



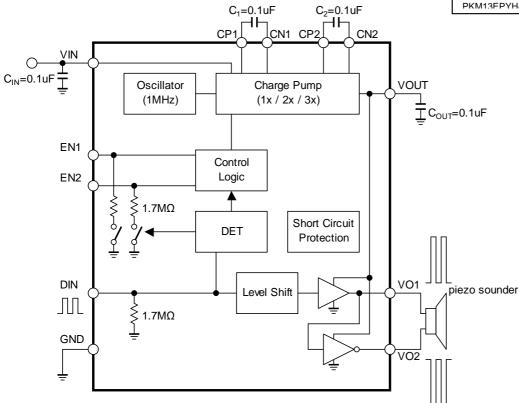
■APPLICATION CIRCUIT 1 (Single-end output)



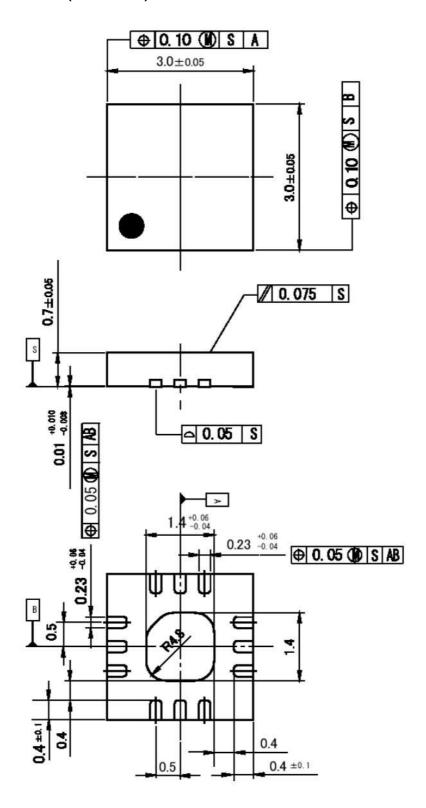
■APPLICATION CIRCUIT 2 (Differential output)

☐ Recommended Parts

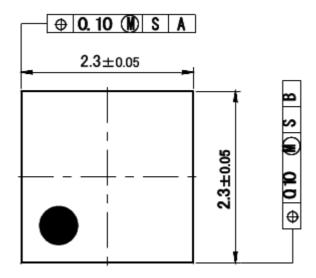
Piezo-sounder/ Piezo-buzzer PKLCS1212E2400-R1(muRata) PKLCS1212E4001-R1(muRata) PKM13FPYH4000-A0(muRata)

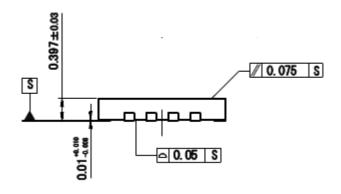


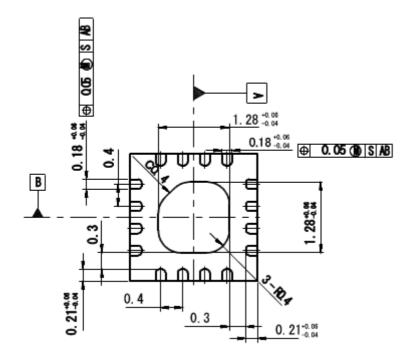
■PACKAGE INFORMATION (EQFN12-JE)



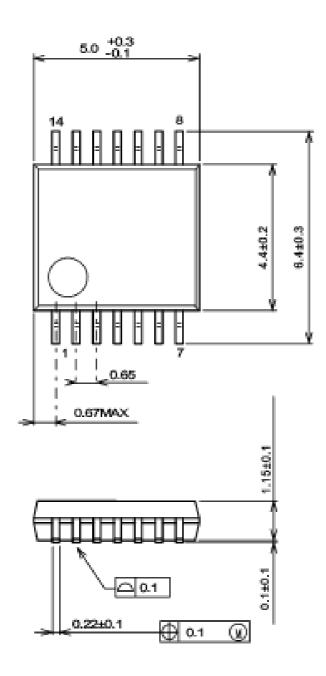
■PACKAGE INFORMATION (EQFN16-G2)

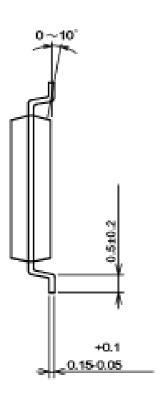






■PACKAGE INFORMATION (SSOP14)





[CAUTION]

The specifications on this databook are only given for information , without any guarantee as regards either mistakes or omissions. The application circuits in this databook are described only to show representative usages of the product and not intended for the guarantee or permission of any right including the industrial rights.