





DAC8563 Dual 16 bit DAC Module Output Positive and Negative 10V | Light Beads | - AliExpress

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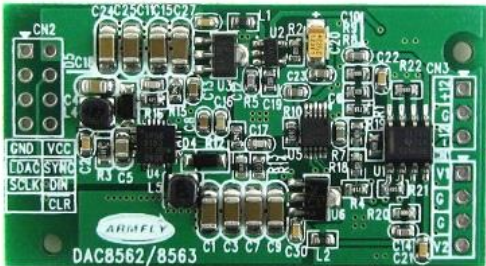
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OVERVIEW

CUSTOMER REVIEWS (0)

SPECIFICATIONS

Report Item

Product name: DAC8563 16-bit DAC module

Model: DAC8563

Power supply voltage: 2.7-5.5V [When 3.3V power supply, the output voltage can also be plus or minus 10V]

Number of channels: 2-way (through 1 DAC8563)

Output voltage range: -10V ~ +10V [Customers can change the output range to 0-10V. Use a soldering iron to switch between 2 solder joints, no need to replace components]

Output drive capability: with operational amplifier drive, the maximum output current is 10mA Load

Quick Notes Page 1

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Device

SCR Thyristor series
Solid state relay

Output voltage range: $-10V \sim +10V$ [Customers can change the output range to $0 \sim 10V$. Use a soldering iron to switch between 2 solder joints, no need to replace components]

Output drive capability: with operational amplifier drive, the maximum output current is 10mA. Load resistance $> 1K \Omega$

Resolution: 16 bits

Power consumption: less than 20mA

MCU interface: high-speed SPI (50M) supports 3.3V and 5V microcontrollers

DAC output analog bandwidth: 350KHz

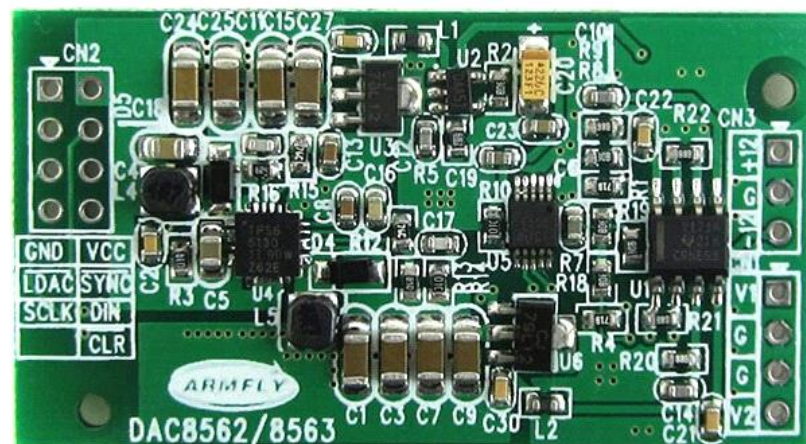
DAC output response: 10uS to 0.003% FSR

NOTE:

- (1) DAC8562 and DAC8563 are fully compatible. The only difference is that when the CLR pin is valid, DAC8562 data is set to 0, and DAC8563 data is set to 32767. Note that this is the internal data of the DAC and does not indicate the output voltage. For modules with $-10 \sim +10V$ output, DAC8562 outputs $-10V$ and DAC8563 outputs $0V$.
- (2) Whether using DAC8562 or DAC8563 chip, as long as the software is not started, the default output voltage of this module is $0V$.
- (3) When the CLR pin is floating, the voltage is about 1.9V, which is easy to be interfered and the output is cleared. Therefore, even if the CLR control function is not used, this CLR pin needs to be connected to a fixed level (recommended to connect to GND). CLR is edge-triggered, and it is cleared only when the falling edge signal occurs.

Features:

1. The output has nothing to do with the supply voltage; the module has a positive and negative 12V boost circuit.
2. Self-adaptive MCU level (2.7-5V are all possible)
3. The output voltage can reach plus or minus 10V
4. The default output is $0V$ when power is on (when the software is not started)
5. Lead out the positive and negative 12V power pin headers, which is convenient for customers to use.



用户可使用烙铁切换焊点，自行改变输出范围



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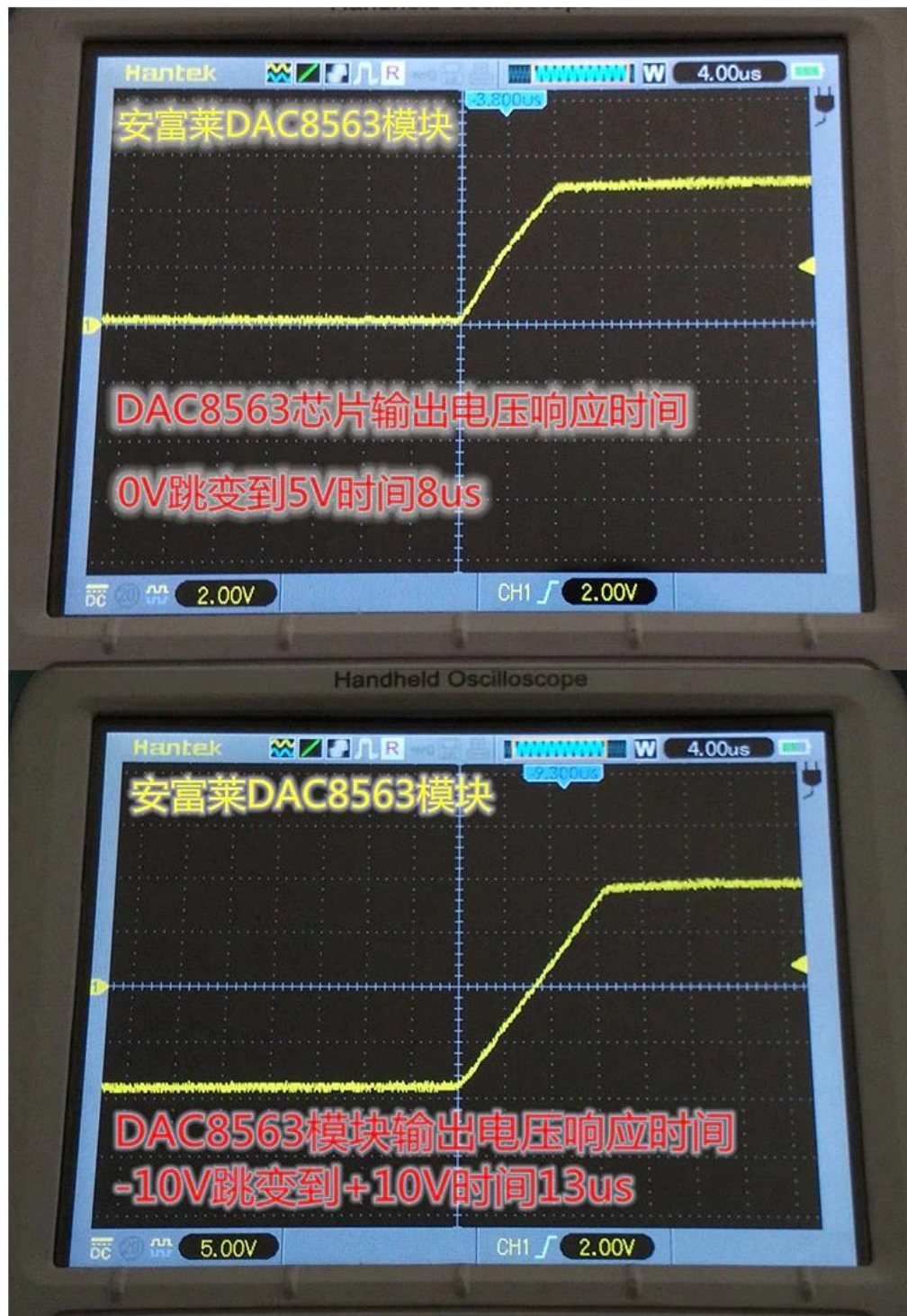
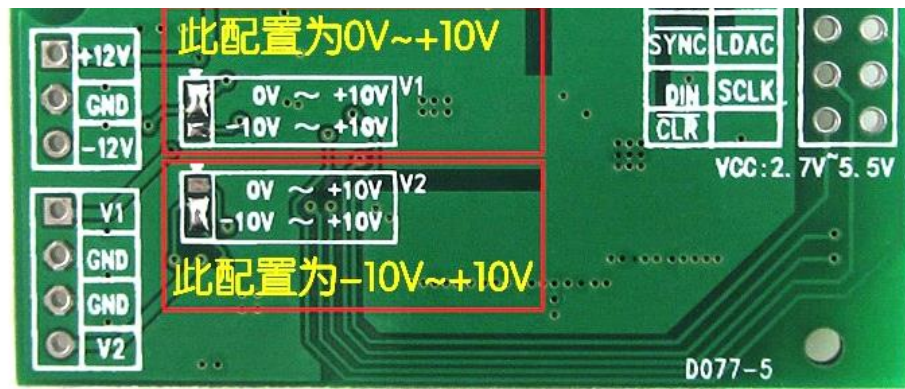
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Supporting routines: Provide examples of STM32F407 and STC89C52.

STC89C52 wiring method: (11.0592M crystal oscillator) Power on and output a full amplitude triangle wave (from -10V to +10V). Any IO port can be used without hardware SPI.

DAC8562 module STC89C52 development board

VCC ----- 5.0V (3.3V)

GND ----- GND

SYNC ----- P1.0 SPI chip select

LDAC - connect to GND (or the microcontroller is driven to 0)

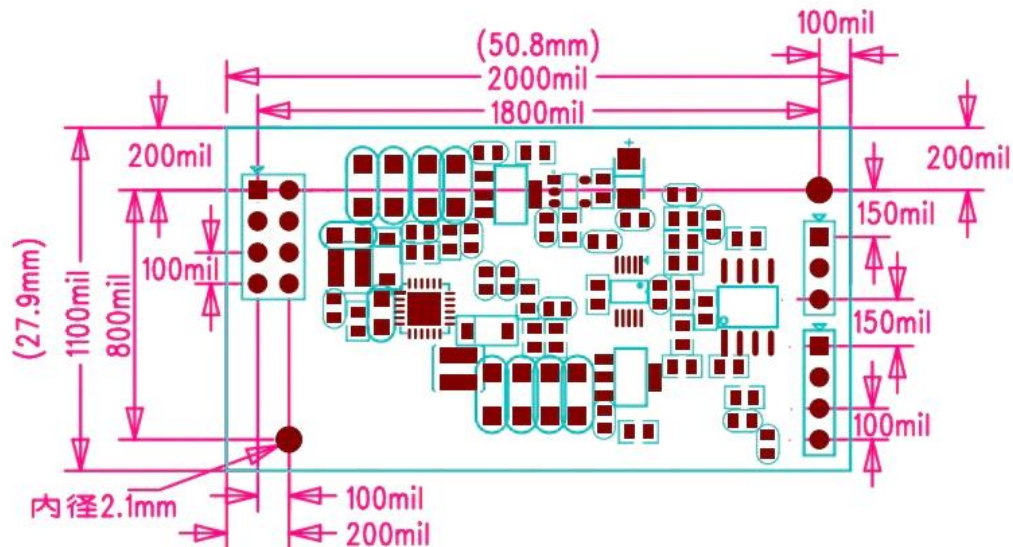
DIN ----- P1.2 SPI MOSI

SCK ----- P1.3 SPI clock

CLR ----- Connect to GND (or the MCU is driven to 0) to avoid clearing the output when the port line is suspended

【安富莱】DAC8562/DAC8563 模块_尺寸图

D077-5



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