

64.CSE1003-Digital Logic and Design Lab

Duration: 9.00 AM to 10.50 AM

Max.Marks = 50

| Distribution of 50 Marks | | | | | | | |
|--------------------------|-----------|------------------------------------|--|-----------|--------------------------------------|------|-------|
| Grade Element | Objective | Theory&/ Tables/Model graphs | Schematic/ Circuit/ Logicdiagrams/ Relevant Equations | Execution | Result/ Discussion/ Conclusion | Viva | Total |
| Max. Marks | 2 | 10 | 18 | 5 | 10 | 5 | 50 |
| Marks Earned | | | | | | | |

(Q) Design a 5-bit odd parity generator. Odd parity bit means that the number of 1's in the code including the parity bit is an odd number. A 5-bit code in which four of the bits (A, B, C, D) represents the information to be sent and fifth bit (x), represents the parity bit.

Viva Questions:

3. Explain the differences among a truth table, a state table, a characteristic table, and an excitation table.
4. Also, explain the difference among a Boolean equation, a state equation, a characteristic equation, and a flip-flop input equation.