

Department of Electronic and Telecommunication Engineering

University of Moratuwa, Sri Lanka

EN3030 - Circuits and Systems Design



# FPGA Based Processor Design

## Project Report

Submitted by

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## **Abstract**

Design procedure of a Finite Duration Impulse Response(FIR) bandpass Digital Filter which satisfies a set of prescribed specifications, is described in this report where windowing method in conjunction with the Kaiser window is used for the designing procedure. Operation of the filter was analyzed with a combination of sinusoidal signals. The design was implemented and tested using **MATLAB R2018a** of the MathWorks Inc.

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***\*PDF is clickable***

### **Note:**

All the materials and executable MATLAB R2018a Live Script related to the project can also be found at <https://github.com/bimalka98/Digital-Signal-Processing>

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## Bibliography