

existing prototype PCB. Error in the circuit may exist.  
Note: This is only based off of the author's interpretation of an  
Author: Theodore Cox

**FSI**

Sheet:  
File: PRIME-D.kicad\_pcb

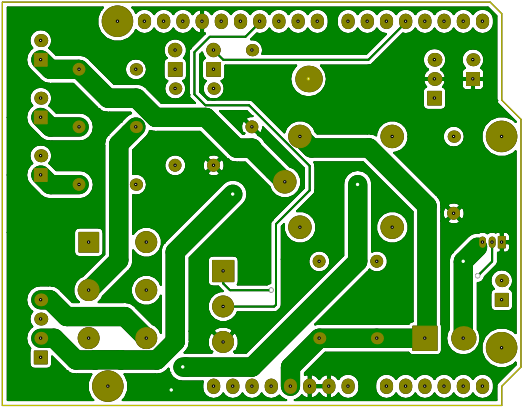
**Title: Prime D**

Size: A4 Date: 2019-02-18

KiCad E.D.A. kicad (5.0.2)-1

**Rev: R0**

Id: 1/4



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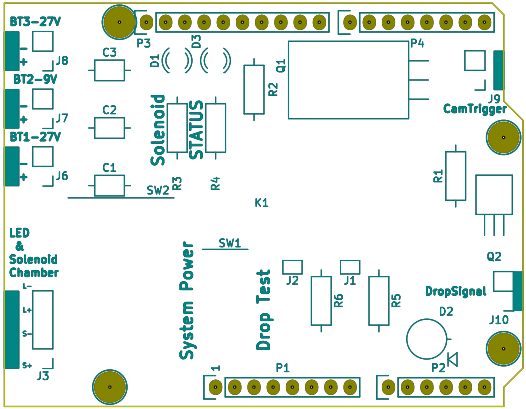
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