# LaTeX Template for the Weekly Reports

David Merkl

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APP_STARTUP_STATE
appInitUsartManager
initTimer

APP_STARTJOIN_NETWORK
ZDO_StartNetworkReq

APP_TRANSMIT
encode Message
send Message

APP_INIT_ENDPOINT
initEndpoint

APP_AUSGABE_STATE
calculating outpout
of SCD,SHT,CCS

APP_INIT_TRANSMITDATA
initTransmitData

APP_CCS_READ_RESULT_REG_STATE
I2C-Read: 4 bytes
(eCO2 — TVOC)

APP_RESET_CCS_SW_STATE
I2C-Write: 0xFF
0x11 0xE5 0x72 0x8A
SW_RESET_REG
SW_RESET_SEQUENCE

APP_CCS_WRITE_RESULT_REG_STATE
I2C-Write: 0x02
ALG_RESULT_DATA_REG

APP_CCS_HW_ID_WRITE_REG_STATE
I2C-Write: 0x20
HW_ID_REG

APP_READ_RH_SHT_STATE
I2C-Read: 2 bytes
TODO ask Achim

APP_CCS_HW_ID_READ_STATE
I2C-Read: 1 byte
Hardware ID has to be 0x81

APP_CALL_FOR_READ_RH_SHT_STATE
I2C-Write: TODO
TODO ask Achim

APP_CCS_CHANGE_TO_APPSTATE_STATE
I2C-Write: 0xF4
BOOTLOADER_APP_START

APP_READ_TEMP_SHT_STATE
I2C-Read: 2 bytes
TODO ask Achim

APP_CCS_WRITE_MEAS_REG_STATE
I2C-Write: 0x01 0xTODO
MEAS_MODE_REG
(Measure every TODO)

APP_CALL_FOR_READ_TEMP_SHT_STATE
I2C-Write: TODO
TODO ask Achim

APP_CCS_WRITE_STATUS_REG_STATE
I2C-Write: 0x00
STATUS_REG

APP_READ_SCD_STATE
I2C-Read: 9 bytes
TODO ask Achim

APP_CCS_READ_STATUS_REG_STATE
I2C-Read: 1 byte
Bit 0: error
Bit 3: data ready

APP_CALL_FOR_READ_SCD_STATE
I2C-Write:TODO
TODO ask Achim

APP_RESET_SCD_STATE
I2C-Write: 0x3F 0x86
TODO ask Achim

APP_INIT_SENSOR_STATE
I2C-Write: 0x21 0xB1
TODO ask Achim
HAL_StartAppTimer(30 s)