**Ve370 Introduction to Computer Organization Homework 4**

1. Exercise 4.2.2
2. Exercise 4.2.3
3. Exercise 4.2.4
4. Exercise 4.6.5
5. Exercise 4.7.1
6. Exercise 4.7.2
7. Exercise 4.7.3
8. Exercise 4.8.1
9. Exercise 4.8.2
10. Exercise 4.11.2
11. Exercise 4.11.3
12. Modify the MIPS single cycle implementation based on Figure 4.24 to support the instruction BNE.
13. Modify the MIPS single cycle implementation based on Figure 4.24 to support the instruction JAL.
14. Modify the MIPS single cycle implementation based on Figure 4.24 to support the instruction JR.