



# A Bulk-Parallel Priority Queue in External Memory with STXXL

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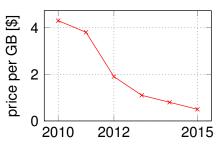


### **Motivation**



HDD/SSD transfer rate		
2002	2015	
HDD: 60 MB/s	HDD: 180 MB/s	
	SSD: 500+ MB/s	

### Server SSD price trend



Sorting speed		
Intel Xeon E5-2650 v2		
Sorting 1 GB of random		
integers (64-bit)		
1 core	85 MB/s	
16 cores	1290 MB/s	

Source: Gartner, Market Trends: Evolving HDD and SSD Storage Landscapes

# **History and Classification**



### **External Memory**

Brodal, Katajainen

Worst-case efficient externalmemory priority queues (1998) Sanders

Fast Priority Queues for Cached Memory (2000)

#### Concurrent

Sundell, Tsigas

Fast and lock-free concurrent priority queues for multi-thread systems (2003)

#### Relaxed

Alistarh et. al

The SprayList: a scalable relaxed priority queue (2014)

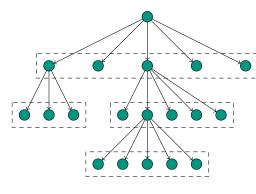
#### Parallel EM

Beckmann et. al.

Parallel multiway merge for the sequence heap (2009)

### **Bulk Push**



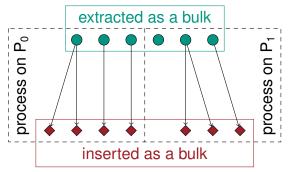


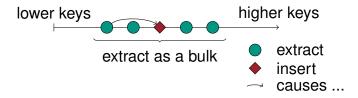
[] bulk push

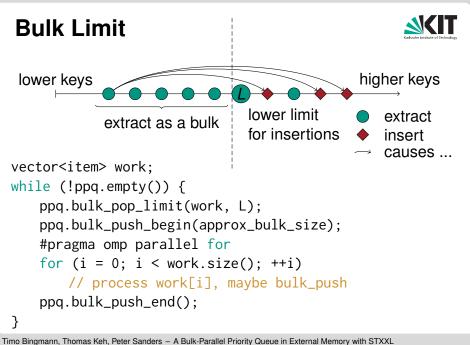
```
ppq.bulk_push_begin(
   approx_bulk_size);
#pragma omp parallel for
for (...) {
 ppq.bulk_push(value,
     thread_id);
ppq.bulk_push_end();
```

# **Bulk Pop**



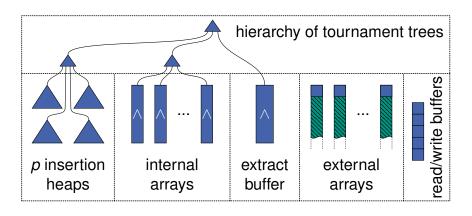






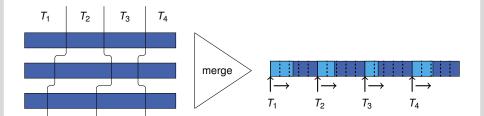
## **Components**





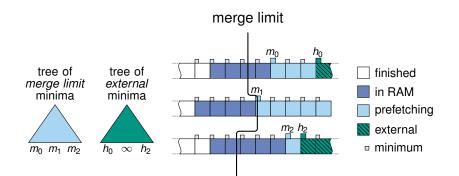
# **Parallel Multiway Merge**





### **Prefetch Prediction**





## **Experiments**



#### **Platforms**

A-Rotational	B-SSD
Intel Xeon X5550 2.66 GHz	Intel Xeon E5-2650 v2 2.6 GHz
2 sockets, 8 + 8 HT cores	2 sockets, 16 + 16 HT cores
48 <i>GB</i> RAM	128 <i>GB</i> RAM
8/17 <i>GB/s</i> (1/8 cores)	12/16 <i>GB/s</i> (1/16 cores)
6x Seagate SV35.5 1 <i>TB</i>	4x Samsung SSD 840 Evo 1 <i>TB</i>
85–170 <i>MB/s</i> read/write	512/475 <i>MB/s</i> read/write
parallel: 740 <i>MB/s</i> R/W	parallel: 2.0/1.6 <i>GB/s</i> R/W

### Competitors

Bulk-Parallel Priority Queue (PPQ)

STXXL Priority Queue (SPQ-S)

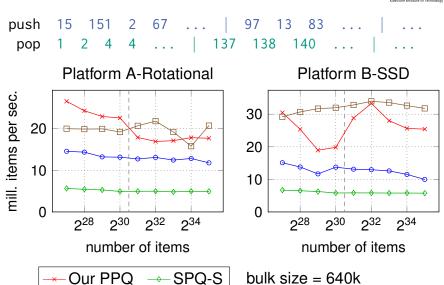
STXXL Priority Queue + Parallel Multiway Merge (SPQ-P)

STXXL Sorter

## Insert-All-Delete-All

SPQ-P



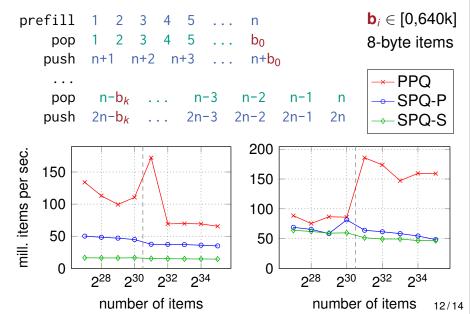


8-byte items

Sorter

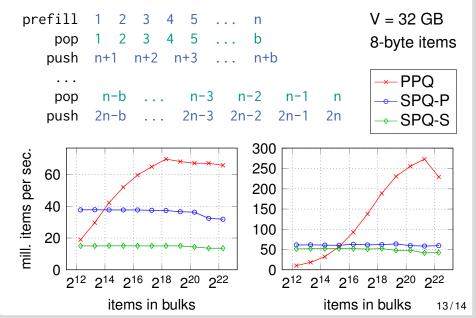
## **Random Bulk Rewrite**





## Variate Bulk Size

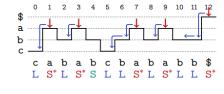




### **Future Work**



Integration in eSAIS (external suffix and LCP array construction)



- Overlap Application and Priority Queue work
- Experiments with other internal memory PQs