

D

C

B

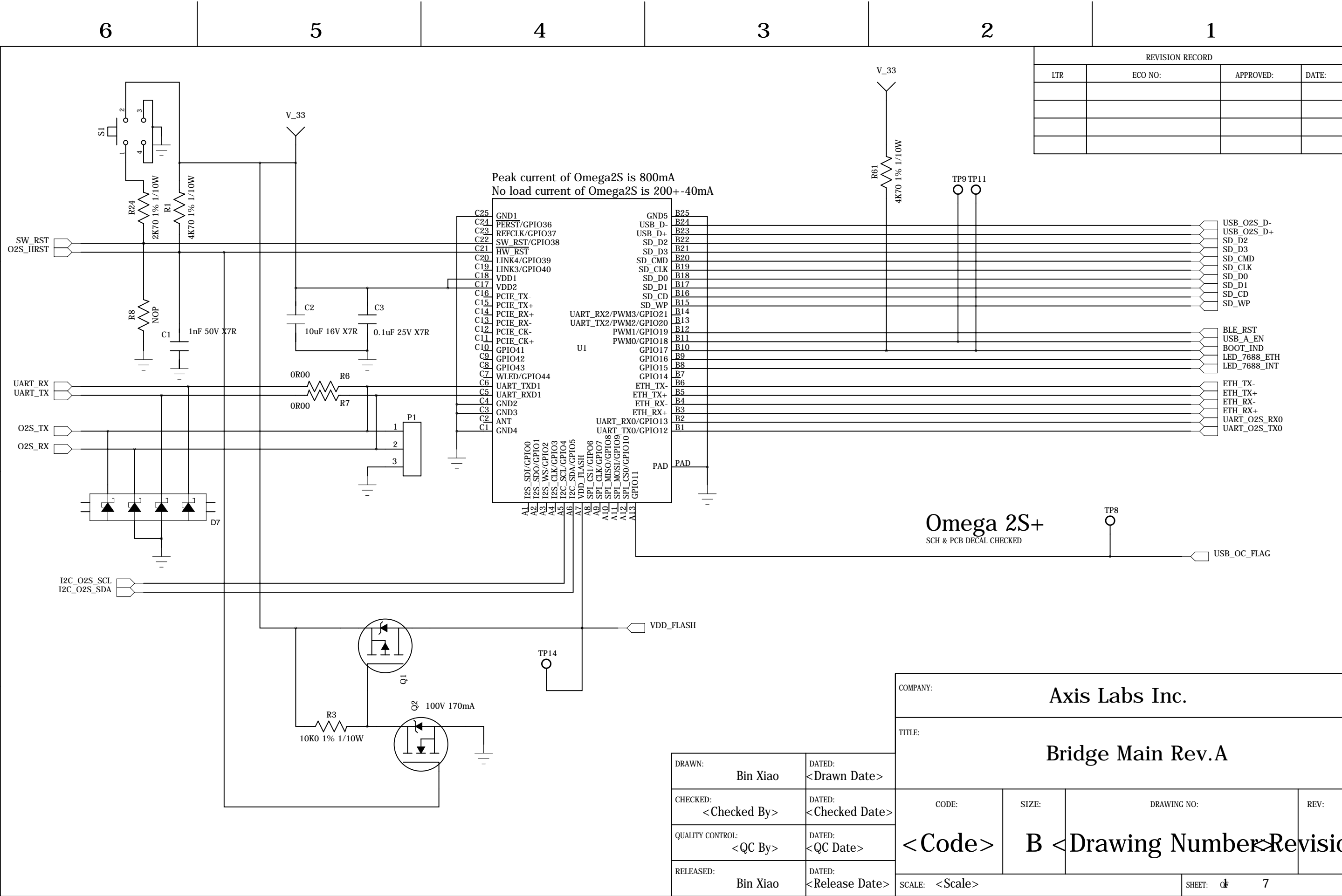
A

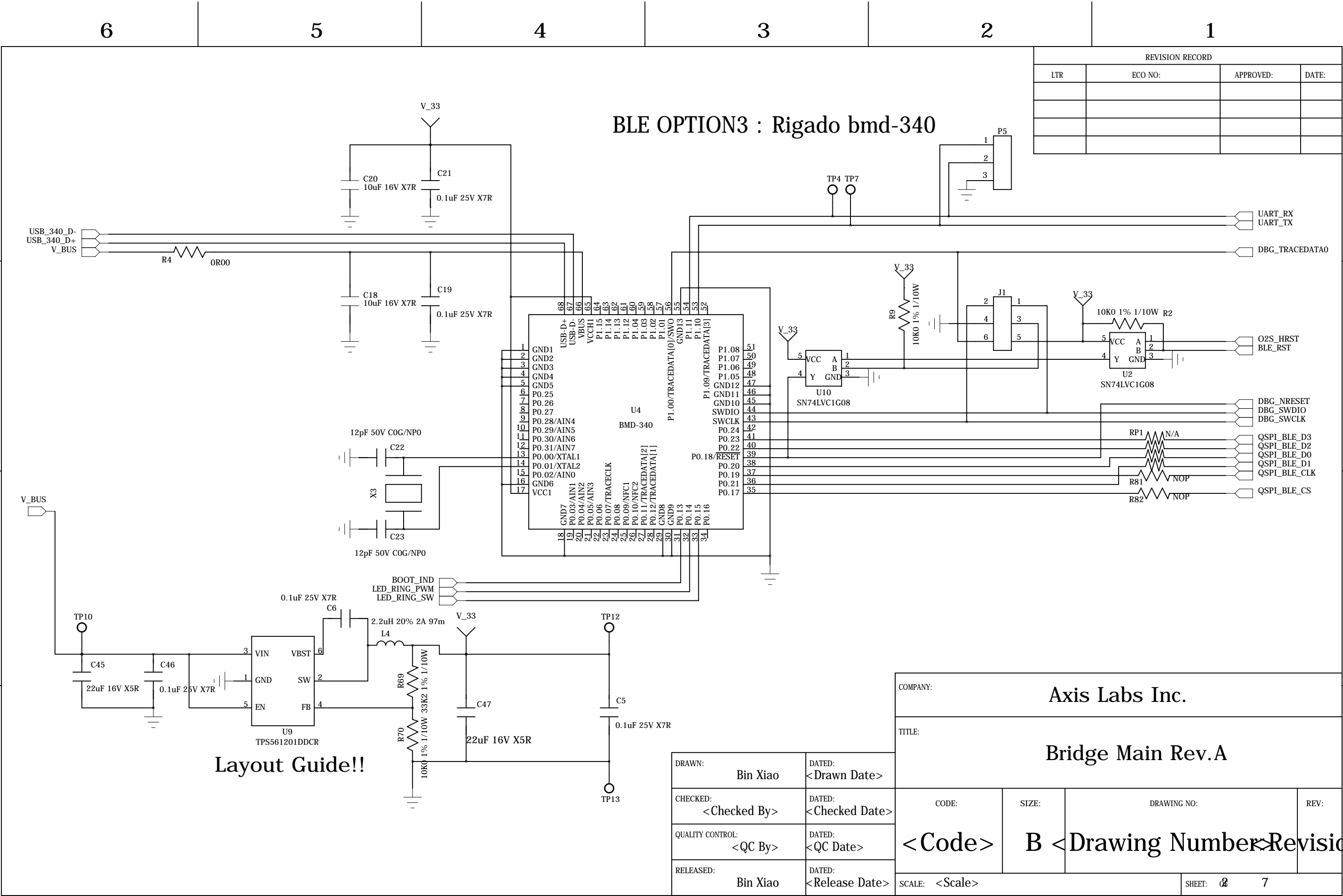
D

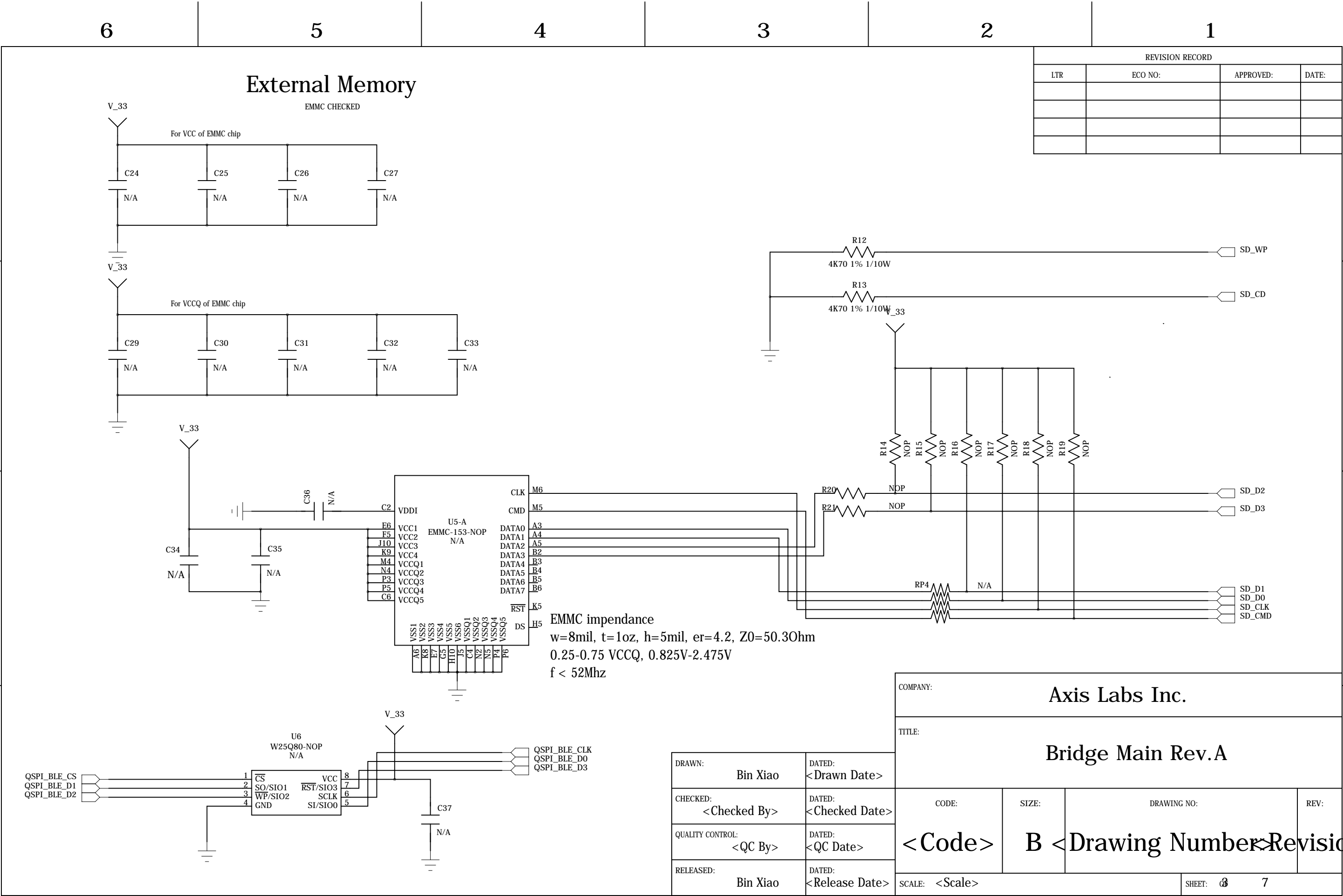
C

B

A







COMPANY:

Axis Labs Inc.

TITLE:

Bridge Main Rev.A

DRAWN:

Bin Xiao

CHECKED:

<Checked By>

QUALITY CONTROL:

<QC By>

RELEASED:

Bin Xiao

DATED:

<Drawn Date>

DATED:

<Checked Date>

DATED:

<QC Date>

DATED:

<Release Date>

CODE:

SIZE:

DRAWING NO:

REV:

<Code>

B

<Drawing Number>

<Revision>

SCALE: <Scale>

SHEET: 37

D

C

B

A

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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

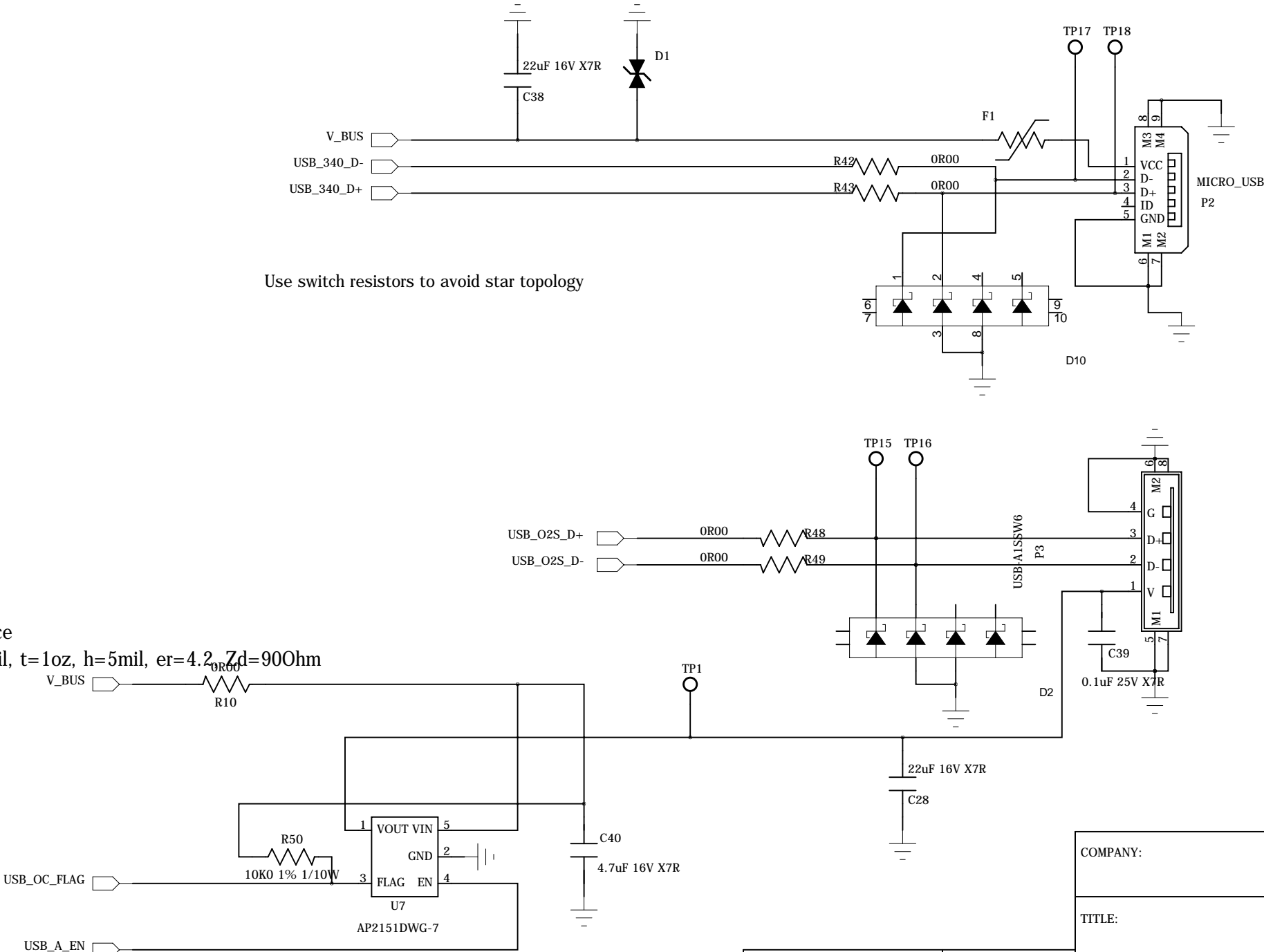
D

C

B

A

USB impedance  
w=6mil, s=6mil, t=1oz, h=5mil, er=4.2, Z<sub>d</sub>=900hm



Use switch resistors to avoid star topology

USB 2.0 Interface  
USB SCH PCB CHECKED

COMPANY:		Axis Labs Inc.	
TITLE:		Bridge Main Rev.A	
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	B	<Drawing Number>	<Revision>
SCALE: <Scale>		SHEET: 4 OF 7	

DRAWN:	Bin Xiao	DATED:	<Drawn Date>
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	Bin Xiao	DATED:	<Release Date>



6

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C

B

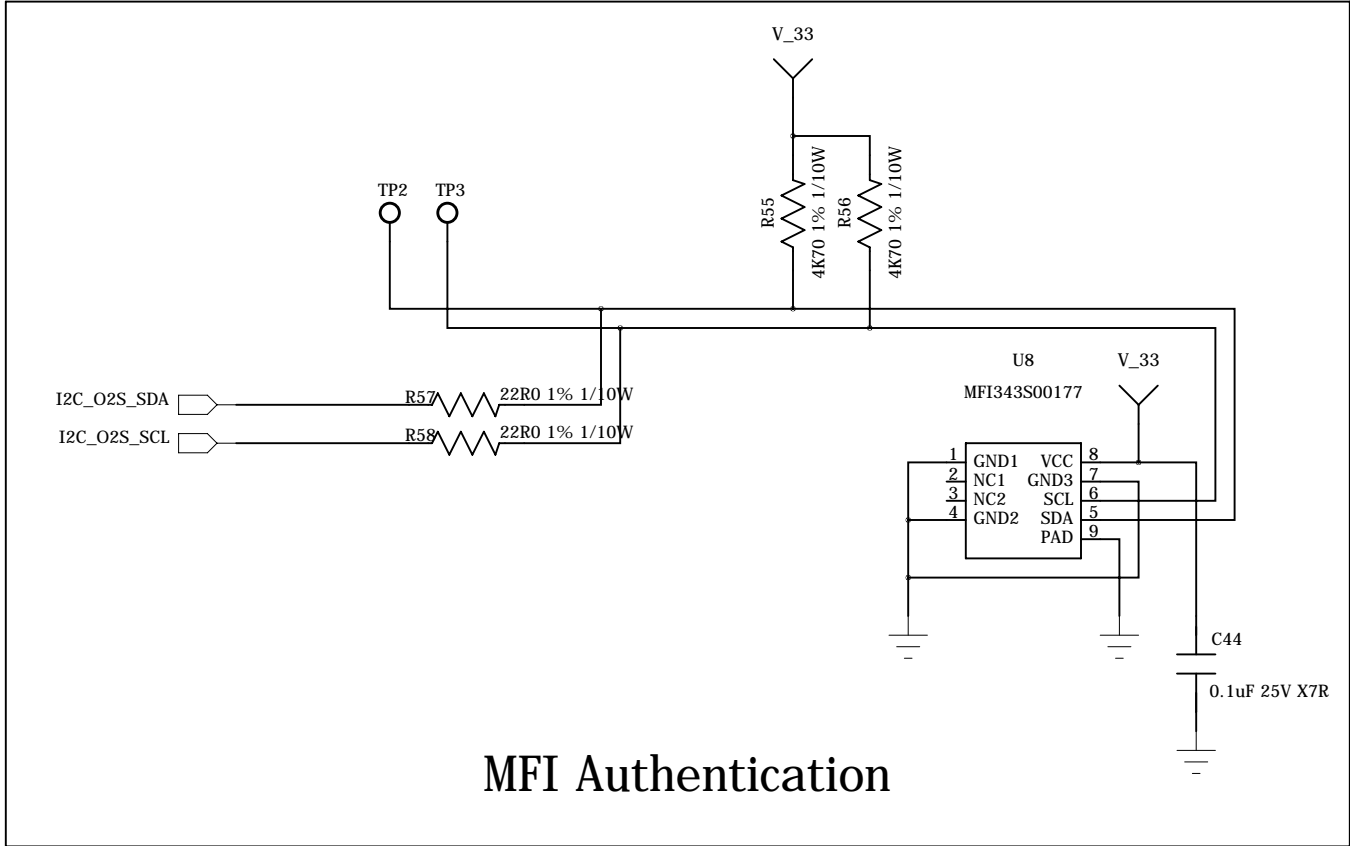
A

D

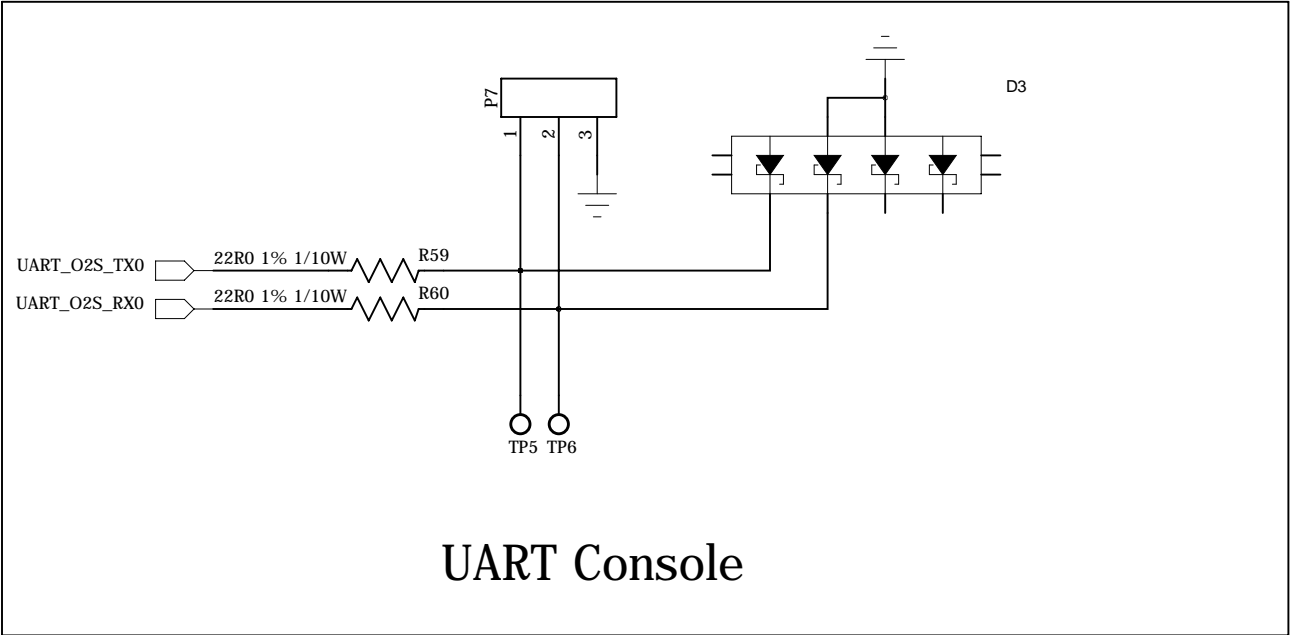
C

B

A



MFI Authentication



UART Console

REVISION RECORD

LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Axis Labs Inc.			
TITLE: Bridge Main Rev.A			
CODE: <Code>	SIZE: B	DRAWING NO: <Drawing Number>	REV: <Revision>
SCALE: <Scale>			SHEET: 6 7

DRAWN: Bin Xiao	DATED: <Drawn Date>
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: Bin Xiao	DATED: <Release Date>

