



SoCs

Espressif offers integrated,
reliable and energy-efficient wireless SoCs

32-bit MCU | Wi-Fi | Bluetooth LE | Thread/Zigbee | AI Functions*



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Choosing the ESP products you need has never been easier!



IDF Component Manager

Easily integrate the components into your existing IDF projects.



ESP32-P Series



ESP32-P4 Series

32-bit RISC-V MCU

- High-performance MCU with RISC-V dual-core and single-core microprocessors
- 128 KB HP ROM, 16 KB LP ROM, 768 KB HP L2MEM, 32 KB LP SRAM, 8 KB TCM
- Powerful image and voice processing capability
- 16 MB or 32 MB PSRAM in the chip's package





ESP32-P Series		ESP32-S Series		ESP32-C Series		ESP32-H Series		ESP32 Series		ESP8266 Series	
SoC	Buy	Variants	Core	Dimensions (mm)	Pins	RAM/ROM (KB)	Flash (MB)	PSRAM (MB)	Module		
 ESP32-P4		ESP32-P4NRW16 ESP32-P4NRW32	HP Dual Core/LP Single Core	QFN 10×10	104	128 KB HP ROM, 16 KB LP ROM, 768 KB HP L2MEM, 32 KB LP SRAM, 8 KB TCM	N/A	16 32	N/A		

ESP32-S Series

ESP32-S3 Series

Hardware Guideline

32-bit MCU & 2.4 GHz Wi-Fi & Bluetooth 5 (LE)

- Xtensa® 32-bit LX7 dual-core processor that operates at up to 240 MHz
- 512 KB of SRAM and 384 KB of ROM on the chip, and SPI, Dual SPI, Quad SPI, Octal SPI, QPI, and OPI interfaces that allow connection to flash and external RAM
- Additional support for vector instructions in the MCU, which provides acceleration for neural network computing and signal processing workloads
- Peripherals include 45 programmable GPIOs, SPI, I2S, I2C, PWM, RMT, ADC and UART, SD/MMC host and TWAI™
- Reliable security features ensured by RSA-based secure boot, AES-XTS-based flash encryption, the innovative digital signature and the HMAC peripheral, “World Controller”

SoC	Buy	Variants	Core	Dimensions (mm)	Pins	RAM/ROM (KB)	Flash (MB)	PSRAM (MB)	Module	Development Kit
 ESP32-S3		ESP32-S3 ESP32-S3R2 ESP32-S3R8 ESP32-S3R8V ESP32-S3FN8 ESP32-S3FH4R2	Dual Core	QFN 7×7	56	384 KB ROM, 512 KB SRAM, 16 KB RTC SRAM	N/A N/A N/A 8 8 4	N/A 2 8 8 N/A 2	ESP32-S3-WROOM-1 ESP32-S3-WROOM-1U ESP32-S3-WROOM-2 ESP32-S3-MINI-1 ESP32-S3-MINI-1U	ESP32-S3-DevKitC-1 ESP32-S3-DevKitC-2 ESP32-S3-DevKitC-3 ESP32-S3-DevKitC-4 ESP32-S3-DevKitC-5 ESP32-S3-DevKitC-6
 ESP32-S3-PICO-1		ESP32-S3-PICO-1-N8R2 ESP32-S3-PICO-1-N8R8	Dual Core	LGA 7×7	56	384 KB ROM, 512 KB SRAM, 16 KB RTC SRAM	8 Quad	2 8 Octal	N/A	N/A





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ESP32-P Series

ESP32-S Series

ESP32-C Series

ESP32-H Series

ESP32 Series

ESP8266 Series

- High-performance 240 MHz single-core CPU
- Ultra-low-power performance: fine-grained clock gating, dynamic voltage and frequency scaling
- Security features: eFuse, flash encryption, secure boot, signature verification, integrated AES, SHA and RSA algorithms
- Peripherals include 43 GPIOs, 1 full-speed USB OTG interface, SPI, I2S, UART, I2C, LED PWM, LCD interface, camera interface, ADC, DAC, touch sensor, temperature sensor
- Availability of common cloud connectivity agents and common product features shortens the time to market

SoC	Buy	Variants	Core	Dimensions (mm)	Pins	RAM/ROM (KB)	Flash (MB)	PSRAM (MB)	Module	Development
 ESP32-S2		ESP32-S2 ESP32-S2R2	Single Core	QFN 7×7	56	128 KB ROM, 320 KB SRAM, 16 KB RTC SRAM	N/A N/A	N/A 2	ESP32-S2-WROOM ESP32-S2-WROOM-I ESP32-S2-WROVER ESP32-S2-WROVER-I	ESP32-S2 ESP32-S2 ESP32-S2-I ESP32-S2-I
 ESP32-S2F		ESP32-S2FH2 ESP32-S2FH4 ESP32-S2FN4R2	Single Core	QFN 7×7	56	128 KB ROM, 320 KB SRAM, 16 KB RTC SRAM	2 4 4	N/A N/A 2	ESP32-S2-MINI-1 ESP32-S2-MINI-1U	ESP32-S2 ESP32-S2 ESP32-S2-I



ESP32-C Series

ESP32-C6 Series

Hardware Guideline

32-bit RISC-V MCU & 2.4 GHz Wi-Fi 6 & Bluetooth 5 (LE) & IEEE 802.15.4





- 32-bit RISC-V single-core processor that operates at up to 160 MHz
- State-of-the-art power and RF performance
- 320 KB ROM, 512 KB SRAM, 16 KB Low-power SRAM on the chip, and works with external flash
- 30 (QFN40) or 22 (QFN32) programmable GPIOs, with support for SPI, UART, I2C, I2S, RMT, TWAI and PWM

SoC	Buy	Variants	Core	Dimensions (mm)	Pins	RAM/ROM (KB)	Flash (MB)	PSRAM (MB)	Module	Development
 ESP32-C6		ESP32-C6 ESP32-C6FH4	Single Core	QFN 5×5 /	40 32	512 KB SRAM, 320 KB ROM	0 4	N/A	ESP32-C6-WROOM-1 ESP32-C6-MINI-1	ESP32-C6 ESP32-C6



ESP32-P Series ESP32-S Series ESP32-C Series ESP32-H Series ESP32 Series ESP8266 Series

- 32-bit RISC-V single-core processor with a four-stage pipeline that operates at up to 160 MHz
- State-of-the-art power and RF performance
- 400 KB of SRAM and 384 KB of ROM on the chip, and SPI, Dual SPI, Quad SPI, and QPI interfaces that allow connection to flash
- Reliable security features ensured by RSA-3072-based secure boot, AES-128-XTS-based flash encryption, the innovative digital signature and the HMAC peripheral, hardware acceleration support for cryptographic algorithms
- Rich set of peripheral interfaces and GPIOs, ideal for various scenarios and complex applications



SoC	Buy	Variants	Core	Dimensions (mm)	Pins	RAM/ROM (KB)	Flash (MB)	PSRAM (MB)	Module	Development
 ESP32-C3		ESP32-C3 ESP32-C3FH4 ESP32-C3FH4X ESP32-C3FN4 (EOL)	Single Core	QFN 5×5	32	400 KB RAM, 384 KB ROM, 8 KB RTC SRAM	N/A 4	N/A	ESP32-C3-WROOM-02 ESP32-C3-WROOM-02U ESP32-C3-MINI-1 ESP32-C3-MINI-1U	ESP32-C3-DevKitC-1 ESP32-C3-DevKitC-2 ESP32-C3-DevKitC-3 ESP32-C3-DevKitC-4 ESP32-C3-DevKitC-5 ESP32-C3-DevKitC-6 ESP32-C3-DevKitC-7 ESP32-C3-DevKitC-8 ESP32-C3-DevKitC-9 ESP32-C3-DevKitC-10 ESP32-C3-DevKitC-11 ESP32-C3-DevKitC-12 ESP32-C3-DevKitC-13 ESP32-C3-DevKitC-14 ESP32-C3-DevKitC-15 ESP32-C3-DevKitC-16 ESP32-C3-DevKitC-17 ESP32-C3-DevKitC-18 ESP32-C3-DevKitC-19 ESP32-C3-DevKitC-20 ESP32-C3-DevKitC-21 ESP32-C3-DevKitC-22 ESP32-C3-DevKitC-23 ESP32-C3-DevKitC-24 ESP32-C3-DevKitC-25 ESP32-C3-DevKitC-26 ESP32-C3-DevKitC-27 ESP32-C3-DevKitC-28 ESP32-C3-DevKitC-29 ESP32-C3-DevKitC-30 ESP32-C3-DevKitC-31 ESP32-C3-DevKitC-32 ESP32-C3-DevKitC-33 ESP32-C3-DevKitC-34 ESP32-C3-DevKitC-35 ESP32-C3-DevKitC-36 ESP32-C3-DevKitC-37 ESP32-C3-DevKitC-38 ESP32-C3-DevKitC-39 ESP32-C3-DevKitC-40 ESP32-C3-DevKitC-41 ESP32-C3-DevKitC-42 ESP32-C3-DevKitC-43 ESP32-C3-DevKitC-44 ESP32-C3-DevKitC-45 ESP32-C3-DevKitC-46 ESP32-C3-DevKitC-47 ESP32-C3-DevKitC-48 ESP32-C3-DevKitC-49 ESP32-C3-DevKitC-50 ESP32-C3-DevKitC-51 ESP32-C3-DevKitC-52 ESP32-C3-DevKitC-53 ESP32-C3-DevKitC-54 ESP32-C3-DevKitC-55 ESP32-C3-DevKitC-56 ESP32-C3-DevKitC-57 ESP32-C3-DevKitC-58 ESP32-C3-DevKitC-59 ESP32-C3-DevKitC-60 ESP32-C3-DevKitC-61 ESP32-C3-DevKitC-62 ESP32-C3-DevKitC-63 ESP32-C3-DevKitC-64 ESP32-C3-DevKitC-65 ESP32-C3-DevKitC-66 ESP32-C3-DevKitC-67 ESP32-C3-DevKitC-68 ESP32-C3-DevKitC-69 ESP32-C3-DevKitC-70 ESP32-C3-DevKitC-71 ESP32-C3-DevKitC-72 ESP32-C3-DevKitC-73 ESP32-C3-DevKitC-74 ESP32-C3-DevKitC-75 ESP32-C3-DevKitC-76 ESP32-C3-DevKitC-77 ESP32-C3-DevKitC-78 ESP32-C3-DevKitC-79 ESP32-C3-DevKitC-80 ESP32-C3-DevKitC-81 ESP32-C3-DevKitC-82 ESP32-C3-DevKitC-83 ESP32-C3-DevKitC-84 ESP32-C3-DevKitC-85 ESP32-C3-DevKitC-86 ESP32-C3-DevKitC-87 ESP32-C3-DevKitC-88 ESP32-C3-DevKitC-89 ESP32-C3-DevKitC-90 ESP32-C3-DevKitC-91 ESP32-C3-DevKitC-92 ESP32-C3-DevKitC-93 ESP32-C3-DevKitC-94 ESP32-C3-DevKitC-95 ESP32-C3-DevKitC-96 ESP32-C3-DevKitC-97 ESP32-C3-DevKitC-98 ESP32-C3-DevKitC-99 ESP32-C3-DevKitC-100
 ESP8685		ESP8685H4	Single Core	QFN 4×4	28	400 KB RAM, 384 KB ROM, 8 KB RTC SRAM	2, 4	N/A	ESP8685-WROOM-01 ESP8685-WROOM-03 ESP8685-WROOM-04 ESP8685-WROOM-05 ESP8685-WROOM-06 ESP8685-WROOM-07	ESP8685-DevKitC-1 ESP8685-DevKitC-2 ESP8685-DevKitC-3 ESP8685-DevKitC-4 ESP8685-DevKitC-5 ESP8685-DevKitC-6 ESP8685-DevKitC-7 ESP8685-DevKitC-8 ESP8685-DevKitC-9 ESP8685-DevKitC-10 ESP8685-DevKitC-11 ESP8685-DevKitC-12 ESP8685-DevKitC-13 ESP8685-DevKitC-14 ESP8685-DevKitC-15 ESP8685-DevKitC-16 ESP8685-DevKitC-17 ESP8685-DevKitC-18 ESP8685-DevKitC-19 ESP8685-DevKitC-20 ESP8685-DevKitC-21 ESP8685-DevKitC-22 ESP8685-DevKitC-23 ESP8685-DevKitC-24 ESP8685-DevKitC-25 ESP8685-DevKitC-26 ESP8685-DevKitC-27 ESP8685-DevKitC-28 ESP8685-DevKitC-29 ESP8685-DevKitC-30 ESP8685-DevKitC-31 ESP8685-DevKitC-32 ESP8685-DevKitC-33 ESP8685-DevKitC-34 ESP8685-DevKitC-35 ESP8685-DevKitC-36 ESP8685-DevKitC-37 ESP8685-DevKitC-38 ESP8685-DevKitC-39 ESP8685-DevKitC-40 ESP8685-DevKitC-41 ESP8685-DevKitC-42 ESP8685-DevKitC-43 ESP8685-DevKitC-44 ESP8685-DevKitC-45 ESP8685-DevKitC-46 ESP8685-DevKitC-47 ESP8685-DevKitC-48 ESP8685-DevKitC-49 ESP8685-DevKitC-50 ESP8685-DevKitC-51 ESP8685-DevKitC-52 ESP8685-DevKitC-53 ESP8685-DevKitC-54 ESP8685-DevKitC-55 ESP8685-DevKitC-56 ESP8685-DevKitC-57 ESP8685-DevKitC-58 ESP8685-DevKitC-59 ESP8685-DevKitC-60 ESP8685-DevKitC-61 ESP8685-DevKitC-62 ESP8685-DevKitC-63 ESP8685-DevKitC-64 ESP8685-DevKitC-65 ESP8685-DevKitC-66 ESP8685-DevKitC-67 ESP8685-DevKitC-68 ESP8685-DevKitC-69 ESP8685-DevKitC-70 ESP8685-DevKitC-71 ESP8685-DevKitC-72 ESP8685-DevKitC-73 ESP8685-DevKitC-74 ESP8685-DevKitC-75 ESP8685-DevKitC-76 ESP8685-DevKitC-77 ESP8685-DevKitC-78 ESP8685-DevKitC-79 ESP8685-DevKitC-80 ESP8685-DevKitC-81 ESP8685-DevKitC-82 ESP8685-DevKitC-83 ESP8685-DevKitC-84 ESP8685-DevKitC-85 ESP8685-DevKitC-86 ESP8685-DevKitC-87 ESP8685-DevKitC-88 ESP8685-DevKitC-89 ESP8685-DevKitC-90 ESP8685-DevKitC-91 ESP8685-DevKitC-92 ESP8685-DevKitC-93 ESP8685-DevKitC-94 ESP8685-DevKitC-95 ESP8685-DevKitC-96 ESP8685-DevKitC-97 ESP8685-DevKitC-98 ESP8685-DevKitC-99 ESP8685-DevKitC-100

ESP32-C2 Series

Hardware Guideline

32-bit RISC-V MCU & 2.4 GHz Wi-Fi & Bluetooth 5 (LE)

- 32-bit RISC-V single-core processor that operates at up to 120 MHz
- State-of-the-art power and RF performance
- 576 KB ROM, 272 KB SRAM (16 KB for cache) on the chip
- 14 programmable GPIOs: SPI, UART, I2C, LED PWM controller, General DMA controller (GDMA), SAR ADC, Temperature sensor

SoC	Buy	Variants	Core	Dimensions (mm)	Pins	RAM/ROM (KB)	Flash (MB)	PSRAM (MB)	Module	Development
 ESP8684		ESP8684H2 ESP8684H4	Single Core	QFN 4×4	24	272 KB SRAM, 576 KB ROM	2, 4	N/A	ESP8684-MINI-1 ESP8684-MINI-1U ESP8684-WROOM-01C ESP8684-WROOM-02C ESP8684-WROOM-02UC ESP8684-WROOM-03 ESP8684-WROOM-04C ESP8684-WROOM-05 ESP8684-WROOM-06C ESP8684-WROOM-07	ESP8684-DevKitC-1 ESP8684-DevKitC-2 ESP8684-DevKitC-3 ESP8684-DevKitC-4 ESP8684-DevKitC-5 ESP8684-DevKitC-6 ESP8684-DevKitC-7 ESP8684-DevKitC-8 ESP8684-DevKitC-9 ESP8684-DevKitC-10 ESP8684-DevKitC-11 ESP8684-DevKitC-12 ESP8684-DevKitC-13 ESP8684-DevKitC-14 ESP8684-DevKitC-15 ESP8684-DevKitC-16 ESP8684-DevKitC-17 ESP8684-DevKitC-18 ESP8684-DevKitC-19 ESP8684-DevKitC-20 ESP8684-DevKitC-21 ESP8684-DevKitC-22 ESP8684-DevKitC-23 ESP8684-DevKitC-24 ESP8684-DevKitC-25 ESP8684-DevKitC-26 ESP8684-DevKitC-27 ESP8684-DevKitC-28 ESP8684-DevKitC-29 ESP8684-DevKitC-30 ESP8684-DevKitC-31 ESP8684-DevKitC-32 ESP8684-DevKitC-33 ESP8684-DevKitC-34 ESP8684-DevKitC-35 ESP8684-DevKitC-36 ESP8684-DevKitC-37 ESP8684-DevKitC-38 ESP8684-DevKitC-39 ESP8684-DevKitC-40 ESP8684-DevKitC-41 ESP8684-DevKitC-42 ESP8684-DevKitC-43 ESP8684-DevKitC-44 ESP8684-DevKitC-45 ESP8684-DevKitC-46 ESP8684-DevKitC-47 ESP8684-DevKitC-48 ESP8684-DevKitC-49 ESP8684-DevKitC-50 ESP8684-DevKitC-51 ESP8684-DevKitC-52 ESP8684-DevKitC-53 ESP8684-DevKitC-54 ESP8684-DevKitC-55 ESP8684-DevKitC-56 ESP8684-DevKitC-57 ESP8684-DevKitC-58 ESP8684-DevKitC-59 ESP8684-DevKitC-60 ESP8684-DevKitC-61 ESP8684-DevKitC-62 ESP8684-DevKitC-63 ESP8684-DevKitC-64 ESP8684-DevKitC-65 ESP8684-DevKitC-66 ESP8684-DevKitC-67 ESP8684-DevKitC-68 ESP8684-DevKitC-69 ESP8684-DevKitC-70 ESP8684-DevKitC-71 ESP8684-DevKitC-72 ESP8684-DevKitC-73 ESP8684-DevKitC-74 ESP8684-DevKitC-75 ESP8684-DevKitC-76 ESP8684-DevKitC-77 ESP8684-DevKitC-78 ESP8684-DevKitC-79 ESP8684-DevKitC-80 ESP8684-DevKitC-81 ESP8684-DevKitC-82 ESP8684-DevKitC-83 ESP8684-DevKitC-84 ESP8684-DevKitC-85 ESP8684-DevKitC-86 ESP8684-DevKitC-87 ESP8684-DevKitC-88 ESP8684-DevKitC-89 ESP8684-DevKitC-90 ESP8684-DevKitC-91 ESP8684-DevKitC-92 ESP8684-DevKitC-93 ESP8684-DevKitC-94 ESP8684-DevKitC-95 ESP8684-DevKitC-96 ESP8684-DevKitC-97 ESP8684-DevKitC-98 ESP8684-DevKitC-99 ESP8684-DevKitC-100



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ESP8266 Series

ESP32-H2 Series

32-bit RISC-V MCU & Bluetooth 5 (LE) & IEEE 802.15.4

- 32-bit RISC-V single-core processor that operates at up to 96 MHz
- 320 KB SRAM, 128 KB ROM, 4 KB LP Memory, and works with external flash
- 19 programmable GPIOs, with support for UART, SPI, I2C, I2S, Remote Control Peripheral, LED PWM, Full-speed USB Serial/JTAG Controller, GDMA, MCPWM
- Can be used for building Thread end devices, as well as Thread border router and Matter bridge by combining it and ESP Wi-Fi SoC

SoC	Buy	Variants	Core	Dimensions (mm)	Pins	RAM/ROM (KB)	Flash (MB)	PSRAM (MB)	Module	Development Board
 ESP32-H2		ESP32-H2FH2 ESP32-H2FH4	Single Core	QFN 4×4	32	320 KB SRAM, 128 KB ROM, 4 KB LP Memory	2 4	N/A	ESP32-H2-MINI-1 ESP32-H2-MINI-1U ESP32-H2-WROOM-02C	ESP32-H2



ESP32 Series

ESP32 SoCs

Hardware Guideline

32-bit MCU & 2.4 GHz Wi-Fi & Bluetooth/Bluetooth LE

- Two or one CPU core(s) with adjustable clock frequency, ranging from 80 MHz to 240 MHz
- +19.5 dBm output power ensures a good physical range
- Classic Bluetooth for legacy connections, also supporting L2CAP, SDP, GAP, SMP, AVDTP, AVCTP, A2DP (SNK) and AVRCP (CT)
- Support for Bluetooth Low Energy (Bluetooth LE) profiles including L2CAP, GAP, GATT, SMP, and GATT-based profiles like BluFi, SPP-like, etc
- Bluetooth Low Energy (Bluetooth LE) connects to smart phones, broadcasting low-energy beacons for easy detection
- Sleep current is less than 5 μ A, making it suitable for battery-powered and wearable-electronics applications
- Peripherals include capacitive touch sensors, Hall sensor, SD card interface, Ethernet, high-speed SPI, UART, I2S and I2C

SoC	Buy	Core	Dimensions (mm)	Pins	RAM/ROM (KB)	Flash (MB)	PSRAM (MB)	Module	Development Board	For
 ESP32-D0WD-V3		Dual Core	QFN 5×5	48	520 KB SRAM 448 KB ROM 16 KB RTC SRAM	N/A	N/A	ESP32-WROOM-32E ESP32-WROOM-32UE ESP32-WROOM-DA ESP32-WROVER-E ESP32-WROVER-IE ESP32-DU1906 ESP32-DU1906-U	ESP32-DevKitC ESP-WROVER-KIT ESP32-LyraT ESP32-LyraT-Mini ESP32-LyraTD-MSC ESP32-LyraTD-SYNA ESP32-Vaquita-DSPG ESP32-Korvo ESP32-Ethernet-Kit ESP32-Korvo-DU1906	For



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ESP32 Series

ESP8266 Series



ESP32-U4WDH


 Dual Core
(PCN-2021-021)
(PCN-2021-021)

QFN 5×5

48

 520 KB SRAM
448 KB ROM
16 KB RTC
SRAM

4

N/A

[ESP32-MINI-1](#)
[ESP32-MINI-1U](#)
[ESP32-DevKitM-1](#)

ESP32-PICO-V3



Dual core

LGA 7×7

48

 448 KB ROM
520 KB SRAM
16 KB RTC
SRAM

4

N/A

[ESP32-PICO-V3-ZERO](#)[ESP32-PICO-V3-ZERO-DevKit](#)

ESP32-PICO-V3-02



Dual Core

LGA 7×7

48

 448 KB ROM
520 KB SRAM
16 KB RTC
SRAM

8

2

[ESP32-PICO-MINI-02](#)
[ESP32-PICO-MINI-02U](#)
[ESP32-PICO-DevKitM-2](#)

ESP32-PICO-D4



Dual Core

LGA 7×7

48

 448 KB ROM
520 KB SRAM
16 KB RTC
SRAM

4

N/A

N/A

[ESP32-PICO-KIT](#)

ESP32-D0WD

 Not recommended for
new designs →
[ESP32-D0WD-V3](#) /
[ESP32-U4WDH](#)


Dual core

QFN 5×5

48

 520 KB SRAM
448 KB ROM
16 KB RTC
SRAM

N/A

N/A

[ESP32-WROOM-32D](#)
[ESP32-WROOM-32U](#)
[ESP32-WROOM-32SE](#)
[ESP32-WROVER-B](#)
[ESP32-WROVER-IB](#)
[ESP32-DevKitC](#)
[ESP-WROVER-KIT](#)
[ESP32-LyraTD-DSPG](#)


ESP32-D0WDQ6-V3

 Not recommended for
new designs →
[ESP32-D0WD-V3](#) /
[ESP32-U4WDH](#)


Dual Core

QFN 6×6

48

 520 KB SRAM
448 KB ROM
16 KB RTC
SRAM

N/A

N/A

N/A

N/A



ESP32-D0WDQ6

 Not recommended for
new designs →
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[ESP32-U4WDH](#)


Dual core

QFN 6×6

48

 520 KB SRAM
448 KB ROM
16 KB RTC
SRAM

N/A

N/A

[ESP32-WROOM-32](#)[ESP32-DevKitC](#)

ESP32-S0WD

 Not recommended for
new designs →
[ESP32-D0WD-V3](#) /
[ESP32-U4WDH](#)


Single core

QFN 5×5

48

 520 KB SRAM
448 KB ROM
16 KB RTC
SRAM

N/A

N/A

[ESP32-SOLO-1](#)[ESP32-DevKitC](#)


ESP8266 Series


ESP8266 SoCs


Hardware Guideline




- High-performance 160 MHz single-core CPU
- +19.5 dBm output power ensures a good physical range
- Sleep current is less than 20 μ A, making it suitable for battery-powered and wearable-electronics applications
- Peripherals include UART, GPIO, I2C, I2S, SDIO, PWM, ADC and SPI

SoC	Buy	Variants	Core	Dimensions (mm)	Pins	RAM/ROM (KB)	Flash (MB)	PSRAM (MB)	Module	Development
 ESP8266EX Recommended upgrade \rightarrow ESP8684		N/A	Single core	QFN 5 \times 5	32	160 KB RAM	N/A	N/A	ESP-WROOM-02D ESP-WROOM-02U ESP-WROOM-02 ESP-WROOM-S2	ESP8266 ESP-L01
 ESP8285 Recommended upgrade \rightarrow ESP8684		ESP8285N08 ESP8285H16	Single core	QFN 5 \times 5	32	160 KB RAM	1 2	N/A	N/A	N/A


Longevity Commitment


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