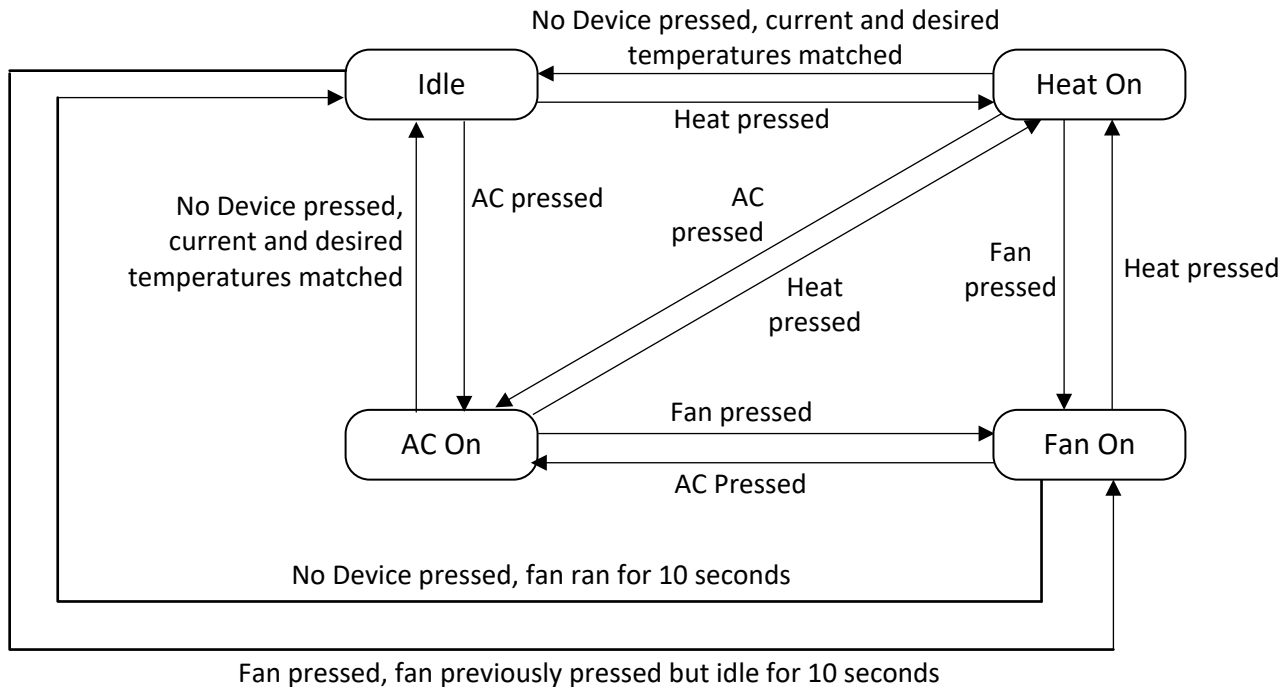


Finite State Diagram



Sequence Diagram for Processing of a Clock Tick with the Heater Selected

