CAN

BlueBoard Clock & Power Config

- SystemInit()

    - CSRCSEL -- to select clock source (usually XTAL)
    - CCLKCFG -- to divide PLL clock (CCOCLK) to get CPU clock.
    - PLL0CFG -- to config PLL -- M & N value

        - CCOCLK = OCLK * 2M / N

    - PCLKSRC -- to divide CCLK to get Peripheral clock

        - PCLK = CCLK / x

            - x can be 1, 2, 4 or 8.
            - 4 is default (00).

    - PCONP -- to enable power for each Peripheral

Brown-Out Detector

- If Vcc voltage drops below a threshold level, BOD reset the CPU. This avoids malfunctioning due to less voltage.
- After reset, programmer should check whether reset is due reset cct or BOD and take appropriate actions.