REVANTH A V M K

Final year Undergraduate

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Career Objective

I intend to establish myself as a VLSI enthusiast looking forward to an internship / job in the field of VLSI, contributing to the company's growth and thereby ensuring personal growth within the organization.

Education

Bachelor of Engineering in EEE

MVGR College of Engineering	7.70*/10 CGPA	2019-2023

Intermediate : MPC

Sri Chaitanya Junior college	9.3/10 CGPA	2017-2019

Class 10th

Ravindra School	9.3/10 CGPA	2016-2017

Skills

- *Technical :* Digital Electronics, RTL Design.
- *Languages :* Verilog, System Verilog.
- *Developer Tools :* Xilinix Vivado, Keil uVision, Proteus 8 pro, MATLAB Simulink.
- *Soft Skills :* Communication, Leadership, Time management.

Certifications

- VLSI design basics - IIT GUWAHATI
- Digital electronics -udemy
- Basics of powershell - udemy
- Verilog HDL -udemy
- Salesforce Developer Catalyst - Salesforce trailhead

Projects

IMPLEMENTATION OF LOADABLE MOD COUNTER (MOD 13) | Verilog

- Implemented a UP-DOWN counter with load setting by which we can load data into counter.
- After loading data counter starts counting from data which is been loaded.

UART and FSM designs | Verilog

- Implemented a UART module to print a single character and also implemented mealy and moore machines.
- Implemented binary up-down counter with a reset pin.

IMPLEMENTATION OF SINGLE AND DUAL PORT SRAM | Verilog

- Learnt about RAM and implemented single port SRAM and dual port SRAM using verilog.

Remote health monitoring system | IOT

- Implemented a smart health monitoring system which measures the BPM, SPO2 levels and body temperature of the user.
- ESP8266 is used to transfer the data and we can get data in laptop, smart phone, or any device which is capable of browsing and having an internet connectivity.

About

- *Languages known :* English, Telugu, Hindi.
- *Interests :* Coding, travel, local meetups.
- *Hobbies :* playing cricket, listening to music, web browsing, playing table tennis.