

Address Aurangabad, Bihar, India

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Date of Birth 30.06.1996

PROFILEGATE QUALIFIED 2020, SCORE - 438 (ECE)

SOFT SKILLS

- ✓ Good communication written and oral skills
- ✓ Excellent conceptual and analytical skills
- ✓ Effective interpersonal skills

PERSONALITY

Communicative

Punctuality

Creativity

Organized

LANGUAGES

English $\bigstar \bigstar \bigstar \bigstar \bigstar \bigstar$ Hindi $\bigstar \bigstar \bigstar \bigstar \bigstar \bigstar \bigstar$

TECHNICAL SKILLS

Programming Languages: Verilog, Basic SV. Core skills: Digital Circuit, CMOS, FPGA, STA.

Protocols: SPI,I2C,PCIe.

Tools: Xilinx ISE/Vivado, Cadence Virtuoso, Blender, Visual

Studio.

Operating Systems: Windows, Linux.

Rishi Raj

Linkedin

http://linkedin.com/in/rishi-raj-ab6b2312a/

EDUCATION

2020 - 2022

Punjab Engineering College Chandigarh

Qualification: Master of Technology

Specialization: VLSI Design

Aggregate: 89.3%

2014 - 2018

Pune University

Qualification: Bachelor of Engineering

Specialization: Electronics and Telecommunication

Engineering Aggregate: 63.1%

2013

Bihar School Examination Board

Qualification: H.S.C Aggregate: 72.6%

2011

Bihar School Examination Board

Qualification: S.S.C Aggregate: 75.4%

WORK EXPERIENCE

01.2022 - 01.2023

L&T Technology Services, Mysuru

Position: Engineer

- Worked on FPGA Design & Verification.
- Simulation and Implementation on Xilinx Vivado.
- Hardware implementation on Zynq 7000 SoC.
- Language used Verilog,SV and Verification methodology like UVM.
- SoCarchitecture and peripherals of PCIe.Familiarity with logic simulation and debug.
- Experience in Low power SoC design Synthesis / Understanding of Timing concepts(STA).



INDUSTRIAL EXPOSURE

- Industrial visit to Nashik Thermal power plant.
- Industrial visit to Giant meter wave telescope, Narayan Gaon Pune.
- Johnson controls India engineering commerzone IT Park Pune.

EXTRACURRICULAR & POSITIONS OF RESPONSIBILITY

- Zion (2016), Event Coordinator- SWITCH Event (Cultural Fest) DYPIET, PUNE.
- Participated in 'PARAKRAM (annual fest.) CRICKET TOURNAMENT' in F.E, S.E, T.E, B.E.
- AKHIL BHARTIYA SANSKRITI GYAN PARIKSHA-2007, 2008, 2009 (achieving 83% marks).

ACHIEVEMENT

- BE project selected in International conference on emerging trends in science, engineering and technology 2018(Volume 118 No. 24 url: http://www.acadpubl.eu/hub/).
- COURSERA certification course in Introduction to FPGA Design for Embedded Systems.
- NPT EL certification course in 'INT ERNET OF THINGS'
 & 'NUMERICAL METHOD FOR ENGINEERS'.
- TECHNICAL COMMUNICATION FOR SCIENTIST & ENGINNERS, IIT BOMBAY certification course.
- BLENDER & 3D ARCHITECTURAL VISUALISATION, IIT
 BOMBAY Certification course.
- NATIONAL CHILDREN'S SCIENCE CONGRESS 2010 (DIST. LEVEL) Organized by NCSTS:- WINNER.
- BIHAR STATE CHILDREN'S SCIENCE CONGRESS-10(STATE LEVEL) Organized by NCSTS:- RUNNER UP.

HOBBY



Football



Cricket



Badminton



Tennis



Table Tennis

ACADEMIC PROJECT

- M.Tech Project:
- "LINEAR REGULATOR DESIGN IN POWER MANAGEMENT BLOCK" This project objective is oriented to design which enhance the performance of linear voltage regulator for portable high frequency battery operated electronic devices. The design target toachieve an input regulating voltage range of 2.8 to 5.5V irrespective of output capacitance value for a load current of 75mA with a tolerance of 3%.
- "BOOTH'S ALGORITHM USING FPGA" The main objective is to implement the Booth algorithm on Xilinx software to analyse the time performance. Implementation of Booth's algorithm on the CPU and FPGA both simultaneously to compare the implementation time.
- B.E Project:
- "DESIGN AND DEVELOPEMENT OF ELECRICAL STIMULATION UNIT FOR PAIN CONTROL" In this project we have Designed a circuit (5v dc supply, buzzer circuit, freq. generator, driver circuit, o/p transformer) which generates electrical current then the obtained electrical energy is given to the patient in the form of soft impulses through electrodes which is placed on the skin of the patient to avoid persistent and sensitive pain.