

MATHIREDDY VINAY

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PROFILE

I aspire to have my career where I will experience new challenges at every point of work and would convert all my abilities, knowledge and interest into a meaningful contribution to the company.

EDUCATION

Bachelor of Technology, Electronics and Communication Engineering (ECE), Rajiv Gandhi 2019 – 2023
University Of Knowledge and Technologies IIIT SRIKAKULAM
CGPA :9.15

Pre University Course (Maths, Physics, Chemistry ,IT), 2017 – 2019
Rajiv Gandhi University Of Knowledge and Technologies IIIT SRIKAKULAM.
8.9

Secondary Education(SSC), Z.P.P HIGH SCHOOL, KOTANANDURU, KAKINADA DIST. 2016 – 2017
CGPA :9.8

SKILLS

- UVM
- Verilog
- Python
- SystemVerilog
- DigitalElectronics

TOOLS

- ModelSim
- Quartus Prime.
- NI Multisim
- Matlab

PROJECTS

Verification of D Flip-Flop using SystemVerilog
Development of SystemVerilog code required for D Flip-Flop..

Reverse Vending machine using Verilog HDL
Developed the RTL code for DUT and also developed the testbench for the verification of Reverse Vending machine

Synchronous FIFO using Verilog HDL
The synchronous FIFO is designed using Dual Port RAM and developed Verilog code and required testbench

Digital clock using Verilog HDL
Development of Verilog code and testbench required for digital clock and simulated in Modelsim.

Line follower Robot using Arduino
The line follower robot senses a black line by using a sensor and then sends the signal to Arduino. Then Arduino drives the motor according to sensor's output.

COURSES

SystemVerilog for Verification Part1,Udemy

VLSI Design Trainee at SURE Trust

Python for Everybody ,University of Michigan,Coursera

SOFT SKILLS

- Problem Solving.
- Adaptability
- Time Management
- Self Motivated

LANGUAGES

- English
- Telugu

DECLARATION

I hereby declare that all the above-mentioned information is true and correct to the best of my knowledge.

Vinay

MATHREDDY VINAY
Nuzvid, 08-02-2023