

## Pravallika Reddy

pravallikareddy521@gmail.com, 8970820521

Anantapur-515001, Karnataka

### Career Objective

To obtain a career in Industry as a Physical Design Engineer, where I can contribute my skills for organization's success and improving my technical ability while being resourceful, innovative and flexible.

### Core Competancy

- Profeciency in Logical Design
- Scripting skills with Unix, Perl, TCL
- Chip floor planning, dealing with IR power constraints
- Automatic placement, clock tree synthesis, and routing
- Hands-on experience on ICC2 compiler from Synopsys
- Clear understanding of ASIC design flow

### Education Details

<b>Advanced Diploma in ASIC Design</b>	<b>2023</b>
RV-VLSI Design Center	
<b>Bachelor Degree in Electronics and Communication</b>	<b>2021</b>
R L Jalappa Institue of Technology, with 7.86 CGPA	
	<b>2017</b>
Narayana Jr. College, with 90 %	
<b>SSLC</b>	<b>2015</b>
Viswa Bharathi English medium High School, with 98 %	

## Projects worked on

### IBM

Client: PMI

APPLICATION DEVELOPER-IBM CLOUD FULLSTACK

Jan-2022 to Dec-2023

### Openpages

#### Description

I have worked on the specific requirement where users need to be assigned roles, groups and profile which are raised in other tool to link with this product and reflect in their user profile

#### Tools

Sql, python

#### Challenges

- setting up the environment and learning the languages was responsibility in starting
- worked as Team member and first tried to set the data source in the backend.
- Then helped with some part of code to assign the roles groups and profile accordingly.

---

## Domain Specific Project

### RV-VLSI and Embedded Systems Design Center

Graduate Trainee Engineer

Oct-2022 to Jan-2023

### Lakshya

#### Description

Designing Quality of chip by reducing power, area and increasing performance. Optimized output without any violations.

#### Tools

ICC2 shell

#### Challenges

- Reducing the IR drop after many iteration
- Reducing congestion in the design
- minimizing DRC errors

## **B.E / B.Tech Academic Project**

R L Jalappa Institute of Technology

### **Design and implementation of smart movable road divider using IoT**

#### **Description**

Road dividers will automatically move to the other side of the road where the density of the vehicles is less which is detected by the sensors used and which is connected to the controller and sends the signal to the motor to move accordingly.

#### **Tools**

Hardware : PIC16F877A-I/P, LCD, Power supply, Sensors, Relay drivers, DC motor Software : Embedded C, MP Lab IDE, PICKIT2

#### **Challenges**

- Choosing the microcontroller which completes our requirements and in budget was bit challenging.