Nabirasool D

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Career Objective

Intend to build a career with committed and dedicated people, which will help me to realize my potential, and my skill set in VLSI Physical design field and to help the organization grow.

Technical Skills

- Good knowledge of entire physical design process from floorplan to GDS generation.

- Exposure on few tools like Icc2, Prime time, LEC

- Over all knowledge (icc2 compiler) on Floorplanning, Placement, Timing Optimization, CTS, Routing, Timing Analysis.

- Basic knowledge on multi voltage domains.

- Ability to rapidly learn new concepts

Academic Qualification

Courses	Institution	University	Year of Completion
Mtech/VLSI System Design	St.Johns College of Engineering and technology, Yemmiganur	Jawaharlal Nehru Technological University, Ananthapuram	2019-22
B.E/ECE	St.Johns College of Engineering and technology, Yemmiganur	Jawaharlal Nehru Technological University, Ananthapuram	2015-19
Intermediate	Govt.Jr. College, Aspari	Intermediate Education Examination Board	2013-15
SSLC	ZPH School, Joharapuram	Andhrapradesh secondary board	2013

Academic Project Details:

➢ **M.Tech (2022)**

St.Johns college of engineering and technology, Yemmiganur – 518360

Academic Project: Image Enhancement On FPGA using Verilog HDL
Tool: Xilinx ISE 14.3, MATLAB 2021a

INTERNSHIP: **Physical design implementation of 28nm**

BLOCK LEVEL- ORCA_TOP
- Technology node – 28 nm
- Instance count – 52k
- No of clocks – 5
- No of macros – 40
- No of layers – 9
- Frequency – 440 MHz

  Tool – ICC2

Strengths
- Analytical and Self-learner.
- Flexibility and Friendly nature.
- Punctual and Goal Oriented

Trainings and Certifications

- Successfully completed the online Industrial training on VLSI PHYSICAL DESIGN from VLSI GURU INSTITUTE Bangalore Using Synopsys ICC2

Declaration:

I hereby confirm that the above-mentioned information is true to the best of my knowledge.

Place: Bangalore

Signature

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