

## Yaswantha Someswara Rao Raja

rysomeswararao@gmail.com, 9642273385

Narasaraopeta-522601, Andra Pradesh

### Career Objective

Intend to build a career with committed and dedicated people, which will help me to realize my potential, enhance my skill set in the field of High speed VLSI Physical design and help the organization grow.

### Core Competancy

- Good Understanding of the ASIC design Flow.
- Good knowledge in VLSI Physical Design APR flow.
- Hands on experience in Synopsys, STA Prime Time Shell.
- Hands on experience in 40nm technology using Synopsys ICC2 tool for Block Level implementation.
- Comprehensive knowledge on Physical Flow stages like Floor Planning, IR Drop analysis, Place and Route, CTS, Timing Analysis, DRC/LVS.
- Hands on experience in EDA tool, Perl and Unix/Linux environment.
- Familier with Industry standard Fusion Compiler methodologies from Synopsys.
- Independent thinker, creativity, strong problem solving skills and analytical ability, thinking out of the box.

### Education Details

<b>Advanced Diploma in ASIC Design</b>	<b>2023</b>
RV-VLSI Design Center	
<b>Bachelor Degree in Electrical and Electronics</b>	<b>2020</b>
Gudlavalleru Engineering College, with 8.54 CGPA	
	<b>2016</b>
Bhavana Junior College, with 84 %	
<b>SSLC</b>	<b>2014</b>
Guntur Oxford High School, with 90 %	

## Domain Specific Project

### RV-VLSI Design Center

Graduate Trainee Engineer

Oct-2022 to Jan-2023

### Block Level implementation of a Full Chip

#### Description

Block Level implementation of a Full Chip which incorporates 34 Macros, 38887 standard cells with a voltage of 1.1V working at a frequency of 833MHz. It used a total of 6 Clocks and 7 Metal Layers of 40nm technology.

#### Tools

ICC2\_Shell, PT\_Shell.

#### Challenges

- Creating a floorplan without congestion, IR, timing, noise and Routing issues.
- Validating the timing performance of a design by checking all possible paths based on setup and hold.
- Place standard cells available in synthesized netlist with optimization by considering routability.
- Analyzing and fixing DRC and LVS issues.

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## B.E / B.Tech Academic Project

Gudlavalleru Engineering College

### SMART AGRICULTURAL DRONE SPRAYER

#### Description

Project describes the development of Quad-Copter UAV and the spraying mechanism. In this project we also discuss integration of sprayer module to quad copter system. The discussed system involves designing a prototype which uses simple cost effective.

#### Tools

1. Fly Sky FS-CT6B 6-Channel 2.4 Ghz Transmitter and Receiver. 2. Flight Control Board. 3. KK 2.1 Multirotor Control Board. 4. Brushless 40A ESC.

#### Challenges

- 1. Lifting two liters of Pesticide Tank with A2212-2200kv BLDC motors. 2. Binding Flysky 6-channel transmitter and Receiver over 200m from ground.