

# B. MANORANJAN PATRA

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## CONTACT

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## SUMMARY

A passionate electronics engineer with the basic knowledge of system verilog and electronics, keen to upskill in the electronics domain and carve a career in the same.

## PERSONAL EXPERIENCE

### ELECTRONICS ENGINEER

- ✓ Knowledge on testbench flow, from generator, driver to Dut and back from Dut to monitor . Basics of system verilog.
- ✓ Digital electronics. combinational circuits and sequential circuits .
- ✓ OOPS concepts polymorphism, abstraction, inheritance, encapsulation.

## SKILLS

### Electronics

- Digital electronics
- System verilog
- Test bench flow
- UVM
- OOPS concept

## EDUCATION

### B-TECH-(ELECTRONICS & TELECOM.)

8.46 cgpa [Igit sarang]

2017-2021

### HIGH SCHOOL(XI+XII)

84.8%(xii) [MP&EV english medium school (CBSE)]

2015 - 2017

### SCHOOL

9.8 cgpa(x) [Kendriya vidyalaya khurda road]

2005 - 2015

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## PROJECT(B-TECH)

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- DESIGN AND ANALYSIS OF HYBRID DATA SECURITY ALGORITHM WITH MESSAGE INTEGRITY TEST

This project aims to create a virtual model for a new cryptography algorithm which is implemented using hamming code technique with message integrity test. to ensure end to end encryption of a 256 bit data. for encryption, technologies used are- 256 bit message digest generation unit, modified DES encryption unit , hamming code.

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## EXTRA CURRICULAR

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- National level khokho
- Participant of The Bharat Scouts and Guides
- Participant of Science and Mathematics olympiads

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## LANGUAGES KNOWN

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- English
- Hindi
- Telugu
- Odia



I HEREBY DECLARE THAT THE ABOVE INFORMATION PROVIDED IS TRUE TO THE BEST OF MY KNOWLEDGE.

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