Pritam Soni

To work with maximum potential, with an opportunity of working with a diverse group of people and enhancing my professional skills with learning and experience for my career.

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☎ 09163732852

📍 Kolkata, India

in linkedin.com/in/pritamsoni04

○ github.com/skynatepro

SKILLS

Verilog   Python

Physical Design

Digital Electronics

System Verilog

Object-Oriented Programming (OOP)

Linux   TCL

LANGUAGES

English
Full Professional Proficiency

Hindi
Native or Bilingual Proficiency

Bengali
Native or Bilingual Proficiency

EDUCATION

Masters of Technology
Department of Physics, The University of Burdwan

07/2020 - 08/2022   GPA: 8.17
Electronics and Communication Engineering (Microwaves)

▫ Design and Implementation of Circularly Polarized Patch Antenna at NAVIC L5 Band

Bachelors of Technology
RCC Institute of Information Technology, MAKAUT

07/2016 - 07/2020   GPA: 8.00
Electronics and Communication Engineering

▫ Multiple Detection Using NODE MCU in WSN Configuration

PROFESSIONAL TRAININGS

Internship in RTL Design and Verification
Maven Silicon

05/2022 - 07/2022

Microelectronics Technology and VLSI Design
Jadavpur University

06/2018 - 08/2018

Tasks

▫ Designed a PN Junction Diode using Si wafer and tested it

AVR Microcontrollers
GSSG Tech Solutions LLP (Kolkata)

01/2018 - 02/2018

Tasks

▫ Worked on Atmega 16/32 Microcontrollers
▫ Worked on Arduino Microcontrollers

CERTIFICATES

VLSI Physical Design (02/2022 - 04/2022)
NPTEL

Hardware Modelling using Verilog (06/2018 - 08/2018)
NPTEL

Design and Analysis of VLSI Subsystems (02/2022 - 04/2022)
NPTEL

PROJECTS

Smart Stick for Blind Person Embedded with Features of Obstacle detection and Navigation Technology (01/2018 - 04/2020)

▫ Got Second Prize in the competition

PUBLICATION

A Study on Adders used in VLSI
Published in PRAYAS 2019 Second Edition ISBN: 978-93-5346-951-1