

# Sardhar Hussain Shaik

## Physical Design Engineer

A fresher in the field of engineering, seeking a full-time position in my domain, where I can apply my knowledge and skills for continuous improvement with learning new concepts, ideas and develop innovative solutions to problems.

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## PROFESSIONAL TRAINING

### Physical Design Engineer Trainee VLSIGURU Training Institute

08/2022 - 01/2023

Bangalore

#### Courses

- Hands on expertise in Block Level Physical Design Flow on 28nm Technology.
- Implemented block level multi voltage Floor plan Design meeting the power and timing requirements.
- Capable of resolving placement, CTS & Routing stage Timing, constraints and congestion issues.
- Clock Tree Synthesis implementation and optimization.
- Handled detail Routing including LVS,DRC and Antenna violation fixes.
- Generated Multiple TCL Scripts to solve the issues.
- Working knowledge on Static Timing Analysis (STA), Timing Closure Methodologies, Parasitic Extraction and Synthesis.
- Hands on experience in working with ICC2, Prime Time, StarRC /Xtract.
- Good understanding skills with Digital Electronics, CMOS, Linux and TCL scripting and basic knowledge on python.

## B.TECH

### Ellenki College of Engineering and Technology

06/2018 - 02/2022

6.6

#### Courses

- Electronics and Communications Engineering

## Diploma

### Government Institute of Electronics

06/2013 - 12/2016

70%

#### Courses

- Electronics and Communications Engineering

## WORK EXPERIENCE

### PCB Design Intern

#### Resolute Electronics Pvt Ltd

06/2016 - 12/2016

Hyderabad

#### Achievements/Tasks

- Designed the schematic layout of various types of PCBs on EAGLE software. Achieved 39% efficiency using creative design methodologies.
- Post the Design process, tested the PCB for various checks using Multimeter.

## SKILLS

Digital Electronics

GVIM

LINUX

CMOS

TCL

ICC2

PRIME TIME

STARRC

## PERSONAL PROJECTS

Portable roadside sensors for vehicle counting, classification and speed measure with IOT.  
(01/2021 - 06/2021)

Hand gesture controlled wheel chair with device control for physically challenged. (06/2020 - 12/2020)

## CERTIFICATES

Physical Design Flow course from Udemy  
(01/2023 - 01/2023)

[https://drive.google.com/file/d/1JcvK-hKBd-\\_SGHU3XVS55KZp49ipWdUt/view?usp=share\\_link](https://drive.google.com/file/d/1JcvK-hKBd-_SGHU3XVS55KZp49ipWdUt/view?usp=share_link)

Python Programming (05/2022 - 05/2022)

[https://drive.google.com/file/d/1JcvK-hKBd-\\_SGHU3XVS55KZp49ipWdUt/view?usp=share\\_link](https://drive.google.com/file/d/1JcvK-hKBd-_SGHU3XVS55KZp49ipWdUt/view?usp=share_link)

Participated in VLSI Hackathon conducted by IIT Bombay and designed a CMOS NOR gate with ESIM Tool.

PCB designing from CDAC (07/2015 - 08/2015)

## LANGUAGES

English

Professional Working Proficiency

Hindi

Full Professional Proficiency

Telugu

Full Professional Proficiency

Urdu

Native or Bilingual Proficiency

## INTERESTS

Cricket

Movies