

# NIDHI KANWAT



MTech (Pursuing) - VLSI Engineering(ECE)  
National Institutes of Technology, Jaipur (MNIT)  
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## EDUCATION QUALIFICATION

Qualification	University/Board	Institute	Passing Year	CGPA / %
P.G.(M.Tech)	MNIT	MNIT	2023	8.14
Graduation	IIIT Kota	IIIT Kota	2020	7.04
Intermediate	CBSE	T.P.S, Jhunjhunu	2016	88.40%
Matriculation	CBSE	D.P.S, Jhunjhunu	2014	8.6

## WORK EXPERIENCE

**Physical Design Intern | Company: INTEL**

**Aug 2022 - June 2023**

**Project: Optimizing Delay for different metal layers**

**Description:** Created netlist files(.sp) of different driving strength of cells at different length i.e. 500um & 500pm for higher metal layers and finding knee point of delay vs number cells using the Dynamic time analysis tool.

**Project: Fixing timing paths in RF and FUB**

**Description:** Working on two live projects of Intel, for fixing timings, routing & overlapping in register files(memory of 2.3 KB), and Functional Unit Blocks and achieved milestones in it. Worked to optimize placements and routing in FUB.

**Summer Training | Company: NORTH WESTERN RAILWAY, JAIPUR**

Domain: Signaling System Automatic Electronic Exchange and Data Logger

**May 2019 - July 2019**

## KEY PROJECTS

**Project: Design and comparison of different technology in CMOS Inverter**

**Description:** Design and comparison of CMOS Inverter between 180 nm and 90 nm, first simulate the circuit by using LTSpice tool then calculate the different parameter (ON current, OFF current, delay, power dissipation, power, noise margin) then compare the performances.

**Project: Vending Machine**

**Description:** Vending Machine using the FSM concept, based on user requirement and the amount of money given by the user, the machine will dispense either water bottle without returning charges and will accordingly maintain the count of this product.

**Project: Plant Monitoring Design system**

**Description:** Monitoring the health of plants using Arduino Uno using different sensors like Temp and Humidity Sensor (DHT 11), Soil Moisture sensor(YL-38+YL-39), and Ultrasonic sensor.

## TECHNICAL SKILLS

- **Subjects:** Digital Electronics, Digital Integrated Circuits, Static Time analysis Semiconductor devices.
- **Programming Languages:** Verilog, Basic knowledge of C, C++, Unix commands.
- **Software:** INTEL specific Design flow tool, Static Time analysis tool and Dynamic time analysis tool, Cadence Virtuoso, VTCAD, HDL Bits, Dev C++, Silvaco.

## SCHOLASTIC ACHIEVEMENTS

- Got 1-year Internship offer in Hardware profile by **INTEL**, **PHILIPS**, and **STMicroelectronics**.
- Placed in **PIE INFOCOMM** in B.Tech campus placement.
- Participated in the Zine Workshop of **MNIT** in September 2016.
- Participated in **Robotics** Workshop in **IIT Bombay** 2019
- Volunteer at **Isha foundation**.

## EXTRA CURRICULAR-ACTIVITIES

- Runner up in “**vine-o-mania**” competition in **Flair Fiesta’18**.
- Team project of Light follower using **Arduino**.
- Secured 1<sup>st</sup> position in 100m race and 200m in Sports meet & was **Athletics Captain** at **IIIT Kota**.
- Participated in Inter-Sports Meet in **MNIT**, **IIIT-KACHIPURAM**, **IIIT-JABALPUR**, and **BITS-PILANI**
- Conducted various webinars during the pandemic through the **Isha foundation** platform.
- Secured 3<sup>rd</sup> position in 100m and 200m race, long jump, and relay race in Inter-Sports meet in **IIIT Allahabad**.

## INTERESTS/HOBBIES

- Sports
- Yoga
- Travelling
- Social Activities & Leading the team