

**KiranaKumar**

**+91 8884369053**

**DFT Engineer**

**kiranakumarpatil@gmail.com**

**Career Objective:**

To obtain employment with a company that offers a positive atmosphere to learn and implement new skills and technologies for the betterment of the organization.

**Professional Experience:**

Total experience : 2.2 year

Previous Company : Eximius Design India (Pvt) Ltd from Dec 2019 to June 2021

Present Company : Capgemini from July 2022 to till Date

**Qualification** : BE in Electronics and communication [2014-2018]

**Core Expertise:**

- 2.2 year of experience in DFT.
- MBIST DRC, MBIST Insertion and Validation.
- Experience in block level pattern generation for Regular , Repair , AFD.
- Good understanding on MBIST.
- Good Knowledge on JTAG , Scan , compression , ATPG, simulation.

**Projects Handled:**

**Project 1 : PVC**

Tools Used : Mentor graphics- Tessent , Synopsys -Vcs

Technology : 10nm.

**Client : INTEL** [DEC 2019 – JUN 2021]

**Responsibilities:**

- Ownership of two sub chip's , Handled Mbist insertion and validation in Block level.
- Checked for setup issues & DRC violations and debugging the failures
- Checked for Design issues and debug memory failures
- Pattern generation for Regular, Repair, AFD.
- Done Mbist validation on SOC and Block Level .
- Done ITPP (Intel Test Pattern Processor) Conversion and Validation.

**Project 2 : RS-3**

Tools Used : Cadence Genus, Cadence Modus Test, NCSIM.

Technology : 16nm.

**Client : MICROCHIP – ODC** [AUG 2022 – FEB 2023]

Responsibilities:

- Ownership of two subchip : Handling Stuck-at and TFT DRC debug, TPI insertion experiments to meet the required coverage. Handling the unit and SDF simulation. Debugging the simulation miscompares.
- Ownership of one top level Partition: DRC debug, ATPG and top-level simulations with unit . Simulation miscompare debug.

**Personal Skills:**

- Quick learner.
- Multi-Tasking.
- Possess Leadership qualities, creativity and organizing skills.
- Effective Team Player.
- Flexible to environment.

**Personal Profile:**

Date of Birth - 27/7/1996

Nationality - Indian

Languages Known - Kannada, English, Hindi.

Permanent Address A/P : Kulgod , Tq : Mudalagi , Dist : Belagavi , KARNATAKA Pin : 591310.

**Declaration:**

I hereby declare that the above-mentioned information is correct to the best of my knowledge  
and I bear the responsibility for the correctness of the above-mentioned particulars.

Kiranakumar. A. Patil