Rajat Singh

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Career Objective

Trained in Physical Design implementation of Integrated IC's. Seeking for the role in growth-oriented Company where I can contribute my idea and upgrade my skills as well as knowledge.

Core Competancy

- Knowledge of TCL Scripting and Linux
- Good knowledge of Static Timing analysis
- Basic Hand-on Experience on tools: Synopsys ICC2
- Knowledge of APR flow involving Synthesis, Floorplan, Placement, Clock tree synthesis, Routing.
- Good knowledge of Logic Design

Education Details

Advanced Diploma in ASIC Design - Physical Design	2022
RV-VLSI Design Center	
Bachelor Degree in Electronics and Communication	2022
Shantilal Shah Government Engineering College, with 8.37 CGPA	
	2017
Lions English School, with 55 %	
SSLC	2015
Lions English School, with 72.2 %	

Domain Specific Project

RV-VLSI Design Center, Bangalore

Graduate Trainee Engineer

Sep-2022 to Jan-2023

Lakshya

Description

We are working on 40nm technology at block level. In which we are having instance count around 50k, macro count is 34, clock frequency is 1GHz.

Tools

Synopsys ICC2

Challenges

- Did multiple iteration in floorplan to avoid congestion, blocked port, DRC violation, min channel spacing between the macros.
- Solved IR drop Issue by adjusting meal Width, spacing, offset, pitch.
- Solved congestion my doing multiple iteration of placement and applied technique to control congestion.

B.E / B.Tech Academic Project

Shantilal Shah Government Engineering College

smart Vehicle for avoiding accident and automatic speed control.

Description

This project will help in avoiding accident by carelessness of user. It will help in avoiding accident in dark and harsh weather.

Tools

Arduino, blink sensor, ultrasonic sensor, Alarm sensor, smoke sensor

Challenges

• first of all an ultrasonic sensor will continuously radiate whenever an object come under region. The ultrasonic wave reflect back to the sensor. Then microcontroller will take data and send to DC motor, after receiving signal, motor slow down speed.