# CHAITRA R

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## Summary:

- Expertise in Physical design concepts with entry-level exposure in using EDA tools, flows & methodologies.
- Experience in Place & Route, Timing closure, DRC/LVS of partitions/blocks in 28nm technology node.
- Good knowledge of VLSI Digital Design and CMOS technology Concepts.
- Expertise in Static Timing Analysis (STA) concepts.
- Netlist to GDSII Implementation

  Synthesis, Floor-planning, power planning, Place & Route, Clock tree synthesis, Routing and Optimization using Cadence Innovus.

  Signoff - Timing, DRC

## Academic Credentials:

**MTech in VLSI and Embedded Systems 2022**
Dr Ambedkar Institute of Technology, Bengaluru with **9.38 CGPA**.

**Bachelor of Engineering in Electrical & Electronics Engineering 2018**
Don Bosco Institute of Technology, Bengaluru with 65%.

## Training / Internship:

- Successfully completed 6-month **Physical Design** course in **VLSIGURU Training Institute**.
- Development of Embedded System for collision detection using image processing for UAV applications under the guidance of **National Aerospace Laboratories**.

## Publication Details:

- Chaitra R, Dr. Siva Subha Rao Patange and Siddesha K, Development of Embedded System for Collision Detection using Image Processing for an UAV applications on ARM Cortex-A53 Processor, IJREAT International Journal of Research in Engineering & Advanced Technology, Volume 10, Issue 4, Aug-September 2022

## Professional Skills:

- **Languages**: Verilog, C, SDC commands, Linux commands, basics of TCL.
- **Tools**: Synopsys: IC Compiler II, Xilinx ISE Design Suite
- **Operating System**: Windows, Linux.

## Projects:

### 1. 32 Bit RISC Processor [ORCA TOP]

Objective: Hands on expertise in Block Level Physical Design Flow on **28nm** Technology.

Implemented block level multi voltage Floorplan Design meeting the power and timing requirements.
Capable of resolving placement, CTS & Routing stage. Timing, constraints and congestion issues.
Clock Tree Synthesis implementation and optimization. Handled detail Routing including LVS, DRC and Antenna violation fixes.

2. **Development of Embedded System for collision detection using image processing for UAV applications. (MTech Final Year Project)**

   Objective: The project been developed by using the Yolov5 algorithm to detect the collisions in images, videos and in real time. Identifying the name of the collision detected with its class probabilities. Prediction of the Bounding boxes. Performance is evaluated for the accuracy for the trained dataset.

3. **Next Generation automatic energy meter.**

   Objective: The project been developed by using ATMEGA-328 MC. The Automated Energy Meter which has capabilities like remote monitoring and controlling of energy meter. Automatic Meter Reading system (AMR) continuously monitors the energy meter and sends data on request of service provider through SMS. It saves huge human labor. In order to ensure the SMS facility the GSM technology been incorporated.

**Professional Experience**

- 24/7.ai (Digital Interaction Advisor) July 2018- July 2020

**Interpersonal Skills:**

- Good communication and presentation skills.
- Ability to rapidly build relationships and trust.
- Confident and determined.
- Ability to cope up with different situations.
- Good decision-making and better time management.

**Declaration:**

I hereby declare that the above mentioned information is correct up to my knowledge and bear the responsibility for the correctness of all the mentioned particulars.

**Place: Bengaluru, India**

**Regards**,

**Chaitra R**