# **CURRICULUM VITAE**

#### **BODIGADLA KARUNA KUMARI**

**Gender:** Female **DOB:** 25-11-2000

Email: karunakumari0672@gmail.com

Phone: +91 8978972723

LinkedIn: https://www.linkedin.com/in/karuna-kumari-bodigadla-97170218a/



# **Objective**

A highly and hard-working individual looking for a responsible position to gain practical experience and to make use of my interpersonal skills in achieving a result which can add value to the organization.

# **Education Details**

Degree/Examination	Institute/Board/University	Year	CGPA
B-Tech (ECE)	Rajiv Gandhi University of Knowledge Technologies, Nuzvid	2018-2022	7.5
PUC	Rajiv Gandhi University of Knowledge Technologies, Nuzvid	2016-2018	7.5
SSC	Vivekananda Zilla Parishad High School	2016	9.8

# **Work Experience**

# **Design Verification Trainee**

Tech Mahindra Cerium Systems Pvt Ltd (Visakhapatnam)

**Duration:** June 2022 – March 2023

Skills learned: Unix, Verilog, System Verilog, basics of UVM

**Projects:** FIFO project and APB Protocol.

As a Design Verification Trainee, I have good knowledge and hands on experience in Verilog, UNIX,
System Verilog and basics of Universal Verification Methodology (UVM).

#### **Projects**

# 1 - GCD (Greatest Common Divisor)

**Duration:** 2 Weeks **Team Size:** 2

Role Played: Team Member

Skills Used: Euclid's GCD Algorithm Method, FSM Topic in Digital Electronics & Verilog

# 2 - Traffic light controller

**Duration:** 2 Weeks

Team Size: 3

Role Played: Team Member

Skills Used: FSM Topic in Digital Electronics & Verilog

# 3 - Digital clock

**Duration:** 2 Weeks **Team Size:** 4

Role Played: Team Member

Skills Used: Digital Electronics & Verilog

#### 4 - Tic-Tac-Toe game using FSM in VERILOG

**Duration:** 2 Months

Team Size: 5

Role Played: Logic implementation and coding

Skills Used: FSM Topic in Digital Electronics & Verilog

# 5 - Design of Vending machine using Verilog

**Duration:** 4 months

Team Size: 5

Role Played: coding

Skills Used: FSM Topic in Digital Electronics & Verilog

#### **Technical Skills**

Programming Languages: Verilog, System Verilog, Python, MATLAB, UVM(Basic), UNIX.

#### **Area of Interests**

O Digital VLSI Design,

O Design Verification,

O RTL Design.

# **Participation & Presentations in Technical Events and Training programs**

- Participated in technical fests and one of the Volunteers in SDCAC
- I have completed WEB CHIP 2 weeks online internship in digital electronics domain and certified.

#### **Personal Information**

Father's Name: B. Ratnam Raju

Mother's Name: B. Shara

Nationality: Indian

Languages: Telugu, English

**Hobbies:** singing and reading novels

Address: 8-134, Vasanth colony, Kothagudem

Nallajerla Mandal, West Godavari District,

Andhrapradesh-534112

# **Declaration**

I, **BODIGADLA KARUNA KUMARI** hereby declare that the above-mentioned information is corrected up to my knowledge and belief, I bear the responsibility for the correctness of the above-mentioned particulars.

Place: Vizag. BODIGADLA KARUNA KUMARI

Date: 24 Feb, 2023. Name & Signature