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JP Nagar , Bengaluru , Karnataka

Pratima Agnihotri

PHYSICAL DESIGN ENGINEER

EDUCATION

VLSI Physical Design

VLSI Guru Training Institute Apr-Oct 2022

Gained knowledge in VLSI Physical Design and practical experience with Synopsys design tools.

Bachelors in Engineering

Electronics and communication (ECE)

K.S Institute of Technology 2017 - 2021

Aggregate/CGPA : 8.7

Pre-University

JKV Composite PU College 2015-2017

Studies PCMC with **FCD (87%)**

SKILLS

EDA Tools and Softwares:

- Synopsys IC compiler II
- Primetime
- StarRC

Hardware Description Language

- HDL

Scripting

- Shell
- TCL

Programming Languages

- Python
- C, C++

LANGUAGES

- English
- Kannada
- Hindi

PROFILE

Seeking to start a career in the VLSI industry as a physical design engineer by joining an esteemed company, eager to work with experienced professionals in an exciting atmosphere. Working as an Analyst in cloud and DevOps domain for 1.6 years, looking to transition my career to the VLSI industry.

EXPERIENCE

VLSI Guru Training Institute

Physical Design Trainee

Apr-Oct 2022

- Extensive Hands-on Implementation of Physical design flow from **netlist to GDS**
- Developed multi voltage block design using industry leading tools such as **Synopsys ICC2**, primetime and StarRC.
- Demonstrated deep understanding and knowledge of all aspects of backend ASIC design including **floor planning, power-planning, timing closure, signal integrity analysis, and physical verification**.
- Proficient in analyzing **timing reports** and fixing **Setup and Hold violations, LVS, Global congestion**, routing issues and improvised the design.
- Delivering Methodology and design flow for Place and route, **STA, power grid analysis and physical verification** clean design with **LVS, DRC and Antenna fixes**
- Acquired theoretical knowledge on **Cadence Innovus** design flow.
- Strong understanding and knowledge of **Digital Electronics design, CMOS design**, and IC fabrication process.
- Hands-on experience with physical verification like LVS, DRC and Antenna using **primetime and starRC**, with knowledge of sanity checks and signoff
- Working knowledge on **Clock Tree** setup/debug, synthesis and Optimization(**CTS**)
- Acquired **TCL** scripting skills that requires in automation process of the VLSI industry
- Having sound knowledge in **Python scripting** experience from Cloud and DevOps domain

Analyst In Cloud and DevOps

KPMG Global Services

Aug 2021 - Present

- As an Analyst responsible for infrastructure and development in Azure and AWS Cloud