Contact

Phone

7975862050

Email

poojahulikatti@gmail.com

Linked in

https://www.linkedin.com/in/pooja-hulikatti-5829961a3

Expertise

Digital Electronics

CMOS circuits

Linux/Unix, GVIM

Basic TCL scripting

Floorplanning

STA (Static time analysis)

Basic C programming

Good communication

EDA Tools

SYNOPSYS IC COMPILER™ II

SYNOPSYS PRIMETIME®

Pooja Hulikatti

Physical Design Trainee

A highly enthusiastic Electronics And Communication engineer looking forward to utilize my technical skills and enhance my skill set in the field of high speed VLSI Physical design to the fullest and learning more from the industry for self-improvement, while contributing to growth of organization with my technical and logical skills.

Experience

Nov 2022 - Present

PHYSICAL DESIGN -VLSIGURU Training Institute

Trainee

- Worked on process technology 28nm/14nm, Multivoltage design, block level designing.
- Tools used -Synopsys IC Compiler II, Prime Time.
- Initialized floorplan for core area with the utilization of 0.8um.
- Port placement and performed main checks, as design is multivoltage design created voltage area and guard band using TCL scripting.
- Macros are placed using guidelines and based on fly line analysis.
- Power planning is done using higher metal layers.
- All the pg_drc and pg_connectivity violations are fixed manually and analyzing the spacing between the macros.
- CTS High priority given to fix Hold violations, target latency and target skew .
- Physical verification (DRC/LVS) cleaning DRC 's using caliber.
- Performed all sanity checks needed in the design.
- Proficient in Linux scripting and begineer on TcL scripting.

June 2021 - Dec 2022

DXC Technology Pvt. Itd Bangalore

Netapp Storage Administrator

• Client SAP with Vendor NetApp 1.7 years of hands-on experience working on ONTAP OS level troubleshooting, configuration & administration.

Education

Jain College Of Engineering Belgaum, Karnataka

Bachelor of Engineering

2021

8.7 CGPA

Kendriya Vidyalaya PU college Belgaum, Karnataka

Pre-University in Science

2017

81%

Kendriya Vidyalaya No.2 school Belgaum, Karnataka

SSLC 2015

9.0 CGPA

Language

English

Hindi

Kannada

Marathi

Hobbies

Reading

Cooking

Travelling

Singing

Dancing

Project

Underground cable Fault Detection

 Underground cable Fault Detection: This was designed to determine the distance of underground cable fault from base station in km, which allows to service underground cable line faster.

Obstacle detection Robot

• With the help of Arduino programming and sensors the robot was able to detect the obstacles on the way simultaneously sending the messages to phone giving detailing of distance and obstacle detected.

Home Automation

• The main objective of this project is to develop a home automation using an Arduino board with Bluetooth being remotely controlled by smartphone.

Participation

- Robotics workshop using Arduino
- PCB(printed circuit boards) workshop
- Machine learning workshop in BITS
- Hackathon competition
- IEEE Chairperson of Jain College of Engineering Belagavi
- Leader of NSS Social club in Jain college of Engineering Belagavi, attended national level camp in VTU
- LEAD secretary