Kaustubh Shukla

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LI: https://www.linkedin.com/in/kaustubh-

shukla-designandverification/

CAREER OBJECTIVE

To achieve best possible results by making all possible effort with my skills, While keeping honesty and punctuality at highest priority. I believe that learning is a continuous process. My career should be a means to make my life purposeful. I want to make an indelible and distinct place for myself at my workplace and thereby be a valuable resource for my organization.

WORK EXPERIENCE

- Certified Training: Advance VLSI Design and Verification course
 Maven Silicon VLSI Design and Training center, Bangalore
 May 2022 September 2022
- Graduate Apprentice Trainee in Communication Navigation and Surveillance Department(CNS) at Airports Authority of India, Dehradun March 2021 – March 2022

EDUCATION

Degree	Institute/University	Year of Passing	Score
Bachelor of			
Technology			
	Uttarakhand Technical	2018	64.20%
Specialization:	University, Dehradun		
Electronics and			
Telecommunication			
Engineering			
XII	Adarsh Bal Niketan Sr Sec Sch IIT Roorkee UK	2013	67.60%
Х	Adarsh Bal Niketan Sr Sec Sch IIT Roorkee UK	2011	74.10%

TECHNICAL SKILLS

VLSI DOMAIN SKILLS

HDL: Verilog

HVL: SystemVerilog

Verification Methodologies: Constraint Random Coverage Driven Verification

Assertion Based Verification - SVA

TB Methodology: UVM

EDA Tool: Mentor Graphics – Questasim and Xilinx

Domain: ASIC/FPGA front-end Design and Verification

Operating System: Linux

Scripting languages: Perl Scripting

Core Skills: RTL Coding using Synthesizable constructs of Verilog, FSM

Based design, Simulation, CMOS Fundamentals, Code

Coverage, Functional Coverage, Synthesis,

Static Timing Analysis, Assertion Based Verification using

SystemVerilog Assertions.

PROJECTS

• Embedded Inauguration E- Lamp

PERSONAL INFORMATION

• **D.O.B** : 20/11/1994

Father's Name : Dr Shalik Gram Shukla
 Mother's Name : Mrs. Varmati Shukla

• Languages Know : English/Hindi

• Current address : 664 Chaw Mandi Roorkee Distt. Haridwar,

Uttarakhand – 247667

• **Permanent address** : Sahdeiya Baidand, Tehsil Ghorawal, Thana Karma,

Distt. Sonebhadra, U.P-231210

DECLARATION

I hereby declare that the above written particulars are true and correct to the best of my knowledge and belief.

Kaustubh Shukla