



FURQAN ATTAR

OBJECTIVE

To get 100% efficiency in the work allotted for me and to implement my skills in the real world problems. I am seeking a role which makes my skills perfect. My first priority brings the growth of the company



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Dharwad, Karnataka (INDIA)

EDUCATION

Basel Mission School	Dharwad	2006-2016	87.3 %
Prism PU Science College	Dharwad	2016-2018	91 %
SDM College of Engineering	Dharwad	2018-2022	9.3 CGPA

SKILLS

- Digital Electronics Design
- Verilog
- SystemVerilog
Functional Coverage
Assertion
- GVIM Editor

PROTOCOLS

- APB
- SPI

PROJECTS

- Clock Generation using Jitter
for any Frequency and Timestep
- Assertion
Time Period for any Duty Cycle
and Timestep
- Asynchronous Counter
Synchronous counter
- Finite State Machine
- Memory
- FIFO and LIFO using
Parameritized Class
- Adder Verification

LANGUAGE

- English
- Hindi
- Kannada