Karthik V S

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Erode-638052, Tamilnadu

Career Objective

A self-motivated and committed individual seeking a position to aquire and develop a more-rounded skillset and contribute to the organisation's growth

Core Competancy

- Strong understanding of the principles of semiconductor physics and device operation, as well as the ability to apply this knowledge to the design and layout of ICs.
- Proficiency in floorplan and powerplanning stages of the APR flow.
- Understanding of timing and power analysis and the ability to use analysis tools to identify and solve timing and power-related issues.
- Ability to analyze and fix congestion issues during the placement stage.
- Familiarity with tools such as PrimeTime and ICC2.
- Experience with CTS and routing and the ability to fix issues that arise during the routing stage.
- Familiarity with scripting languages such as Perl and TCL.

Education Details

Advanced Diploma in ASIC Design **2023**

RV-VLSI Design Center

Bachelor Degree in **Electronics and Communication** **2022**

vellalar College of Engineering and Technology, with 8.55 CGPA

2018

aet matriculation higher secondary school, with 89.9 %

SSLC **2016**

AET matriculation higher secondary school, with 94.6 %

Domain Specific Project

RV SKILLS: Coentre for Emerging Technologies

Graduate Trainee Engineer Nov-2022 to Jan-2023

Lakshya

Description

Block-Level Implementation of Design from Gate Level Netlist to GDSII including different stages of APR flow

Tools

Synopsis ICC2

Challenges

- Difficulty in meeting IR drop requirement
- Difficulty with congestion, DRC errors.
- Difficulty in meeting timing requirements(set-up and hold).
- Difficulty in fixing antenna violations

B.E / B.Tech Academic Project

vellalar College of Engineering and Technology

Automatic tunnel crack inspection

Description

To develop an image analysing technique using deep learning to detect cracks in the complex internal enviorment of a tunnel.

Tools

Software: MATLAB 2017A Operating System: Windows 10 SL1 Hardware requirments: Processor: Intel core i5-8265u RAM: 8 GB Disk space: 8 GB

Challenges

- Lack of crack data samples. Collecting enough high-quality tunnel lining crack images with rich background and lighting conditions for model training is a challenging task. Poor balance between model size and accuracy.