GUDIBANDI HIMA SAILAJA

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CARRIER OBJECTIVE:

To work in a dynamic environment enables me to utilize my knowledge and learn new things to progress professionally and personally.

ACADEMIC QUALIFICATION:

Bachelor of Technology in Electronics and Communication Engineering 2014 - 2018 Koneru Lakshmaiah Education Foundation, Vaddeswaram, Andhra Pradesh CGPA: 8.46

Intermediate2012 - 2014Sri Chaitanya Junior kalasala.Guntur

Percentage: 89.5

S. S. C. 2011 - 2012

Z.P. High school

CGPA:9.7

PROFESSIONAL TRAINING:

Institute Name: Sumedha IT Institute of Technology - Hyderabad

Duration: Six Months.

Role: Physical Design Engineer.

Tools Used in Training: DC_Shell and ICC2_Shell.

SUMMARY:

- Hands-on experience with EDA tools like ICC2.
- Formulated 28nm technologies.
- Experience in debugging skills like DRC, setup and hold violations.

TECHNICAL SKILLS:

Scripting: Tcsh, Tcl.

Hardware design tools: Design Compiler, IC Compiler II.

• VLSI skills: Scripting, STA, PNR.

PROJECTS:

PROJECT 1:

Project	RP_TOP
Technology	28nm
Tools	Synopsys ICC2
Frequency	400 MHz
Macros	6
Gate count	67k
No. of Clocks	1 master clock

RESPONSIBILITIES:

- Successfully implemented block design from netlist to GDSII.
- Involved in Floor planning, Power Plan, Placement, CTS, Routing, and Static timing analysis.
- Congestion analysis, Timing closure, DRC fixes.

PROJECT 2:

Project	ALU
Technology	28nm
Tools	Design Compiler
Frequency	200 MHz
Gate count	500
No. of Clocks	3 master clocks

RESPONSIBILITIES:

- Successfully implemented synthesis in DC_Shell.
- Design constraints like Input delay, output delay, uncertainty for design under analysis (DUA).
- · Performed sanity checks and Optimization.
- Congestion analysis, Timing closure, DRC fixes.

STRENGTHS:

- Self-motivated.
- Quick learning.
- Time management.

CERTIFICATES:

PEGA CSA certified.

ACHIEVEMENTS:

School topper in the SSC broad examination in 10th standard

PUBLICATIONS:

Title: "Circularly polarized microstrip array antenna for C-Band applications."

Journal: Journal of Advanced Research in Dynamical and Control Systems.

 A rectangular microstrip array antenna with 8 elements with right-hand circular polarization has been designed for space communications. Truncated corners on the patch are made to obtain circular polarization. The gain and efficiency of the antenna are improved when compared with a 16-element HTS antenna.

EXTRACURRICULAR ACTIVITIES:

- A member of the organizing committee of FEMFLARE 2017 conducted in KLEF.
- Actively participated in debates and essay writing conducted in school level and won prizes.
- Member of the social servicing wing of the university while pursuing a bachelor degree.

PERSONAL DETAILS:

- Father's name: G. Rajani Kanth Reddy
- Mother's name: G. Rajya Lakshmi
- Date of birth: 09 08 1997
- Languages are known: Telugu, English, Hindi

DECLARATION:

I hereby declare that the above particulars are accurate to the best of my knowledge.

Place: Hyderabad (G. HIMA SAILAJA)