


ABHIRAM GAVIDI

Final year Undergraduate

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Career Objective

I intend to establish myself as a VLSI professional looking forward to an internship role in the field of VLSI, contributing to the company's growth and thereby ensuring personal growth within the organization.

Education

Bachelor of Engineering in EEE

MVGR College of Engineering	8.19*/10 CGPA	2019-2023
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Intermediate : MPC

Narayana Junior College	9.67/10 CGPA	2017-2019
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Class 10th

Kendriya Vidyalaya	9.8/10 CGPA	2016-2017
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Projects

UART protocol and FSM (Sequence Detector) | Final year project

- Gained an understanding of UART protocol and its use in serial communication. Designed and implemented simple UART modules in Verilog for communication between two digital devices. Familiar with basic UART parameters such as baud rate and data bits.
- Conducted basic simulations to verify the functionality of the UART modules.
- Implemented a Mealy machine which detects a particular sequence and makes output high. When the sequence is not detected it gives an zero output.

Single-Port and Dual-Port SRAM | Verilog

- Familiar with SRAM memory and its use in digital electronics and computer systems. Basic understanding of the difference between SRAM and DRAM. Knowledge of SRAM's volatile nature and fast access time. Experienced in using SRAM as a cache memory in computer systems and in microcontrollers.

Indoor air quality monitoring | IOT

- Implemented a smart air quality monitoring system which measures the temperature, PPM in air.
- ESP8266 is used to transfer the data and we can get data in laptop, smart phone, or any device which is capable of browsing and having an internet connectivity.

Skills

Technical : Good knowledge on Synthesis and Functional verification, Digital Electronics, RTL design.

Languages : Python, Verilog, system verilog.

Developer Tools : MATLAB Simulink, Keil uVision, Proteus 8 pro, xilinx vivado.

Soft Skills : Time management, Communication, Teamwork, Adaptability.

Certifications

- Basics of verilog.
- Basics of VLSI design.
- Salesforce Developer Catalyst - Salesforce trailhead.

About

Languages known : English, Telugu, Hindi.

Interests : Designing, Travel, Basketball.

Hobbies : Playing basketball, content writing, photography.