

## VENKATESH K

**Career intention:** I want to utilize my training skills and knowledge to secure responsible career. I want to be part of success of company. I want to be a leader of the project and lead my team with my passion.

**Professional Training:** An Industry Oriented Trainee in **VLSI PHYSICAL DESIGN** from **VLSI FOR ALL Pvt. Ltd**, Hyderabad, Telangana.

### **Academic Qualifications:**

Qualification	Institution	Year Of Passing	Percentage (CGPA)
<b>Bachelor Of Technology</b> (Electronics and Communication Engineering)	TKR COLLEGE OF ENGINEERING AND TECHNOLOGY, HYDERABAD (T.S)	2018-2022	7.21
<b>Intermediate</b>	Shraddha Junior college, Mahbubnagar	2016-2018	93%
<b>Class 10<sup>th</sup></b>	Eileen memorial school	2016	8.8

### **Project summary:**

#### **ORCATOP:**

Project	ORCATOP
Description	Block level implementation of ORCATOP Design with 40 macros with a frequency of 433 MHz
Challenges	<ul style="list-style-type: none"><li>• Placement of Macros such that they can communicate with each other, I/O ports and with Standard Cells that will be placed later with minimum routing resources.</li><li>• Designing the power plan such that every pre-route row gets PG connections. Creating Routing blockages for pre-route rails at places where power cannot be supplied, to avoid floating shape errors.</li></ul>
Nodes/technologies	32nm
Inst count & macros	54k & 40
tools	ICC2

**Academic project:**

Project	Detection Of Non-Helmet Rider & Extraction of License Using YOLO V2 & OCR
Description	<ul style="list-style-type: none"><li>• The main purpose of this project is to extract the license plate using OCR</li><li>• By using YOLO V2 we will check person with bike.</li><li>• By using YOLO V3 we will check person wearing helmet.</li></ul>

**Skill set:**

- Hands on exposure on Physical Design.
- Good knowledge on Synthesis, Floorplan, Placement, CTS and Routing.
- Theoretical knowledge of Digital Fundamentals
- Good knowledge on STA

**Tools Experience:**

**PNR:** ICC2, Fusion compiler

**Synthesis:** DC, Fusion compiler

**STA:** Open compiler, prime compiler

**Scripting Languages:** Basic of TCL

**Personal details:**

Father name	Kollur Lingappa
Mother name	K Ananthamma
Address	H-no:1-00, Karni(village), Makthal(mdl), Narayanpet(dist.), Telangana-509208
Languages Known	English, Telugu, Kannada
Personal mail id:	kv9427229@gmail.com

**Declaration:**

I hereby declare that the contents of my resume are accurate to the best of my knowledge and verify their authenticity.

**Date:**

**Place: Hyderabad**

**Name**

**K Venkatesh  
6303210627**

