Anamta Warsi

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Objective

Looking for a challenging role in Digital field to utilize my knowledge and imagination for the growth of the Organization as well as to enhance my skills in both theoretical and practical aspects.

Education			
Course / Degree	School / University	Grade / Score	Year
B.Tech	IET , M.J.P. Rohilkhand University , Bareilly	8.23	2023
High school	St. Francis convent Sr. Sec. School, Bareilly	81.6%	2016
Intermediate	St. Francis convent Sr. Sec. School, Bareilly	67.8%	2018

Training

VLSI design

Done internship in the field of VLSI design from IIIT Allahabad.

Duration: 1 month (June 2022-july 2022)

Skills

Knowledge of verilog

Aim spice

Microprocessors and microcontrollers

Hands on cadence

Projects

Two stage Operational amplifier by aim spice

Designed two stage Operational amplifier using aim spice In summer training at IIIT allahabad.

Two stage Operational amplifier using cadence

Designed two stage Operational amplifier by cadence as minor project of University.

Band gap reference (BGR) Circuit

Designed a band gap reference (BGR) circuit using LT Spice as major project of University.

Achievements

Personal : won competitions and designated as secretary of departmental club (BITZ)

Certificate of learning 8085 microprocessor from Bharat Acharya Academy .

Certificate of learning 8051 microcontroller from Bharat Acharya Academy .

Hobbies

Listening music

Reading

Social work

Personal Strengths

Versatility

Honest and patience

Always ready to learn new things

Disciplined

Time management and decision making

Personal Details

Date of birth: 28 Dec 1999

Languages known : English , Hindi

Permanent address : Road no. 1 Rahpura Chaudhary Izzat Nagar Bareilly, Uttar Pradesh 243122