THARAMANI VINAY Design and Verification

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- ## August 25th, 2001
- a male



CARRER OBJECTIVES

A highly motivated, hardworking individual seeking an opportunity to learn and build upon skills that are essential within the industry

EDUCATION

Higher Secondary Examination

EDUCATION	COLLEGE NAME	CGPA/PERCENTAGE	YEAR OF PASSING
BTECH (EEE)	PALLAVI ENGINEERING COLLEGE	7.4 CGPA	2022
DIPLOMA(EEE)	AURORA POLYTECH ACADEMY	80%	2019
SSC	GLOBAL CONCEPT SCHOOL	8.8 CGPA	2016

PROFESSIONAL EXPERIENCE

Internship: VLSI FIRST

- 07/2022 12/2022 | Hyderabad , India.
- Learned advanced topics of Digital Electronics .
- Learned designing of various circuits using Verilog HDL .
- · Gained an understanding of ASIC design flow.
- · Learned verification using System Verilog.
- Universal Verification Methodology .
- I got a very good Knowledge of VLSI RTL Design and some techniques about Constraint Random Coverages Driven Verification.

Industrial Internship: ECIT VHDL/VERILOG (Industrial Training)

- 11/2018 05/2019 | hyderabad, ECIT, India I
- got a good knowledge of Verilog in Xilinx.

TECHNICAL SKILLS

DOMAIN - ASIC Design Flow , Digital Design

HDL HVL Verilog. System Verilog.

VERIFICATION - UVM.

METHODOLOGY

TOOL
Questasim, EDA playground.

EDITOR - Notepad ++.
OPERATING SYSTEM - Windows ,Linux.

PROTOCOL
APB ,AXI, AHB, I2C , SPI , UART .

PROJECTS

APB (Advanced Peripheral Bus)

- 08/2022 11/2022
- Designed in Verilog and verified using System Verilog UVM.
- FSM based design having three states using Verilog, implemented in verification by using system verilog and UVM.

objective:

 The APB protocol is a low-cost, low-power protocol that is commonly used for connecting peripherals such as timers, interrupts, and registers to microprocessors. It is a simple, synchronous protocol with a two-phase handshake between the master (the microprocessor) and the slave (the peripheral device).

Contribution:

- Understanding the Specification of APB protocol. Creating
- · feature extraction.
- · Developing Testcase, Debugging the issues.

AXI(Advanced eXtensible Interface)

- 12/2022
- · Verification by using UVM.

objective:

• To provide high-frequency operation and high-bandwidth and low-latency designs without using complex bridges. It's Provide burst transactions. It has separate address/control and data phases. Like read and write data channels, that can provide low-cost Direct Memory Access (DMA).

Contribution:

- · Understanding the Specification. Creating
- · feature extraction.
- · Debugging the issues.

I2C (Inter Integrated Circuit) Protocol

• The Inter-Integrated Circuit (I2C) protocol is a two-wire synchronous serial communication protocol used to communicate between multiple integrated circuits in embedded systems. I2C uses two signal lines a serial data line (SDA) and a serial clock line (SCL). The project was made using Verilog.

SPI (Serial Peripheral Bus) Protocol

 The Serial Peripheral Interface (SPI) protocol is a synchronous serial communication protocol used to transfer data between microcontrollers and peripheral devices such as sensors, display controllers, and memory devices and used for short distance communication, primarily in embedded systems. The project was made using Verilog.

UART (Universal Asynchronous Receiver Transmitter) Protocol

• The UART protocol operates in a point-to-point topology, meaning that it supports communication between only two devices. The transmitter sends data on the TX line, and the receiver reads the data from the RX line. The baud rate, or the number of bits per second is set by both devices and must be the same for successful communication.

CORE SKILLS

- Digital Electronics
- FSM based design
- STA and CDC
- · Constraints & Randomization Coverages
- · Assertion based verification (SVA)

CERTIFICATES

- Got certified as RTL Design and Verification Engineer from VLSI FIRST.
- Certification in 6 months industrial training on Verilog/VHDL in ECIT with A+ grade.

LANGUAGES

- English
- Telugu
- Hindi

DECLARATION

I hereby declare that all the information contained in this resume is in accordance with facts or truths to my knowledge. I take full authority for the correctness of the written information.