Kalluri Bhavana

Bhavanakalluri22@gmail.com | +91-6304440765

OBJECTIVE

I am seeking a role in the Design field in a company where I can leverage my experience, learn new skills, expand my knowledge and grow with the company.

PROFILE

- Hands On of Verilog HDL
- Knowledge of STA
- Knowledge of Basic Linux commands
- Knowledge of C & Python Programming

WORK EXPERIENCE

ILENSYS TECHNOLOGIES

July22 - Dec22

Hardware Engineer Intern

July22 – Dec22

- circuit simulation using LTspice and PCB designing using KiCad.

ACADEMIC

•	Vignan Institute of Technology and Science, Hyderabad B.Tech in Electronics and Communication Engineering Percentage – 76.66%	2022
•	Narayana Junior College, Hyderabad (SSC Board) Class 12th Percentage – 96%	2018
•	Narayana Concept School, Hyderabad (SSC Board) Class 10th SSC = 9.7/10.0	2016

PROFESSIONAL TRAINING

Advanced Design and Verification (Maven Silicon VLSI Training Institute):
 Modules Covered: Advanced Digital Design, Basics of STA, Linux Fundamentals, Verilog HDL, Advanced Verilog & Code Coverage.

TECHNICAL SKILLS

- HDL: Verilog
- **Programming:** C, C++, Python
- Tools Used: Modelsim, Mobaxterm, questasim, LTspice, Arduino IDE.
- OS: Linux

1. Router 1X3 Project – RTL Design and Verification

HDL: Verilog

EDA Tools: Quartus prime, Modelsim, mobaxterm.

Project Description:

The router accepts data packets on a single 8-bit port and routes them to one of the three outputs channels, channel0, channel1 and channel2.

2. Quality Evaluation of Rice Grains using Morphological Methods

Project Description:

This system mainly highlights the use of image processing and computer vision in the field of agriculture and food industry. The image of the grains is captured using a camera module and it is processed or compared with the trained data and the output that is quality of grains is displayed on the vnc viewer and played on the speaker.

3. IOT Based Door Access Control System using ESP32CAM

Project Description:

The home security system has become vital for every house. Previously, most doors can be open by using traditional ways, such as keys, security cards, password or pattern. However, incidents such as a key loss has led to much worrying cases such as robbery and identity fraud. This has become a significant issue. In this project we have presented a smart Wi-Fi Door Lock using the ESP32 CAM and Telegram app. In this simple working model, when a person is detected by a PIR motion sensor an ESP32CAM captures the face then telegram sends the notifications on phone, so that the owner gives unlock/lock commands in the telegram. It automatically responds to that command.

PERSONAL DETAILS

• LinkedIn Profile: https://www.linkedin.com/in/kalluri-bhavana22/

• **Date Of Birth:** 22/01/2001

• Permanent Address: House No. 9-156, Sai Maruthi Nagar Colony, Boduppal, Hyderabad, Telangana (500092)

• Father's Name: K.S.V.Ramana

• Father's Occupation: Private Employee

Mother's Name: K.Mangala

• Mother's Occupation: Housewife

• Documents: Aadhar/PAN available on request

• Nationality: Indian

DECLARATION