


REHMAN KHAN

ELECTRONICS ENGINEERING STUDENT

Shahjahanpur, Uttar Pradesh 242001 

+91-8318989303, +91-7052722930 

Ark.khan0123@gmail.com 

<https://www.linkedin.com/in/rehman-khan-482b79192> 



OBJECTIVE

I am an enthusiastic and energetic student focused in Electronics Engineering. My interest lies in VLSI. I have a good understanding of Digital Electronics, Verilog. Looking forward to a long-term career in the VLSI Domain.



EDUCATION

Bachelor of Technology (Electronics Engineering) | 2019-23

ALIGARH MUSLIM UNIVERSITY, ALIGARH

Intermediate | 2015-17

ISLAMIA INTER COLLEGE

High School | 2014-15

ISLAMIA INTER COLLEGE



PROFESSIONAL TRAINING

RTL DESIGN AND VERIFICATION TRAINEE | VLSI FOR ALL Pvt. Ltd.

JAN 2023 TO JUNE – 2023

Verilog, System Verilog, UVM.



PROJECT

- IMPLEMENTATION OF UART PROTOCOL USING VERILOG** 02/2023
Implemented UART Communication Protocol in Verilog and also verified by writing Testbench in Verilog. UART Protocol is a Serial Communication protocol that transmits data over two Wires, one for transmitting and one for receiving, using specific Baud Rate.
- DESIGN AND VERIFICATION OF FIFO USING VERILOG** 01/2023
Designed both Synchronous FIFO and Asynchronous FIFO using Verilog and verified by writing testbench. FIFO can be defined as a memory circuit that temporarily stores data in a queue, with the oldest data being retrieved first.
- OBJECT/PEOPLE COUNTER USING MSP430 MICROCONTROLLER** 12/2022
Designed counter using IR Sensor, 7-segment display. IR Sensor used to detect object and 7-segment used to show the number of objects. In this we have used Register Level Programming.



SKILLSET

- PROGRAMMING LANGUAGE**
Verilog, C++, C.
- TOOLS**
Xilinx Vivado, EDA Playground, ELECTRIC VLSI Design Tool, MATLAB/Simulink.
- PROTOCOLS**
SPI, I2C, UART
- OTHER**
Digital Electronics, Static Timing Analysis (STA), MSP430 Lunchbox, Arduino IDE.
- BOARD**
Nexys A7-100T FPGA Board



EXTRA CURRICULAR

- Successfully Completed One Month Course on VLSI SoC Design Using Verilog HDL from MAVEN SILICON.
- Completed 3 Days Bootcamp on Embedded Software by Decibels LAB.
- Got First Position in Photography Contest Organized by Student Union of Aligarh Muslim University, Aligarh.