Sudarshan Dixith G M

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Tumakuru-572102, Karnataka

Career Objective

To learn and improve the knowledge and skills in Physical Design and VLSI domain and contribute to the organizational growth

Core Competancy

- Experience in placement of macro cells in floorplan stage
- Experience in fixing some of floor plan issues using IR drop analysis
- Experience in fixing some DRC issues in standard cell placement stages
- Basics of C Language

Education Details

Advanced Diploma in ASIC Design - Physical Design   **2023**
RV-VLSI Design Center

Bachelor Degree in **Instrumentation Technology**   **2022**
Siddaganga Institute Of Technology, BH Road, Tumakuru, with 8.61 CGPA

    2018
Sarvodaya Pre-University College, Horpete, Tumakuru, with 91.16 %

SSLC   **2016**
Chetana Vidya Mandira High School, Horpete, Tumakuru, with 91.36 %

Domain Specific Project

RV-SKILLS for Emerging Technologies

Graduate Trainee Engineer   Sep-2022 to Jan-2023

Lakshya

Description

> Lakshya is a sub-system which ables to learn various aspects and real world challenges of VLSI physical design flow

Tools

> ICC2 Primetime

Challenges

- Placing of Macrocells in the layout to optimise the area used
- Reducing the congestion in after the placement of standard cells
- Analysing the IR drop on the propsed block and attempting reduce the DRC violations to maximum extent
- Analysing the timing reports generated and to fix some of the timing violations

B.E / B.Tech Academic Project

Siddaganga Institute Of Technology, BH Road, Tumakuru

Water Pollution Monitoring Boat Using IoT

Description

> A system that can float on water bodies which measures the pollutants and send the values to the user.

Tools

> Arudino Uno Raspberry PI Raspbian OS Arduino IDE

Challenges

- Designing a system that can operate on the water bodies remotely . Optimizing the structure to have minimum weight to float on the water body.