

Deepak Majji

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Career Objective

I aspire to work in VLSI domain with maximum potential in a high quality engineering environment and looking to enhance my skills by working on cutting edge technologies in VLSI domain.

Core Competancy

- Good knowledge in PD Flow from Floor plan to Routing
- Basics of scripting languages with TCL.
- Good understanding of the inputs and outputs of all the stages in physical design.
- Hands on knowledge on Physical Design tool ICC2.
- Good knowledge on Static Timing Analysis
- Basics of Clock tree synthesis
- Fixing timing violations after placement and CTS
- Fixing DRCs
- Placement of Macro cells
- Manual placement of Macros based on data flow lines, Macro family and ports.

Education Details

Advanced Diploma in ASIC Design	2022
RV-VLSI Design Center	
Bachelor Degree in Electronics and Communication	2021
ANDHRA UNIVERSITY, with 7.72 CGPA	
	2017
SRICHAITANYA JUNIOR COLLEGE SRIKAKULAM, with 98 %	
SSLC	2015
NARAYANA SCHOOL SRIKAKULAM, with 98 %	

B.E / B.Tech Academic Project

ANDHRA UNIVERSITY

DEVELOPMENT OF SMART VEHICLE COUNTER USING IMAGE PROCESSING

Description

A System has been developed to detect and count dynamic and efficient vehicles on highways, streets and heavily congested lanes. Based on experimental results conducted it is concluded that the accuracy of counting vehicles was in range of 70 to 95%.

Tools

Open CV -Python Software