

# Bharamu S K

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📍 Kengeri Bangalore 560060



I graduated from J.S.S.A.T.E, Bengaluru. Experienced in Embedded Software Engineering and trained in Blended Advanced Design and Verification in Maven Silicon. Passionate about technology and coding.

## PROFESSIONAL EXPERIENCE

### Senior Embedded Engineer, M.S Technology Bangalore

Jul 2019 - Dec 2022

Provides solutions and innovation for Energy Management and communication.

Bangalore, India

#### Achievements/Tasks:

- To Design, Develop, Implement and test the Embedded Software and Hardware.
- Strong knowledge of communication protocol UART, I2C, RS232, RS485, SPI
- Designed and developed the electronic zig for testing of PCB.
- Tool Expertise: Atollic | Arduino | ESP-IDF | Code Compos Studio | Altium | Ki-cad | OrCad.
- Implementation of TCP/IP, MQTT protocols in devices.
- Generating reports, technical manuals, and software development documentation.
- Designing a PCB.

## PROFESSIONAL TRAINING

### Advance Design and Verification training

May 2022 - present

-Maven Silicon Bangalore

Bangalore

### Embedded Systems Trainee

Sep 2018 - May 2019

-Cranes varsity a Training Division of Cranes Software International Ltd

Bangalore

## EDUCATION

### Electronics and Communication Engineering.

Jul 2015 - Jun 2018

J.S.S. Academy of Technical Education Bangalore

### Diploma in Electronics and Communication Engineering

Jul 2012 - May 2015

B.V.V.S Polytechnic Bagalkot

### Secondary Education

Apr 2012

S.S.S.B.V.V.S Hi-School Halingali

## SKILLS

Digital Electronics | Verilog | System Verilog | SVA | UVM | OOPS Concept | STA | Perl | Embedded C.

## TOOLS

Questasim | Modelsim | Quartus Prime |EDA Playground | Linux.

## PROJECTS

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### Router 1x3 Design and verification

The router accepts data packets on a single 8-bit port and routes them to one of the three output channels - channel0, channel1, and channel2.

#### Responsibilities:

- Architected the block-level structure for the design.
- Implemented RTL using Verilog HDL
- Verified the RTL model using the system Verilog
- Synthesized the design

### Energy Meter Reading Using Wi-Fi and BLE

Designed and developed an end node to communicate with the meter using UART and then send the data to the gateway through Wi-Fi or BLE. Gateway uses 4G /2G module to communicate with head end system.

### GAS and Water Meter

In this project, we collected gas and water meter data using n LC sensor or REED switch. and send data using RS485.

### Smart Lock Dual Authentication

The project aims to enhance system security. We used RFID to unlock the system and 4 Digit Password for the next step authentication.

## LANGUAGES

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- Kannada
- English
- Hindi
- Telugu

## HOBBIES

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Playing Cricket | Kabaddi | Travelling | Watching Movies

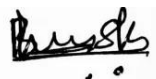
## DECLARATION

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I, hereby declare that the information furnished above is correct to the best of my knowledge

Date:06-01-2023.

Place: Bangalore



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Bharamu S K