

MTech – ECE Guru Jambheshwar University Hisar, 125001 DOB – 17 Sept. 1998 Gender - Female H. No. 590 Mohalla ↑ Lakhan V.P.O Kosli 123302 +91 9466981185 ****

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I have always been fascinated by the Integrated circuits and Computing in general. The still accelerating development in the field is very exciting. It is my dream to work in this industry and make an impact. I am always ready to learn about new things and I am a team player with good communication skills, and self-motivated. I like efficiency and would like to work at a place which allows me to make my share of contribution to the field. I like coding as well as working with the hardware.

Education

Degree/Certificate	University/Board	Institute/School	Year of Passing	CGPA/Percentage
Post-Graduation	Guru Jambheshwar University of S&T Hisar	Guru Jambheshwar University of S&T Hisar	2023	7.78/10 CGPA
Graduation	Punjab Technical University	Chandigarh Engineering Colleges	2020	7.68/10 CGPA
12th	CBSE	Suraj School	2015	80.40%
10th	CBSE	Suraj Sr. secondary School	2013	95%

Skills and Software

- Verilog
- C, Python
- Digital Electronics

- Xilinx Vivado
- System Verilog
- Operating System Win 7,8,10,11

Selected Projects

M.Tech Thesis on NAND Flash Translation Layer

- Address translation schemes (page level mapping and block level mapping)
- We have done garbage collection
- Environment: Jet PyCharm
- Language: Python

Pollution Monitoring Systems

- Monitoring pollution by measuring temperature, humidity, carbon monoxide, and the environment's quality
- Environment: Win-10, Energia18, Chip (Texas Instrument CC3200)
- Language/Database: Embedded C

Patient Health Monitoring System

- It is designed to monitor the health of a patient using IoT
- Doctor can see the real-time pulse graph of the heart and body temperature of the patient on their phone using the Blynk Application
- Environment: Embedded System
- Language/Database: Embedded C

Dec 2021 – 2022

Online Courses

DIGITAL IC DESIGN (Janakiraman Sir (IITM))

- Learned about invertor, metastability, Gate Sizing, Transistor level gates, Static timing analysis
- Learned to characterized D Flip flop, setup time violation and hold time violations

Course system verilog for verification (Maven-Silicon)

• Learned about Assertions, checkers

Courses Undertaken

Core Breadth

- Digital VLSI Design
- Hardware Description Language
- Digital Electronics
- Signal Processing
- IC Fabrication Technology (Nandita Das (IITM))
- Mathematics for Electrical Engineers
- Network Theory
- Engineering Physics

Scholastic Achievements

Innovation and Entrepreneurship Development Center Coordinator

 Selected from hundreds of students as coordinator and helped in shaping student ideas intoreality

Participated in "Tech Quiz" organized by Electronics Project Club.

• Our team get first position in Inter-college Tech Quiz on "Reality and Practicality"

Part of Hostel event management team

• In three-year, tenure organized successfully various event in the Hostel

Extracurricular Activities

Volley Ball Mar 2018

• Played in Inter branch Volley ball tournament (held third position)

Marathon Nov 2017

• Participated in a college 10Km Marathon.

Hobbies/Interests

- Playing Badminton, football
- Watching Movies and TV series and interacting with peoples