

Biral Pradhan

ECE NITW'22

Avid Reader, Explorer, Wonderer



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EDUCATION

B.Tech 2018-22

National Institute of Technology(NITW)
Warangal

08/2018 – Present

8.57 CGPA

Courses

- Digital Systems Design(DSD-I and DSD-II).
- Electronic Devices and Circuits.(EDC-I and EDC-II)

WORK EXPERIENCE

Executive Member

ECE Association NITW

08/2019 – Present

Warangal, India

The official Student Body of the department of Electronics and Communication Engineering (ECE), NITW.

Achievements/Tasks

- Content Writing for various events.
- Graphics Designing for event publicity.
- Mentored in IoT Workshop.
- Mentored in Photoshop and Video Editing Workshop.
- Planning and conducting various events and workshops for the overall development of students.

Executive Member

EA and HAM Club, NITW

09/2019 – Present

Warangal, India

The prestigious technical club of NITW.

Achievements/Tasks

- Mentored in Integrated Circuits Workshop.
- Mentored in Arduino Summer Guidance Program.
- Mentored in Game Automation Workshop.
- Content Writing for various events.
- Graphics Designing for event publicity.

ORGANIZATIONS

Technozion, NIT Warangal (10/2019 – 11/2019)

Organized the event Electronova, on behalf of ECE Association NITW. The 3-day event saw the participation of 100+ students.

SKILLS

Verilog

Xilinx Vivado

System Verilog

PSPICE

MATLAB

C++

Python

Data Structures

PERSONAL PROJECTS

Sanjaya- a Tactile Braille Interface

(02/2020 – 03/2020)

- Sanjaya is a device which will be helpful for conversion of any printed or E-Document to Braille script alphabet by alphabet.
- Placed second in Institute Makeathon'20.

Solo IoT devices for hidden surveillance

(01/2020 – 02/2020)

- Implemented live video feed from two RasPi Cameras, hidden in a display matrix over a common web server. Both cameras had remote web controlled pan and tilt action through a two servo assembly.
- Selected in SIH Internal Campus Hackathon 2020.

32-bit Floating Point Adder (04/2020 – 05/2020)

- IEEE Single Precision Floating Point Adder.

verilogmaster.tech (05/2020 – Present)

- A website for teaching Verilog. Articles aimed to explain the concepts in a very simple, yet detailed manner.

CERTIFICATES

2nd Position- Makeathon 2020 (02/2020 – 03/2020)

Built 'Sanjaya', within 24 hours

Institute Merit Scholarship 2019-20 (06/2020)

Awarded for Excellent Performance in Academics.

Institute Merit Scholarship 2018-19 (07/2019)

LANGUAGES

English

Full Professional Proficiency

Hindi

Full Professional Proficiency

Odia

Native or Bilingual Proficiency

INTERESTS

Novels

Photoshop

Video Editing

Sports