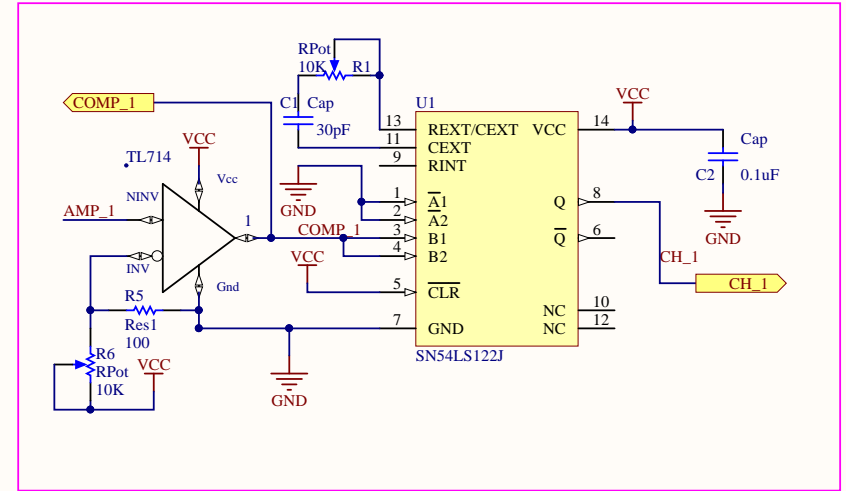
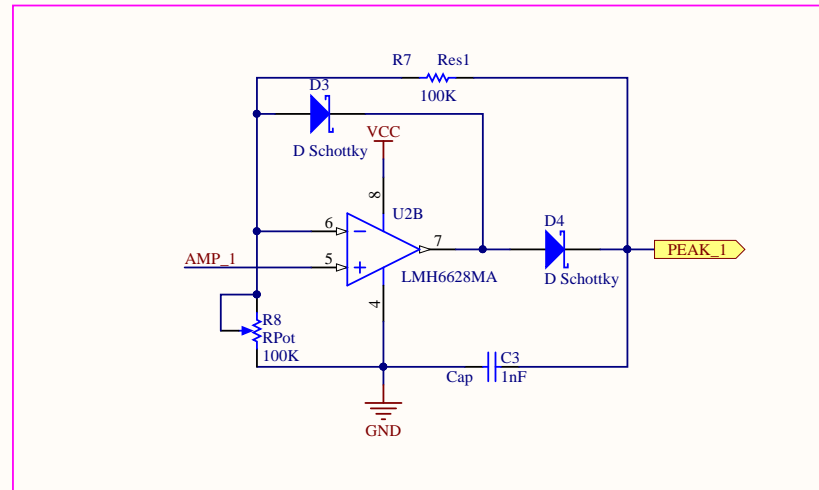


Pre-Amplifier Circuit

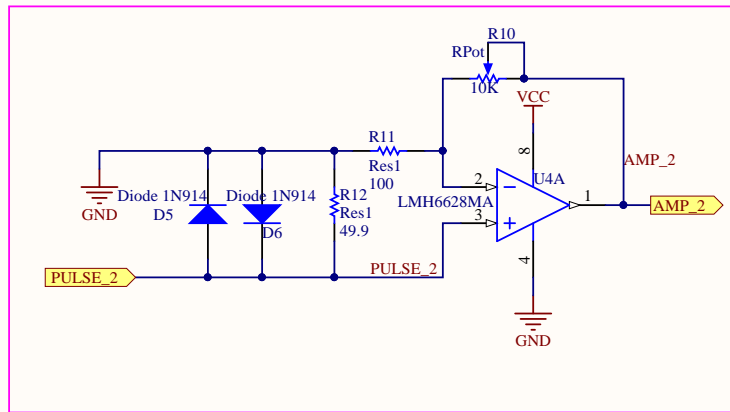


Discrimination Circuit

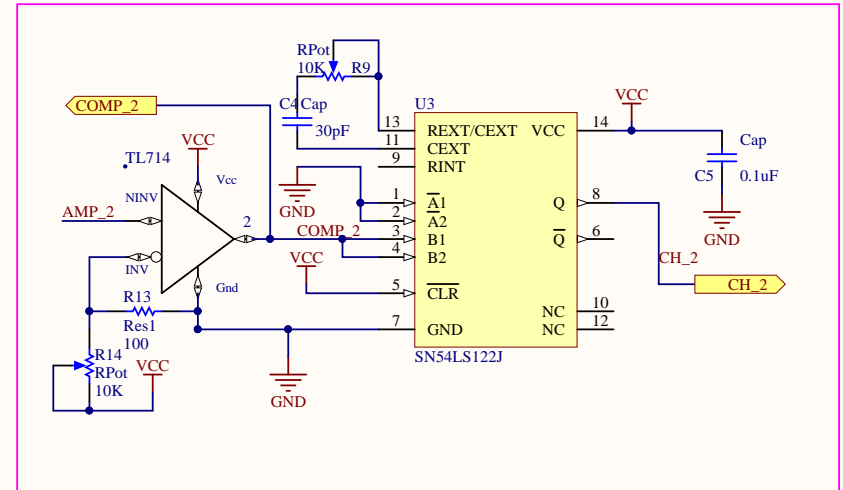


Peak Detector Circuit

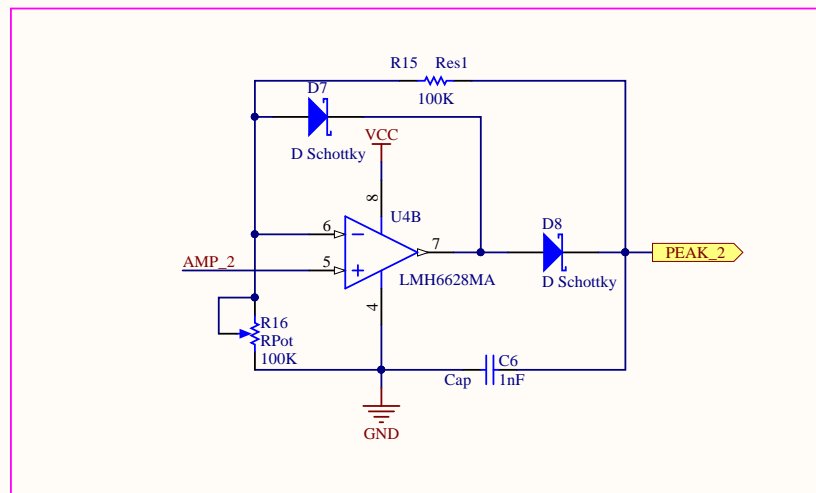
Title			
Channel 1			
Size	Number	Revision	
A4	1	2	
Date:	10/11/2018	Sheet of	SiPM Readout PCB
File:	C:\Users\...\Channel_1.SchDoc	Drawn By:	Sanket Deshpande



Pre-Amplifier Circuit

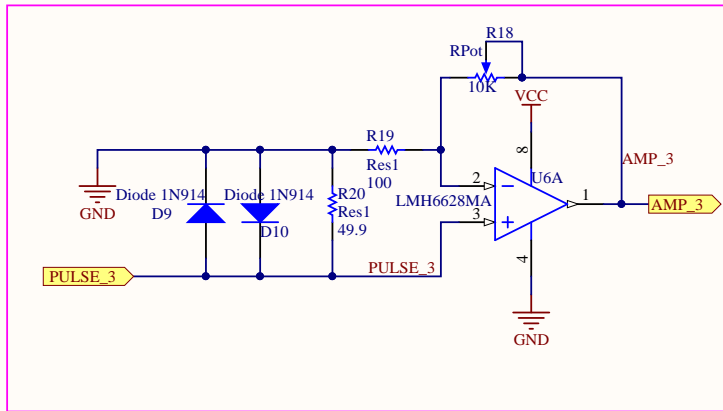


Discrimination Circuit

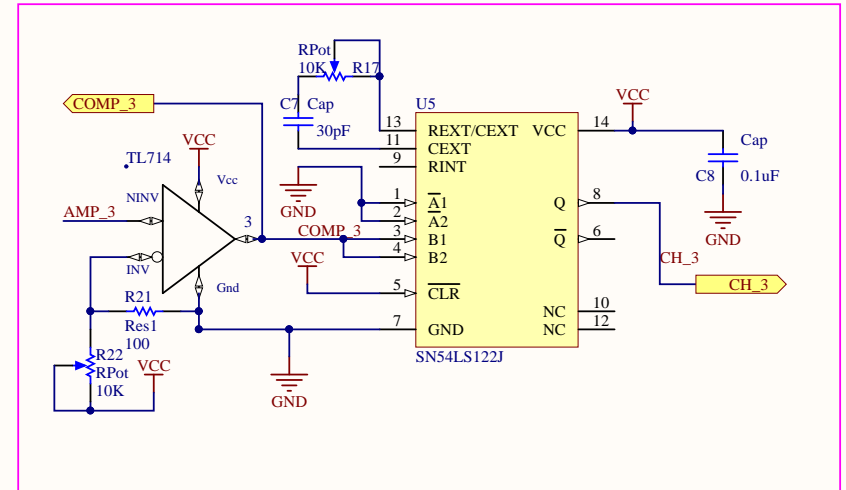


Peak Detector Circuit

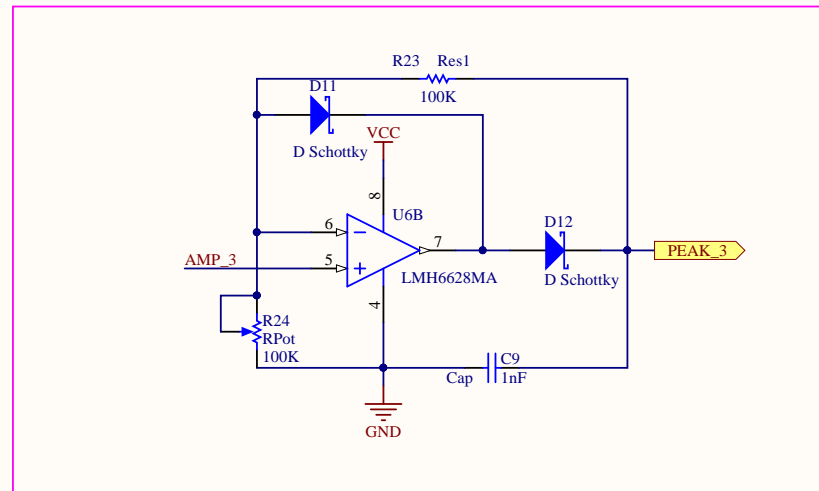
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Size	Number	Revision		
A4	2	2		
Date:	10/11/2018	Sheet of	SiPM Readout PCB	
File:	C:\Users\...\Channel_2.SchDoc	Drawn By:	Sanket Deshpande	



Pre-Amplifier Circuit

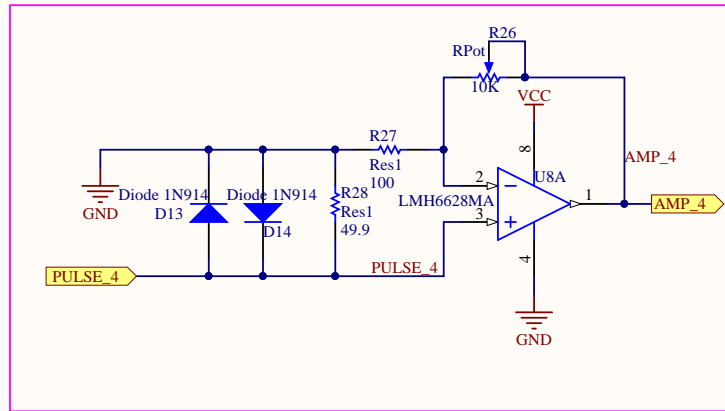


Discrimination Circuit

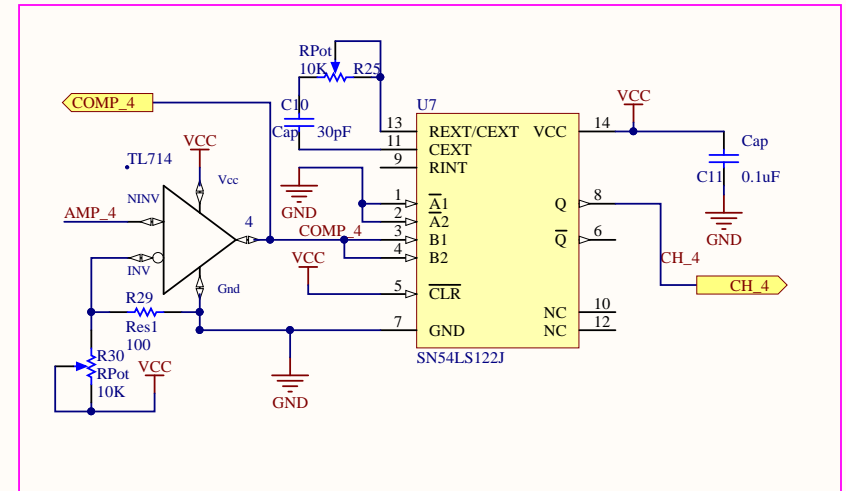


Peak Detector Circuit

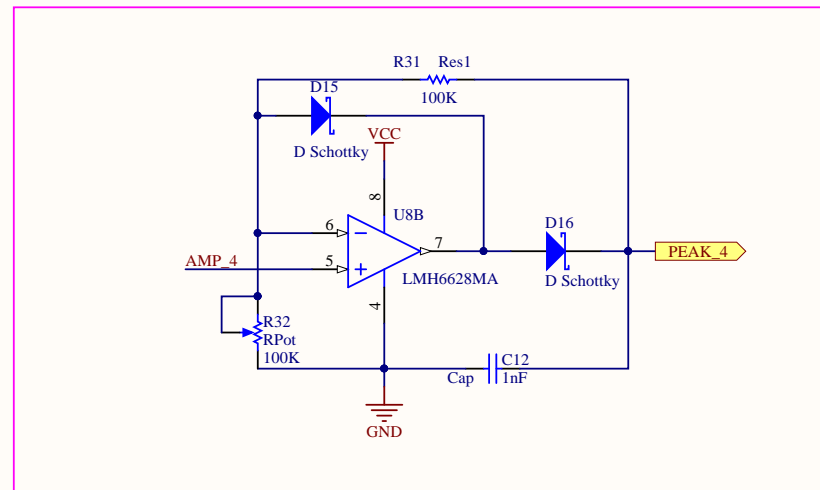
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Channel 3			
Size	Number	Revision	
A4	3	2	
Date:	10/11/2018	Sheet of	SiPM Readout PCB
File:	C:\Users\...\Channel_3.SchDoc	Drawn By:	Sanket Deshpande



Pre-Amplifier Circuit

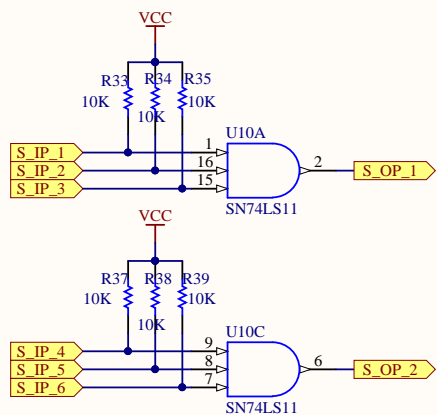


Discrimination Circuit

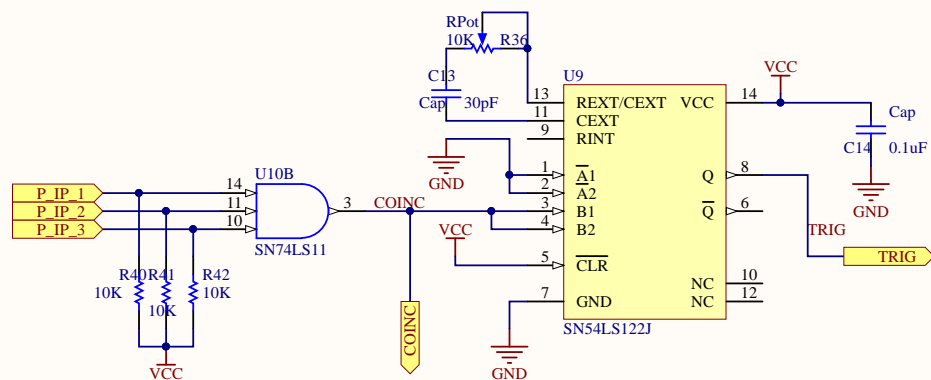


Peak Detector Circuit

Title			
Channel 4			
Size	Number	Revision	
A4	4	2	
Date:	10/11/2018	Sheet of	SiPM Readout PCB
File:	C:\Users\...\Channel_4.SchDoc	Drawn By:	Sanket Deshpande

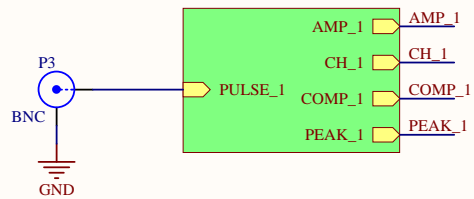
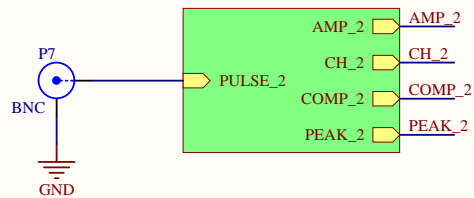
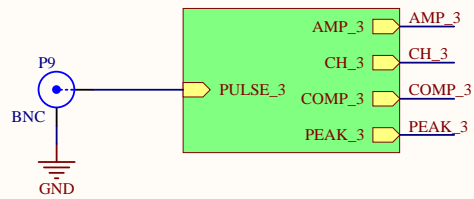
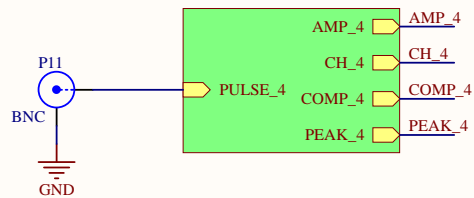


Signal Selection Circuit

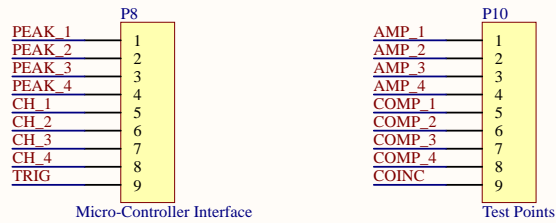


Coincidence Trigger Generation Circuit

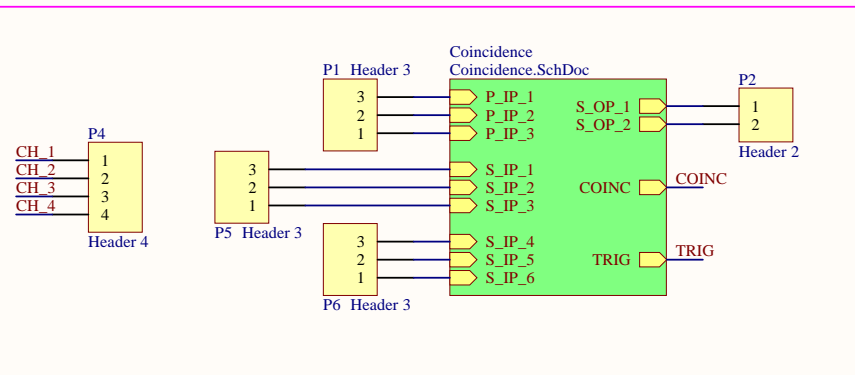
Title		Coincidence	
Size	Number	Revision	
A4		2	
Date:	10/11/2018	Sheet of	SiPM Readout PCB
File:	C:\Users\...\Coincidence.SchDoc	Drawn By:	Sanket Deshpande

Channel 1  
Channel\_1.SchDocChannel 2  
Channel\_2.SchDocChannel 3  
Channel\_3.SchDocChannel 4  
Channel\_4.SchDoc

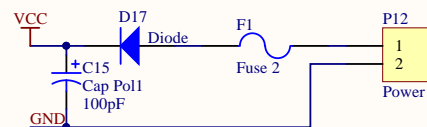
Pulse Processing Circuits



External Interface



Coincidence Selection and Trigger Generation



Power Supply

Title			
Connectors			
Size A4	Number 2	Revision 2	
Date:	10/11/2018	Sheet of	SiPM Readout PCB
File:	C:\Users\...\Connectors.SchDoc	Drawn By:	Sanket Deshpande

