1. A) Instruction set architecture is an abstract model of a computer.

B) RISC and CISE are both instruction set. RISC’s biggest difference from CISC is that it has fixed size of instruction to achieve fast processing speed.

1. A) Conditional move has advantage in predicting the future instruction so it the processor can build the instruction pipeline without big losses.

B) Trying to move (%rdi) to %rax but the problem is that %rdi might be null pointer.

1. 일단 rax=rdx를 넣는다

ㅇ

1. Long switch3(long \*p1, long \*p2, mode\_t action)

{

long result=0;

switch(action){

case MODE\_A:

long A=\*p2;

\*p2=action;

return A;

case MODE\_B:

\*p1+=\*p2;

Return \*p1;

case MODE\_C:

\*p1=59;;

Return \*p2;

case MODE\_D:

\*p1=\*p2;

Return 27;

case MODE\_E:

return 27;

default:

return 12;

}

1. N-60 <5면 펑