Microsoft Signal Integrity Laboratory

15101 NE 40th ST (Studio B)

Redmond, WA 98052

VNA TEST REPORT

|  |  |
| --- | --- |
| **Test Request Information** | |
| Test Request # | 123456 |
| MS Business Unit | SI Test Lab |
| MS Project Code Name | Demonstration VNA |
| MS Build Phase | EV0.1 |
| Request Date | 1/1/2001 |
| Due Date | 7/3/2015 |
| **SI Engineers** | |
| SI Compliance Engineer | RT or CS |
| SI Lab Manager | Johnny Deng (jodeng) |
| SI Test Engineer | JY or TB |



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# Test Overview

These are special notes for our final report to include

# Test Setup

## PNA Test Setup

|  |  |
| --- | --- |
| **Parameters** | **Values** |
| **Start Frequency** | 10.0MHz |
| **Stop Frequency** | 20.0GHz |
| **Frequency Step** | 10.0MHz |
| **IFBW** |  |
| **Avg. On/Off (# of Avg.)** |  |
| **Power level** |  |

## Hardware Apparatus

|  |  |  |  |
| --- | --- | --- | --- |
| **Equipment Description** | **Manufacturer/Model** | **SI #** | **Factory Cal Expired?** |
| **VNA/TDR** |  |  |  |
| **Calibration Module** |  |  |  |
| **Cable 1** |  |  |  |
| **Cable 2** |  |  |  |
| **Cable 3** |  |  |  |
| **Cable 4** |  |  |  |

## Software Apparatus

|  |  |
| --- | --- |
| **Item Description** | **Version/Model #** |
| **PLTS Version** |  |
| **Software PC/Desktop** |  |
| **Operating System** |  |

## 

## DUT Identification

SIDescriptionSI-1000-1HDMI cable 3-metersSI-1000-2DP CableSI-1000-3PCB prototypeSI-1000-4Test Coupon ASI-1000-5Test Coupon Z

## Port Configuration & Probe Identification

ConfigurationVNAPort #VirtualDiff Port #Connected Component (Ploarity)Probe Type (SN)111U31 (+)250-GSSG (7234)31U31 (-)250-GSSG (7234)22J12 (+)250-GSSG (7283)42J12 (-)250-GSSG (7283)211DP\_LANE0\_P\_C (on U201)250-GSSG (7234)31DP\_LANE0\_N\_C (on U201)250-GSSG (7234)22DP\_LANE0\_P\_C (on J23)250-GSSG (7283)42DP\_LANE0\_N\_C (on J23)250-GSSG (7283)

# Test Summary

sNp fileTrace NameSAMPLE.S4PTS0SI-489-1\_SURFACE\_PCIE0\_LANE1\_C1\_P1-2-3\_Sdd1-2.s4pTS1SI-489-2\_SURFACE\_PCIE0\_LANE2\_C2\_P2-3\_Sdd1-1.s4pTS2SI-489-3\_SURFACE\_PCIE0\_LANE3\_C3\_P2\_Sdd2-2.s4pTS3SI-489-4\_SURFACE\_PCIE0\_LANE4\_C3\_P1\_Sdd2-2.s4pTS4SI-489-5\_SURFACE\_PCIE0\_LANE5\_C5\_P4\_Sdd1-2.s4pTS5SI-489-6\_SURFACE\_PCIE0\_LANE6\_C4\_P2\_Sdd1-4.s4pTS6