sw_up_en
INPUT
VCC

db_down[7..0]
BIDIR
VCC

TRI
tri1

tri2
TRI

BIDIR
VCC
db_up[7..0]

sw_down_en
INPUT
VCC

PROJECT	**A-Z80**	MODULE	**data_switch**
DESIGNER	Goran Devic		
DATE	June 14, 2014	REV	1.0

data_switch.bdf