R65C02 TC: Timing in cycles for all Op Codes - Sep. 09th 2018 Jens.Gutschmidt V0.94

103002_	22_TC: Timing in cycles for all Op Codes - Sep. 09 th 2018 Jens.Gutschmidt V0.94															
	Accumulator	Immediate	Zero Page	Zero Page Ralative	Zero Page, X	Zero Page, Y	Absolute	Absolute, X	Absolute, Y	Implied	Relative	(Indirect)	(Indirect, X)	(Indirect), Y	(Absolute Indirect)	(Absolute Indirect, X)
ADC		2 (+1d)	3 (+1d)		4 (+1d)		4 (+1d)	4/p5 (+1d)	4/p5 (+1d)			5 (+1d)	6 (+1d)	5/p6 (+1d)		
AND		2	3		4		4	4/p5	4/p5			5	6	5/p6		
ASL	2		5		6		6	7								
BBR BBS				5/j6/jp7 5/j6/jp7												
BCC				5/J6/Jp7							2/j3/jp4					
BCS											2/i3/ip4					
BEQ											2/j3/jp4 2/j3/jp4					
BIT		2	3		4		4	4/p5								
ВМІ											2/j3/jp4					
BNE											2/j3/jp4 2/j3/jp4					
BPL BRA											j3/jp4					
BRK										7	Jorjp					
BVC											2/j3/jp4					
BVS											2/j3/jp4					
BVS CLC CLD										2						
CLD										2						
CLV										2						
CMP		2	3		4		4	4/p5	4/p5			5	6	5/p6		
CDX		2	3				4						Ť	J. P. J		
CPY		2	3				4									
CPY DEC DEX DEY EOR	2		5		6		6	7								
DEX										2						
DEY		1	2		4		4	A/nE	1/nF	2		E	6	E/mG		
INC	2	2	3 5		6		4 6	4/p5 7	4/p5			5	6	5/p6		
INC INX INY	2		3		0		0	,		2						
INY										2						
JMP							3								6	6
JSR							6									
LDA LDX		2	3		4		4	4/p5	4/p5			5	6	5/p6		
LDX		2	3		4	4	4	A/nE	4/p5							
LDY LSR	2	2	3 5		6		4 6	4/p5 7								
NOP			3		0		0	,		2						
ORA		2	3		4		4	4/p5	4/p5			5	6	5/p6		
PHA										3						
PHX PHY										3						
PHY										3						
PHP PLA										3						
PLX										4						
PLY										4						
PLP										4						
RMB			5													
ROL	2		5		6		6	7								
ROR RTI	2		5		6		6	7		6						
RTS										6						
SBC		2	3		4		4	4/p5	4/p5			5	6	5/p6		
		(+1d)	(+1d)		(+1d)		(+1d)	(+1d)	(+1d)			(+1d)	(+1d)	(+1d)		
SEC										2						
SED										2						
SEI			E							2						
SMB STA			5		4		4	5	5			5	6	6		
STX			3		7	4	4	J	J			J	0			
STY			3		4		4									
STZ			3		4		4	5								
TAX										2						
TAY										2						
TRB			5				6									
TSB TSX			5				6			2						
TXA										2						
TXS										2						
TYA										2						
	OLINIE	COLOR	- Implo	mented b	ut not to	etad	vot (ou	tetandi	na)						•	

BACKGROUND COLOR: Implemented but not tested yet (outstanding)
BACKGROUND COLOR: Not implemented yet (outstanding)
BACKGROUND COLOR: Tested for function and timing
BACKGROUND COLOR: Same State Machine – not fully tested
BACKGROUND COLOR: Test failed

TEXT COLOR: New for 65C02 – not implemented in 6502
TEXT COLOR: changed number of cycles