pin_control_oe   INPUT
                 VCC

address[15..0]   INPUT
                 VCC
clk   INPUT
                 VCC
bus_ab_pin_we   INPUT
                 VCC

NOT
nclk

DFFE
PRN
D   Q
ENA
CLRN
apin_latch

TRI
inst

OUTPUT   abus[15..0]

Repeated 16 times, once for each address pin.

PROJECT	A-Z80	MODULE	address_pins
DESIGNER	Goran Devic		
DATE	June 14, 2014	REV	1.3

address_pins.bdf