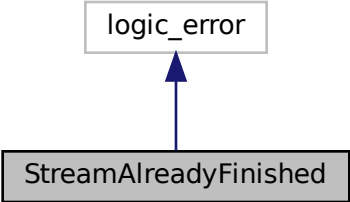


logic_error



```
graph BT; SA[StreamAlreadyFinished] --> LE[logic_error];
```

The diagram illustrates an inheritance relationship. At the bottom is a gray rectangular box with a black border containing the text "StreamAlreadyFinished". A blue arrow points vertically upwards from the top center of this box to the bottom center of a white rectangular box with a gray border above it. The white box contains the text "logic_error".

StreamAlreadyFinished