# Introduction

Describe the purpose of the analysis.  What is the goal of your analysis?  What are you analyzing?

The goal of the analysis is to see how the hit rate of a cache changes with modifications to its implementation. This could be through changes in design, like Least Recently Used (LRU) or First-In-First-Out (FIFO). It could also be through the choice to use fully associative, direct mapping, or n-way associative sets for cache. Our analysis into hit rates will give insight into the most effective cache implementations based on the situation.

# Description of Tests

What were the parameters for each test? Include the values of parameters: cache size, block size, associativity, replacement strategy.

Make sure to specify the associativity for set associative.

Why did you choose these parameters?

# Results

What were the hit rates for the different configurations?

Create plots to show your results.

Example: Line graph of hit rate vs cache size, with separate lines for FIFO and LRU

Make sure your plots are labeled on the x and y axes. y axis should be hit rate, x axis is independent variable. Give units on the independent variable (e.g. bytes)

# Conclusions

How does cache design (direct mapped/set associative/fully associative) affect hit rate?

How does hit rate change with the two replacement policies?

How does cache size affect hit rate?