

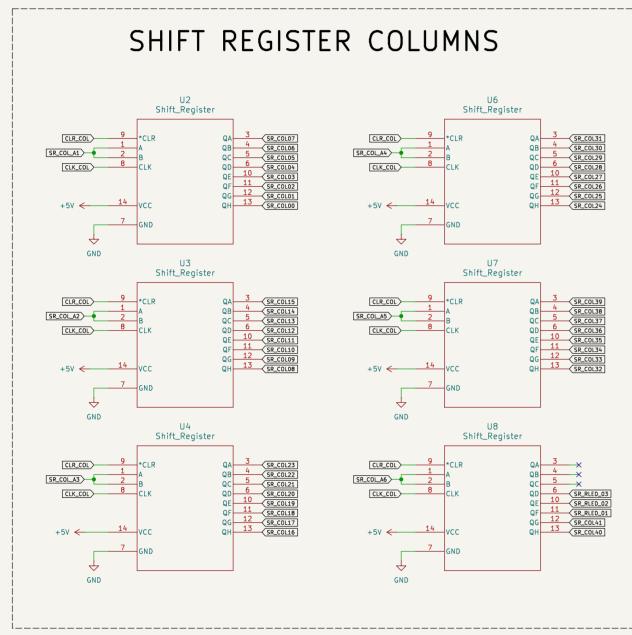
NAT aka Simak aka Mr Simak aka Matej Simacek aka NatSi EDITION 2 EN SN ESP WROOM 32 TXD 5 G34 RXD 24 6 G35 G21 25 SR_COL_A5 7 G32 SR_COL_A4 SR_COL_A3 **♦** GND SR_COL_A2 SR_COL_A1 IO_ROW_24V IO_BZ CLK_COL> 15 G13 SD1 36 SD0

IF WORK NOT!! BLAME MR.GAYASS (FEMBOY :3)

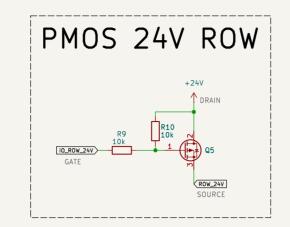
D

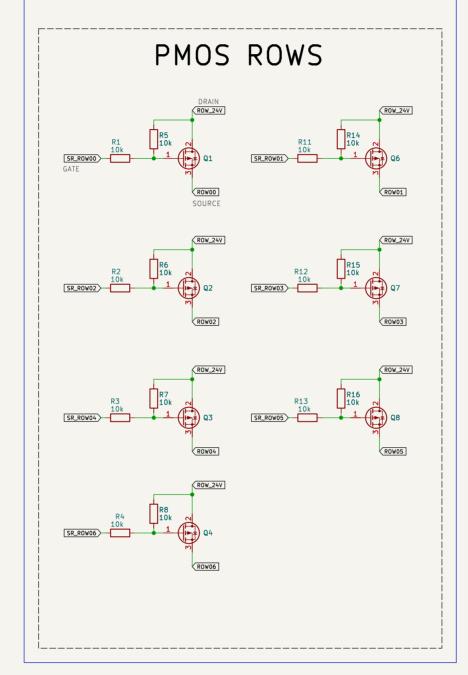
SHIFT REGISTER

SHIFT REGISTER ROW SELECT ROW!!!!!!! U5 Shift_Register CLR_ROW 9 *CLR A B CLK QA 4 SR.ROW00 QB 5 SR.ROW01 QC 6 SR.ROW03 QC QD 10 SR.ROW03 QC QF 11 SR.ROW04 QF 11 SR.ROW06 QF 12 SR.ROW06 QH 13 X



PMOS





NMOS

