

POWER_SUPPLY

File: POWER_SUPPLY.kicad_sch

MCU

File: MCU.kicad_sch

GRAPHICS

File: GRAPHICS.kicad_sch

MOTOR_CONTROL

File: MOTOR_CONTROL_kicad_sch

CONNECTIVITY

File: CONNECTIVITY.kicad_sch

SENSORS

File: SENSORS.kicad_sch

MECHANICS

File: MECHANICS.kicad_sch

SUMEC MK IV aka SMD-V3 board

Made by Bismarx aka MenMenson

CONTACT: Savva Popov, menmenson09@gmail.com, +420 605 570 366

Made in Prague, Czech Republic

SPS NA PROSEKU

Sheet:

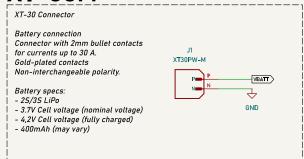
File: SUMEC_MK_IV.kicad_sch

Title: SUMEC_MK_IV

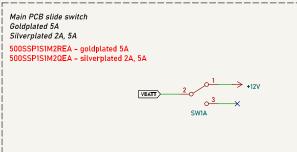
Size: A4 Date: 2024-08-05 Rev. v3.0.9

KiCad E.D.A. 8.0.4 Id: 1/8

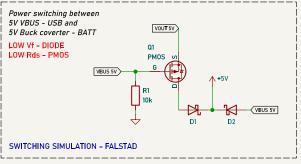
XT-30M



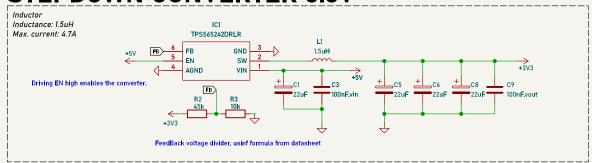
MAIN SWITCH



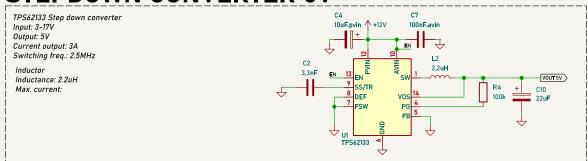
USB POWER SWITCH



STEPDOWN CONVERTER 3.3V USE LDO OR BUCK FOR 3V3

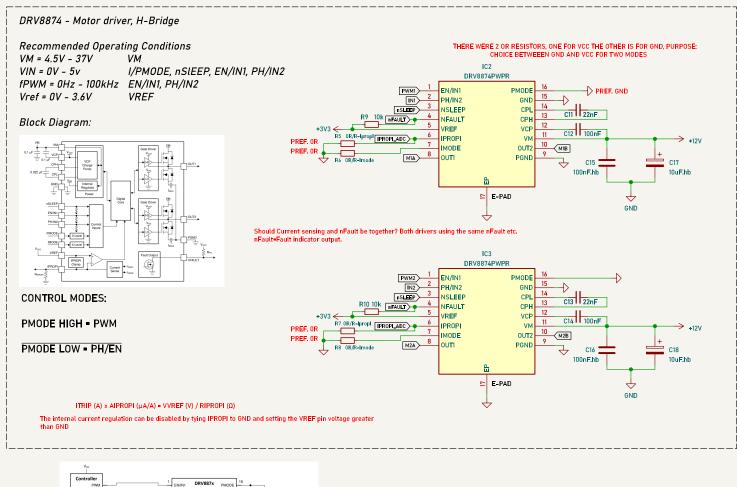


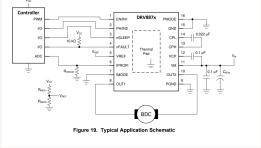
STEPDOWN CONVERTER 5V



GUILTY UNTIL PROVEN INNOCENT

H-BRIDGE





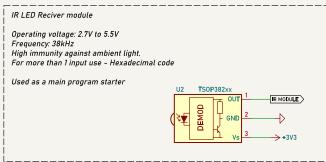
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KjCad E.D.A. 8.0.4 Id: 3/8

IR MODULE



LINE SENSORS

Miniature Reflective
Object Sensor - QRE1113

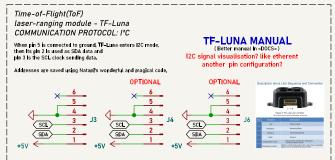
IR reflectance sensor is comprised of two parts - an IR emitting LED and an IR sensitive phototransistor.

Analog output
A 100Ω resistor is placed in series with the LED to limit current. A 10kΩ resistor pulls the output pin high, but when the light from the LED is reflected back onto the phototransistor the output will begin to go lower.

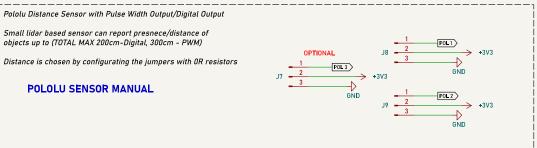
2 front sensors will be connected to the "QRE MiniBoard", rear sensor will be connected using the same connector.

qre signal visualisation? (update it on the qre miniboard itself)

DISTANCE MEASURING



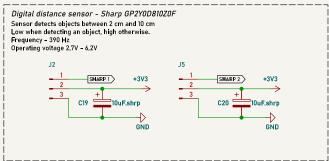
OBJECT DETECTING SENSORS



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SIDE SENSORS



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ld: 4/8

ESP32-S3-WR00M1-N4

GPI00 has an internal pullup resistor, so if it is left unconnected then it will pull high.

GPIO0, GPIO45, and GPIO46 are connected to the chip's internal weak pull-up/pull-down resistors at chip reset. These resistors determine the default bit values of the strapping pins. Also, these resistors determine the bit values if the strapping pins are connected to an external high-impedance circuit.

Default Configuration of Strapping Pins

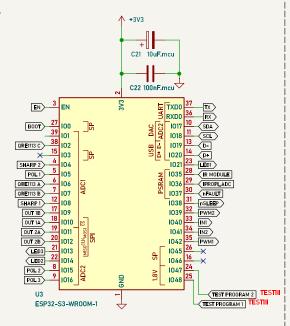
Strapping Pin Default Configuration Bit Value GPI00 Pull-up GPI03 Floating **GPI045** Pull-down **GPI046** Pull-down

Typically these can be used, but you need to make sure they are not in the wrong state during boot.

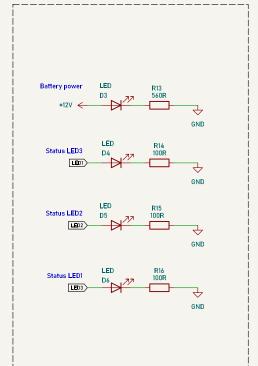
gpio.0 Boot Mode. Weak pullup during reset. (Boot Mode 0=Boot from Flash, 1=Download) gpio.3 JTAG Mode. Weak pull down during reset. (JTAG Config) gpio.45 SPI voltage. Weak pull down during reset. (SPI Voltage 0=3.3v 1=1.8v) gpio.46 Boot mode. Weak pull down during reset. (Enabling/Disabling ROM Messages Print During Booting)

ESP32-S3 Datasheet

working voltage for pins 47 and 48 is 1.8 (test it) Check pins with datasheet, ADC, SP, USB etc Check encoder connection **UART** connection?



LED SIGNALISATION



GPIO9: FSPIHD (not sure what it stands for)

GPI010: FSPICS0 (chip select)

GPI011: FSPID (dual SPI)

GPI012: FSPICLK (clock)

GPI013: FSPIQ (quad SPI)

GPI014: FSPIWP ("write protect")

FSPI Bus Signal	SPI3 Bus Signal	Function		
FSPICLK	SPI3_CLK	Input and output clock in master/slave mode		
FSPICS0	SPI3_CSO	put and output CS signal in master/slave mode		
FSPICS1 ~ 5	SPI3_CS1 ~ 2	Output CS signal in master mode		
FSPID	SPI3_D	MOSI/SIOO (serial data input and output, bit0)		
FSPIQ	SPI3_Q	MISO/SIO1 (serial data input and output, bit1)		
FSPIWP	SPI3_WP	SIO2 (serial data input and output, bit2)		
FSPIHD	SPI3_HD	SIO3 (serial data input and output, bit3)		
FSPIIO4 ~ 7	_	SIO4 ~ 7 (serial data input and output, bit4 ~ 7)		
FSPIDQS	_	Output data mask signal in master mode		

		10 - 10 - 19 - 10 - 10 - 10 - 10 - 10 -		
FSPICLK SPI3_CLK		Input and output clock in master/slave mode		
FSPICS0	SPI3_CS0	Input and output CS signal in master/slave mode		
FSPICS1 ~ 5		Output CS signal in master mode		
		MOSI/SIOO (serial data input and output, bit0)		
FSPIQ	SPI3_Q	MISO/SIO1 (serial data input and output, bit1)		
FSPIWP SPI3_WP FSPIHD SPI3_HD FSPII04 ~ 7 — FSPII0S —		SIO2 (serial data input and output, bit2)		
		SIO3 (serial data input and output, bit3)		
		SIO4 ~ 7 (serial data input and output, bit4 ~ 7)		
		Output data mask signal in master mode		

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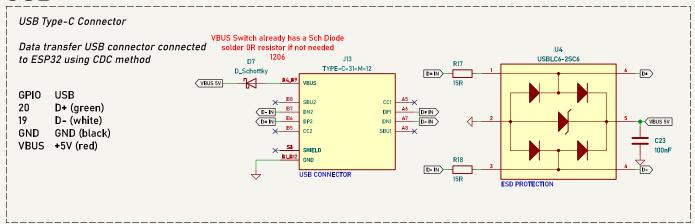
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FSPID is MOSI, FSPIQ is MISO, FSPICSO is chip select, FSPICLK is the clock.

USB



UART



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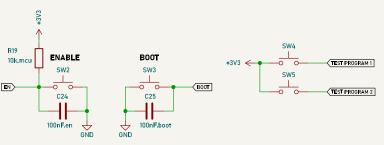
PUSH BUTTONS

Buttons for BOOT and ENABLE functions.

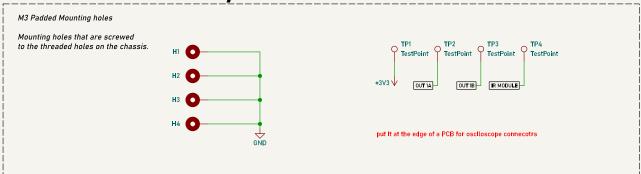
If the device does not support the auto download mode,
you need to get into the download mode manually.
To do so, press and hold the BOOT button and then press
the RESET button once. After that release the BOOT button.

EN-RESET, BOOT-bootloader mode

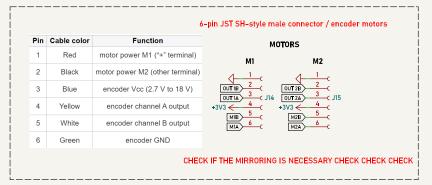
Enable (EN) is the 3.3V regulator's enable pin. It's pulled up, so connect to ground to disable the 3.3V regulator. This means that you can use this pin connected to a pushbutton to restart your ESP32, for example.



MOUNTING HOLES / TEST POINTS



CONNECTORS





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nade by bisinary and melimenson

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