



POWER_SUPPLY

File: POWER_SUPPLY.kicad_sch

MOTOR_CONTROL

File: MOTOR_CONTROL.kicad_sch

SENSORS

File: SENSORS.kicad_sch

MCU

File: MCU.kicad_sch

CONNECTIVITY

File: CONNECTIVITY.kicad_sch

MECHANICS

File: MECHANICS.kicad_sch

GRAPHICS

File: GRAPHICS.kicad_sch

SUMEC MK IV aka SMD-V3 board
Made by Bismarx aka MenMenson
CONTACT: Savva Popov, menmenson09@gmail.com, +420 605 570 366
Made in Prague, Czech Republic
SPS NA PROSEKU

Sheet: /
File: SUMEC_MK_IV.kicad_sch

Title: SUMEC_MK_IV

Size: A4 Date: 2024-07-15

KiCad E.D.A. 8.0.3

Rev: v3.0.0

Id: 1/8

XT-30M

XT-30 Connector

Battery connection

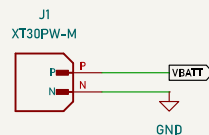
Connector with 2mm bullet contacts for currents up to 30 A.

Gold-plated contacts

Non-interchangeable polarity.

Battery specs:

- 2S/3S LiPo
- 3.7V Cell voltage (nominal voltage)
- 4.2V Cell voltage (fully charged)
- 400mAh (may vary)



LDO VOLTAGE REGULATOR

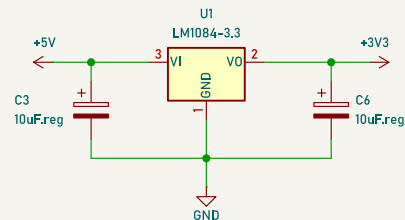
LM1084IS-3.3/NOPB

LDO Voltage Regulator 3.3V/5A

TO-263-3

Tab is output

Max dropout voltage: 1.5V



USE LDO OR BUCK FOR 3V3

USB POWER SWITCH

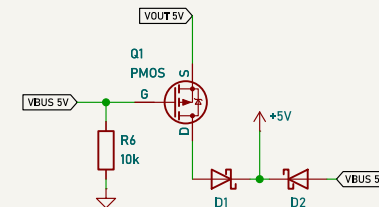
Power switching between

5V VBUS - USB and

5V Buck converter - BATT

LOW Vf - DIODE

LOW Rds - PMOS



SWITCHING SIMULATION - FALSTAD

MAIN SWITCH

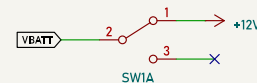
Main PCB slide switch

Goldplated 5A

Silverplated 2A, 5A

500SSIP1S1M2REA - goldplated 5A

500SSIP1S1M2QEA - silverplated 2A, 5A



STEPDOWN CONVERTER

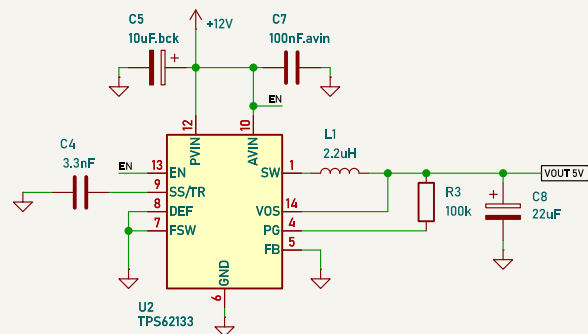
TPS62133 Step down converter

Input: 3-17V

Output: 5V

Current output: 3A

Switching freq.: 2.5MHz



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GUILTY UNTIL PROVEN INNOCENT

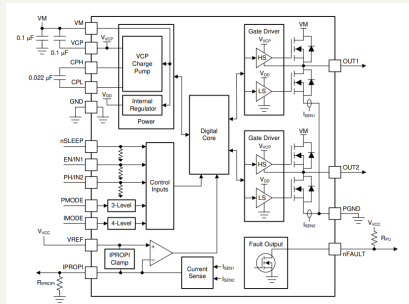
H-BRIDGE

DRV8874 - Motor driver, H-Bridge

Recommended Operating Conditions

VM = 4.5V - 37V VM
VIN = 0V - 5v I/PMODE, nSLEEP, EN/IN1, PH/IN2
fPWM = 0Hz - 100kHz EN/IN1, PH/IN2
Vref = 0V - 3.6V VREF

Block Diagram:



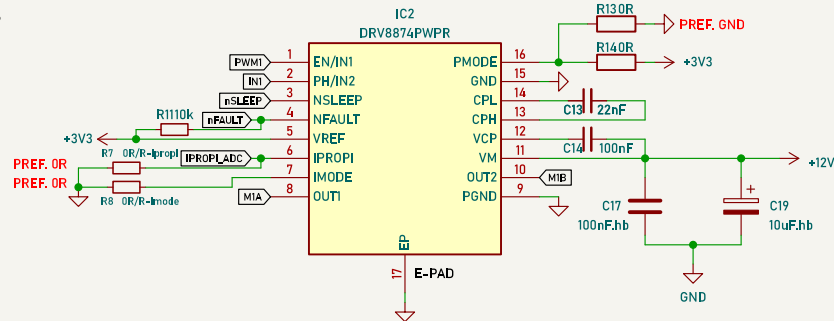
CONTROL MODES:

PMODE HIGH = PWM

PMODE LOW = PH/EN

$$ITRIP(A) \times AIPROPI(\mu A/A) = VVREF(V) / RIPROPI(Q)$$

The internal current regulation can be disabled by tying IPROPI to GND and setting the VREF pin voltage greater than GND



Should Current sensing and nFAULT be together? Both drivers using the same nFAULT etc.
nFault=Fault indicator output.

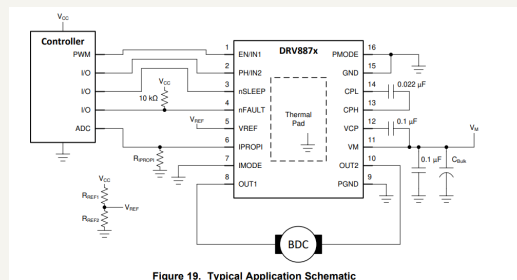
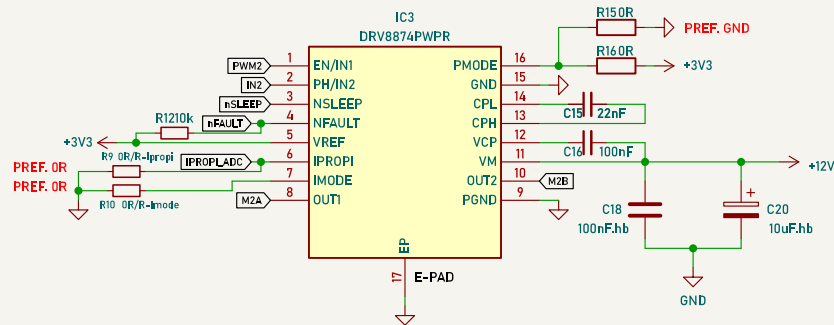


Figure 19. Typical Application Schematic

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A

Used as a main program starter



C

D

A

B

C

D

ESP32-S3-WROOM1-N4

GPIO0 has an internal pullup resistor, so if it is left unconnected then it will pull high.

GPIO0, GPIO45, and GPIO46 are connected to the chip's internal weak pull-up/pull-down resistors at chip reset. These resistors determine the default bit values of the strapping pins. Also, these resistors determine the bit values if the strapping pins are connected to an external high-impedance circuit.

Default Configuration of Strapping Pins

Strapping Pin	Default Configuration	Bit Value
GPIO0	Pull-up	1
GPIO3	Floating	-
GPIO45	Pull-down	0
GPIO46	Pull-down	0

Typically these can be used, but you need to make sure they are not in the wrong state during boot.

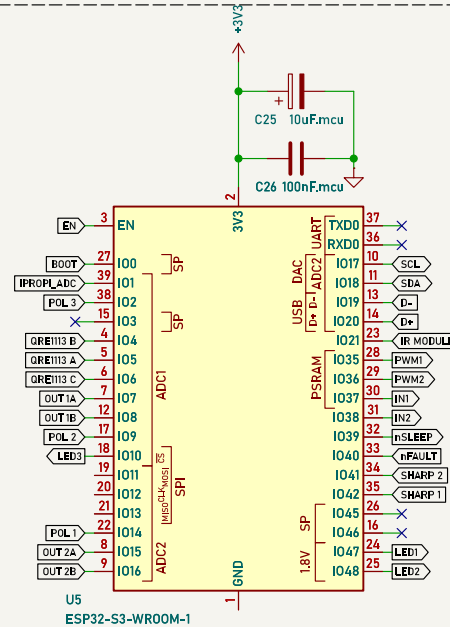
gpio.0 Boot Mode. Weak pullup during reset. (Boot Mode 0=Boot from Flash, 1=Download)

gpio.3 JTAG Mode. Weak pull down during reset. (JTAG Config)

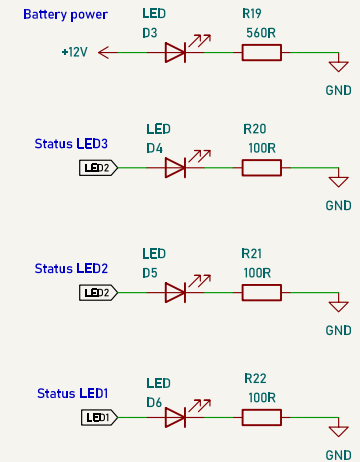
gpio.45 SPI voltage. Weak pull down during reset. (SPI Voltage 0=3.3v 1=1.8v)

gpio.46 Boot mode. Weak pull down during reset. (Enabling/Disabling ROM Messages Print During Booting)

ESP32-S3 Datasheet



LED SIGNALISATION



working voltage for pins 47 and 48 is 1.8 (test it)

GPIO9: FSPIDH (not sure what it stands for)

GPIO10: FSPICS0 (chip select)

GPIO11: FSPID (dual SPI)

GPIO12: FSPICLK (clock)

GPIO13: FSPIQ (quad SPI)

GPIO14: FSPWIP (*write protect*)

FSPID is MOSI, FSPIQ is MISO, FSPICS0 is chip select, FSPICLK is the clock.

FSPI Bus Signal	SPI3 Bus Signal	Function
FSPICLK	SPI3_CLK	Input and output clock in master/slave mode
FSPICS0	SPI3_CS0	Input and output CS signal in master/slave mode
FSPICS1 ~ 5	SPI3_CS1 ~ 2	Output CS signal in master mode
FSPID	SPI3_D	MOSI/SIO0 (serial data input and output, bit0)
FSPIQ	SPI3_Q	MISO/SIO1 (serial data input and output, bit1)
FSPWIP	SPI3_WP	SIO2 (serial data input and output, bit2)
FSPIDH	SPI3_HD	SIO3 (serial data input and output, bit3)
FSPIO4 ~ 7	—	SIO4 ~ 7 (serial data input and output, bit4 ~ 7)
FSPIDQS	—	Output data mask signal in master mode

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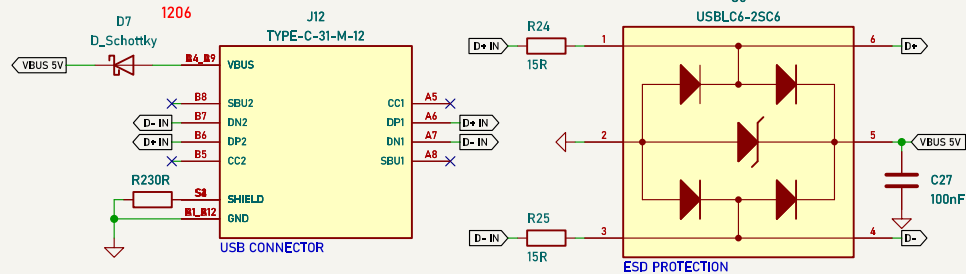
USB

USB Type-C Connector

Data transfer USB connector connected to ESP32 using CDC method

GPI0	USB
20	D+ (green)
19	D- (white)
GND	GND (black)
VBUS	+5V (red)

VBUS Switch already has a Sch Diode
solder 0R resistor if not needed



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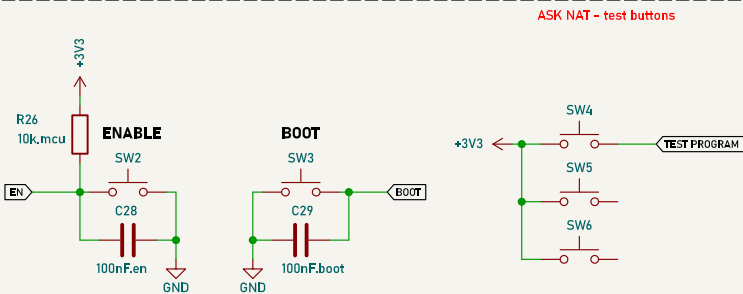
PUSH BUTTONS

Buttons for BOOT and ENABLE functions.

If the device does not support the auto download mode, you need to get into the download mode manually. To do so, press and hold the BOOT button and then press the RESET button once. After that release the BOOT button.

EN=RESET, BOOT=bootloader mode

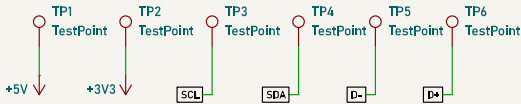
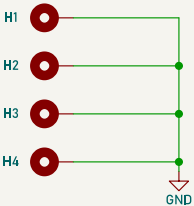
Enable (EN) is the 3.3V regulator's enable pin. It's pulled up, so connect to ground to disable the 3.3V regulator. This means that you can use this pin connected to a pushbutton to restart your ESP32, for example.



MOUNTING HOLES / TEST POINTS

M3 Padded Mounting holes

Mounting holes that are screwed to the threaded holes on the chassis.

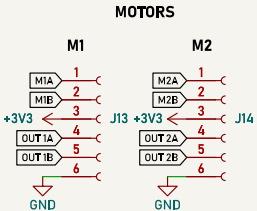


put it at the edge of a PCB for oscilloscope connectrs

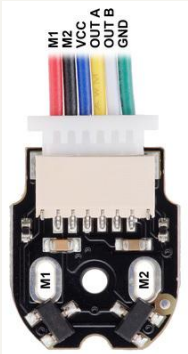
CONNECTORS

6-pin JST SH-style male connector / encoder motors

Pin	Cable color	Function
1	Red	motor power M1 ("+" terminal)
2	Black	motor power M2 (other terminal)
3	Blue	encoder Vcc (2.7 V to 18 V)
4	Yellow	encoder channel A output
5	White	encoder channel B output
6	Green	encoder GND



CHECK IF THE MIRRORING IS NECESSARY CHECK CHECK CHECK



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