

10

9

R8

CDONE)

CRESET

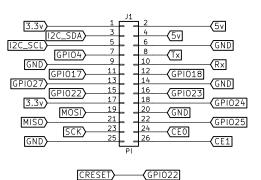
U1D

CDONE

CRESET_B

VCCIO_2

iCE40-LP384-SG32



(GPI024

3.3v

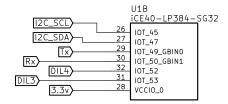
GND)

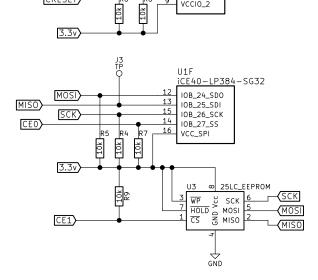
X1 →

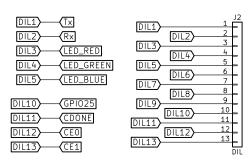
×1 EN MOUT 3

DIL10

ASE−16.000MHz







CDONE)

