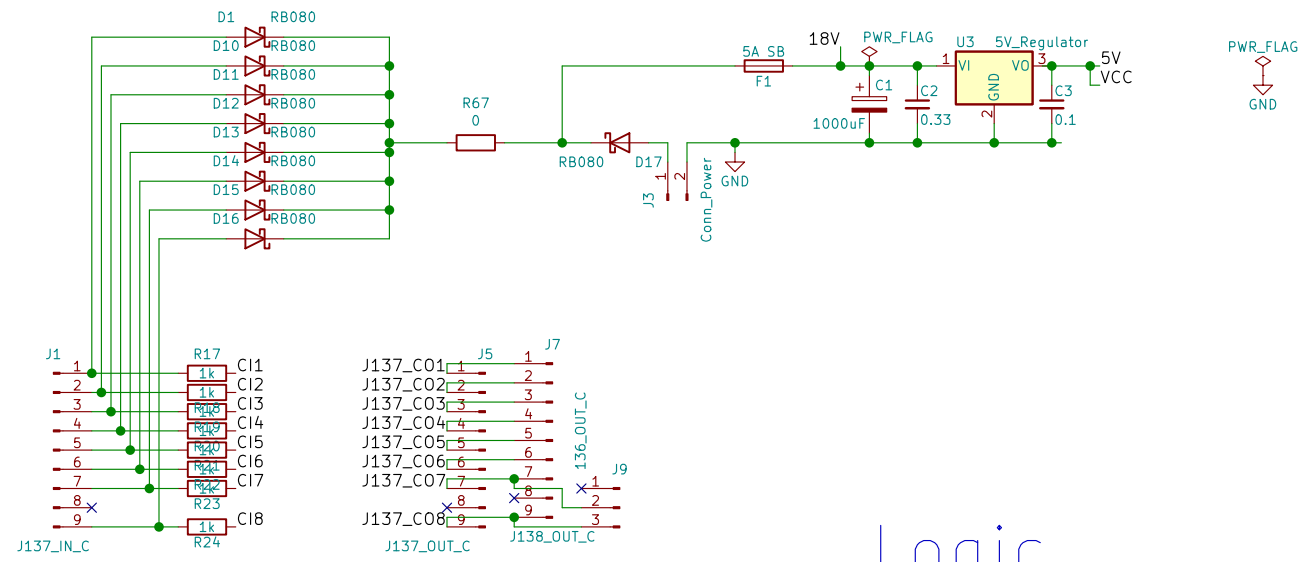
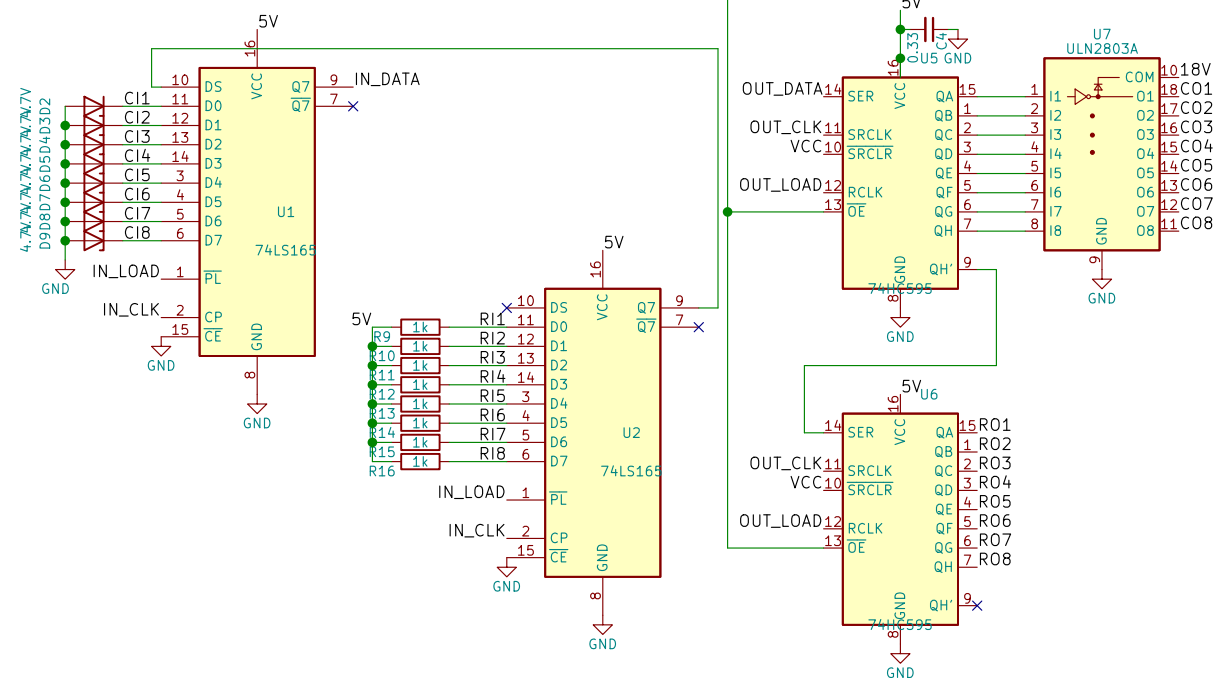
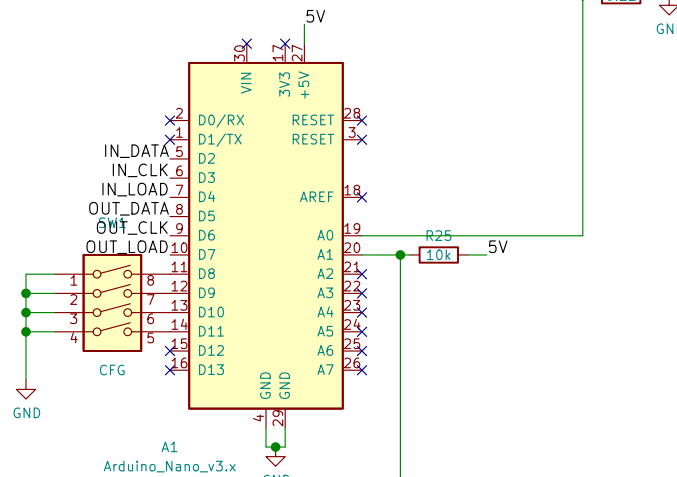
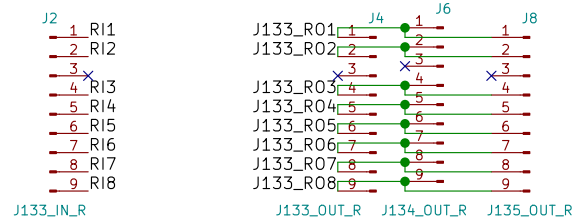


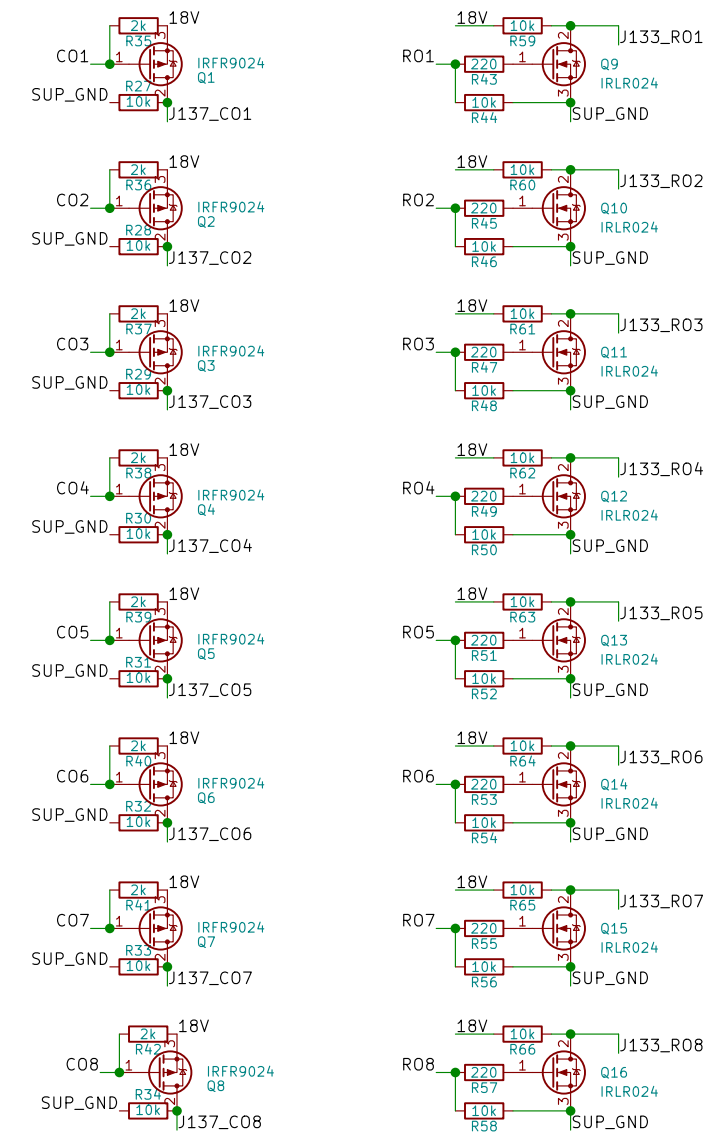
Power



Logic



Output
Columns Rows



Test Points

