

NV11, 2MX32 SDR SDRAM, DUAL RGB, EXTERNAL DVI-I, AGP4X, TV I/F.

PCI DEVICE ID 0X0=0X110 FOR NV11.

STRAPS: AGP4X, FAST WRITE ENA, ADP BIOS, NORMAL PCIAD, 14.318MHZ
STRAP_RAM_TYPE [5:2]=0000-2MX32 SDR SDRAM

HISTORY REV. A00 BASED ON P39-C00:
ADDED L and C components to SII164 PVCC1 and PVCC2

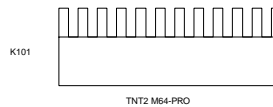
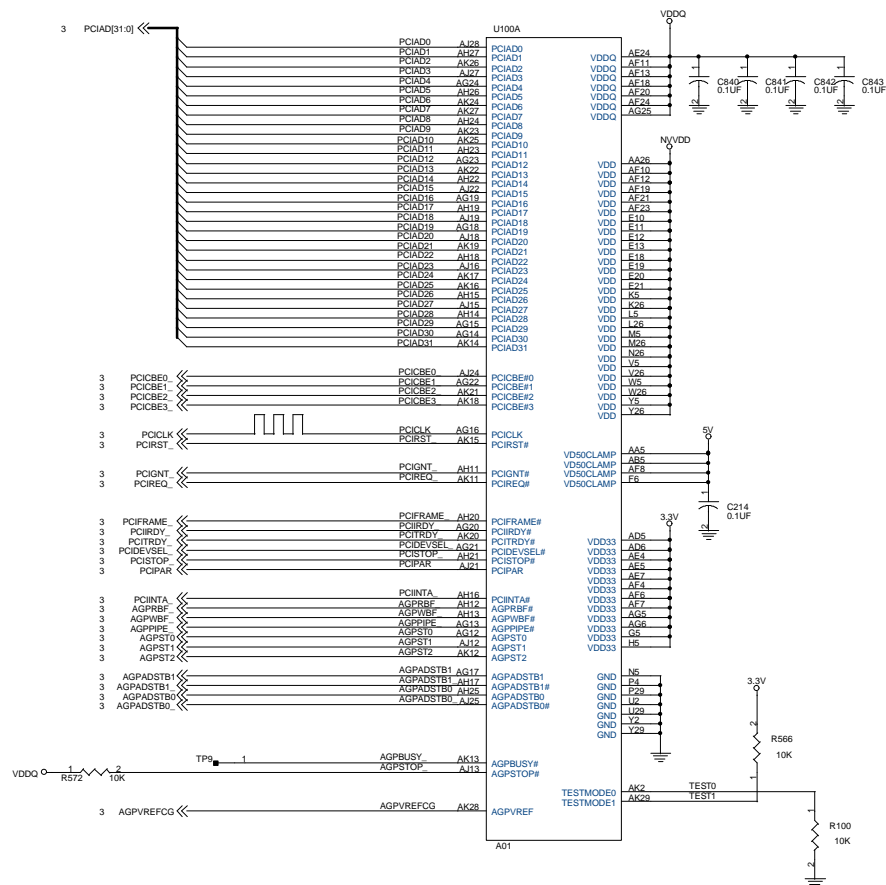


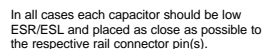
Micro-Star International Co., LTD.

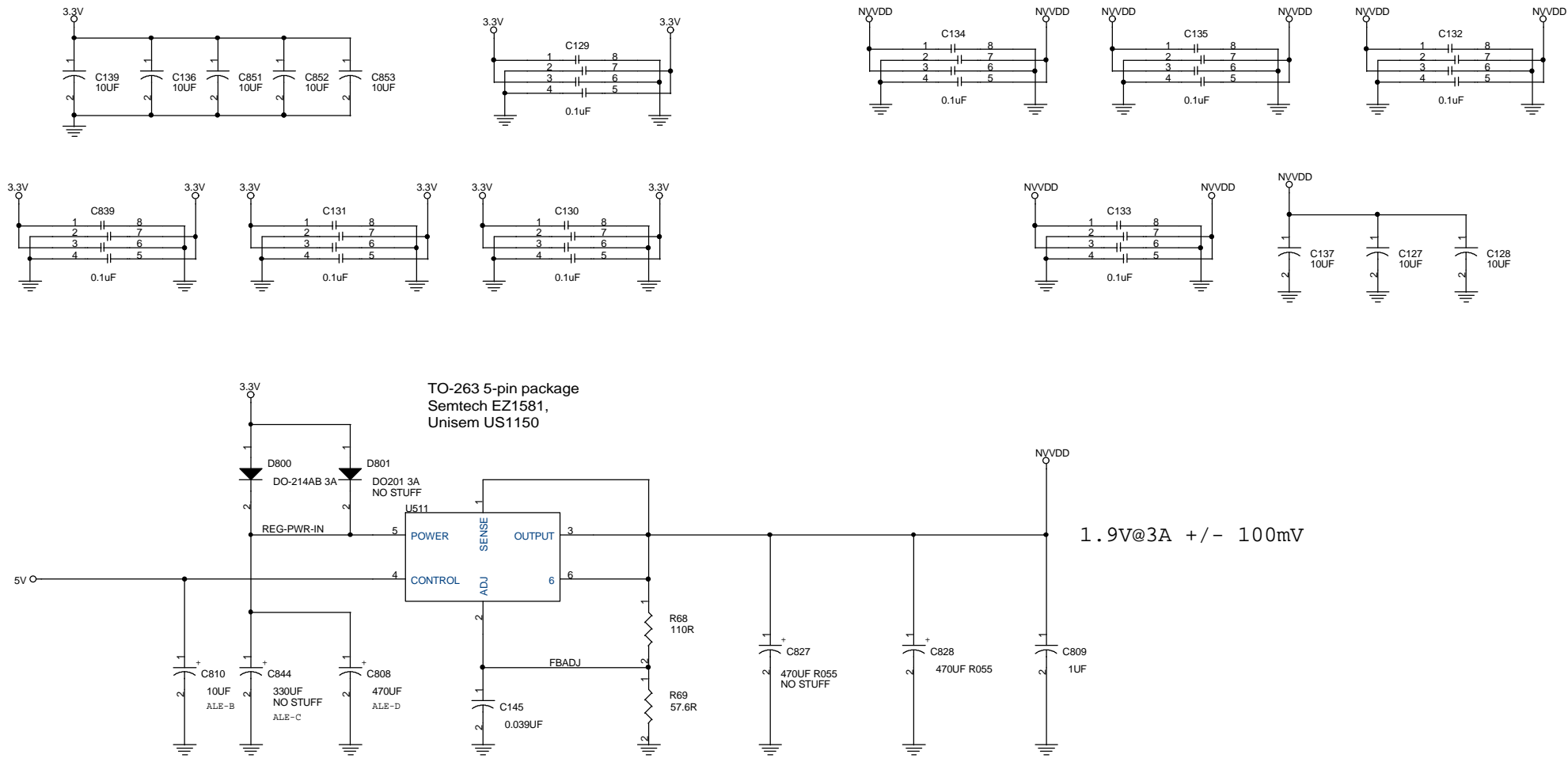
MS-8816 based on P38C01

Size B	Document Number HISTORY	Rev 0C
Date:	Thursday, June 22, 2000	Sheet 1 of 14

{Item}\t{Quantity}\t{Reference}\t{Assembly}{Source Package} {Value}\t
{Assembly}{Source Package} {Value}{PN}{AVL1}{AVL2}{AVL3}{AVL4}{AVL5}







Micro-Star International Co., LTD.

MS-8816 based on P38C01

Size B	Document Number NV11 DECOUPLING	Rev 0C
Date: Thursday, June 22, 2000	Sheet 4	of 14

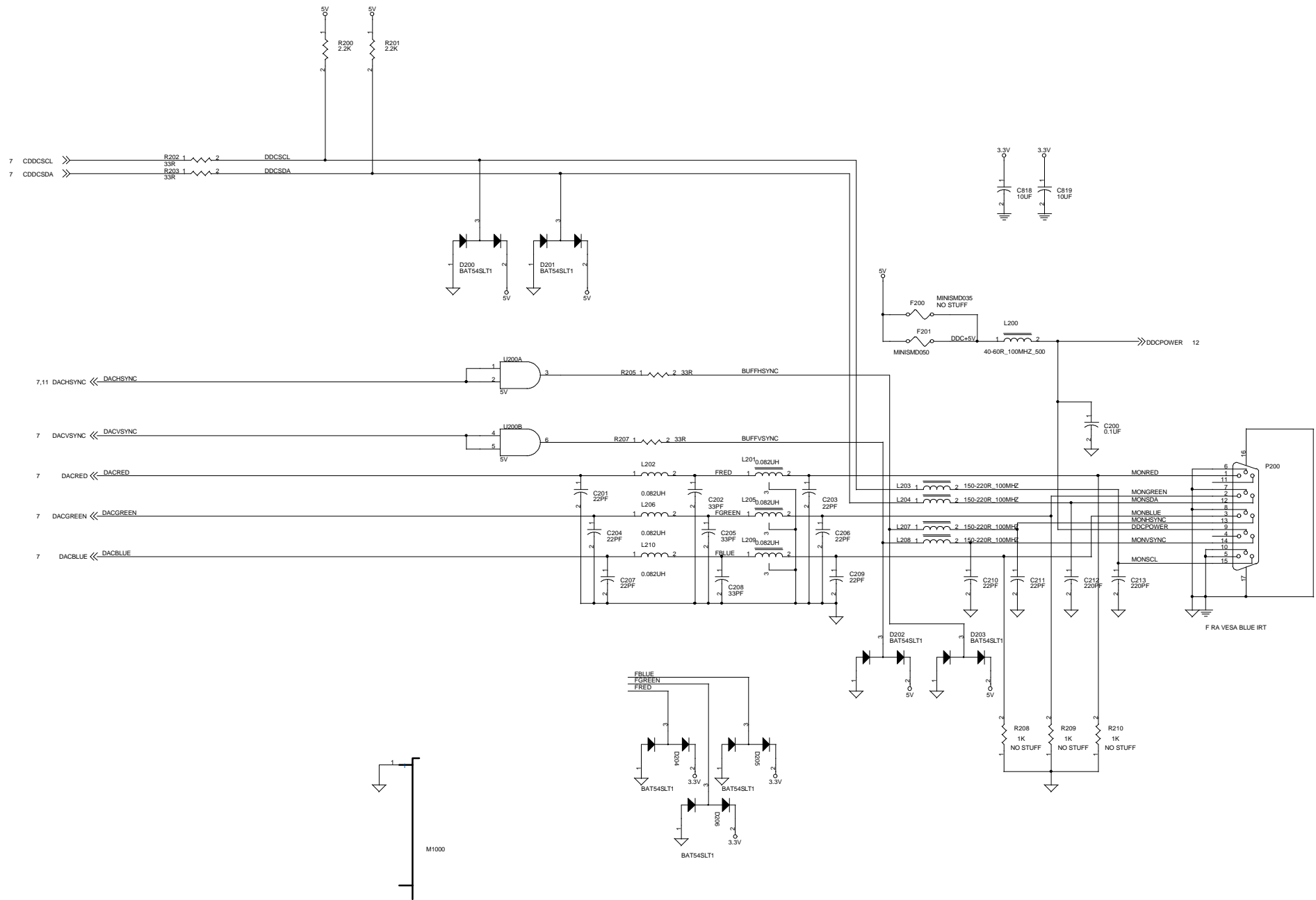
12,13,14 DFPD[11:0] >>
7,11 VIPD[7:0] >>
7,11,13,14 TVED[11:0] >>



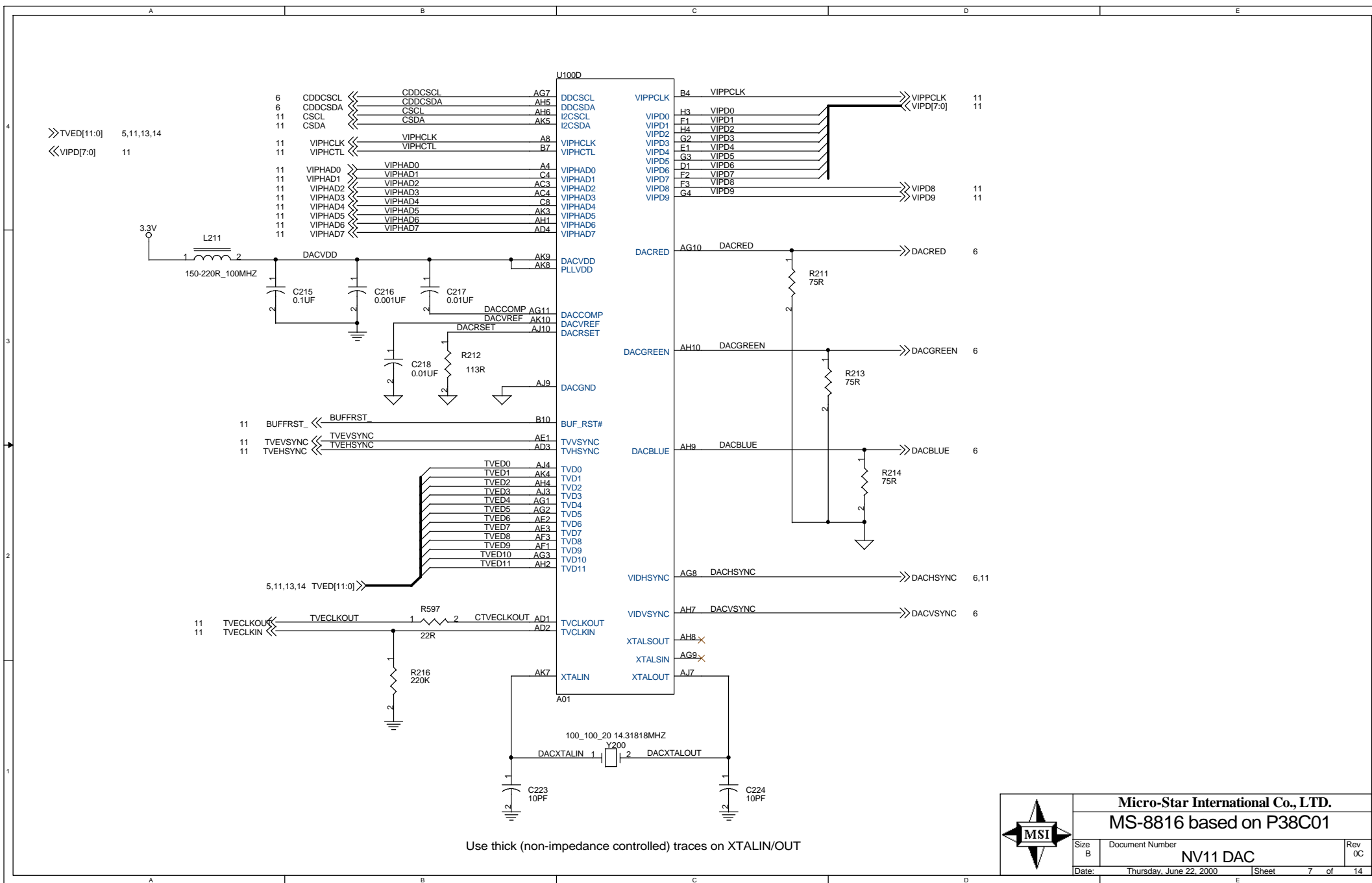
Micro-Star International Co., LTD.

MS-8816 based on P38C01

Size B	Document Number NV11 HOST STRAPS	Rev 0C
Date:	Thursday, June 22, 2000	Sheet 5 of 14



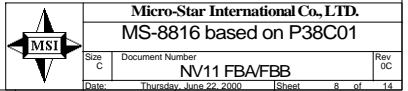
BRACKET WITH RIB VGA (3.235), SVIDEO (2.279)

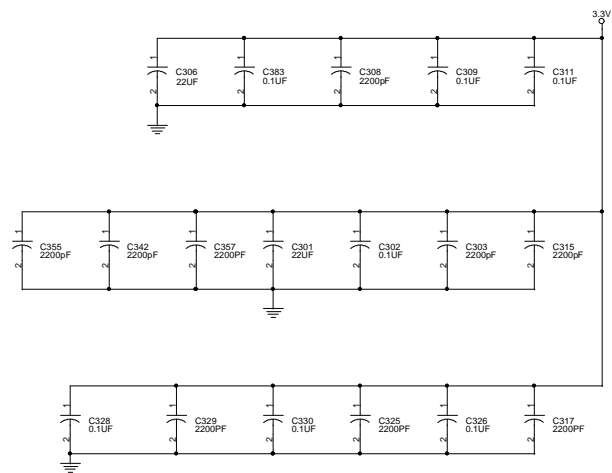
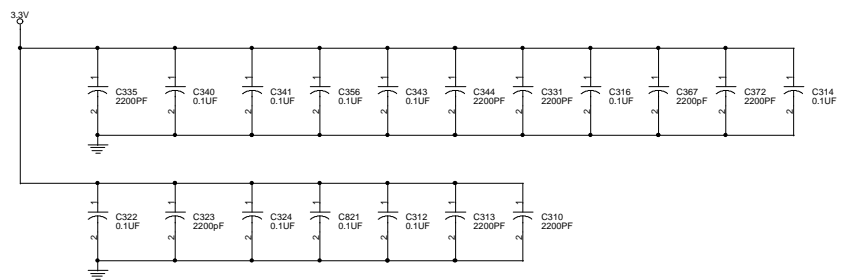


Micro-Star International Co., LTD.

MS-8816 based on P38C01

Size B	Document Number NV11 DAC	Rev 0C
Date: Thursday, June 22, 2000	Sheet 7 of 14	





Micro-Star International Co., LTD.
MS-8816 based on P38C01

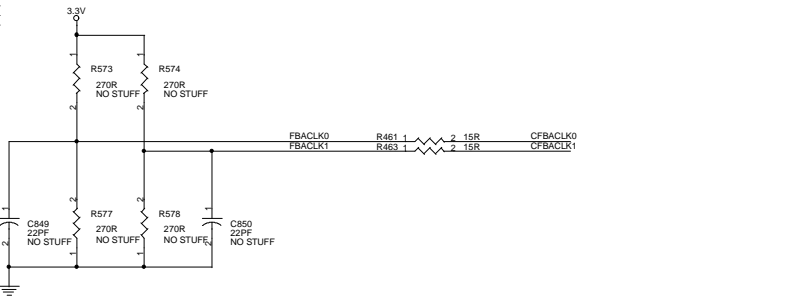
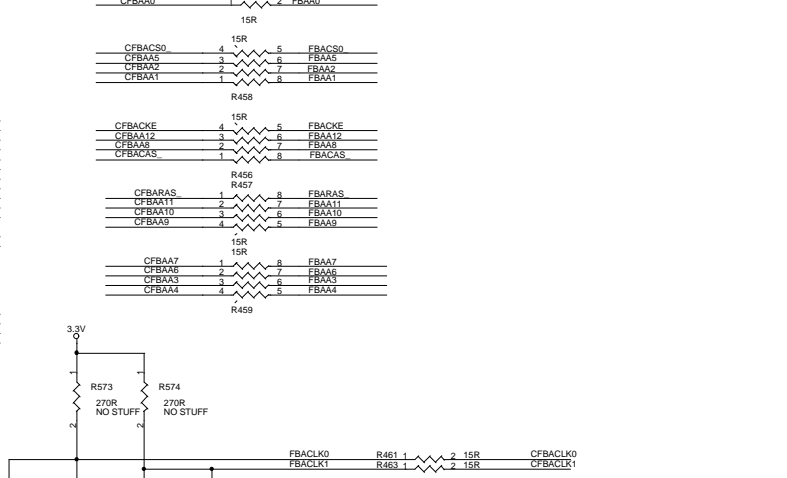
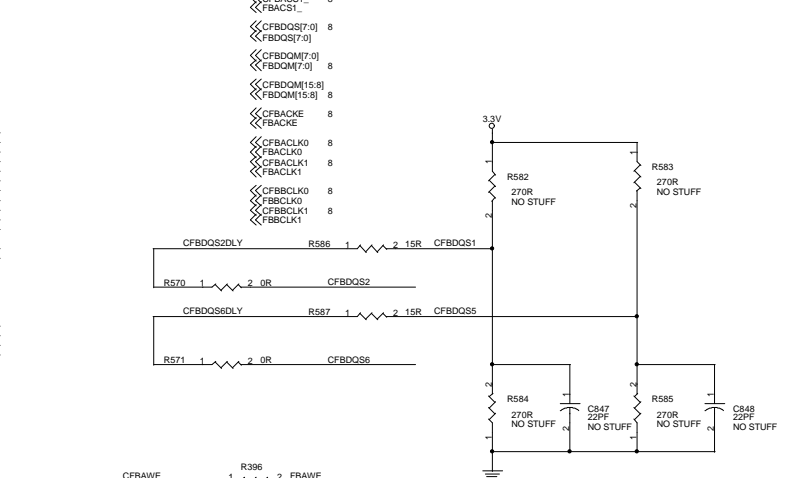
Size
C

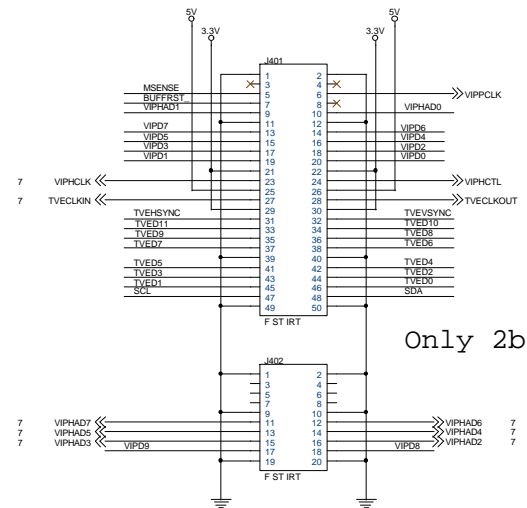
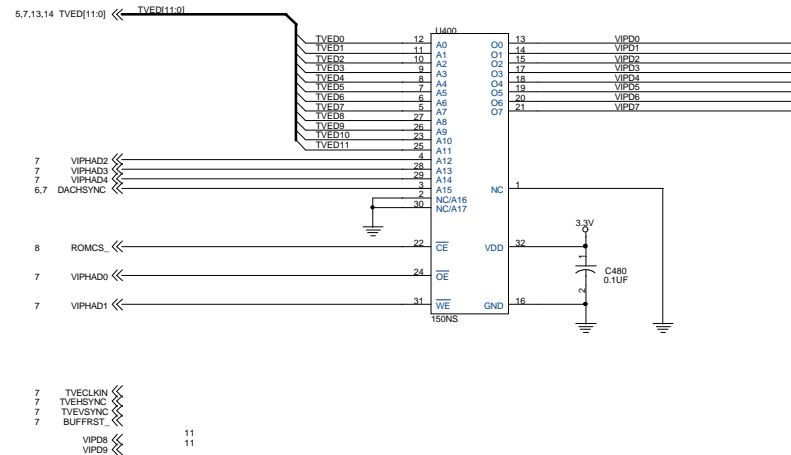
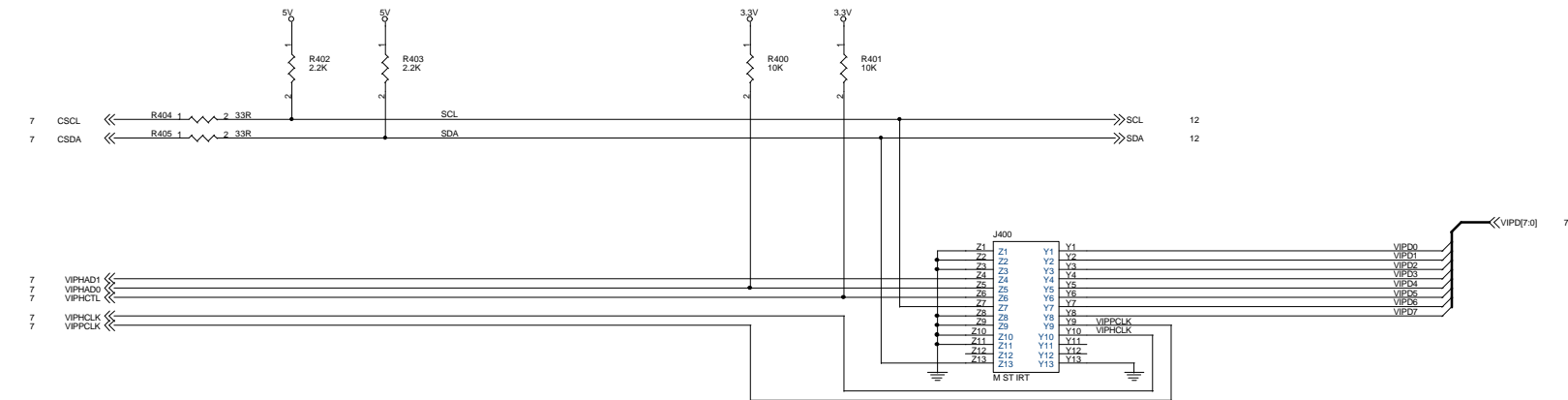
Document Number

FB DECOUPLING

Rev
0C

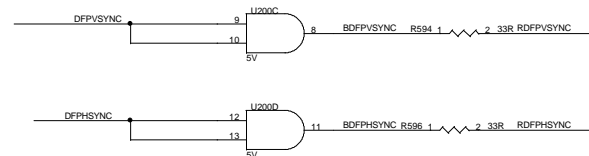
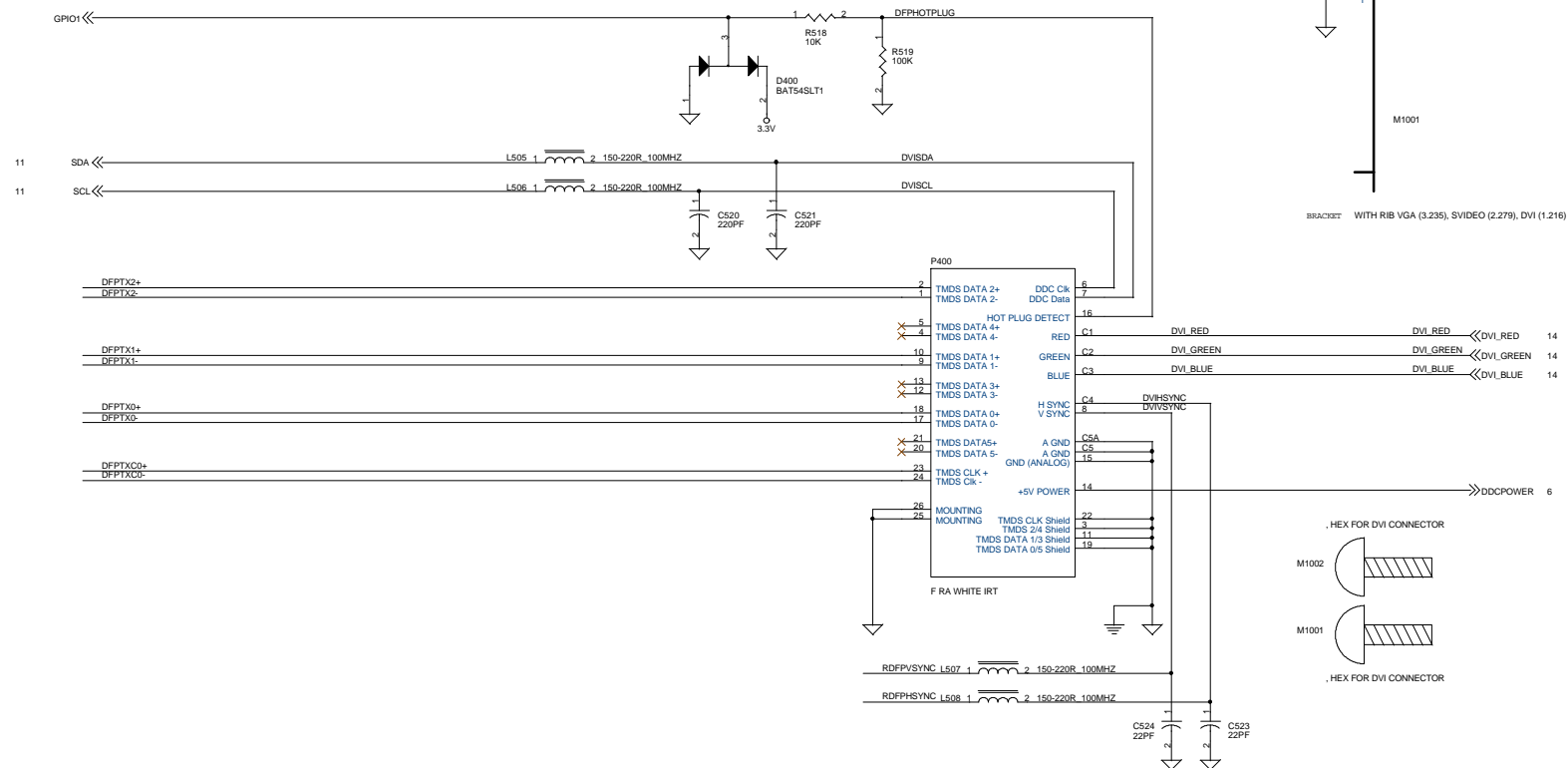
Date: Thursday, June 22, 2000 1 Sheet 9 of 14





Only 2b host/16b data supported.

VIPHAD[7:2]=VIPD[15:10]
(thru pin muxing)



Size C	Document Number DVI-I I/O & TMDS	Rev 0C
Date: Thursday, June 22, 2000	Sheet 12 of 14	

