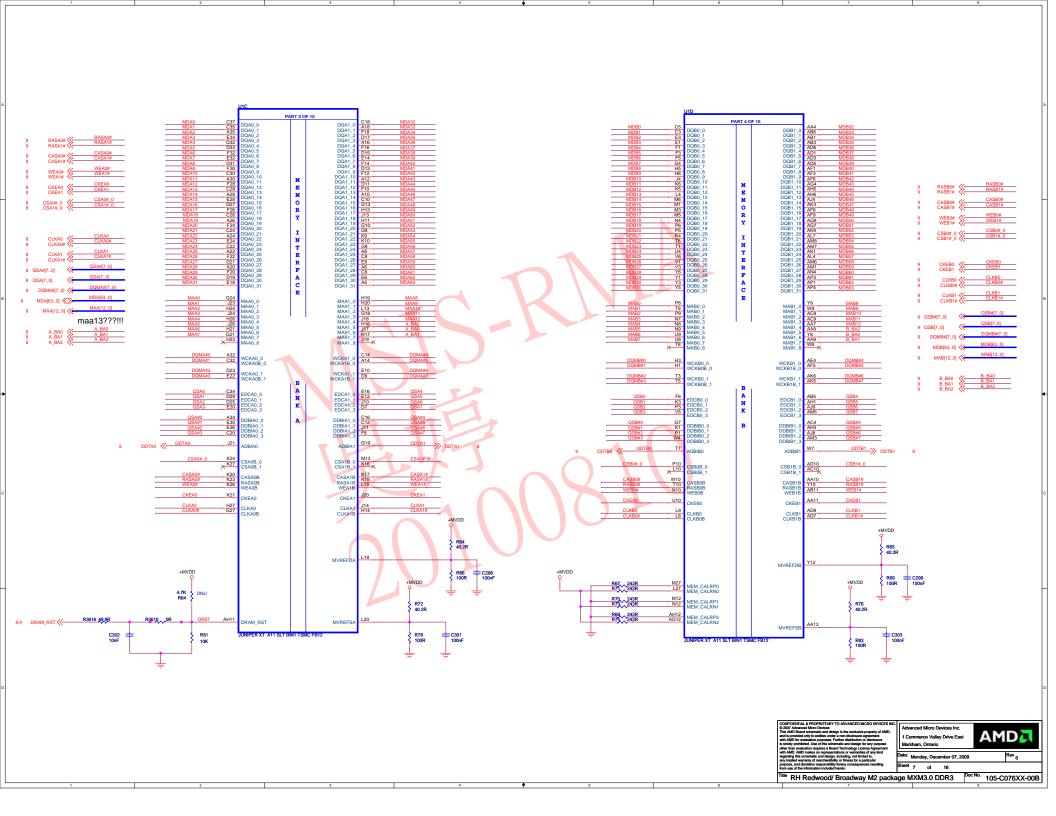


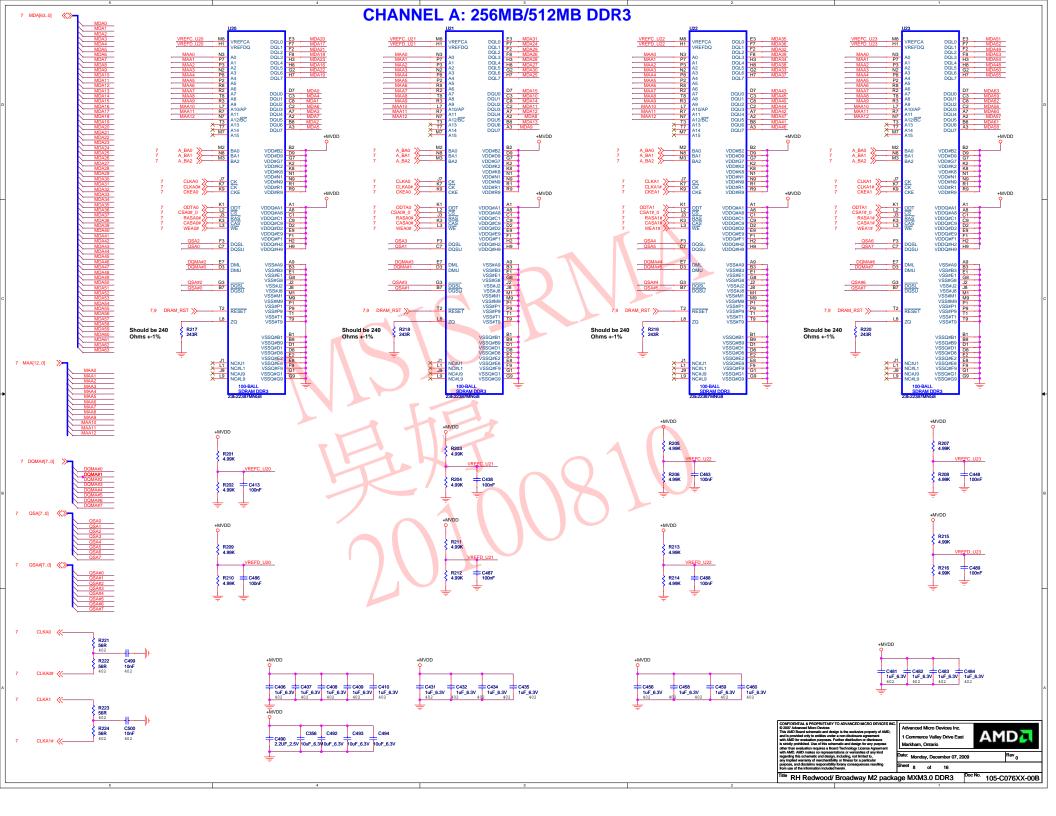
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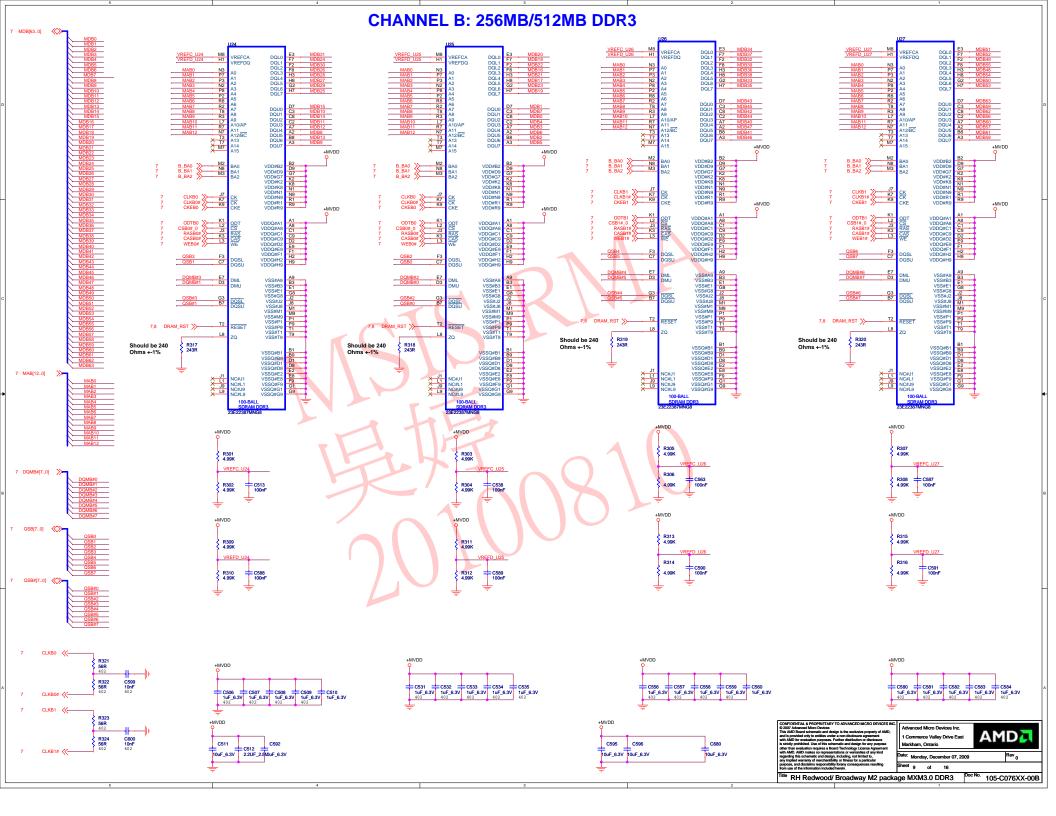
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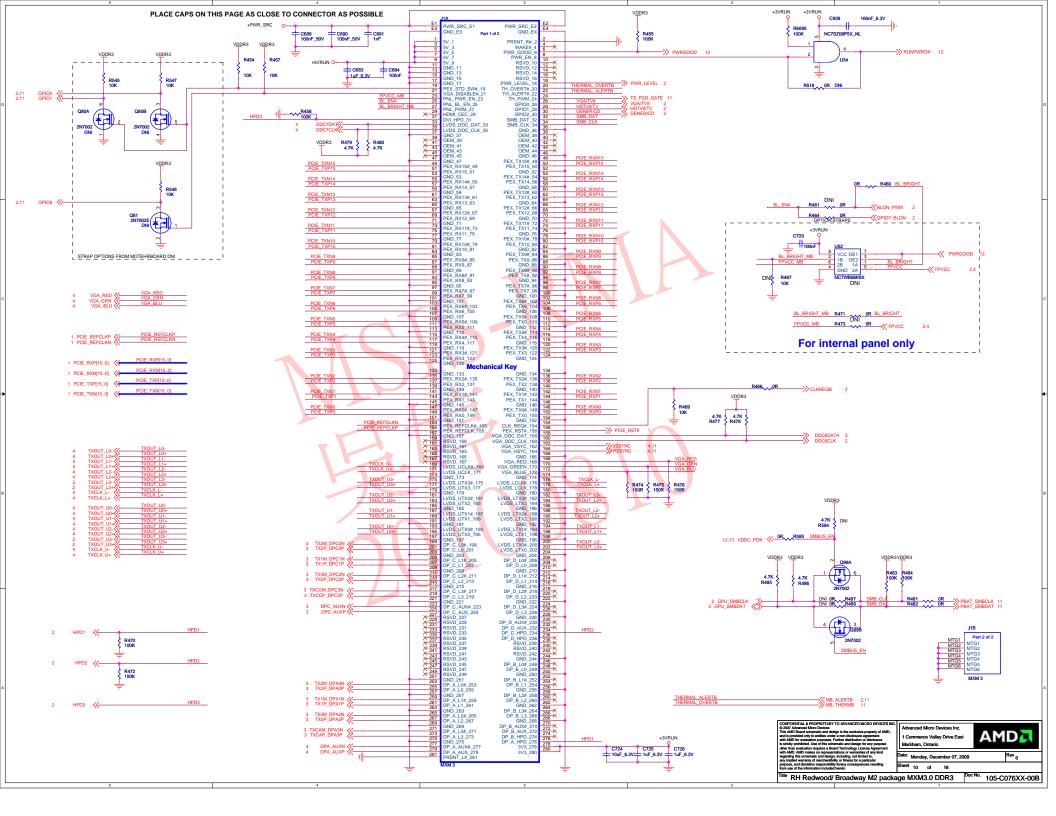


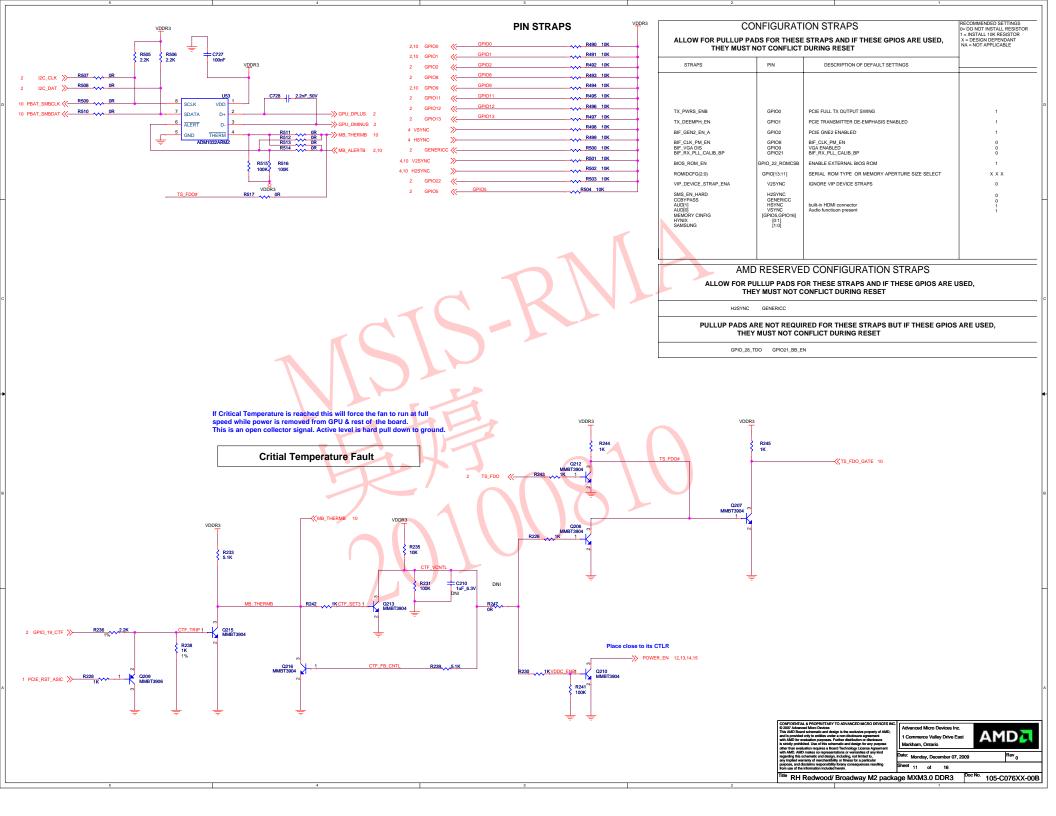
RH Redwood/ Broadway M2 package MXM3.0 DDR3 Doc No. 105-C076XX-00B

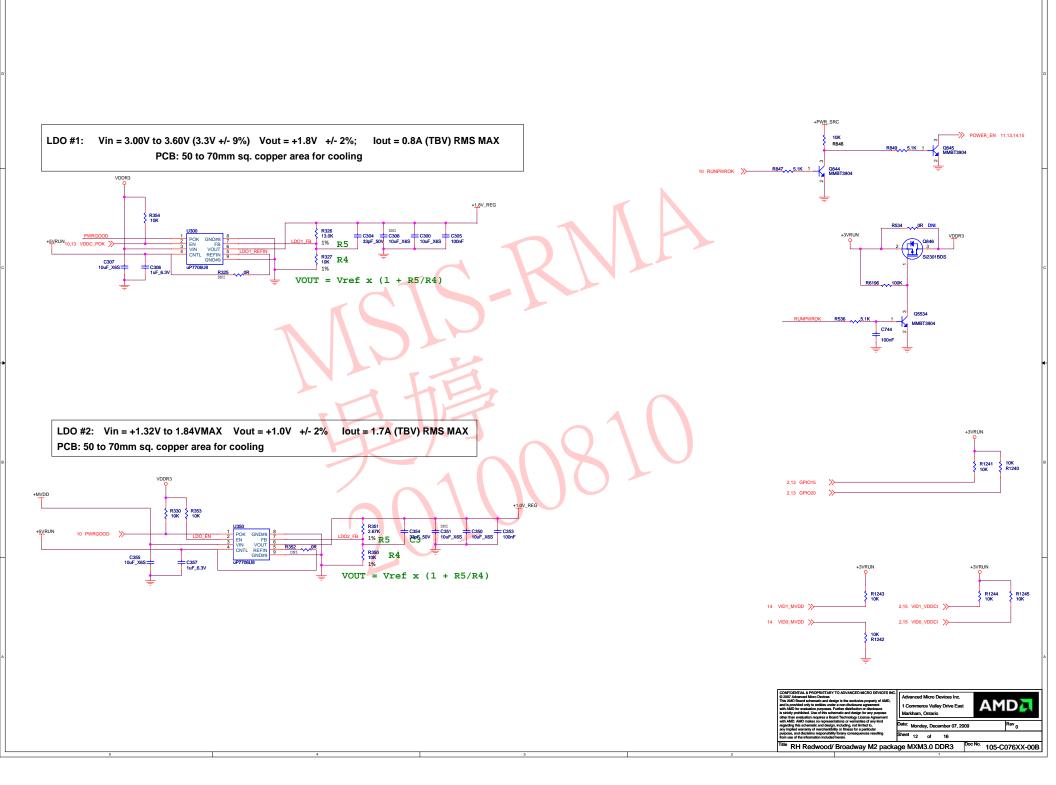


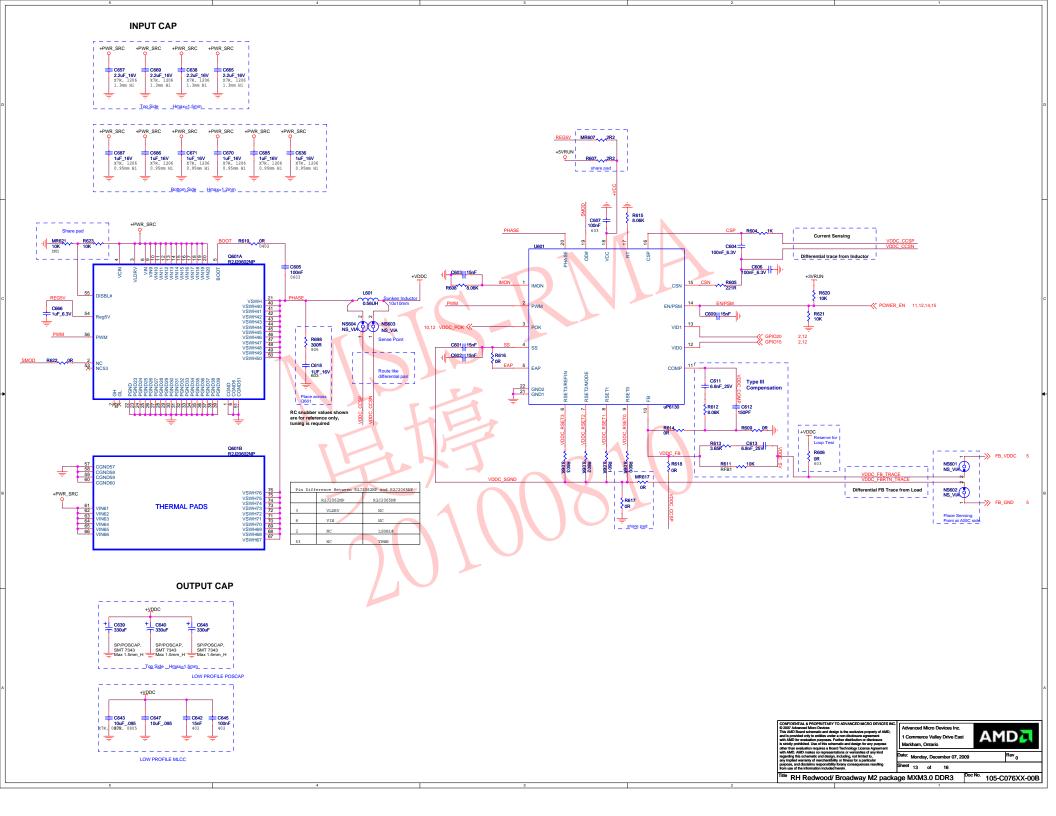


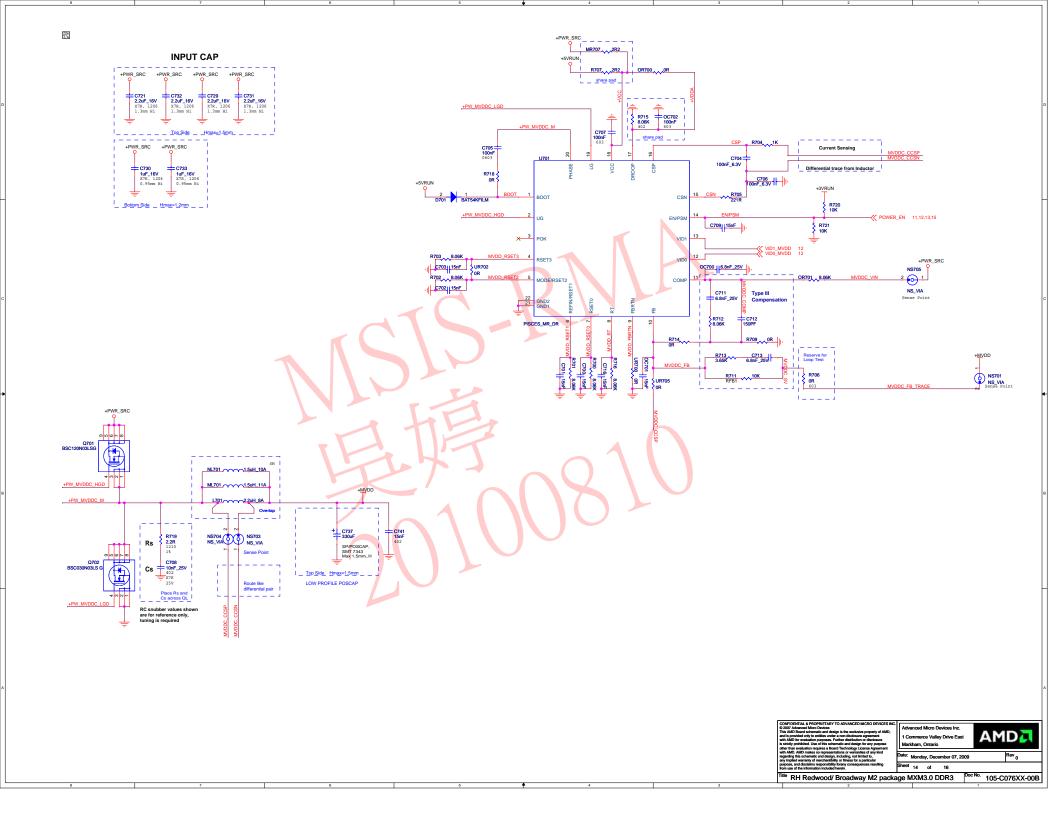


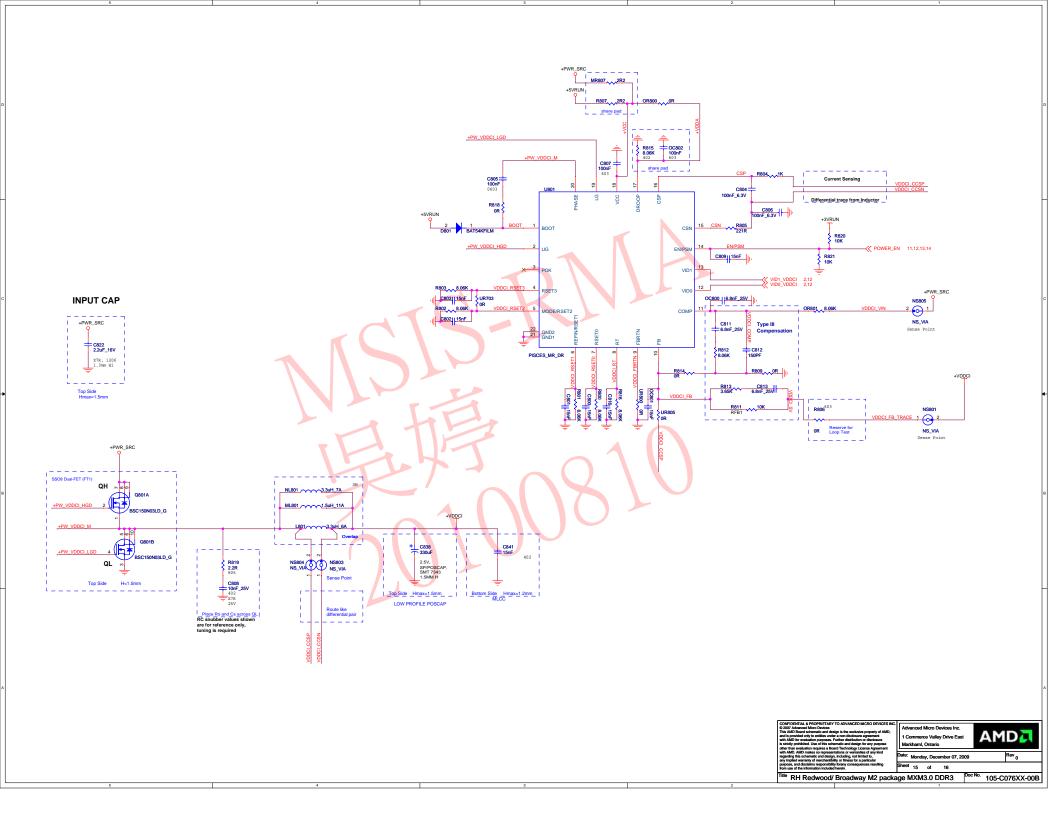












AMD			Title RH Redwood/ Broadway M2 package MXM3	.0 DDR3	Schematic No. 105-C076XX-00B	oroduct specific BOM.		
			REVISION HISTORY NOTE:	This schematic represent For Stuffing options (com	s the PCB, it does not represent any specific ponent values, DNI's,) please consult the esentative to obtain latest BOM closest to the			
Sch Rev	PCB Rev	Date	REVISION DESCRIPTION					
0	00A	09/27/09	Create new SCHs					
	00B	12/03/09	Added PWR_EN short to DrMOS Added DrMOS Thermal Pad					
>				RN 081				
Α.		5	4	3	2		1	