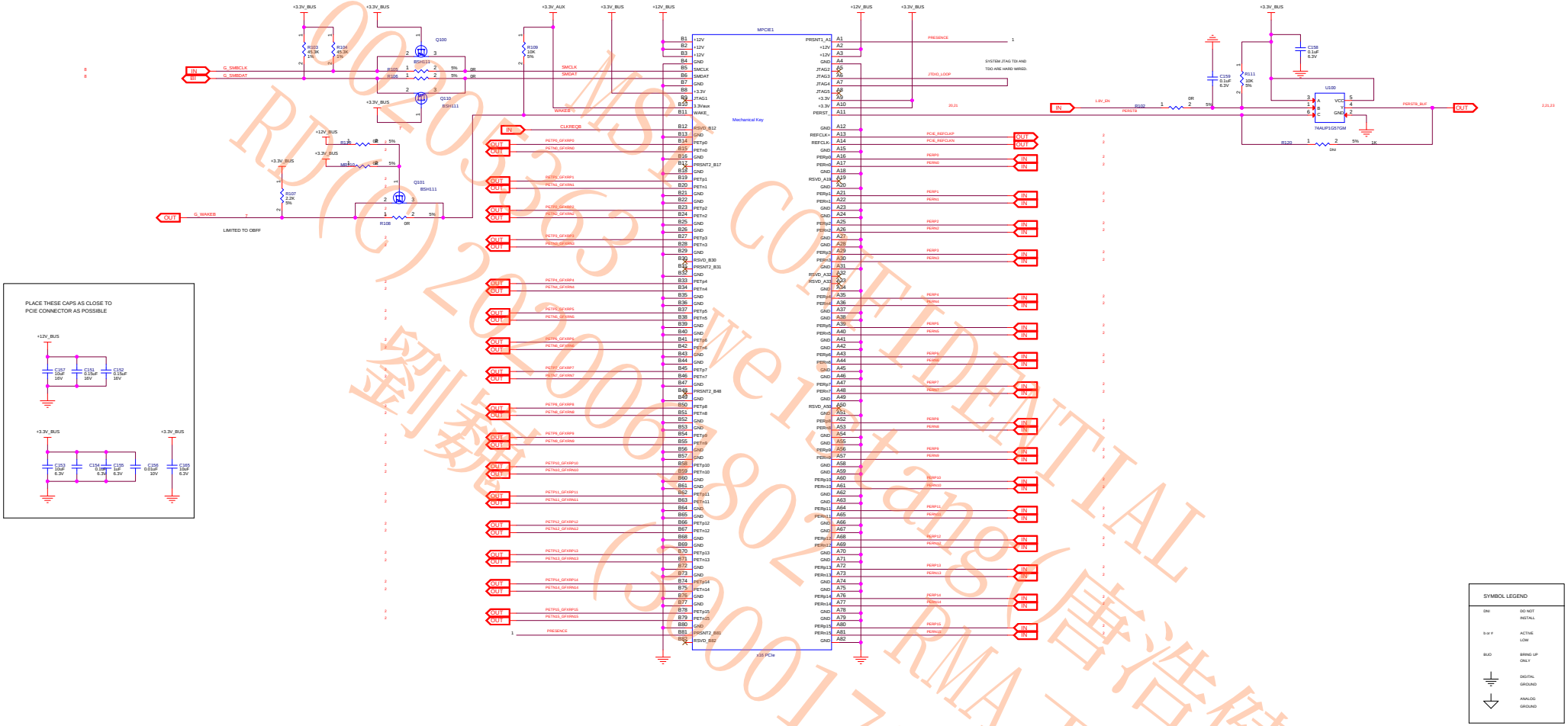
(1) PCI-EXPRESS EDGE CONNECTOR



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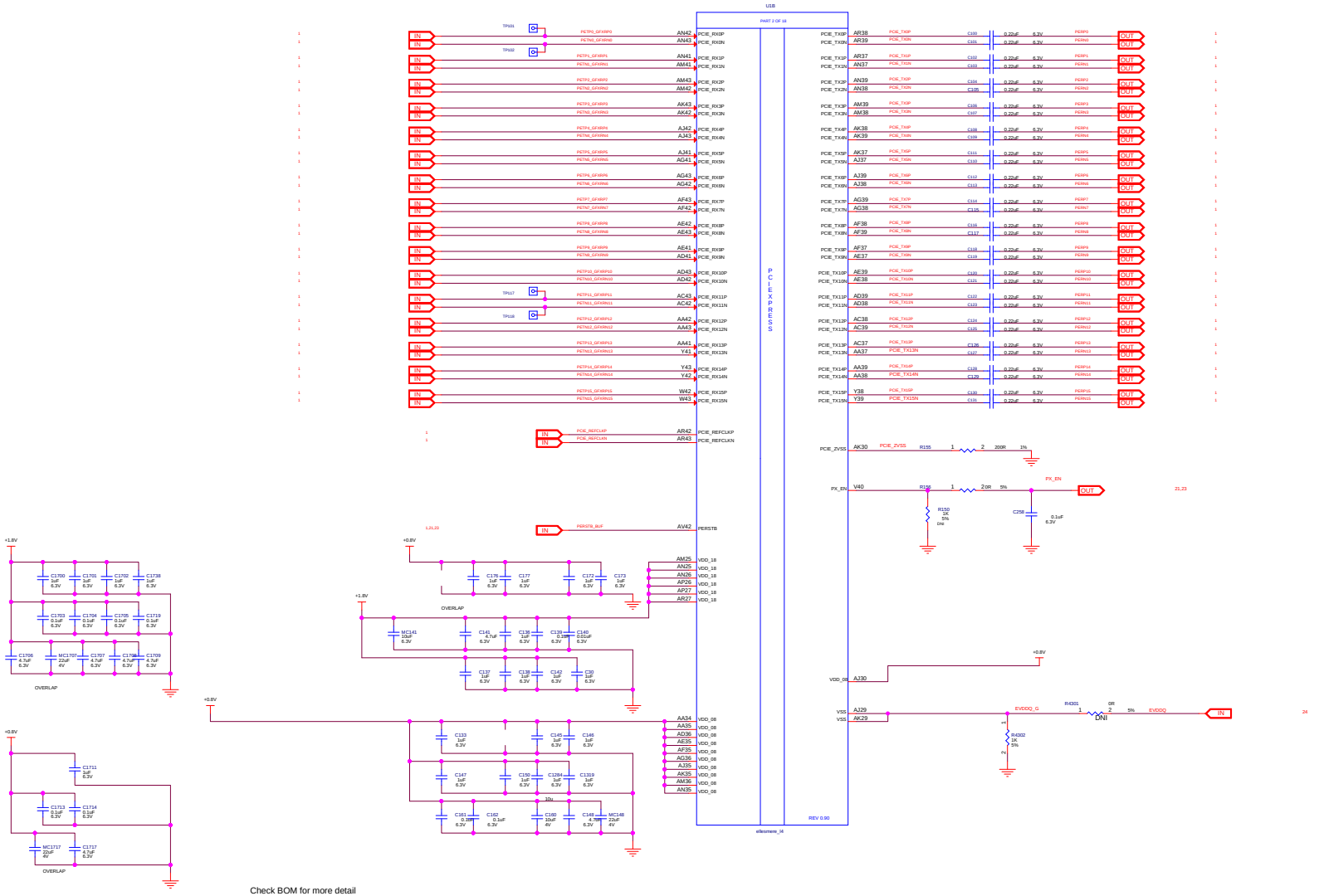
(2) ELLESMERE PCIE INTERFACE



Check BOM for more detail

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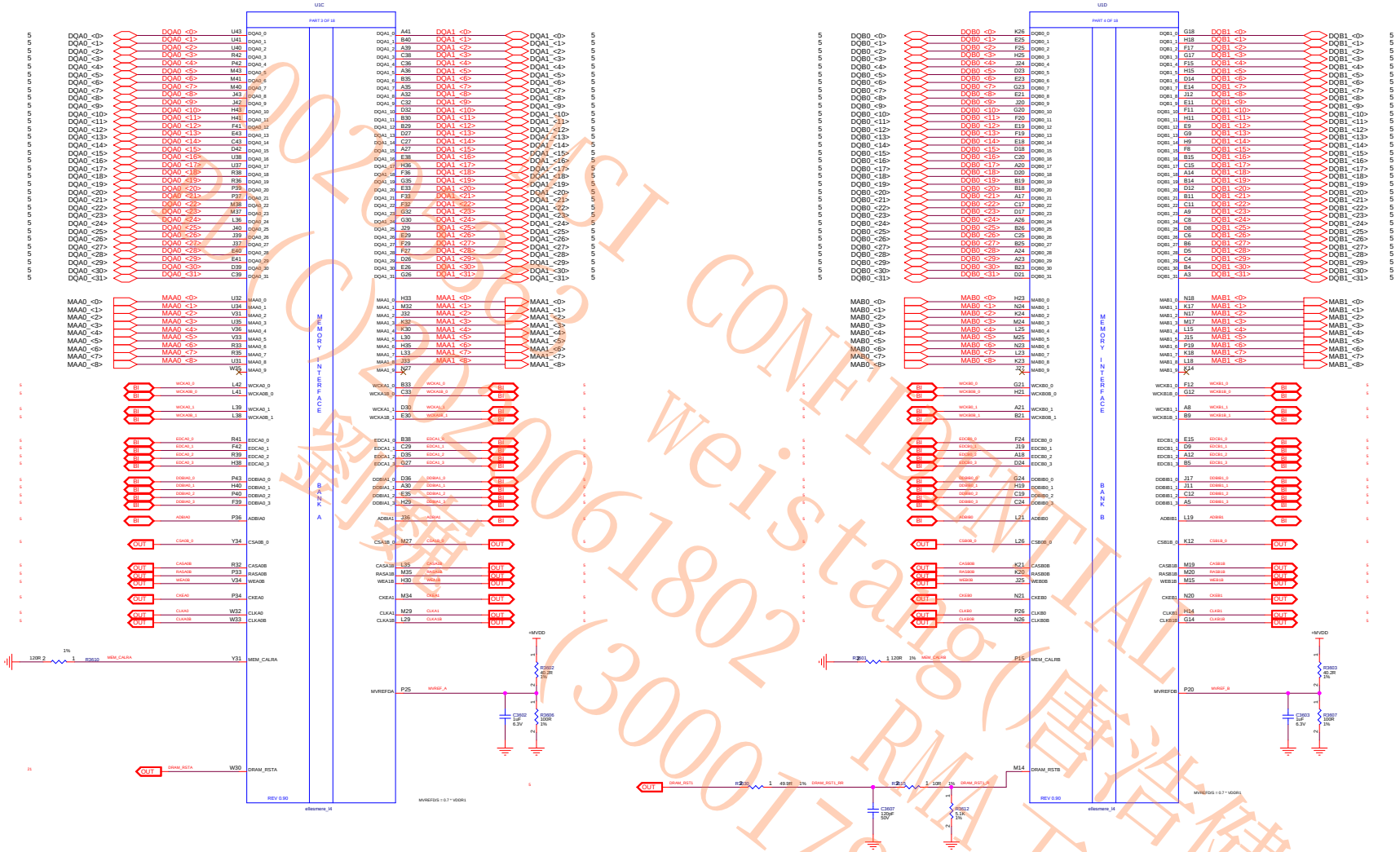
(3) ELLESMERE MEM INTERFACE CH A/B



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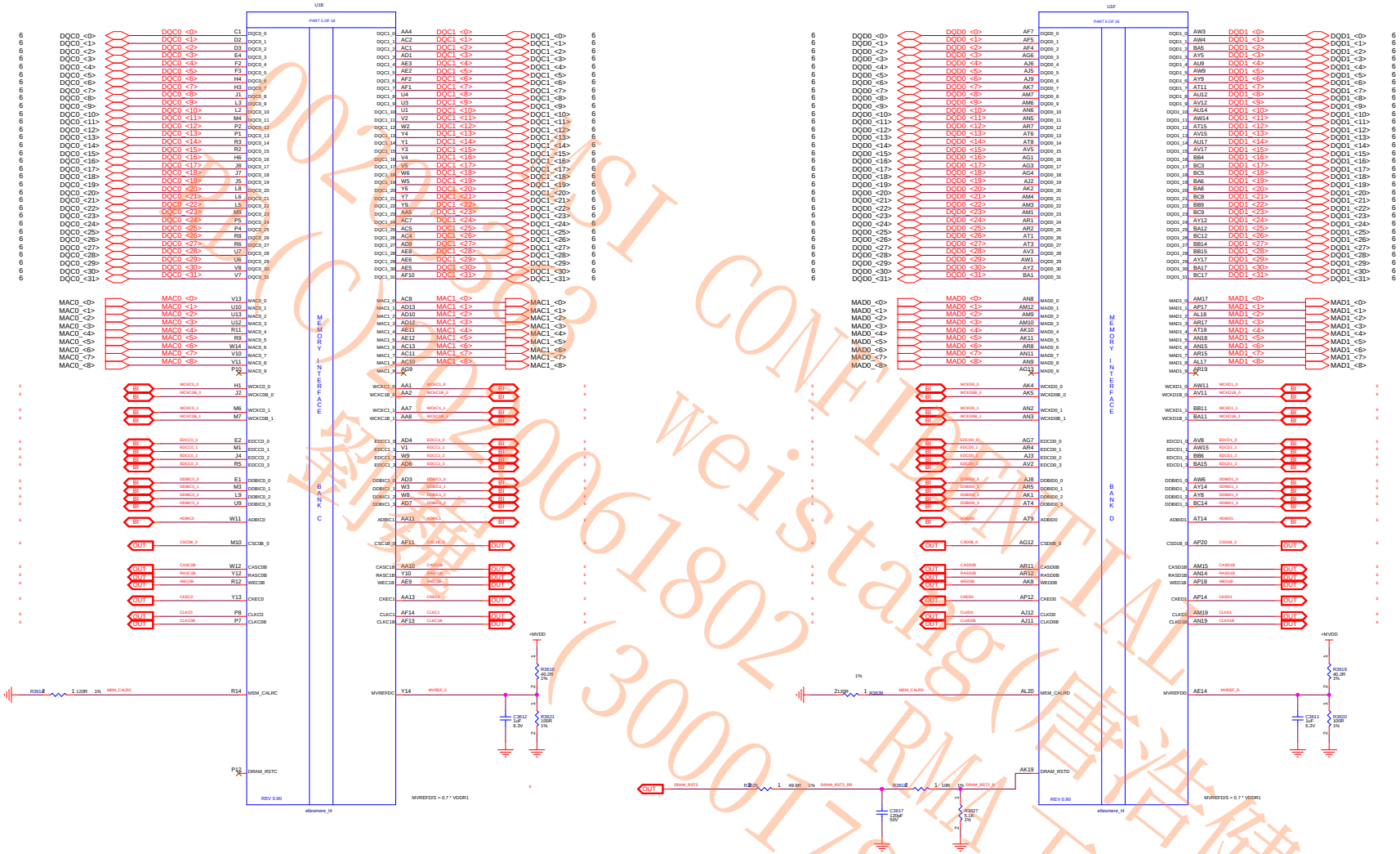
(4) ELLESMERE MEM INTERFACE CH C/D



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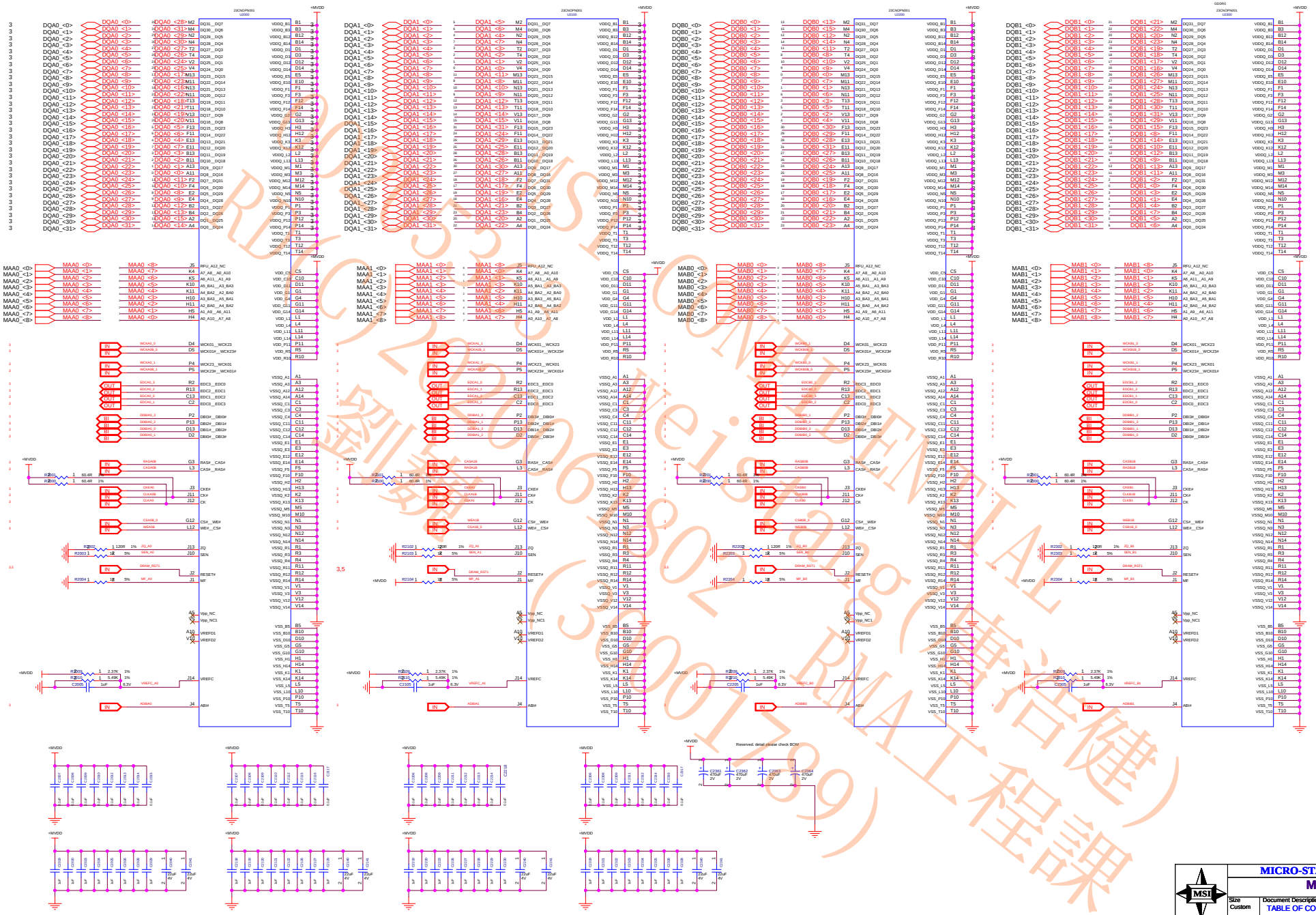
(5) GDDR5 MEMORY CH A/B



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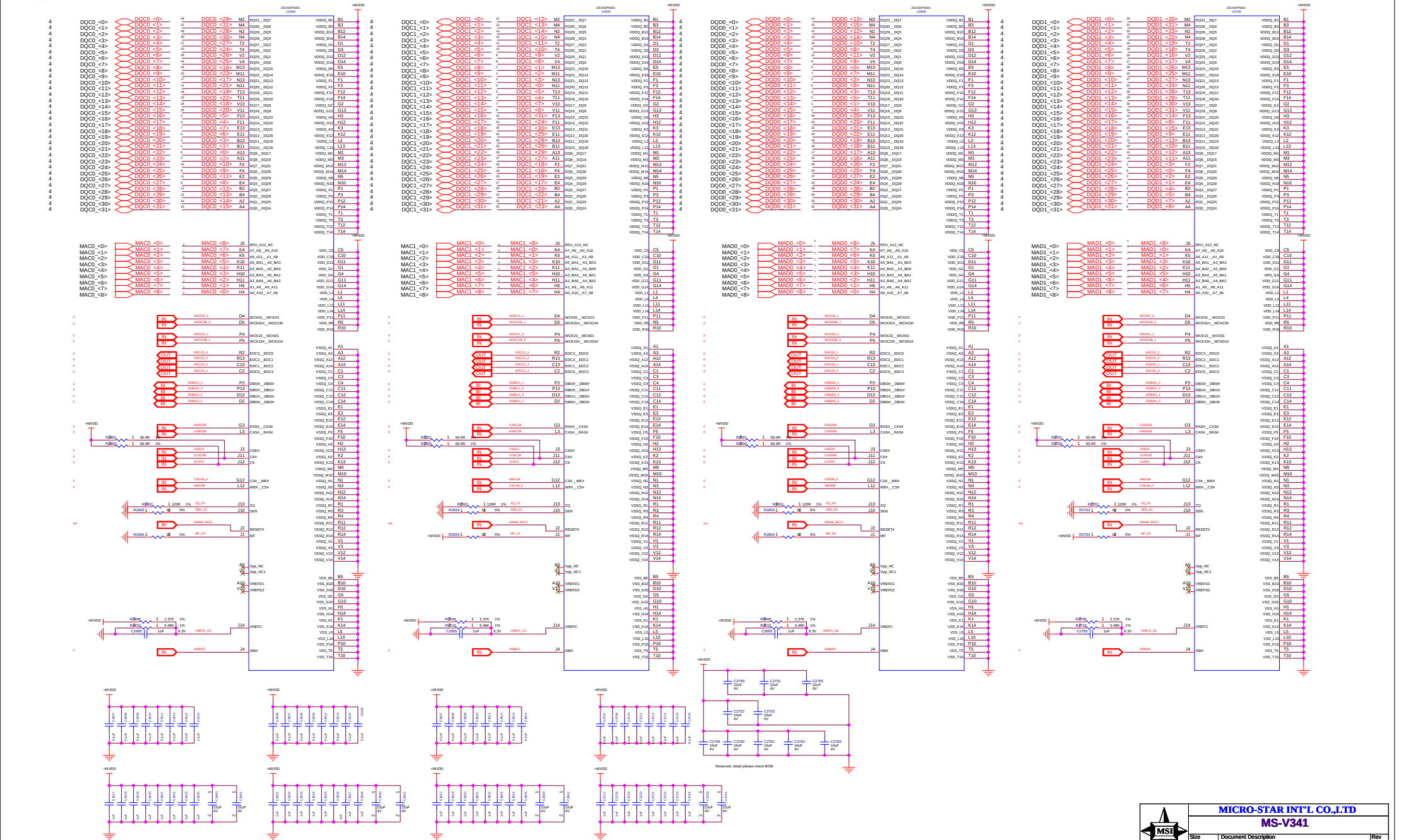
(6) GDDR5 MEMORY CH C/D



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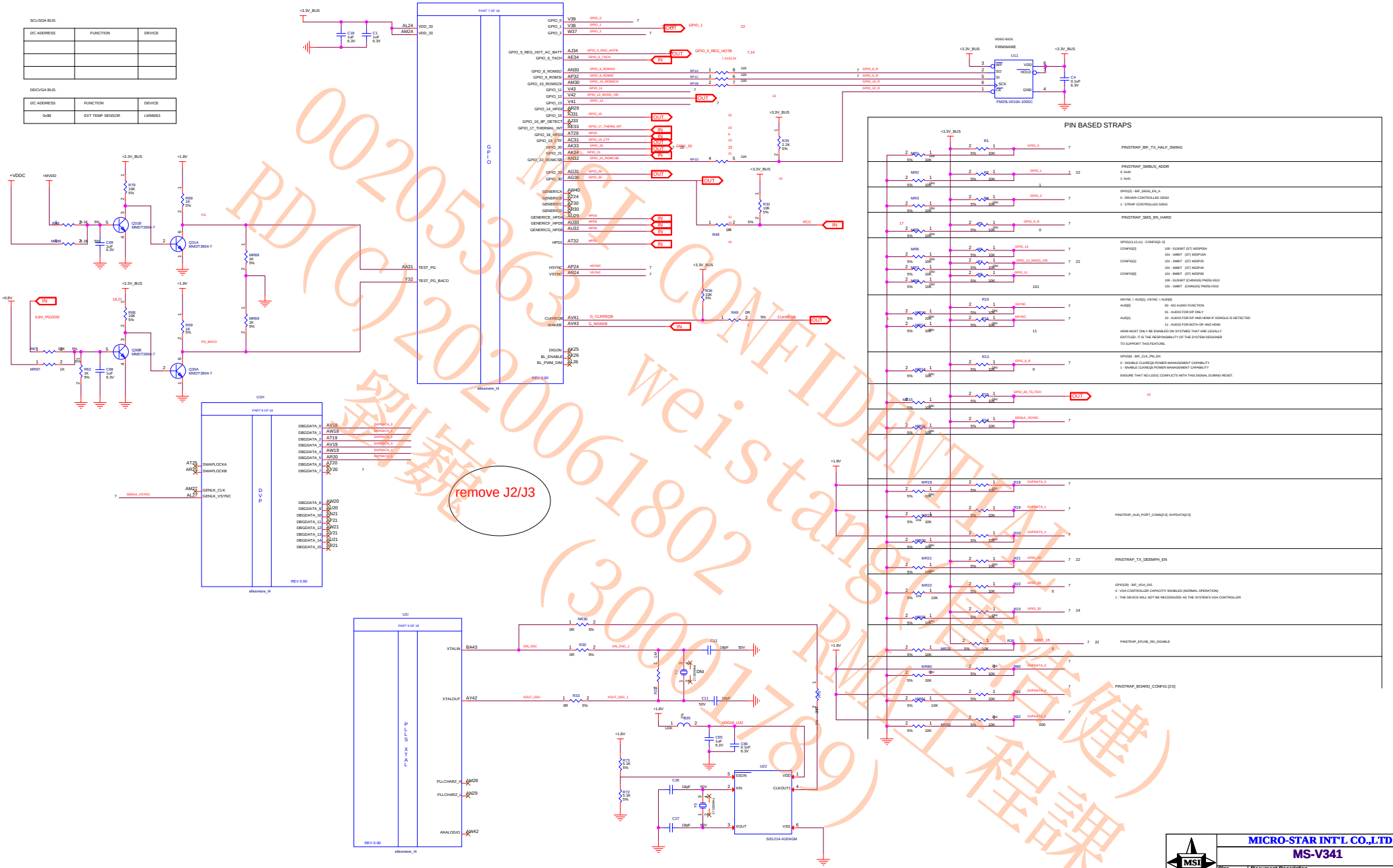
(7) ELLESMERE GPIO STRAP CF XTAL

SCL/SDA BUS:

I2C ADDRESS	FUNCTION	DEVICE

DDC/VGA BUS:

I2C ADDRESS	FUNCTION	DEVICE
0x98	EXT TEMP SENSOR	LM96063



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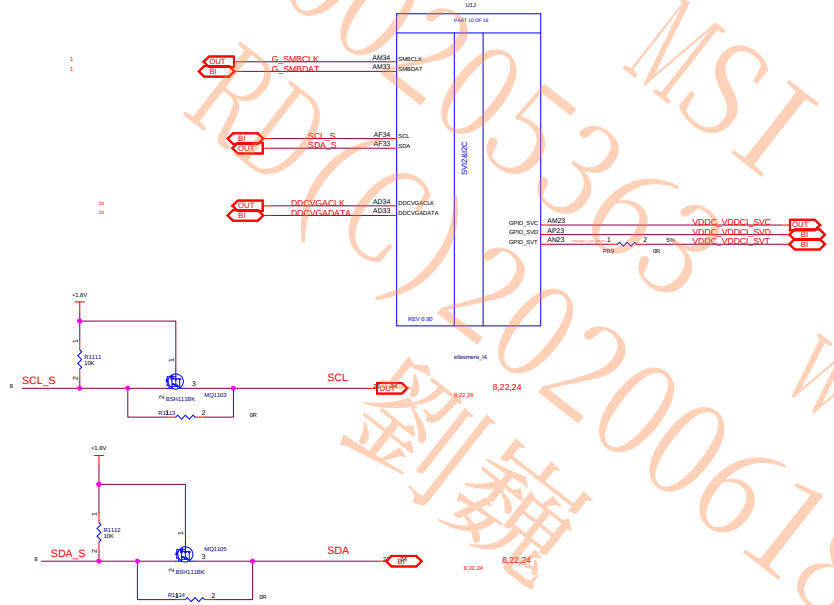
(8) ELLESMERE DAC1 LOCK



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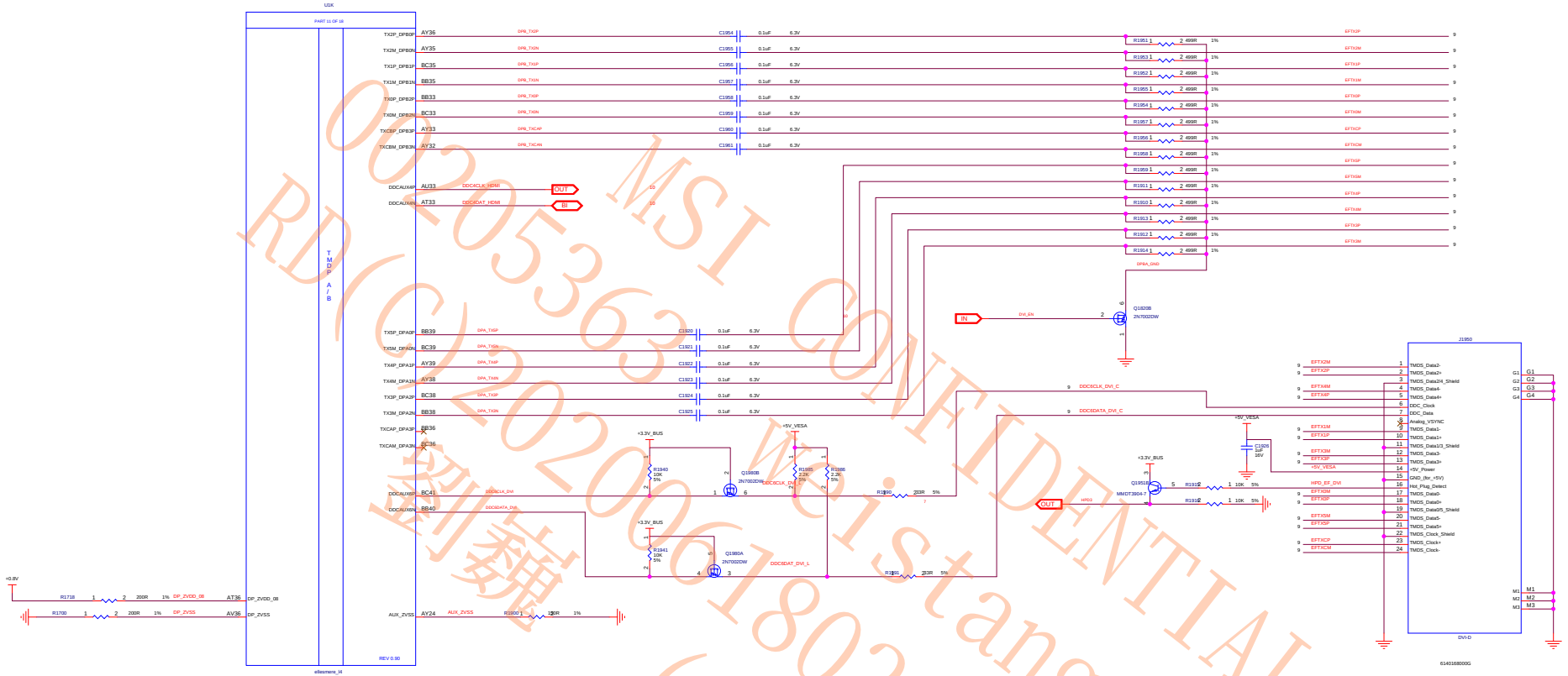
(9) ELLESMERE TMDP A/B



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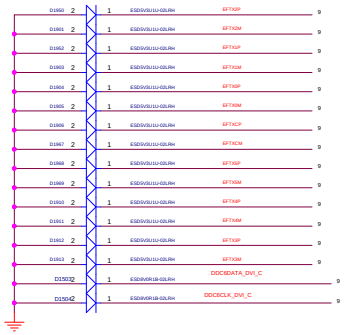

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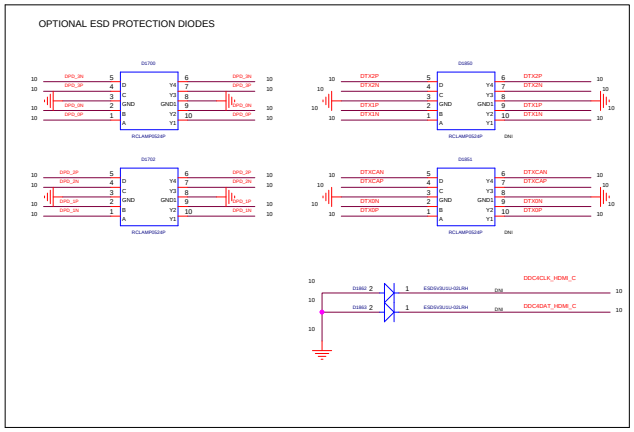
(10) ELLESMERE TMDP C/D



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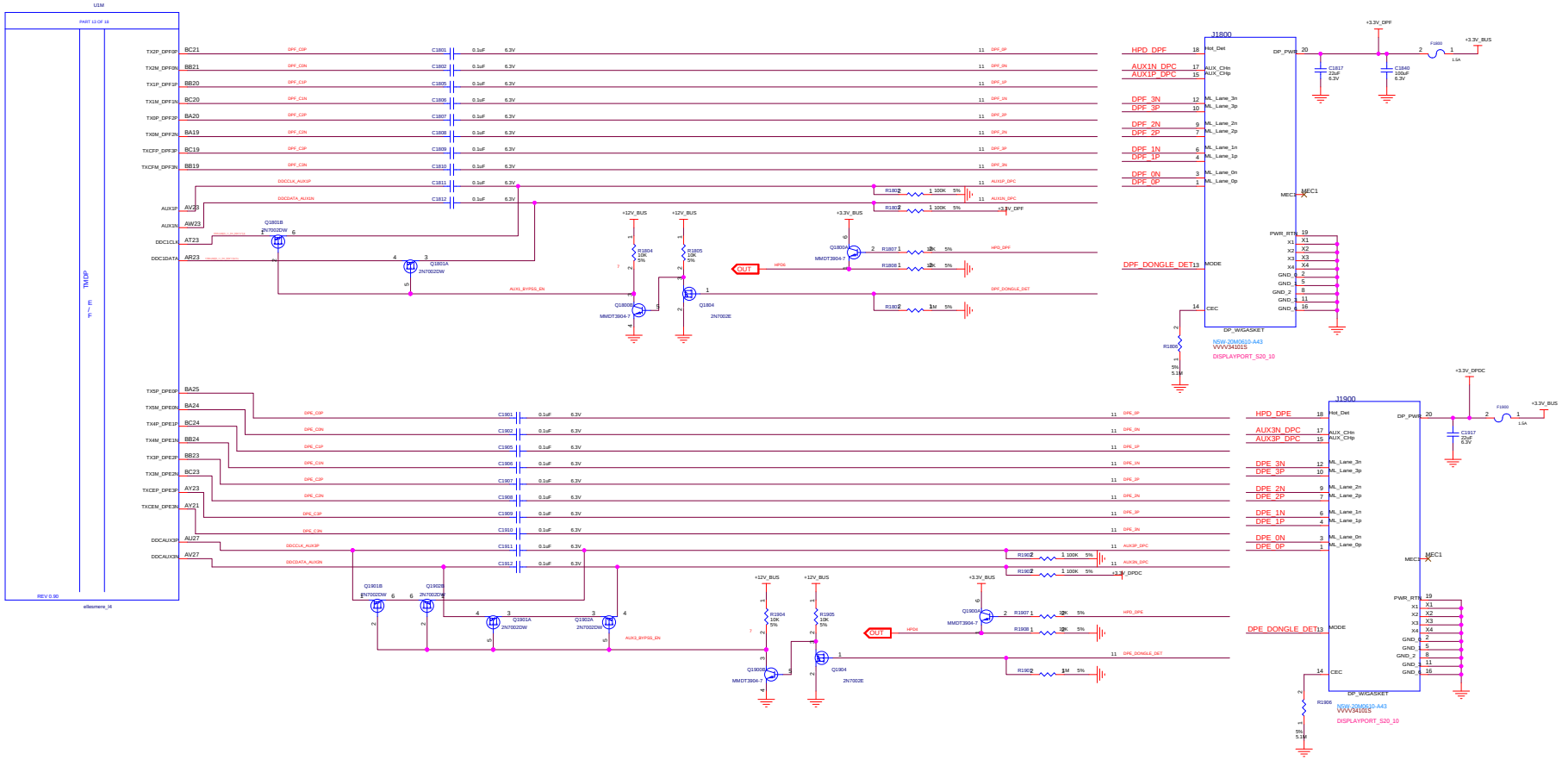
(11) ELLESMERE LVTMDP E/F



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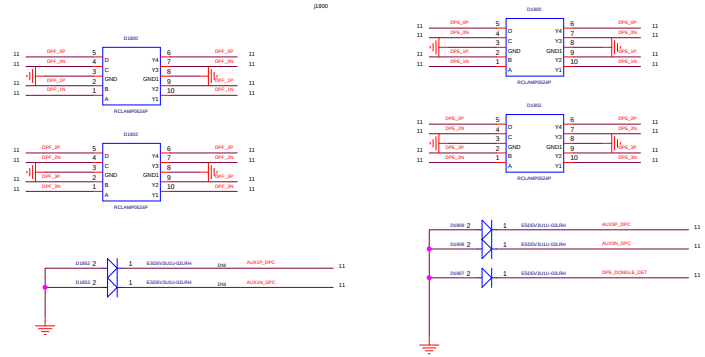

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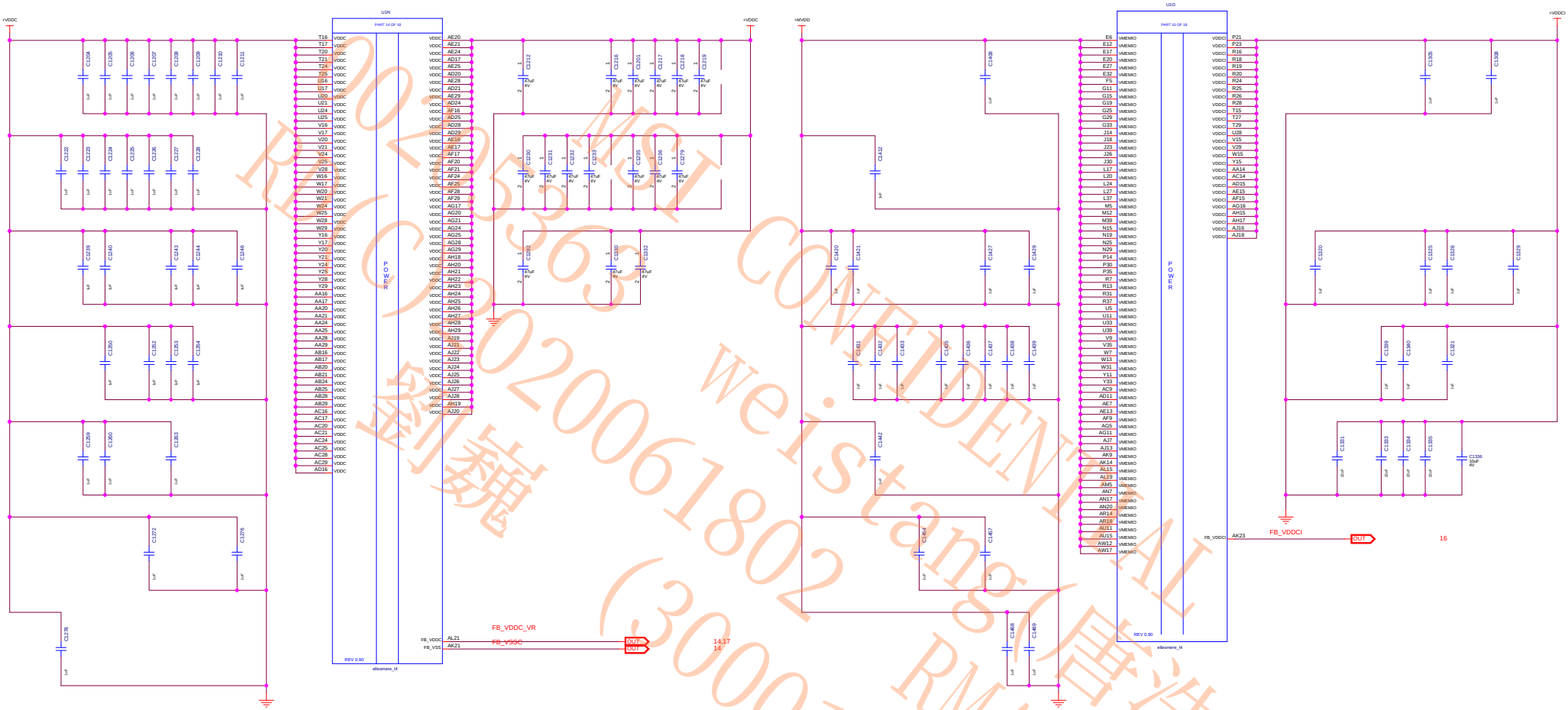
(12) ELLESMERE POWER



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R(C)2006180
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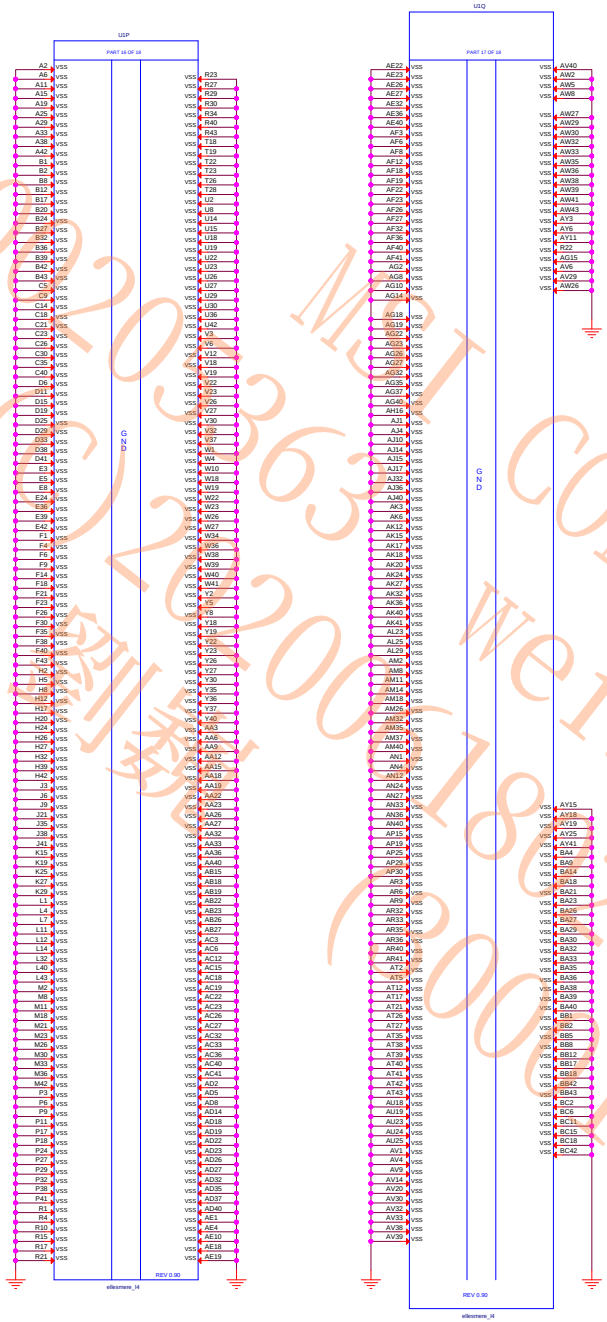
(13) ELLESMERE GROUND



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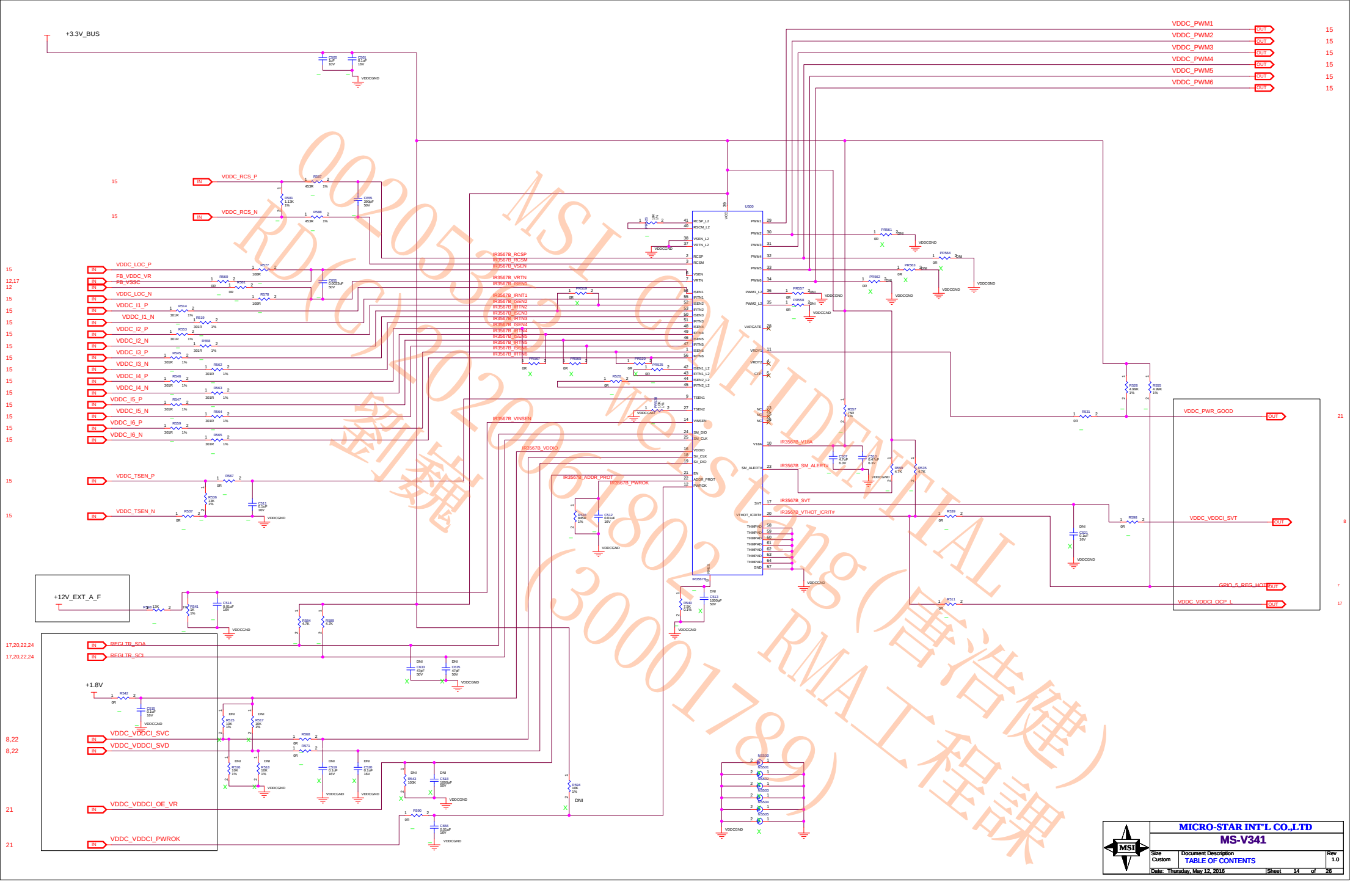

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RD(2020618025)
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Wei Sing(韋浩健)
(300017889)
RMA工程課
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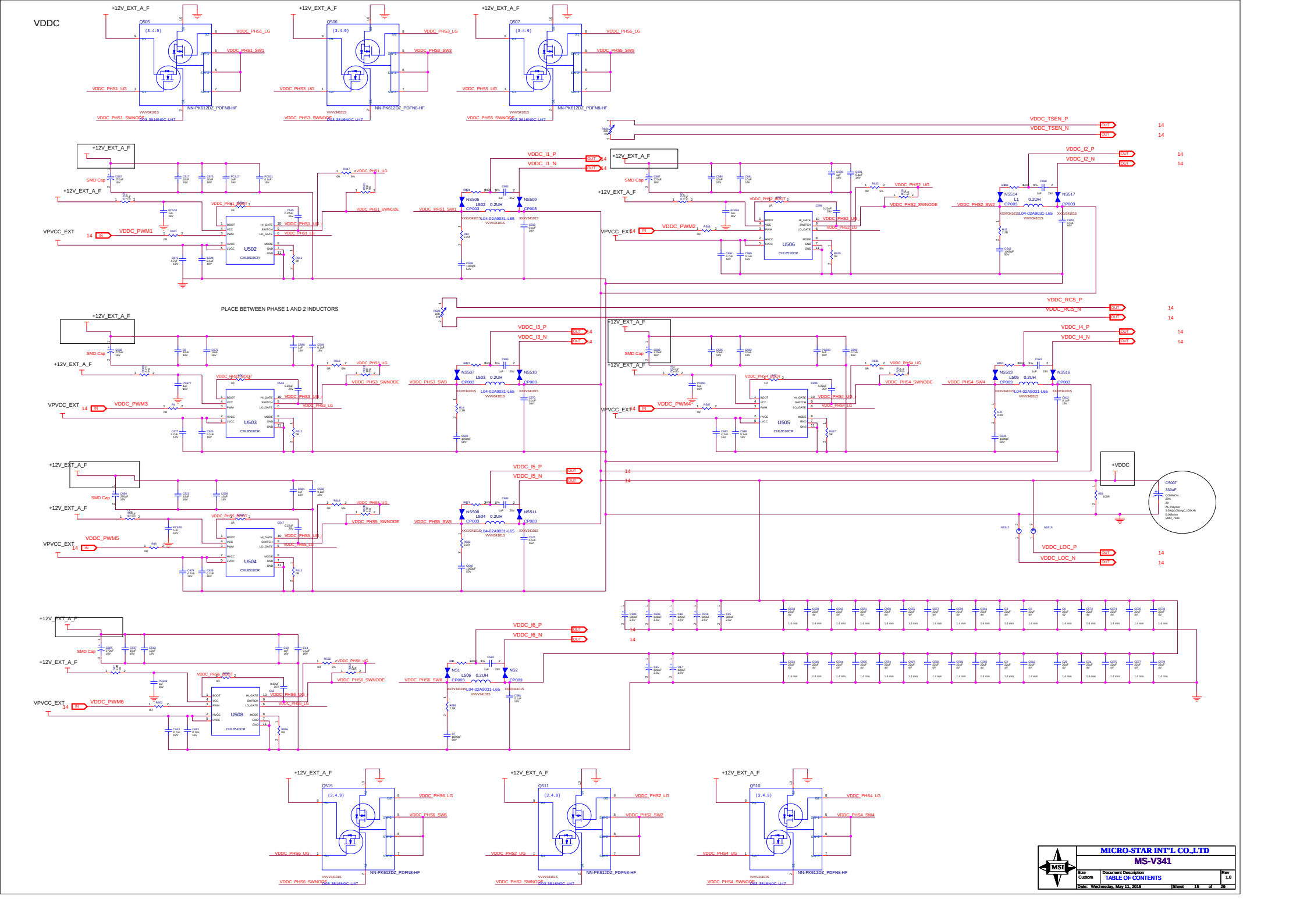
VDDC
PLACE BETWEEN PHASE 1 AND 2 INDUCTORS
VDDC_PHS1_LG
VDDC_PHS1_SW1
VDDC_PHS1_UG
VDDC_PHS1_SWNODE
VDDC_PHS3_LG
VDDC_PHS3_SW3
VDDC_PHS3_UG
VDDC_PHS3_SWNODE
VDDC_PHS5_LG
VDDC_PHS5_SW5
VDDC_PHS5_UG
VDDC_PHS5_SWNODE
VDDC_PHS6_LG
VDDC_PHS6_SW6
VDDC_PHS6_UG
VDDC_PHS6_SWNODE
VDDC_PHS2_LG
VDDC_PHS2_SW2
VDDC_PHS2_UG
VDDC_PHS2_SWNODE
VDDC_PHS4_LG
VDDC_PHS4_SW4
VDDC_PHS4_UG
VDDC_PHS4_SWNODE
VDDC_PWM1
VDDC_PWM2
VDDC_PWM3
VDDC_PWM4
VDDC_PWM5
VDDC_PWM6
VPVCC_EXT
+12V_EXT_A_F
SMD Cap
U502
U503
U504
U505
U506
U508
CHL8510CR
NN-PK612DZ_PDFN8-HF
VDDC_TSEN_P
VDDC_TSEN_N
VDDC_I1_P
VDDC_I1_N
VDDC_I2_P
VDDC_I2_N
VDDC_I3_P
VDDC_I3_N
VDDC_I4_P
VDDC_I4_N
VDDC_I5_P
VDDC_I5_N
VDDC_I6_P
VDDC_I6_N
VDDC_RCS_P
VDDC_RCS_N
VDDC_LOC_P
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+VDDC
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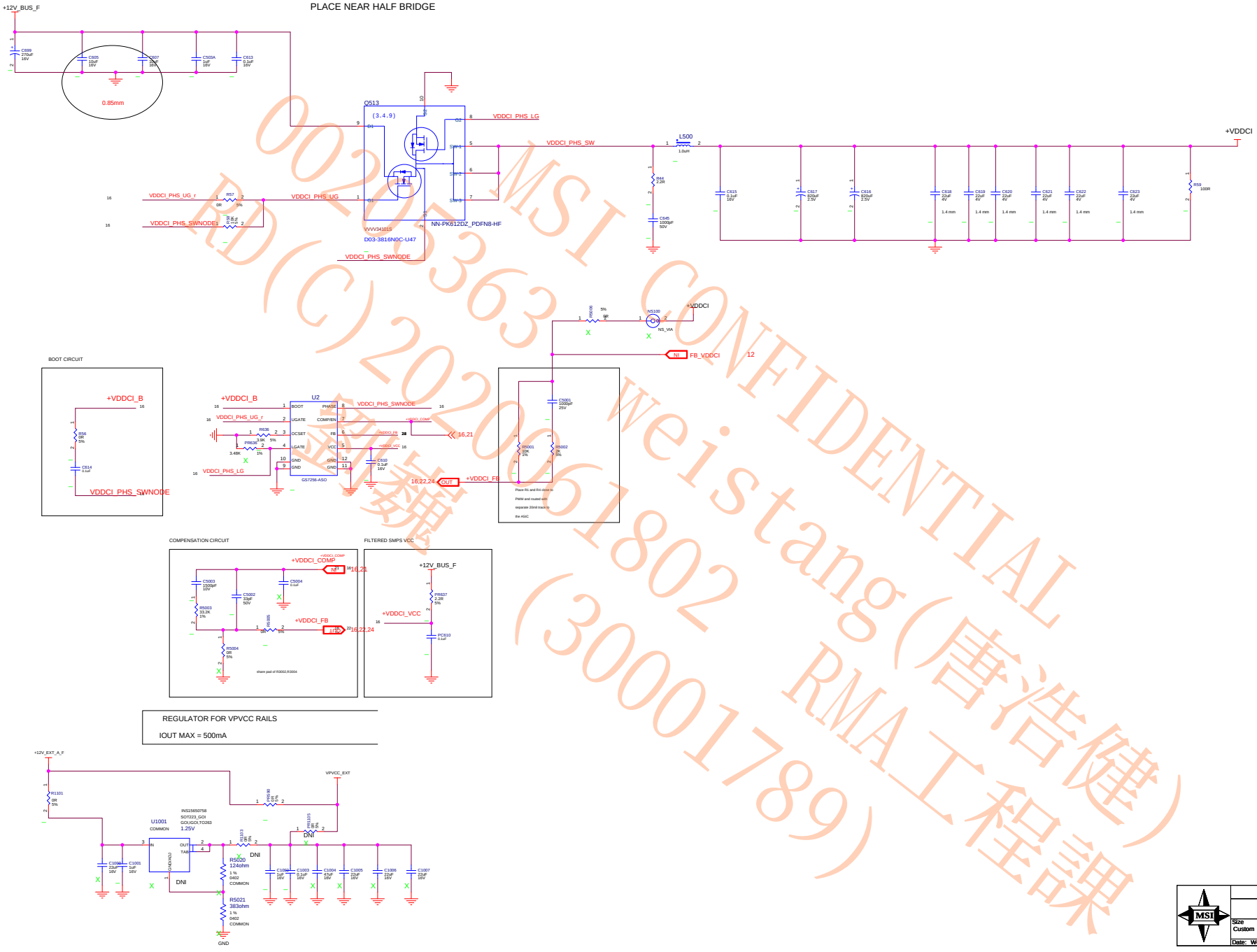

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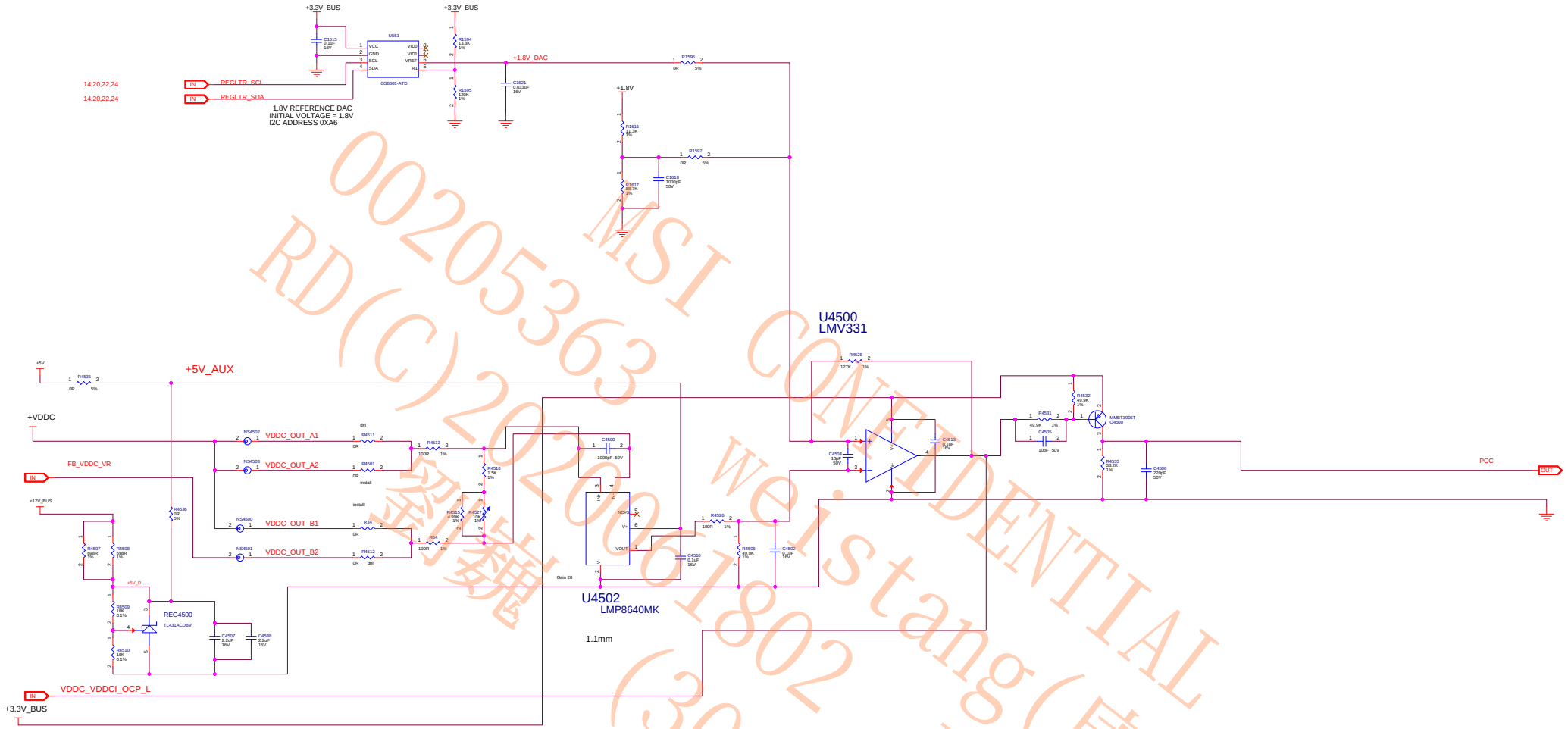

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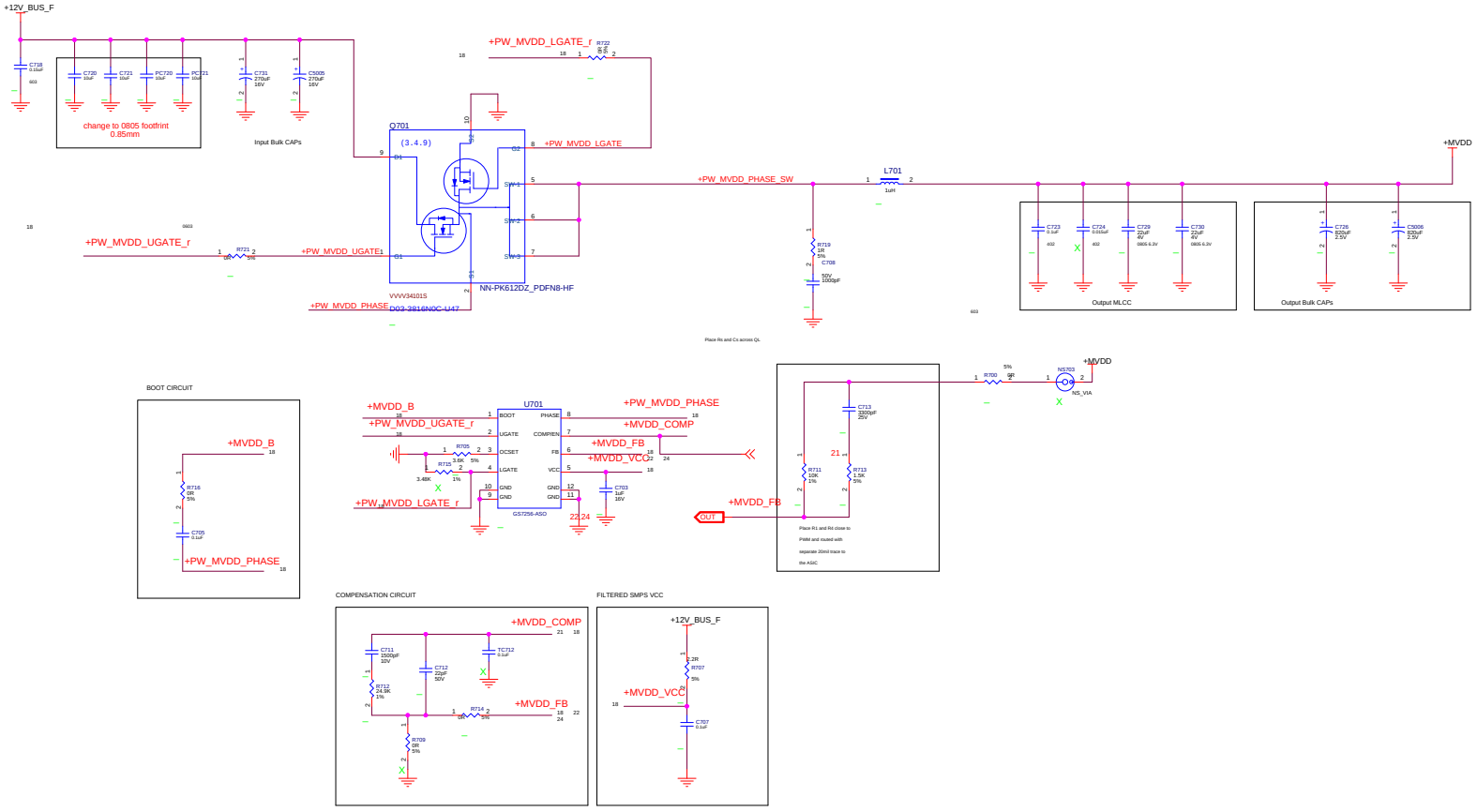
(16) MVDD



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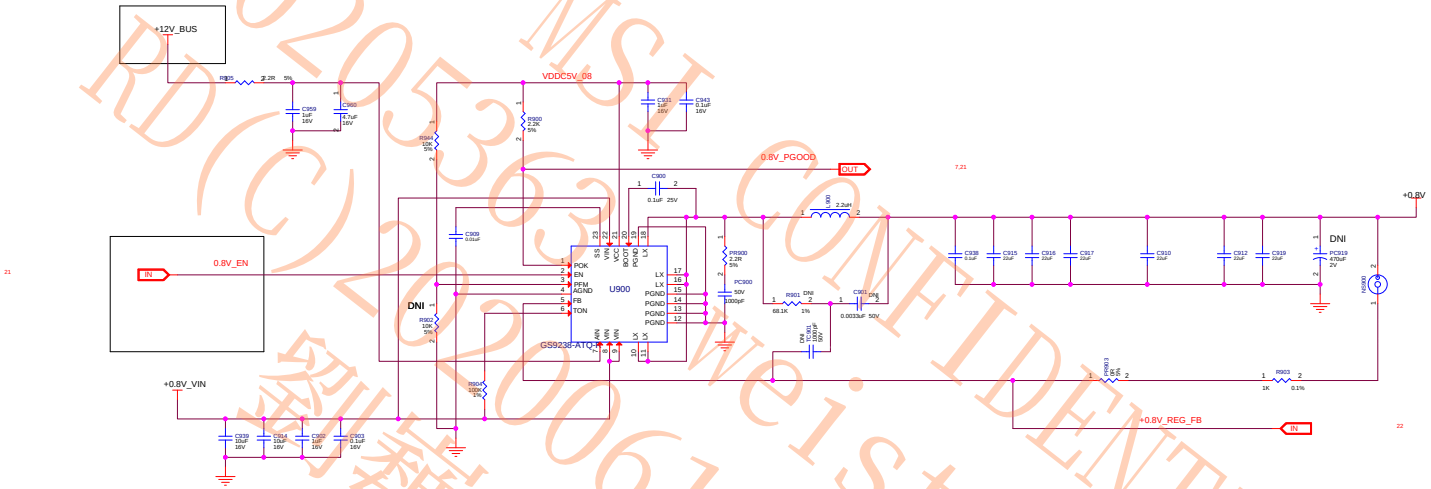
(17) 0.95V



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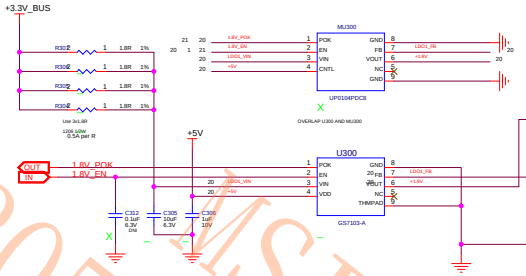
(18) SMALL RAIL REGULATORS , LED_CONTROLLER_ENE3574

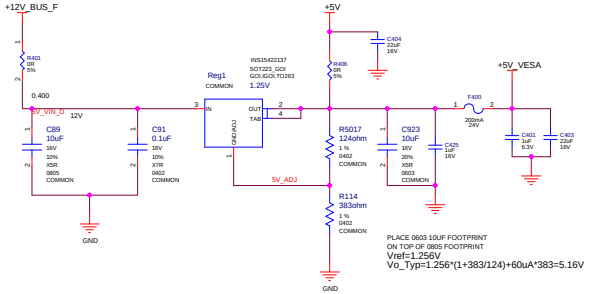
LDO #1: VIN = 3.0V TO 3.6V MAX VOUT = +1.8V +/- 2% IOUT = 1.3A RMS MAX

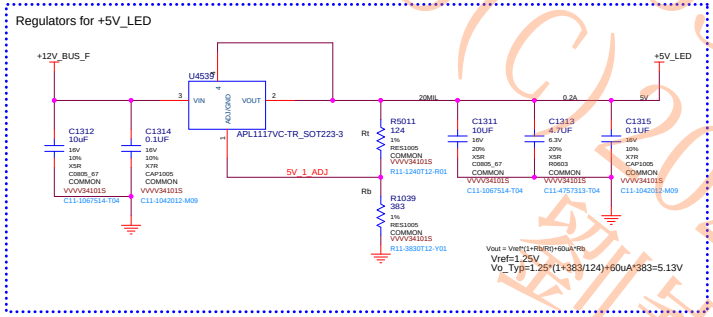
PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING

REGULATOR FOR +5V RAILS

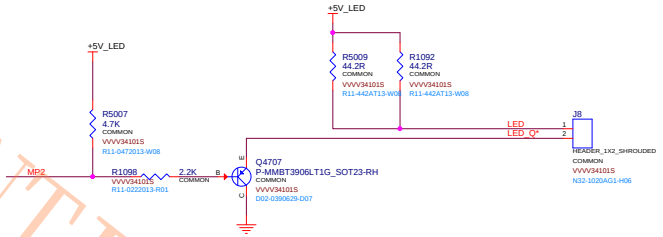
IOUT MAX = 150mA

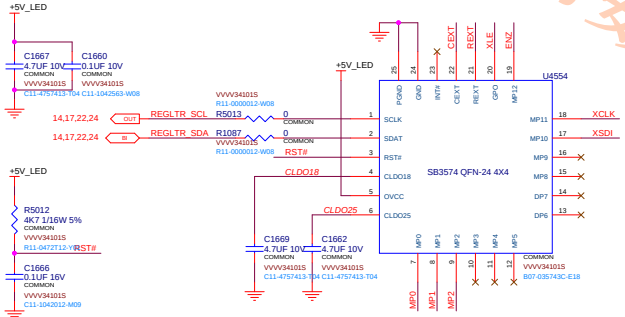


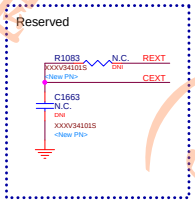


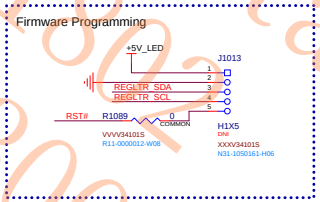


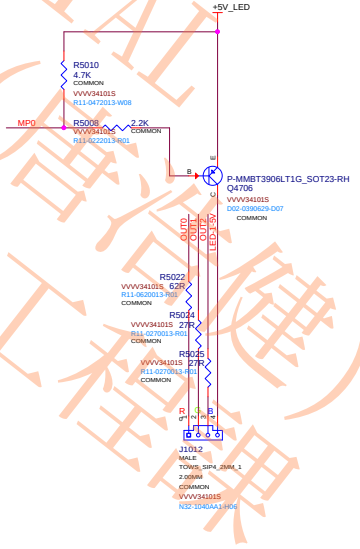
LED Connector

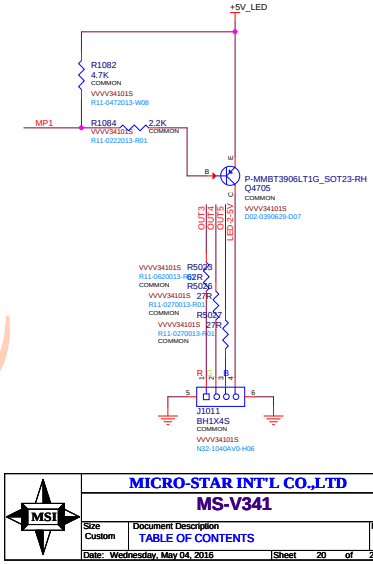


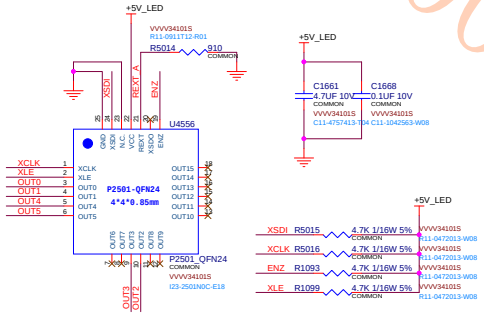












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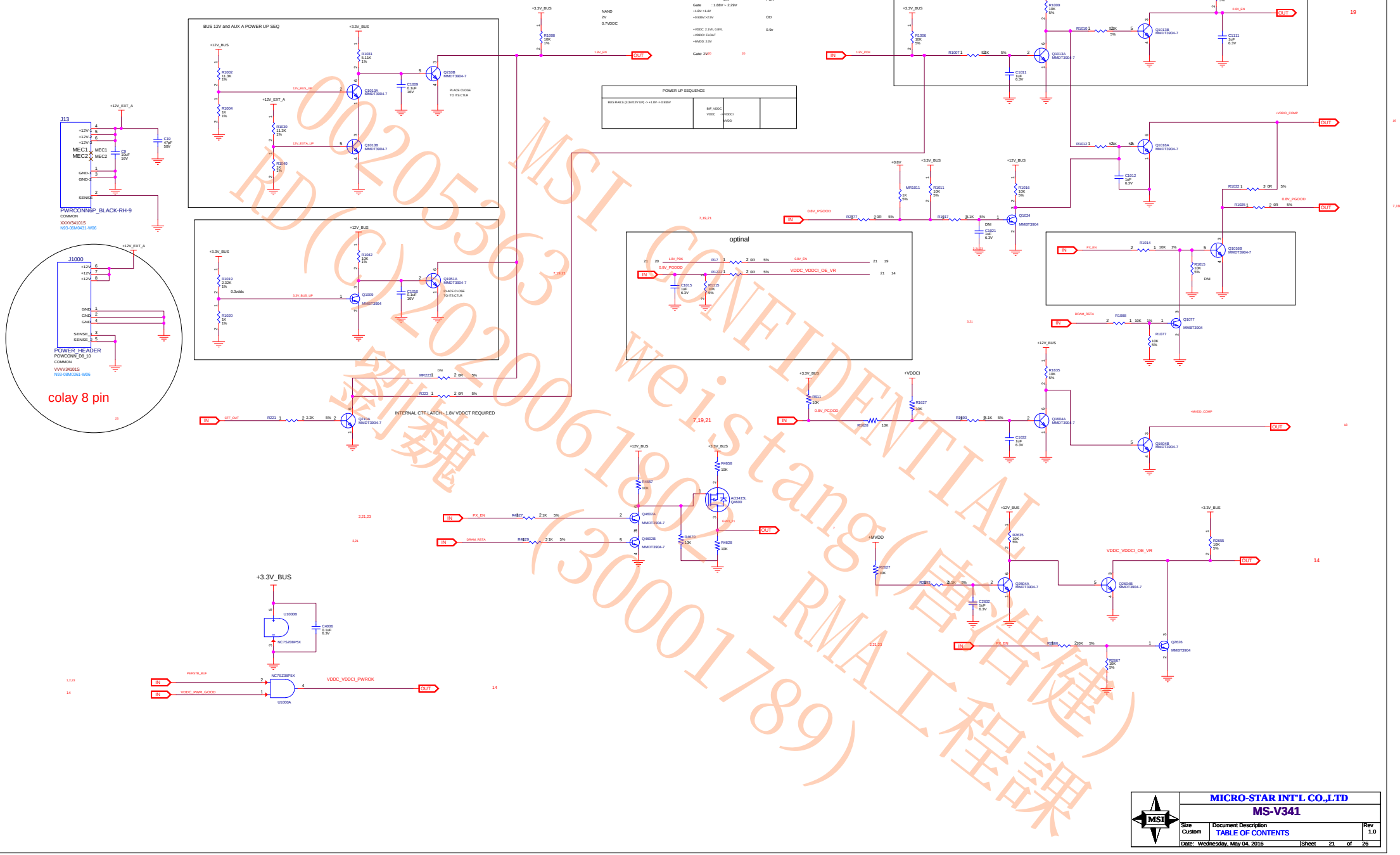
(19) POWER MANAGEMENT



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劉錦(30001789)
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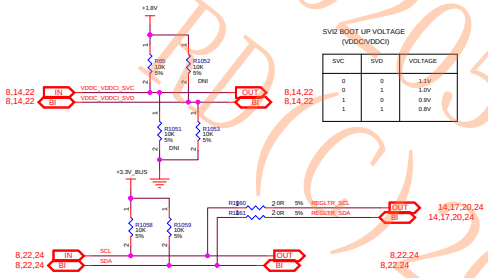

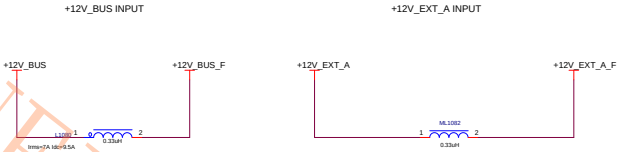

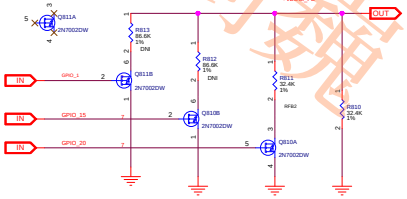

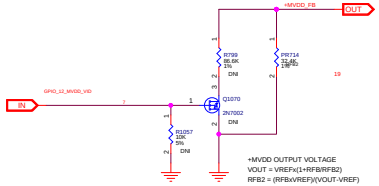

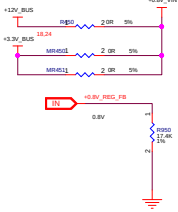

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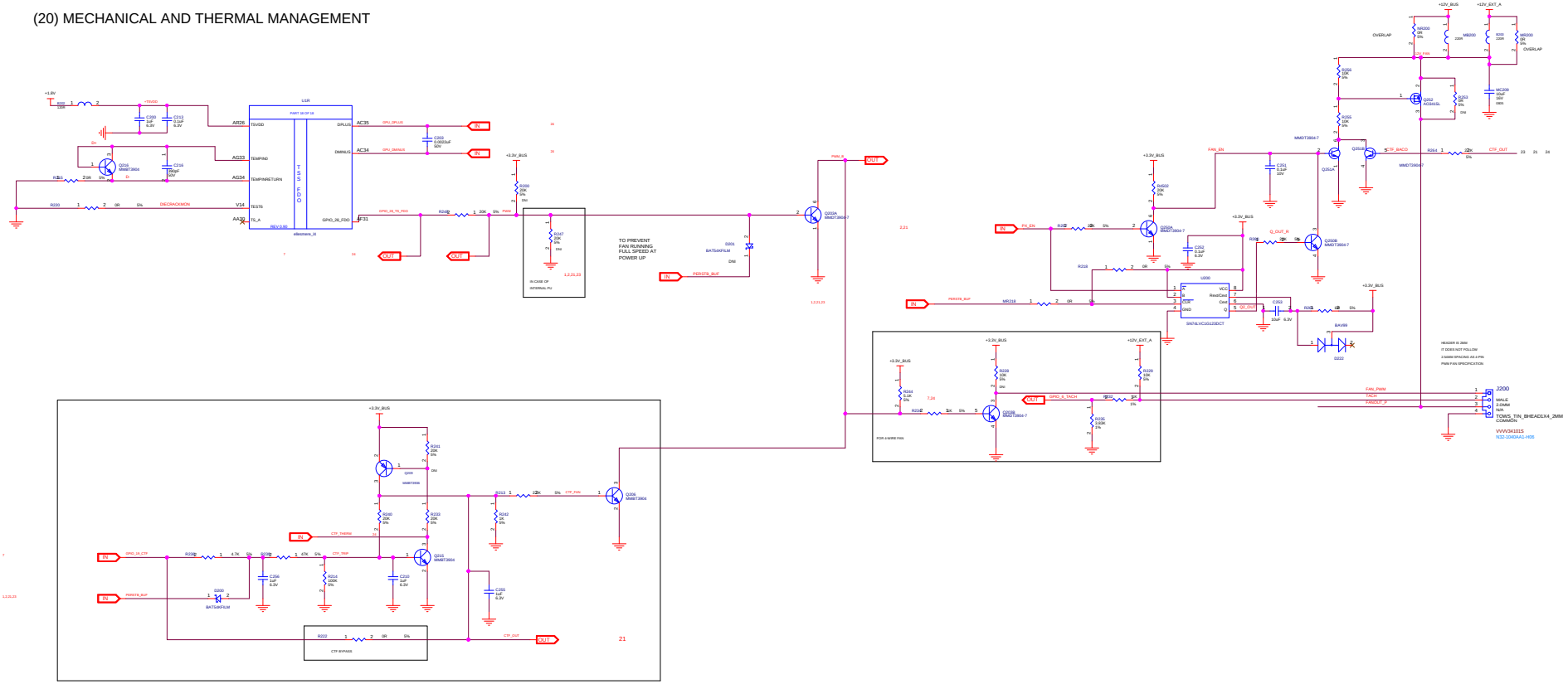
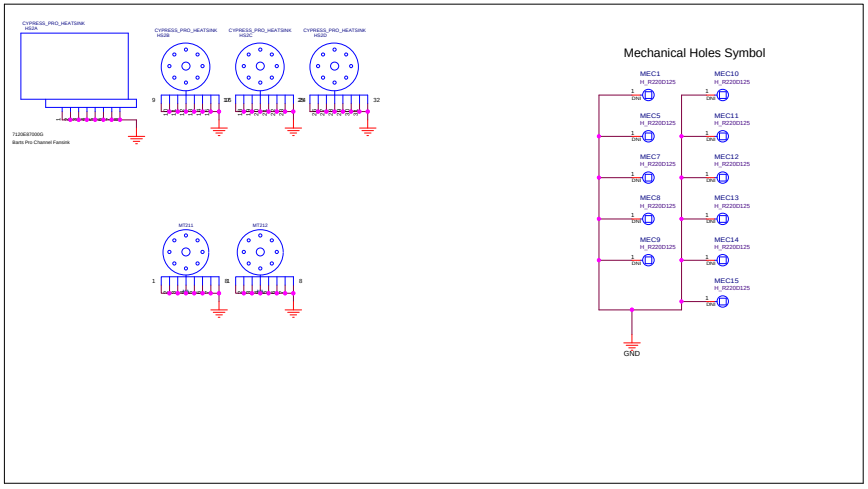
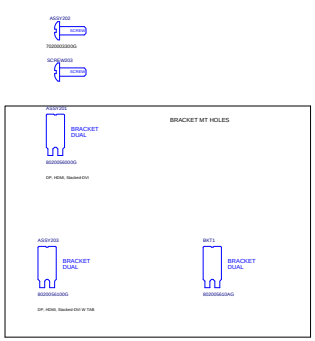
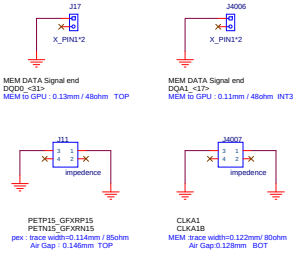
(20) MECHANICAL AND THERMAL MANAGEMENT

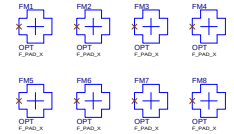




Mechanical Holes Symbol







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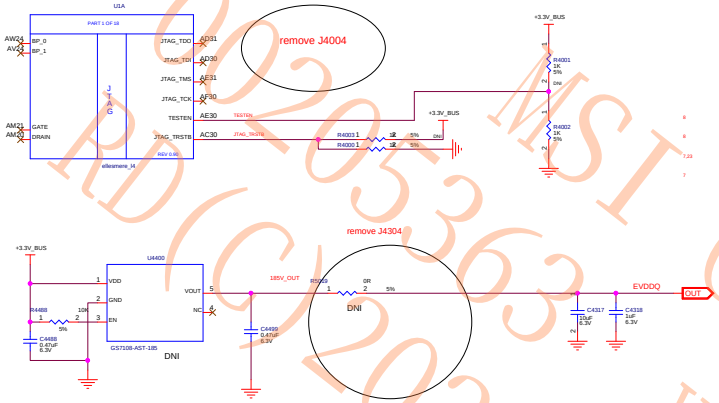
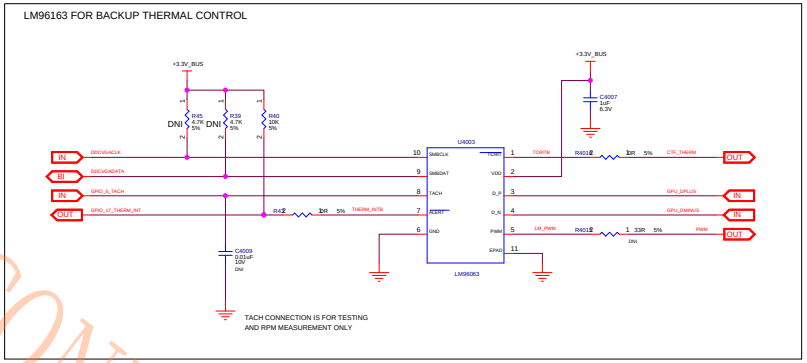
(21) DEBUG CIRCUITS

JTAG



LM96163 FOR BACKUP THERMAL CONTROL



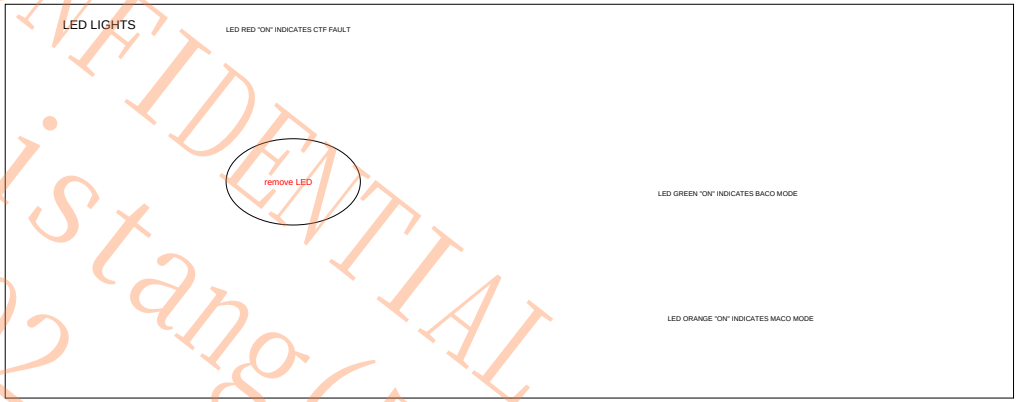
E-FUSE CAPABILITY

DEFAULT = GPIO-CONTROLLED
(MANUAL OPTION AS BACK-UP)

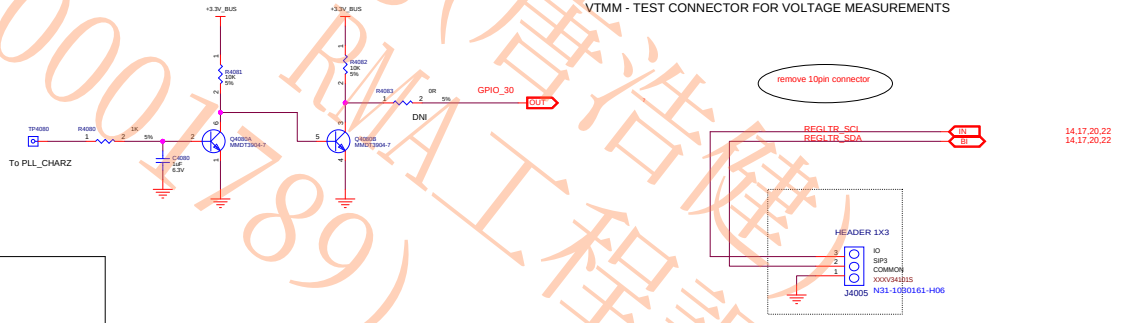
DIGITAL POTS

DIGITAL POTS

LED LIGHTS



VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS



remove J4001/J4002

SWITCHES

BYPASS/DISABLE CTF

MAXIMIZE FAN

0205363 MSI CONFIDENTIAL
PD(C)20200618 weistang(唐浩健)
劉輔 (30001789) RMA工程課

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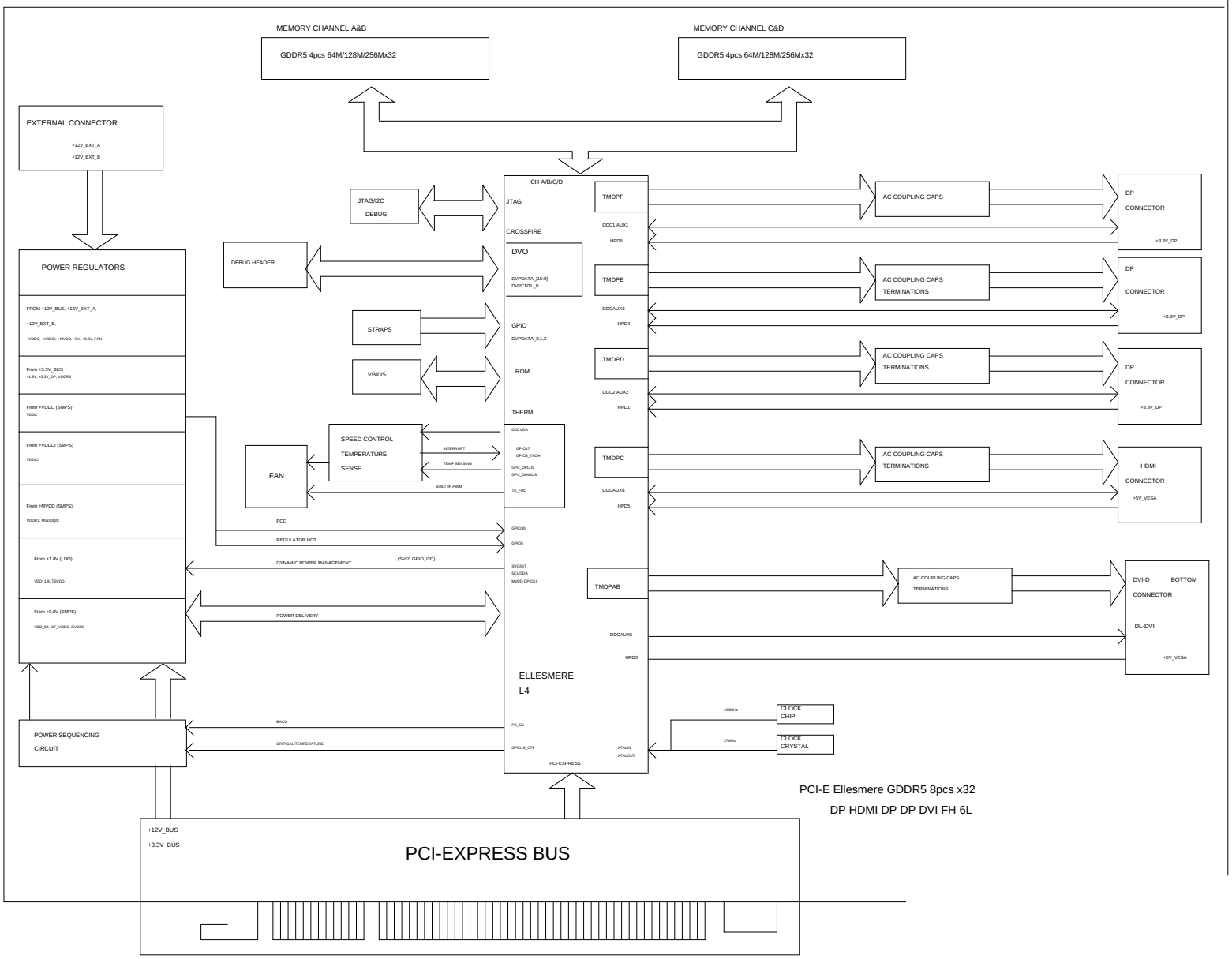

PCI-E Ellesmere GDDR5 8pcs x32

DP HDMI DP DP DVI FH 6L

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0	00A	08/26/2015	
1	00B	10/19/2015	1. Add GPIO1 for dynamic VDDCI 2. update VDDC driver sequence
2	00C	01/21/2016	HDMI: - add series resistor R1880 ~R1887 - add pull down inductor L1880 ~L1887 remove C405, C403, VR400,C410,C414,R405

Page7: remove J2,J3

Page9: enable DVI

Page10: DP change to HDMI

Page15: change dual-N MOS

Page16: change dual-N MOS

Page18: change dual-N MOS

Page20: Add LED circuit

Page21: colay 8pin power connecter

Page24: remove debug circuit

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