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AMD <mark></mark>			Title		Schematic No.	Date:		
		МГ	П	M8X GDDR2		105-B37631-00B	Tuesday, October 16, 2007	
D				REVISION HISTORY	NOTE: This schematic represents the I For Stuffing options (componer Please contact AMD representa	IPCB, it does not represent any specific SKU. nt values, DNI祖, ? please consult the product spec ative to obtain latest BOM closest to the application	cific BOM. n desired.	Rev 0
Sch Rev Date		Date	REVISION DESCRIPTION					
	0	00A	13/07/07	PROTOTYPE BASED ON 105-B24631-00C				
$\exists$	1	00B	2/10/07	2 Regulators were changed, CTF change				
С								С
•								4
В								В
A								A
L								