

P2002

2GB GDDR5, 256b, 64Mx32

Stacked DVI-I/DVI-D + HDMI + DP

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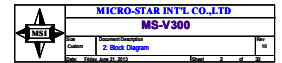
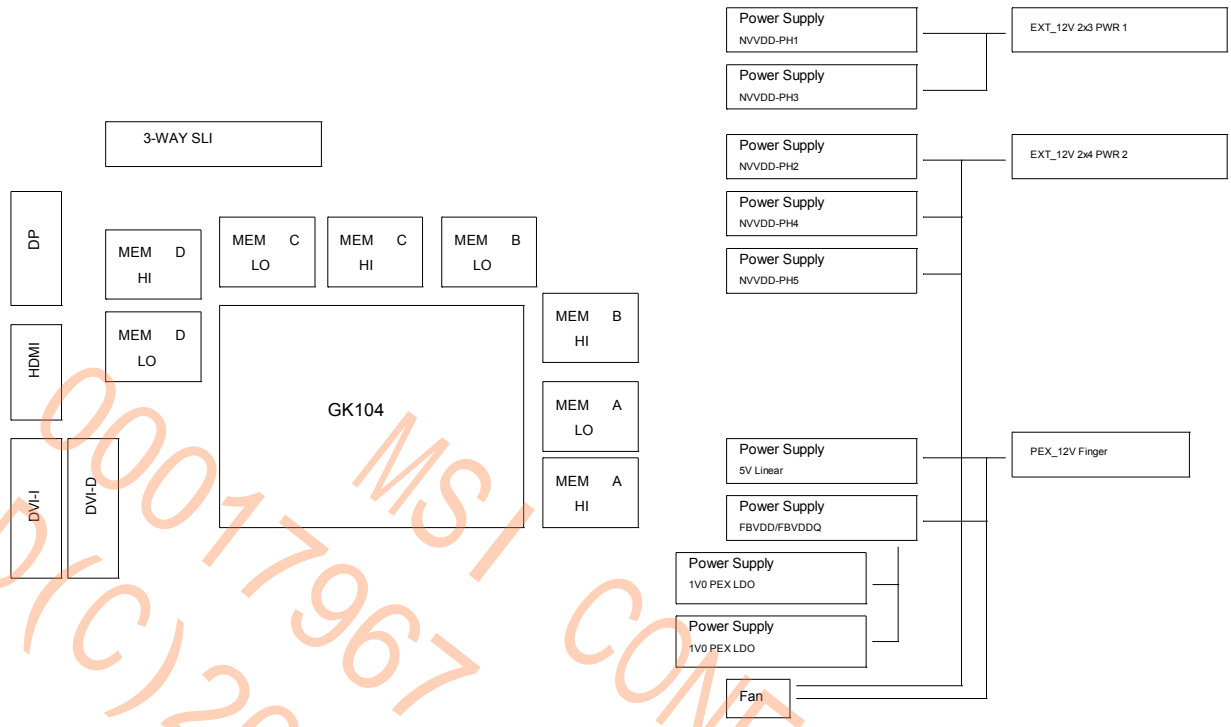
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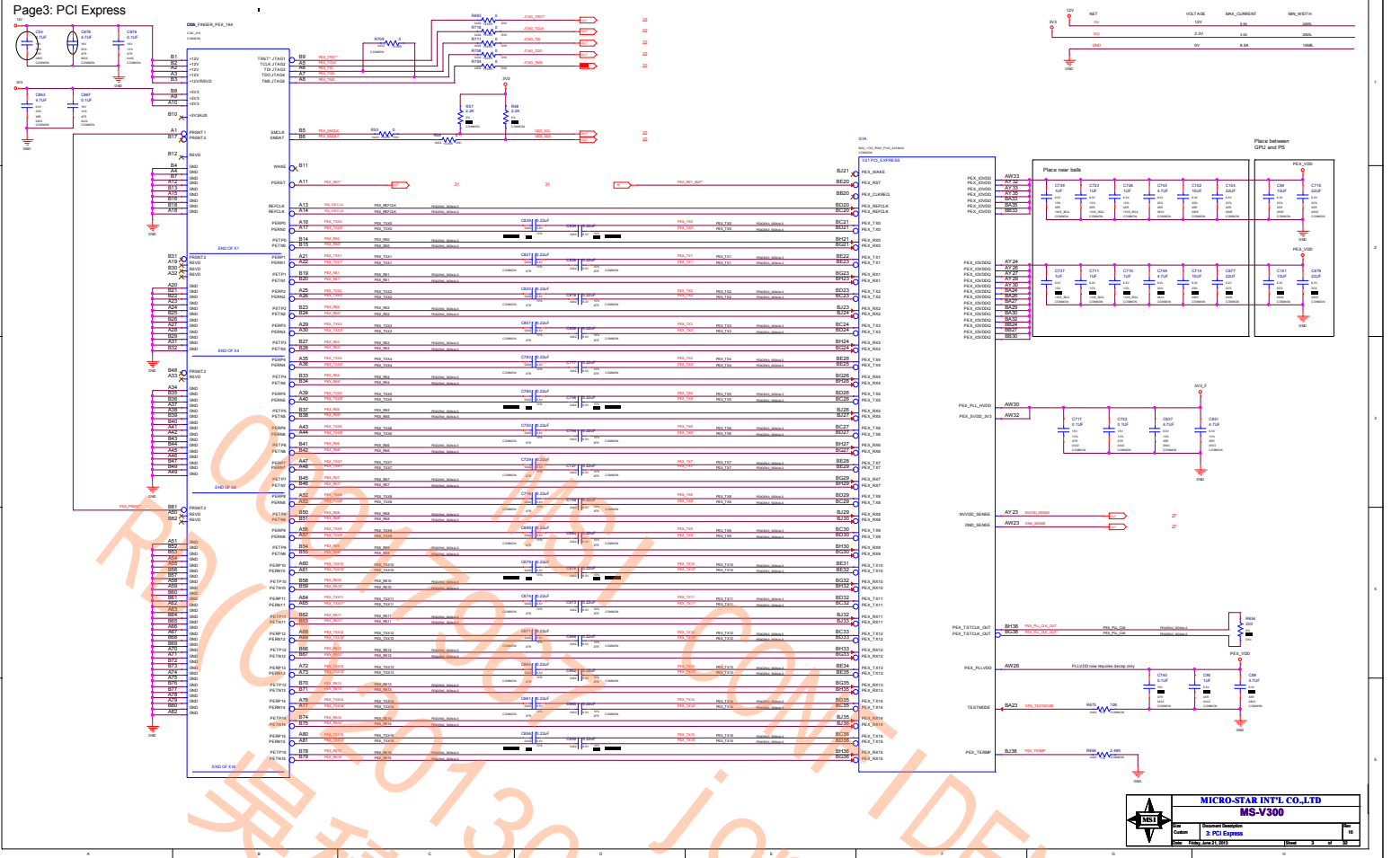
Base on V282-3.1

P23 add net: dual bios & bios swich
P24 add net: PS1_1V_FB
P25 add net: PS3_FBVDDQ_FB
P26 add up1811 & V check point
P27 add net: NCP4206_FB



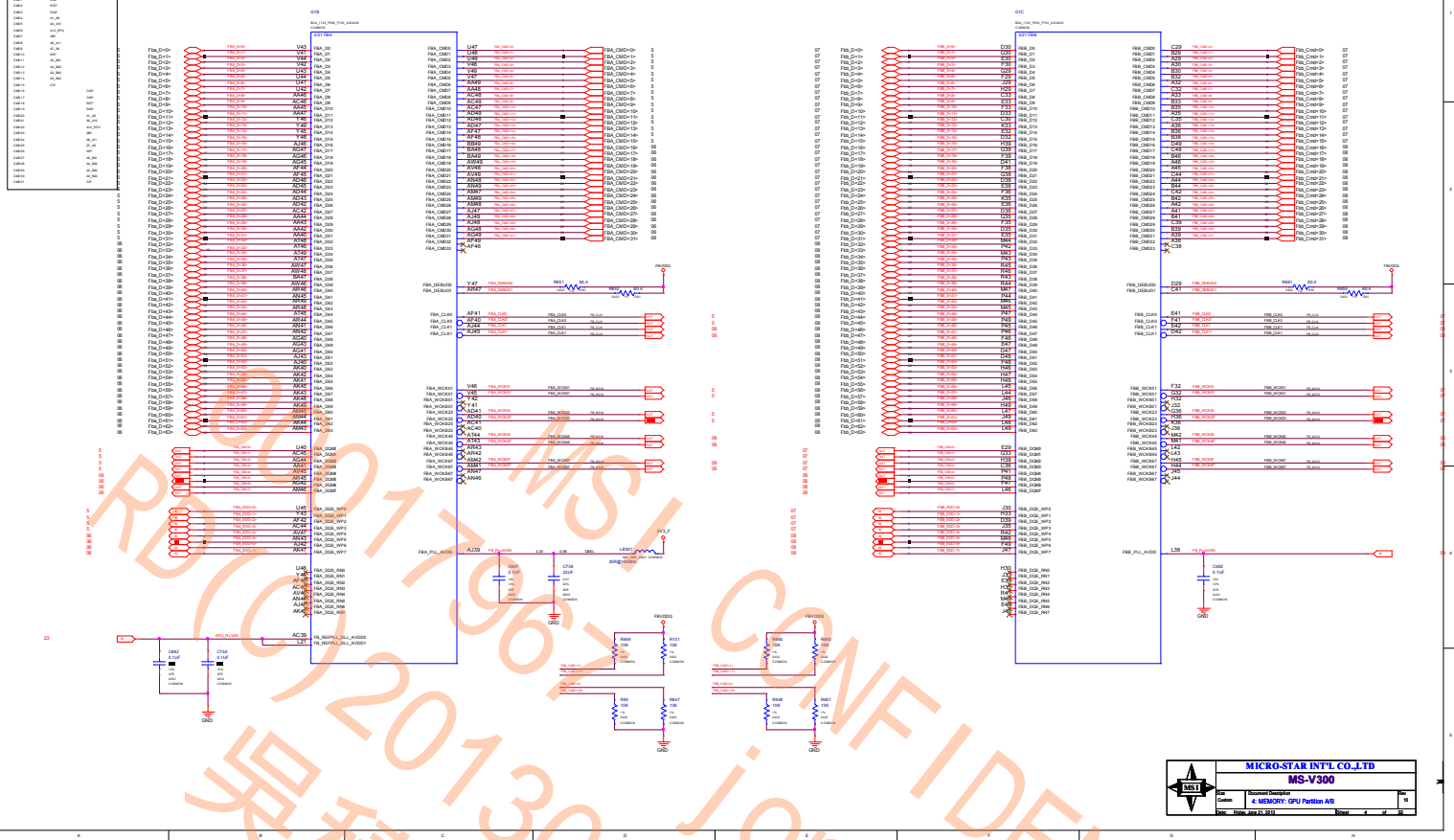


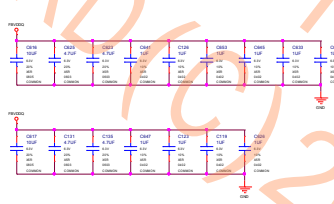
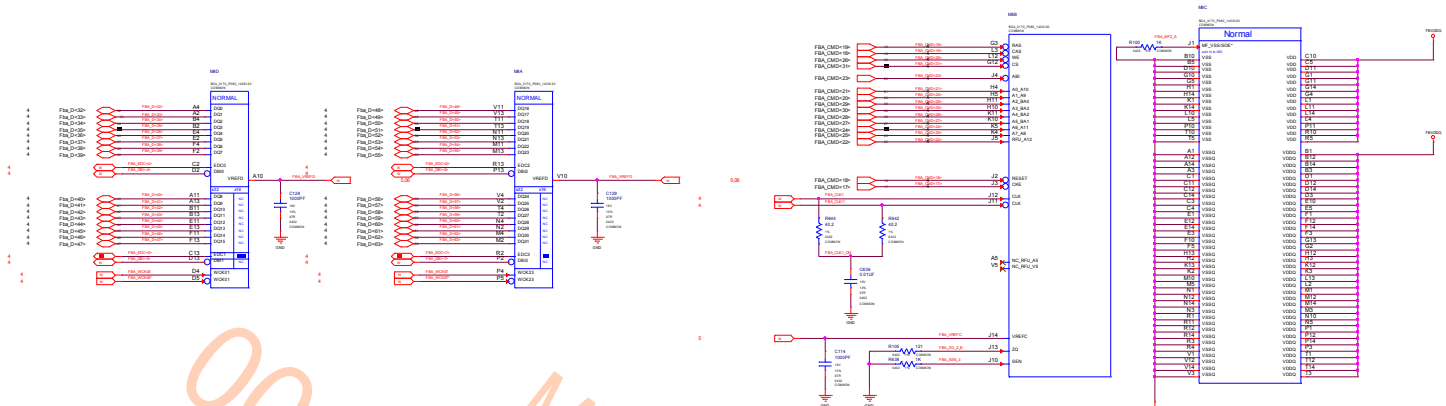
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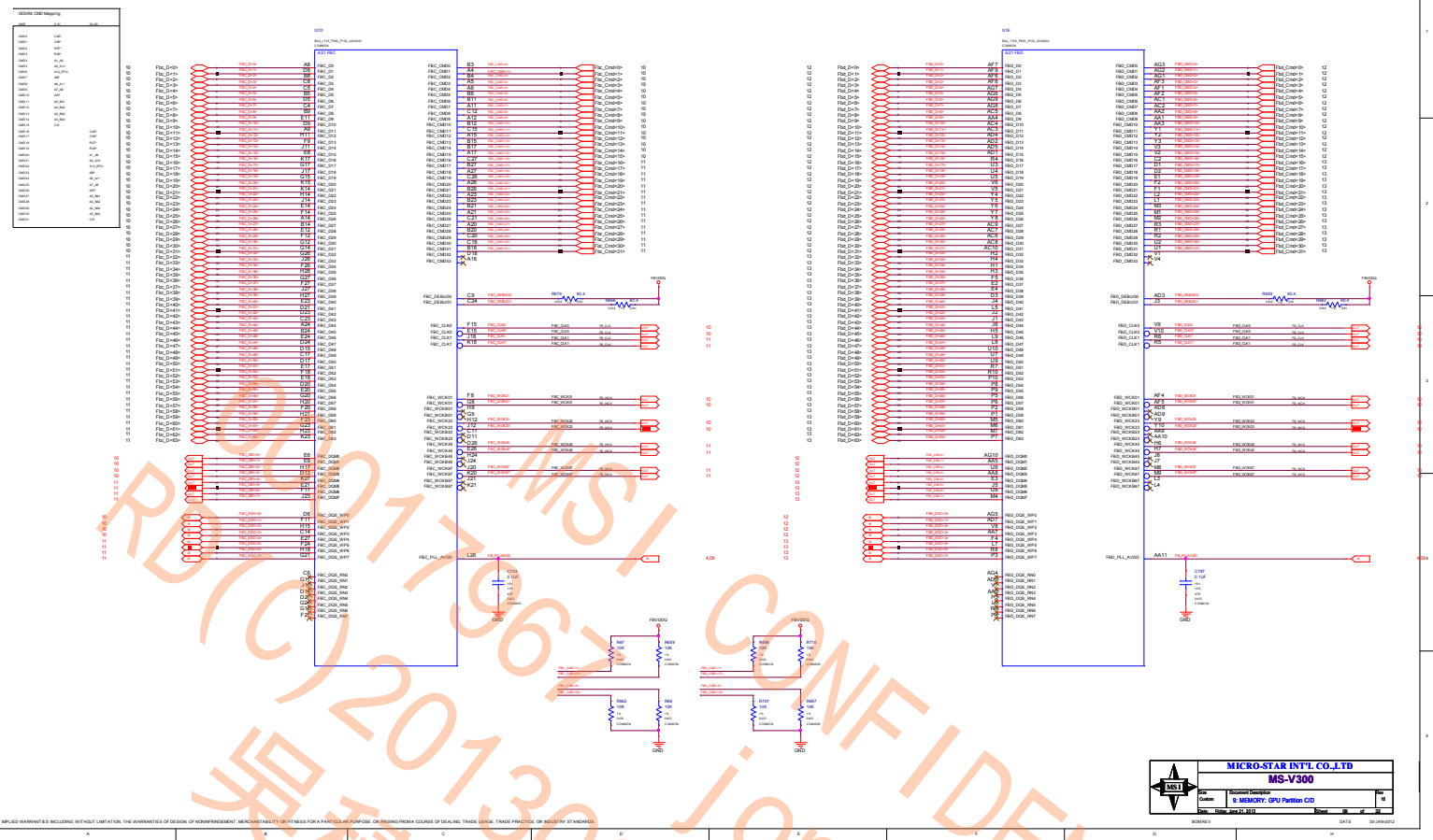


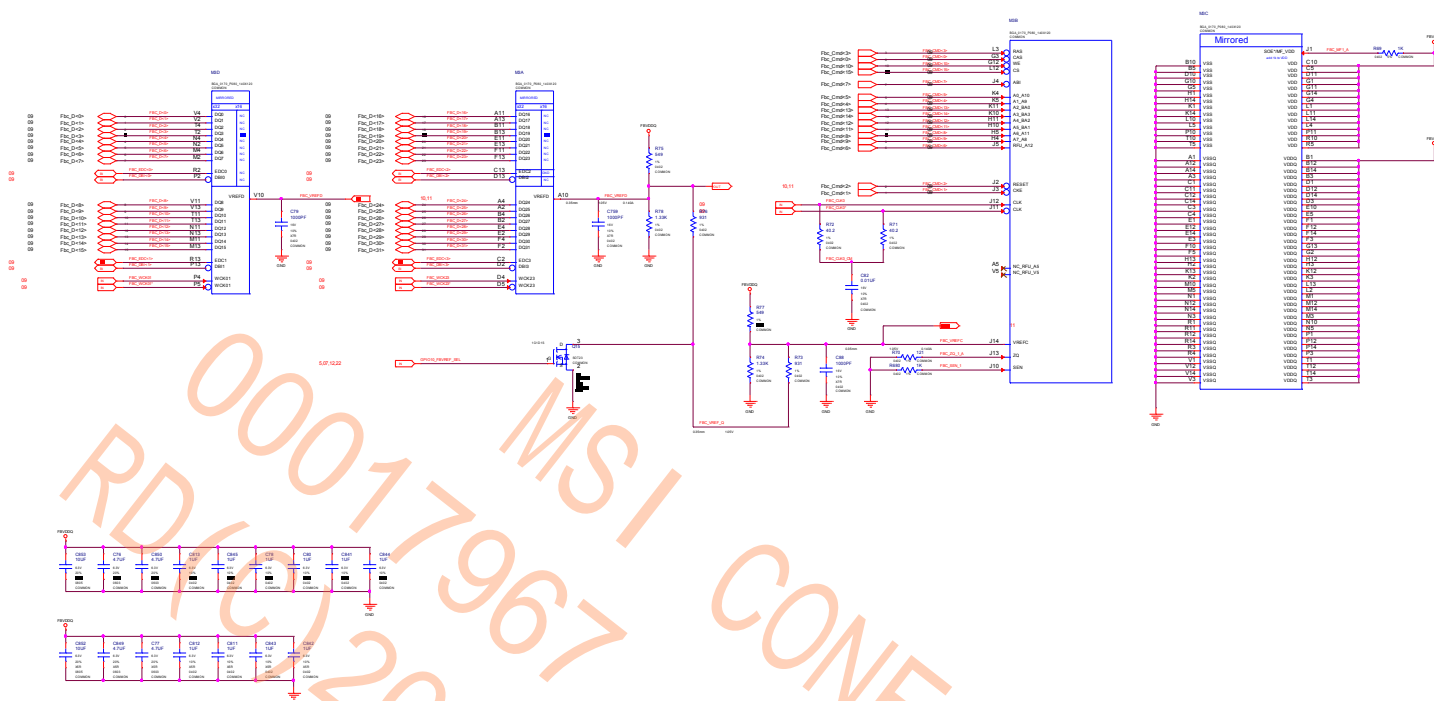
0013092502 jonepei (00011601) RMA工程課

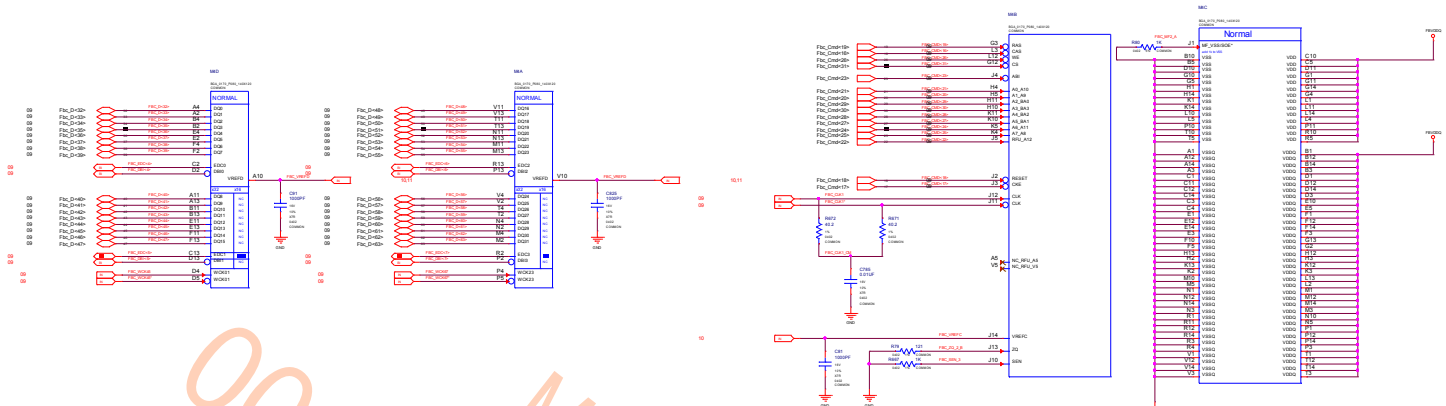
GPU Core Mapping	
GPU0	GPU0
GPU1	GPU1
GPU2	GPU2
GPU3	GPU3
GPU4	GPU4
GPU5	GPU5
GPU6	GPU6
GPU7	GPU7
GPU8	GPU8
GPU9	GPU9
GPU10	GPU10
GPU11	GPU11
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GPU96	GPU96
GPU97	GPU97
GPU98	GPU98
GPU99	GPU99









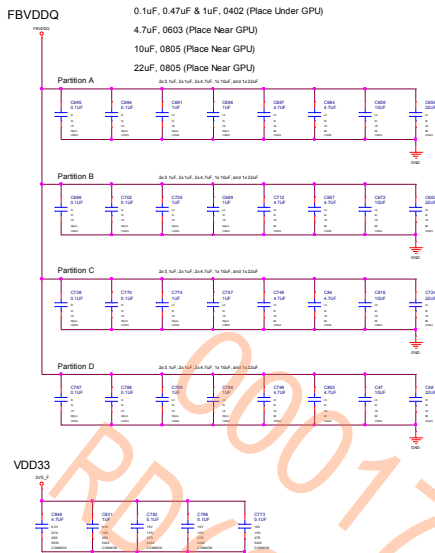


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 RD (C) 2013092502 jonepei (裴亮樂)
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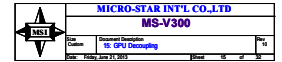
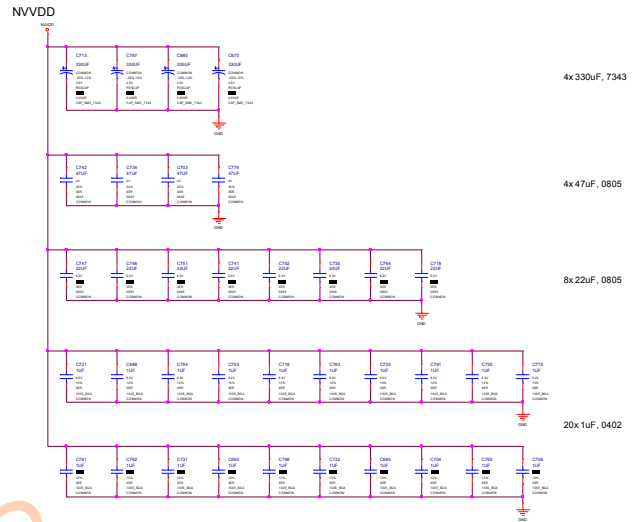
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MS-V300			
Doc	11: MEMORY FBC Partition 63..32	Rev	1.0
Doc	2013092502	Doc	11 01 01

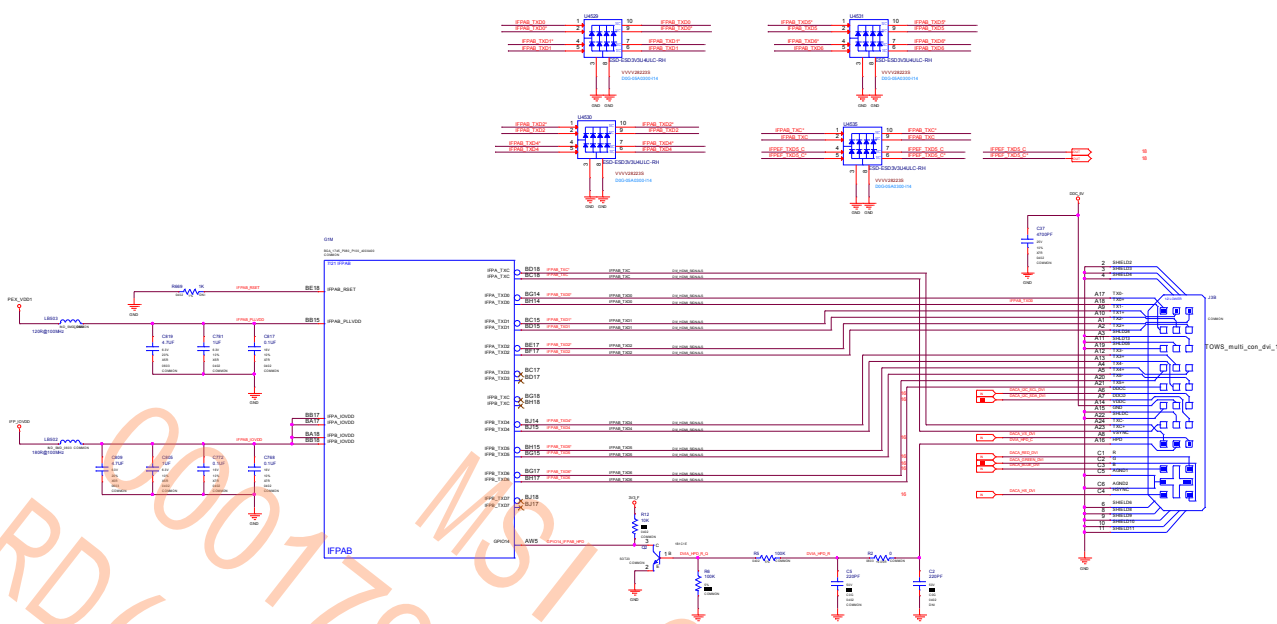


Based on GB2-X GDDR5 FBVDDQ Decap Guideline

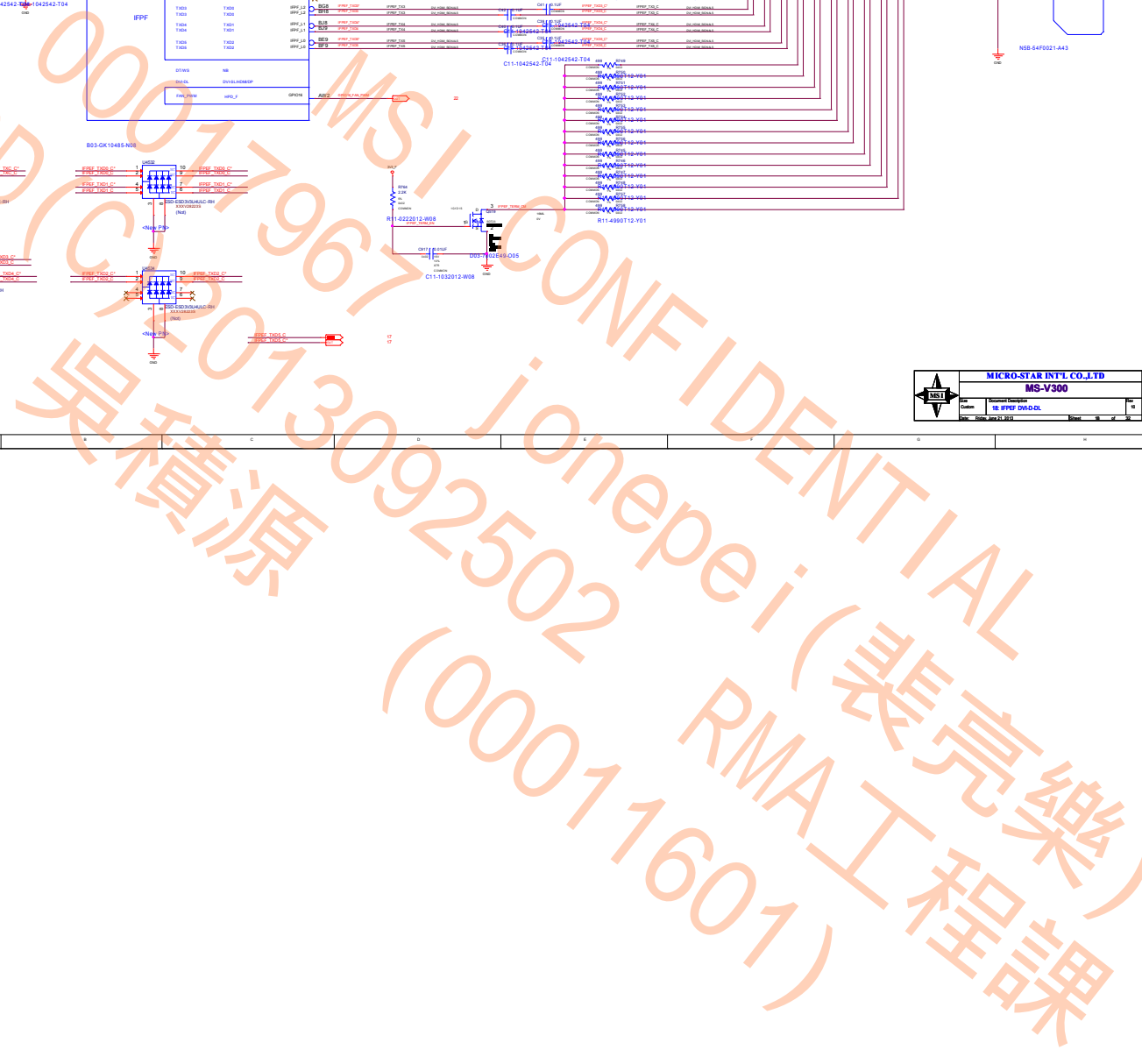


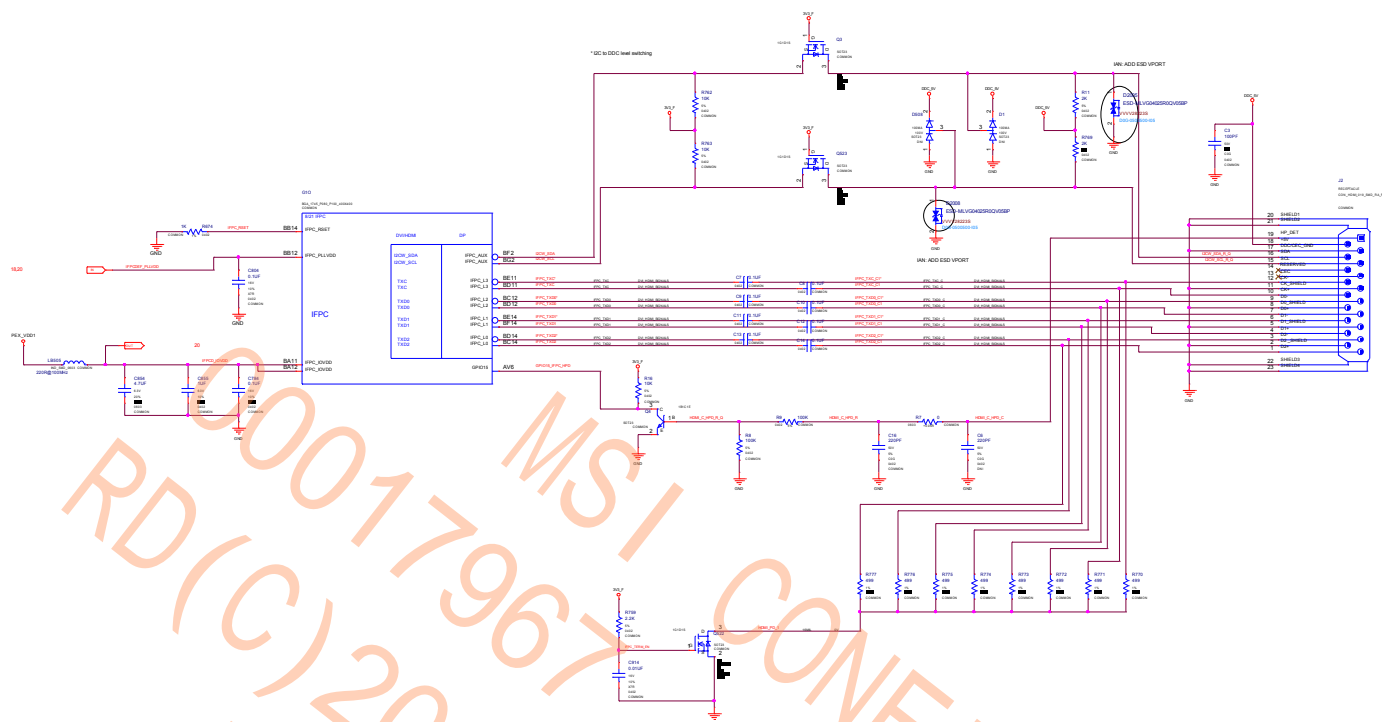
NVVDG Decoupling caps. Place under GPU.



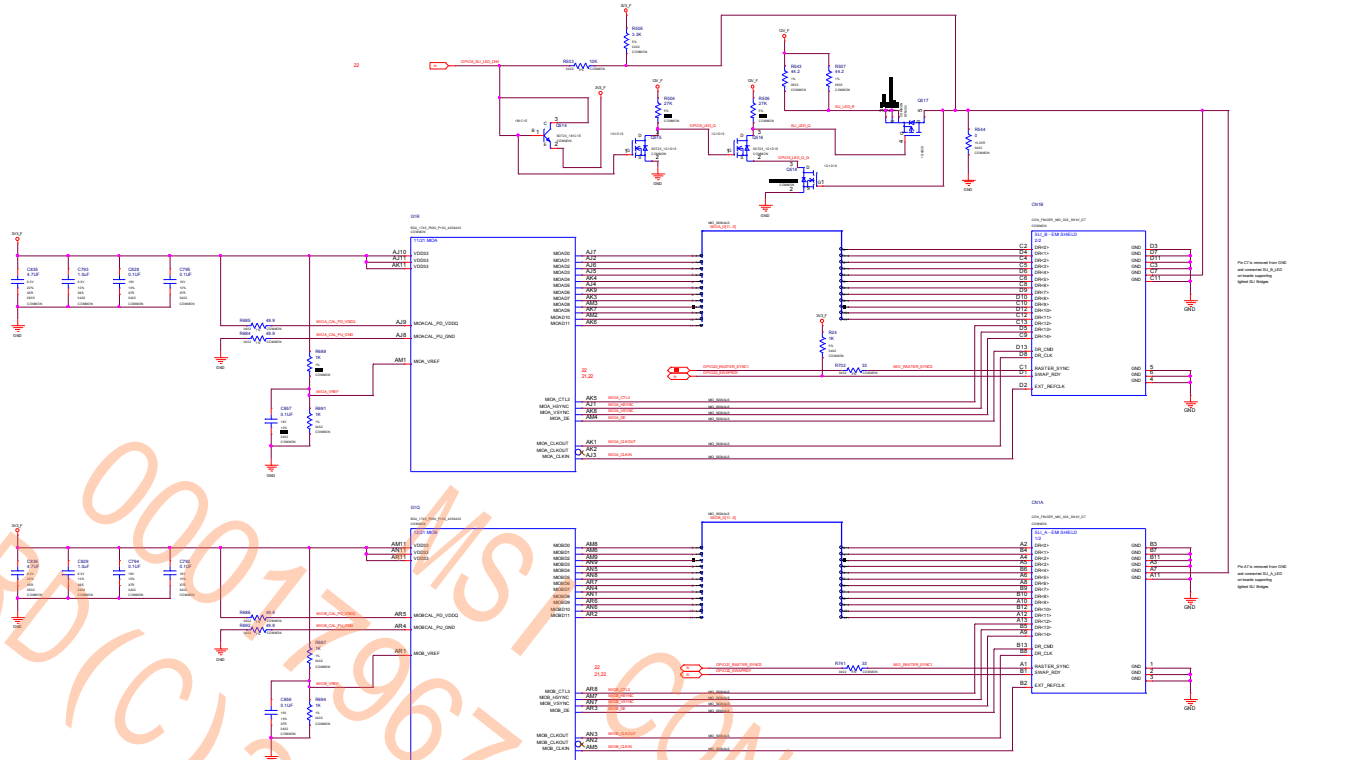


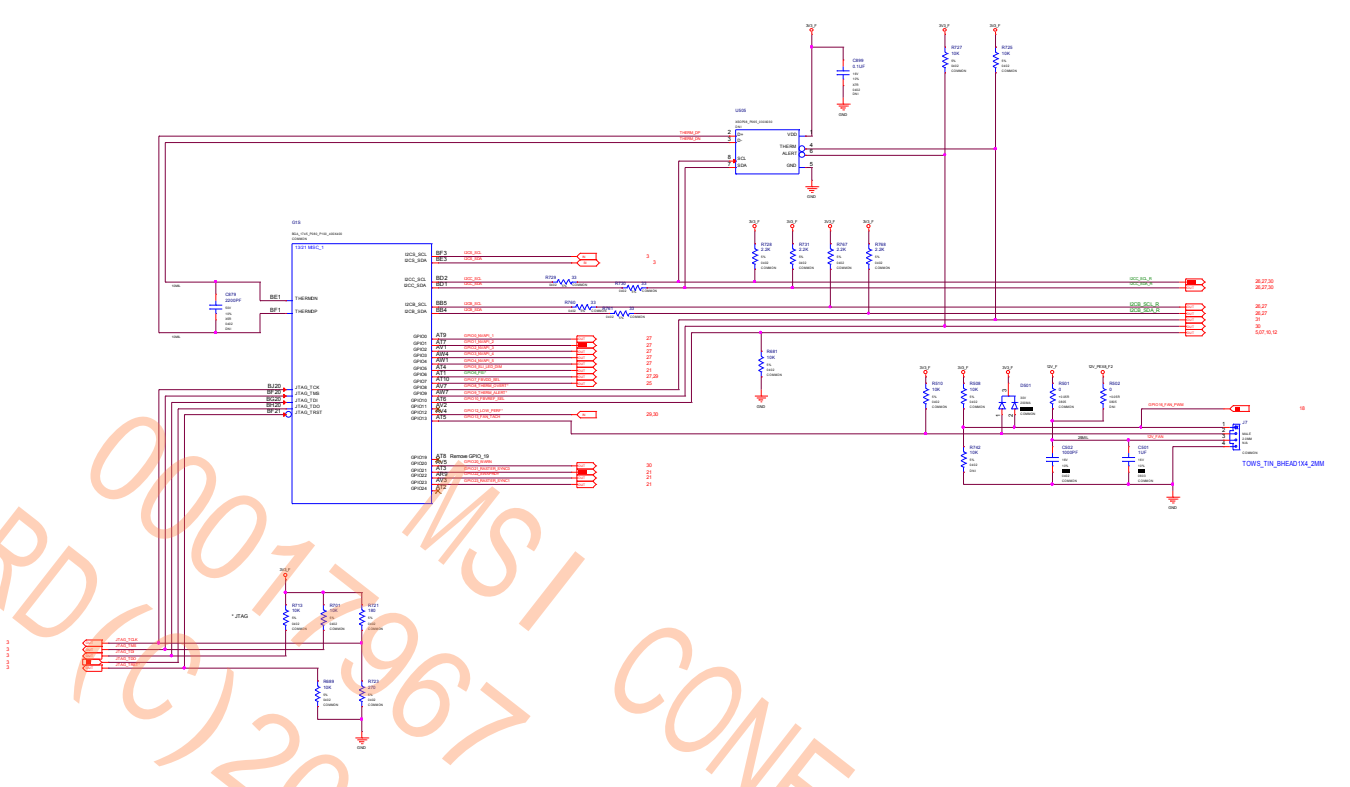
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DATE	2008/03/20	BY	WJ
REV	1.0	APP	WJ




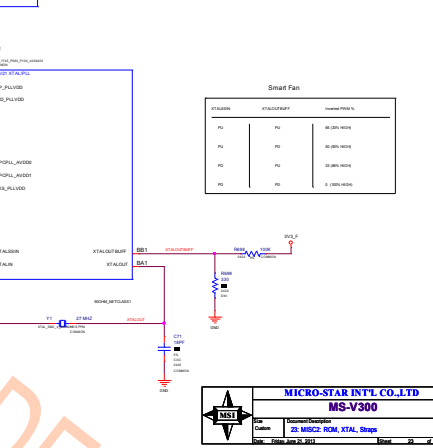


MICRO-STAR INT'L CO., LTD	
MS-V300	
Rev	1.0
Date	2013/09/25
By	MS-V300
Check	
Drawn	
Rev	
Date	
By	
Check	
Drawn	





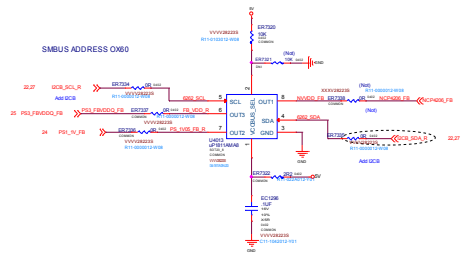
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	MS-V300	
Doc No.	MS-V300-Fan, Thermal, JTAG, GPIO	
Rev.	1.0	

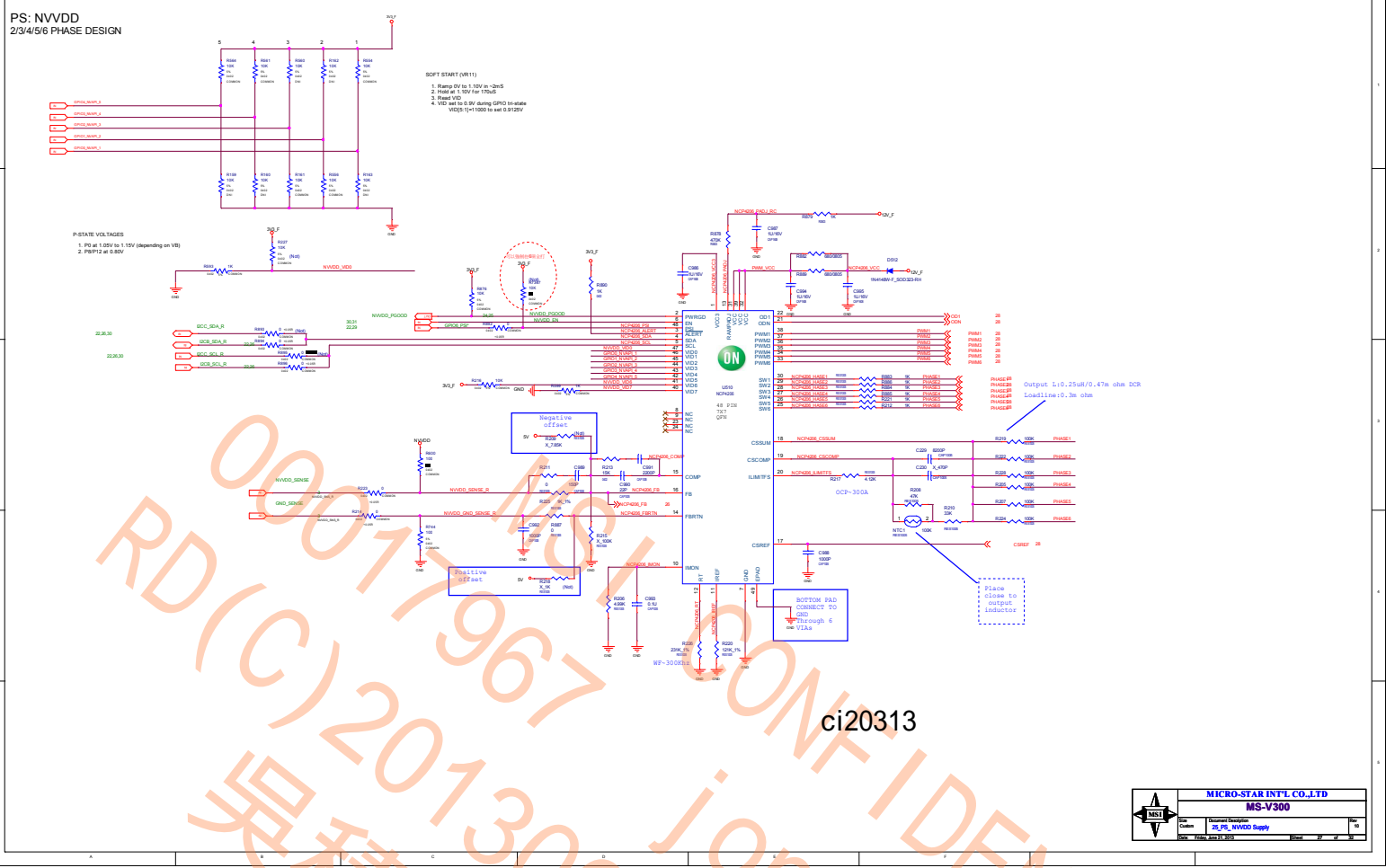
[illegible][illegible]

MICRO-STAR INT'L. CO.
MS-V300

Size	Customer/Particular
25: MISC2 ROM, XTAL, Straps	
Date	Printed: June 21, 1993

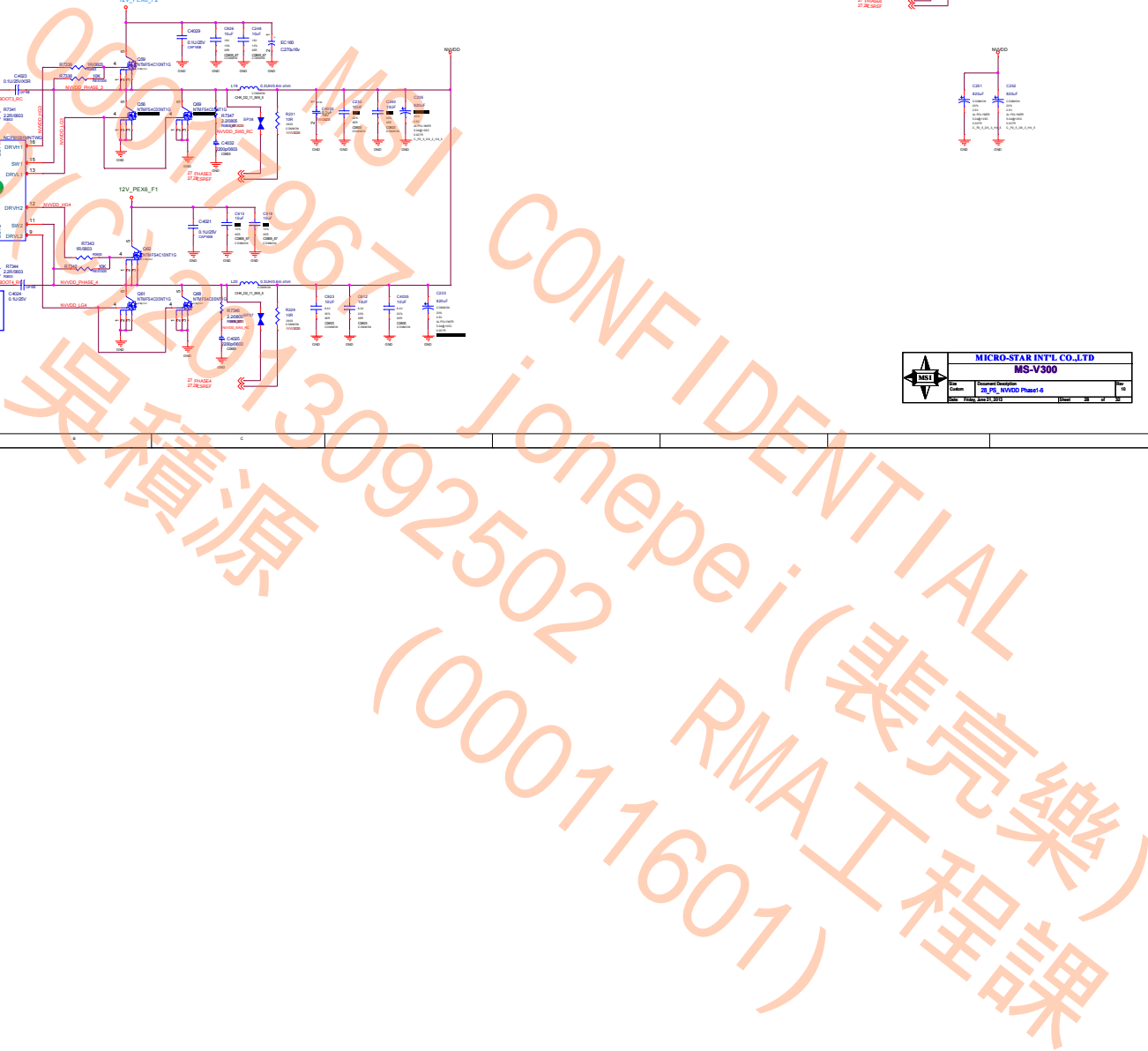




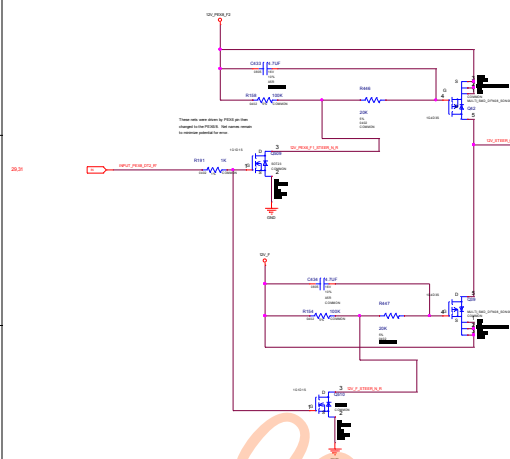


ci20313

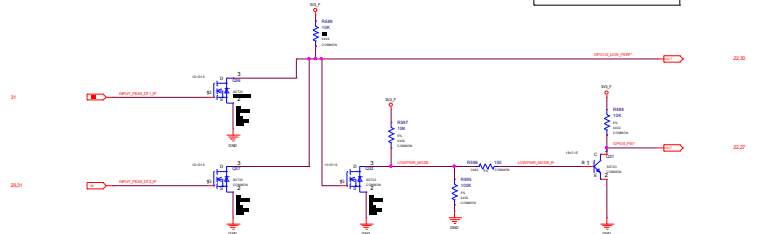
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MS-V300	
Doc No.	MS-V300_Supply
Rev.	1.0
Date	2013.09.25
By	ci20313
Check	ci20313



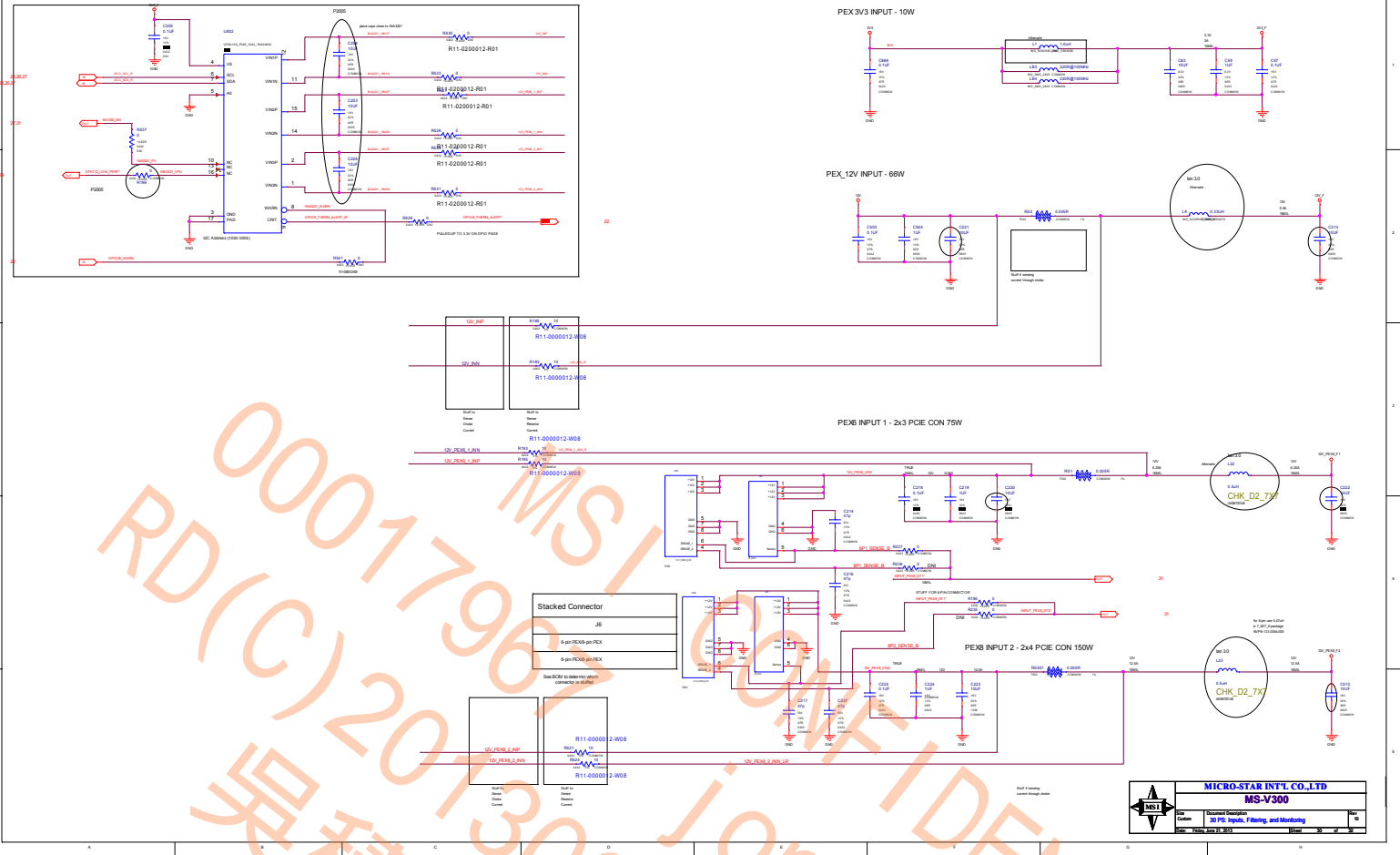
PEX Input - 12V Current Steering FETs

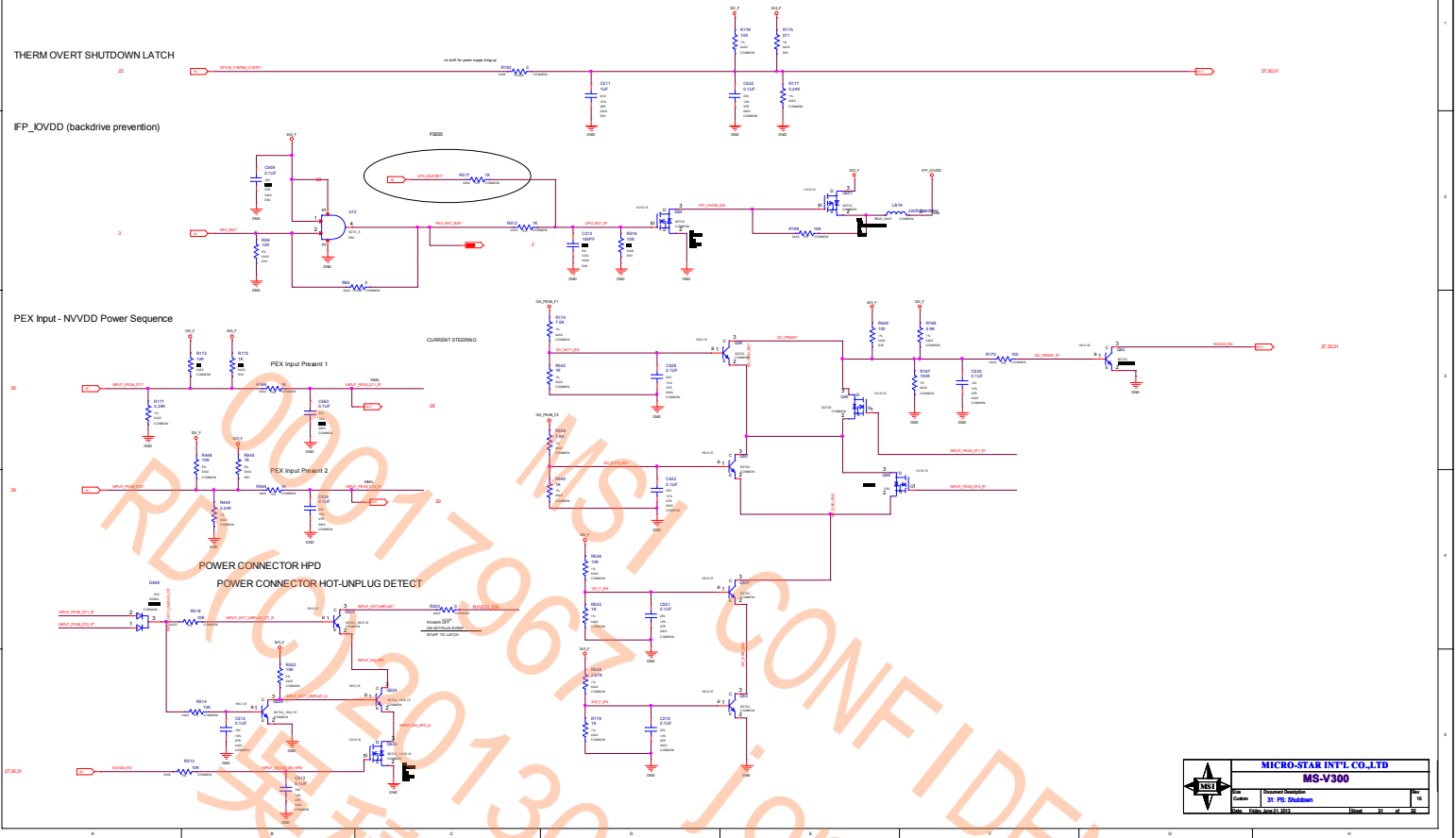


PEX Input - Power Level/PSI Control



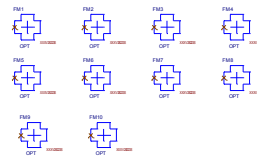
REMOVE GeForce Logo LED





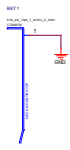
MICRO-STAR INT'L CO., LTD	
MS-V300	
Doc. No.	PS: Shutdown
Rev.	1.0
Date	2008-09-25

optical cross point



Top & bottom each layer set 4 points

Brackets:



Bracket Screw



Hockey Stick



GPU Stiffner



Mechanical Holes Symbol

