

A

B

C

D

E

F

A

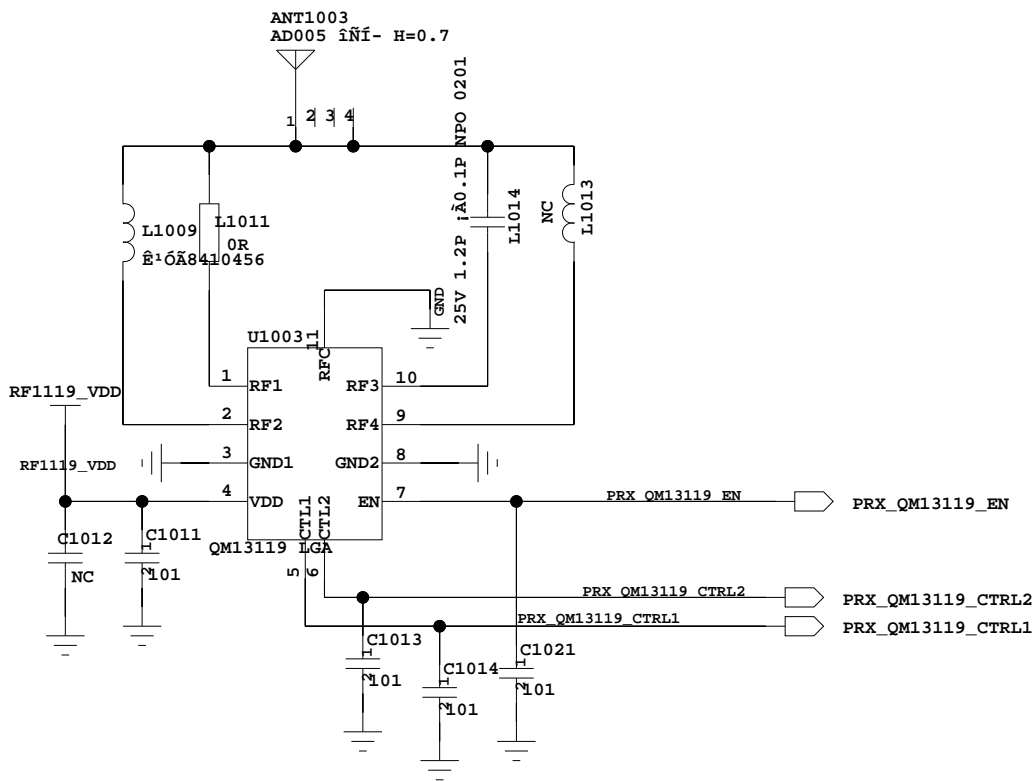
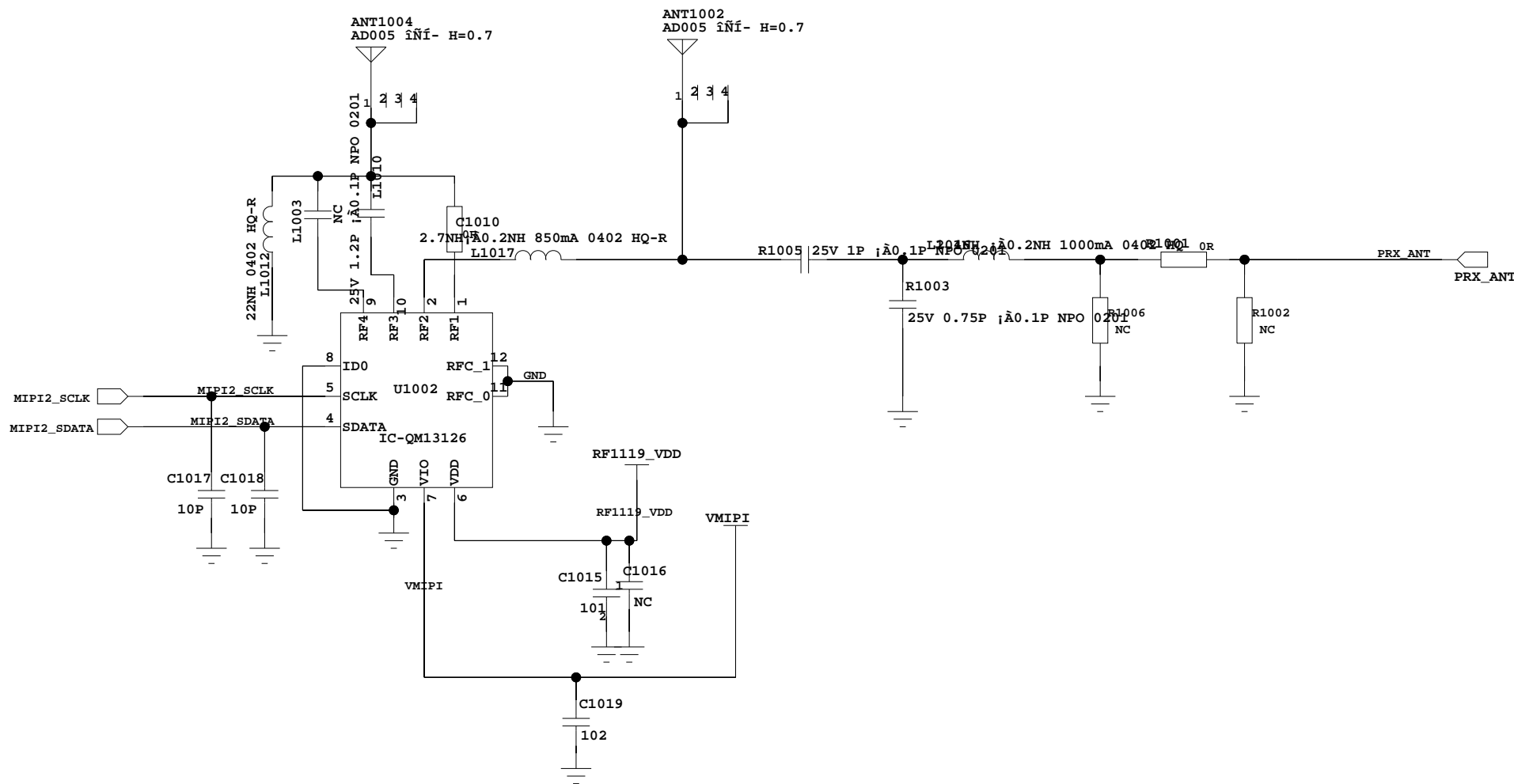
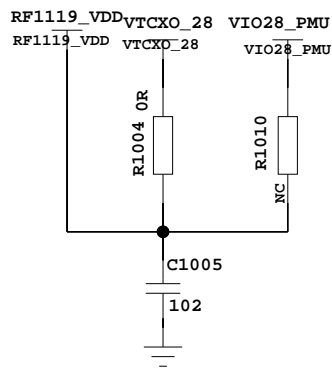
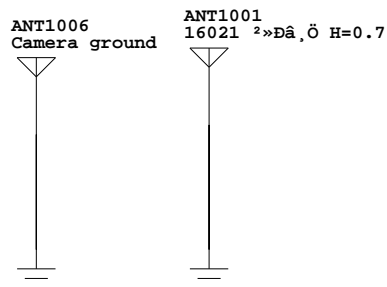
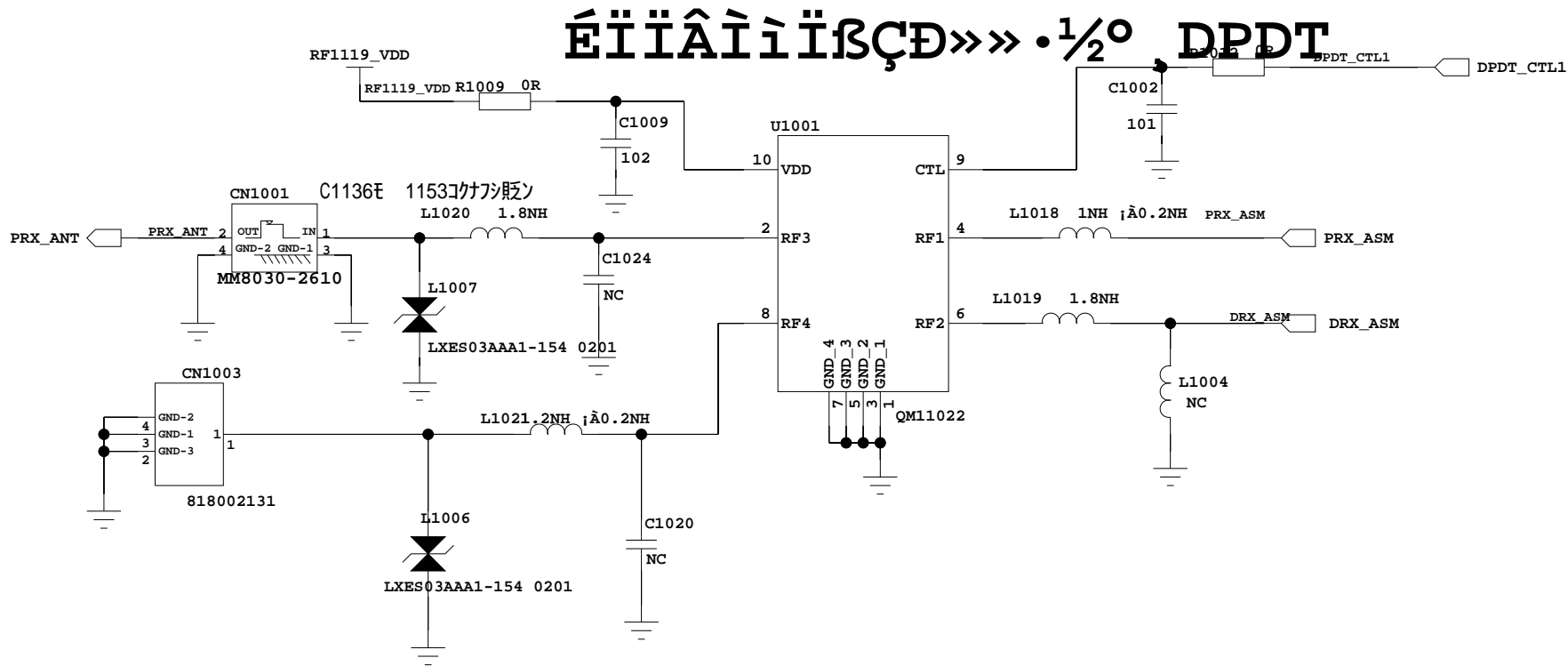
B

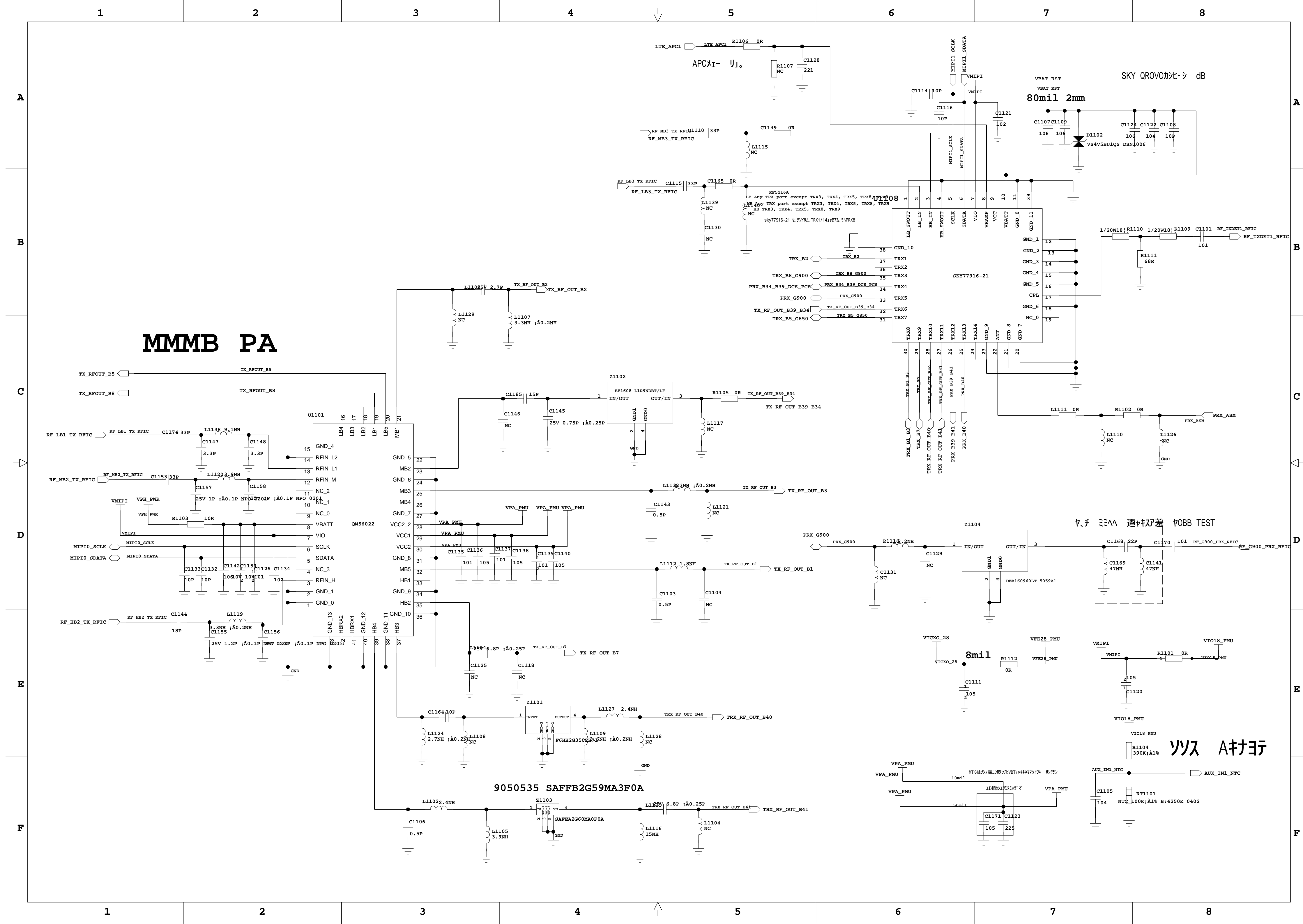
C

D

E

F

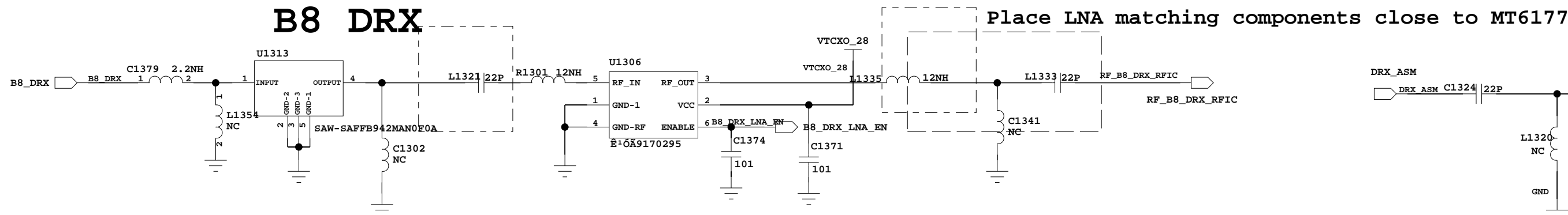




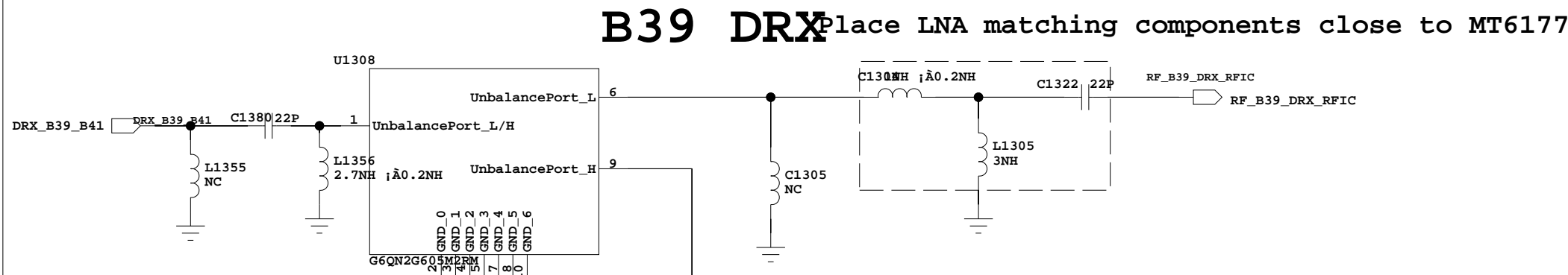


# LTE DRX

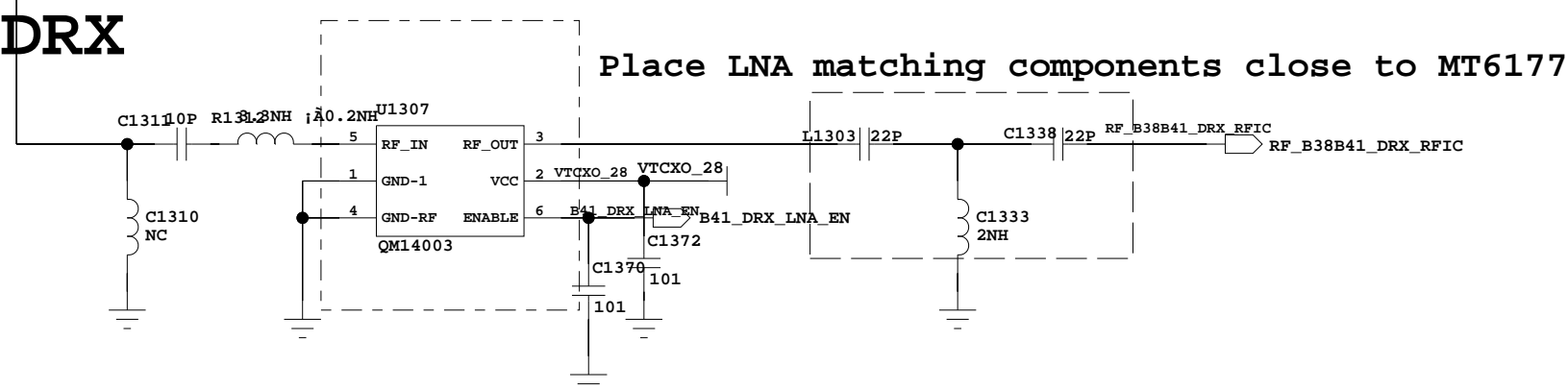
## B8 DRX



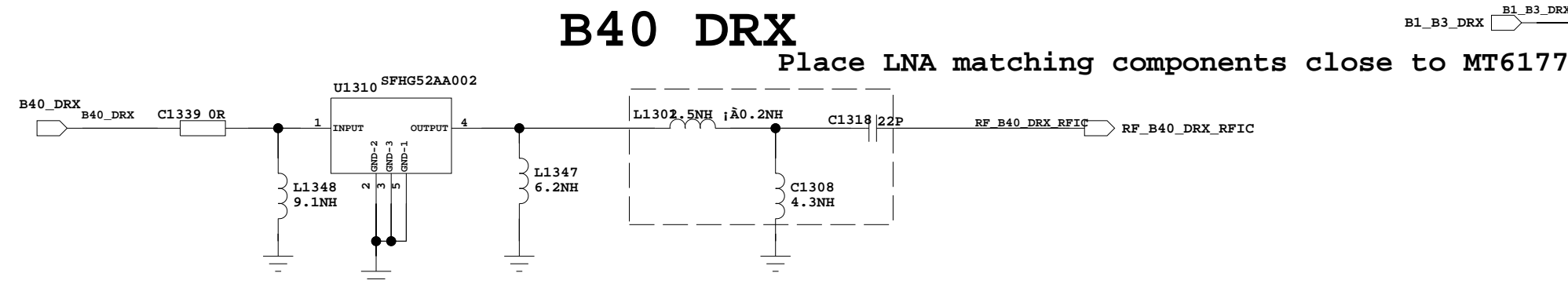
## B39 DRX



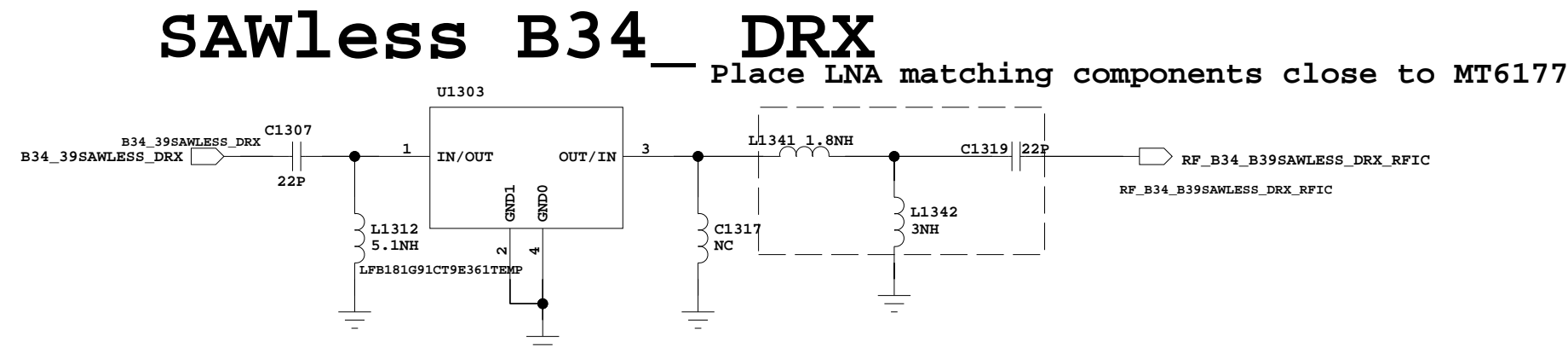
## B38B41 DRX



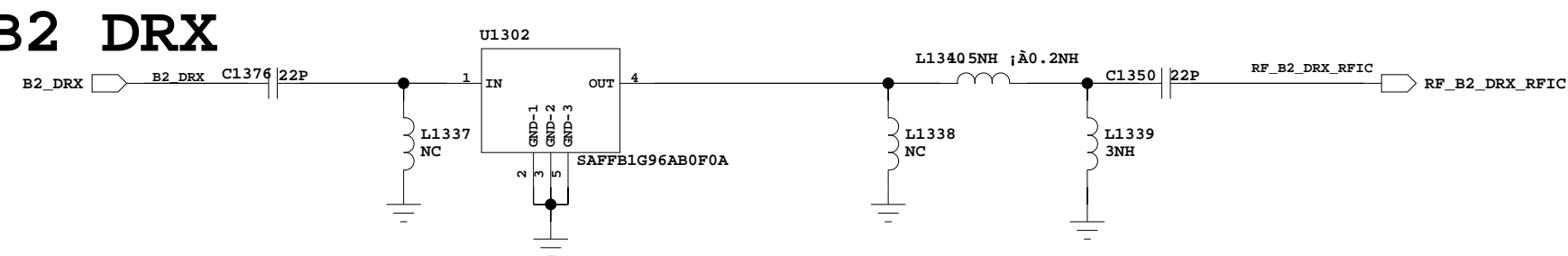
## B40 DRX



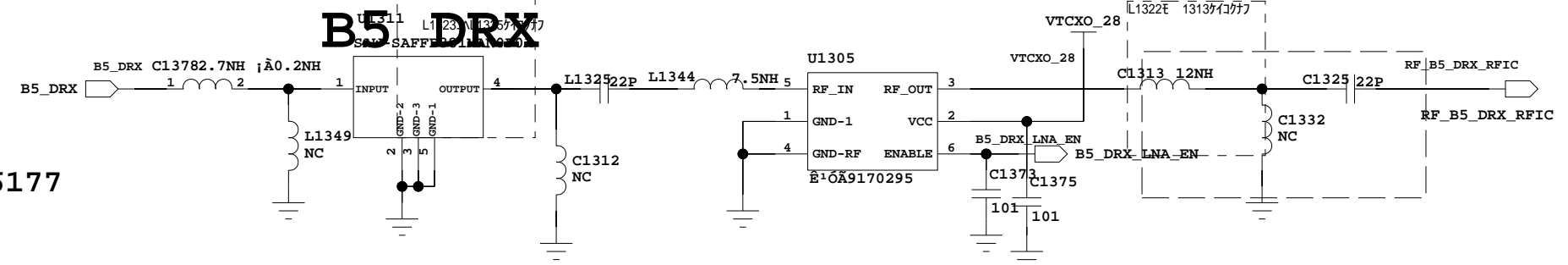
## SAWless B34 DRX



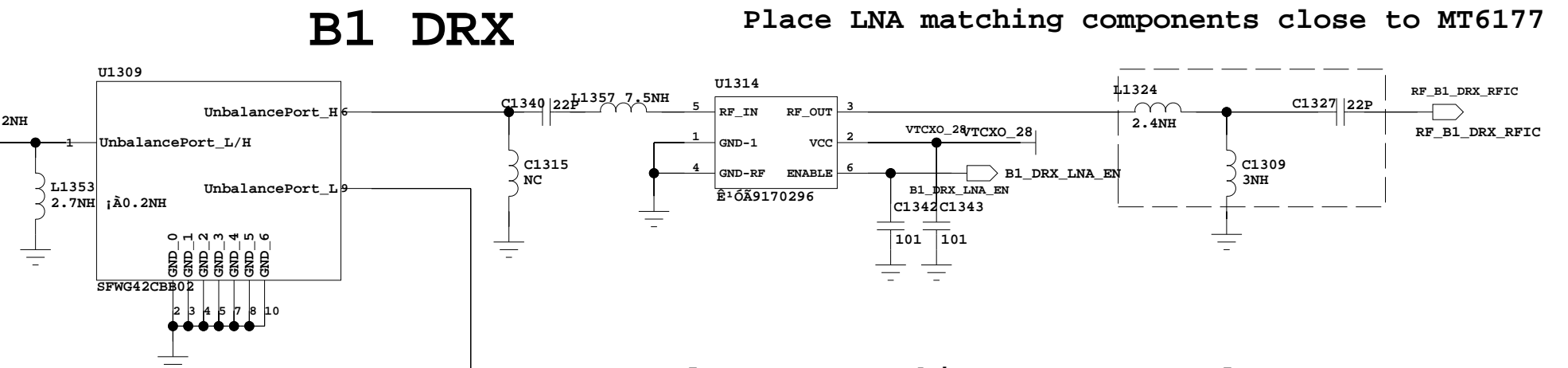
## B2 DRX



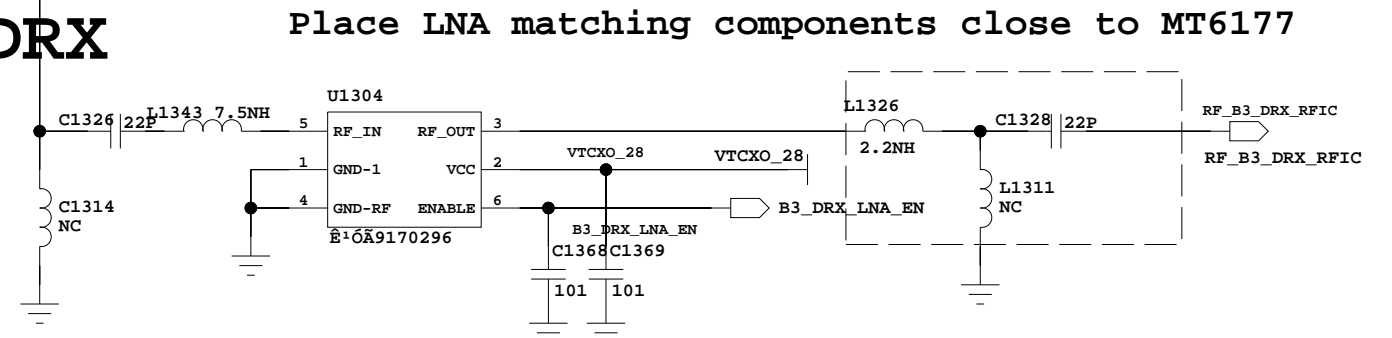
## B5 DRX



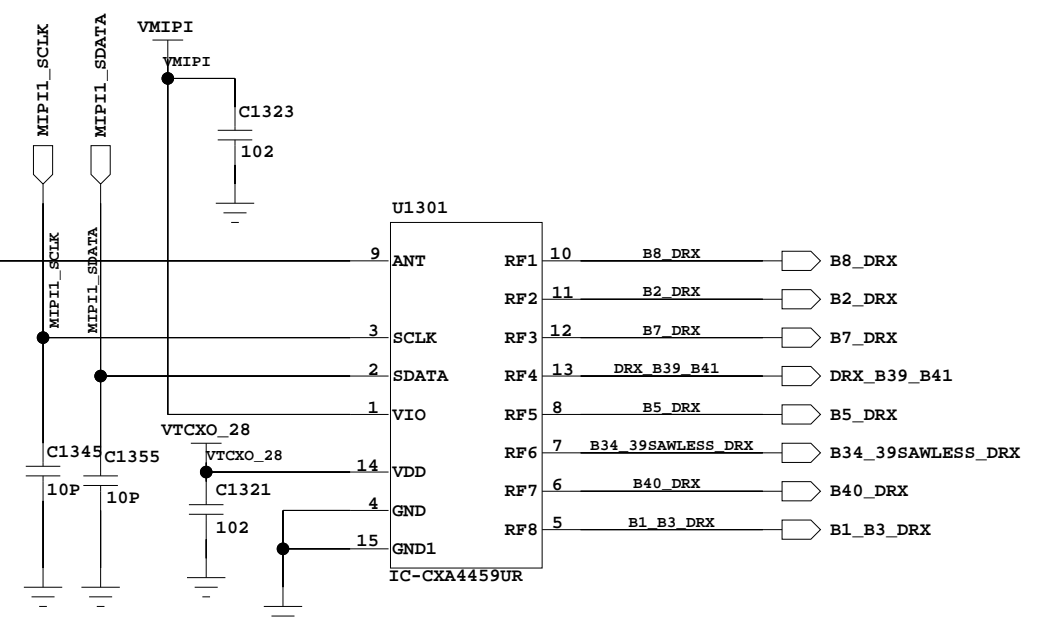
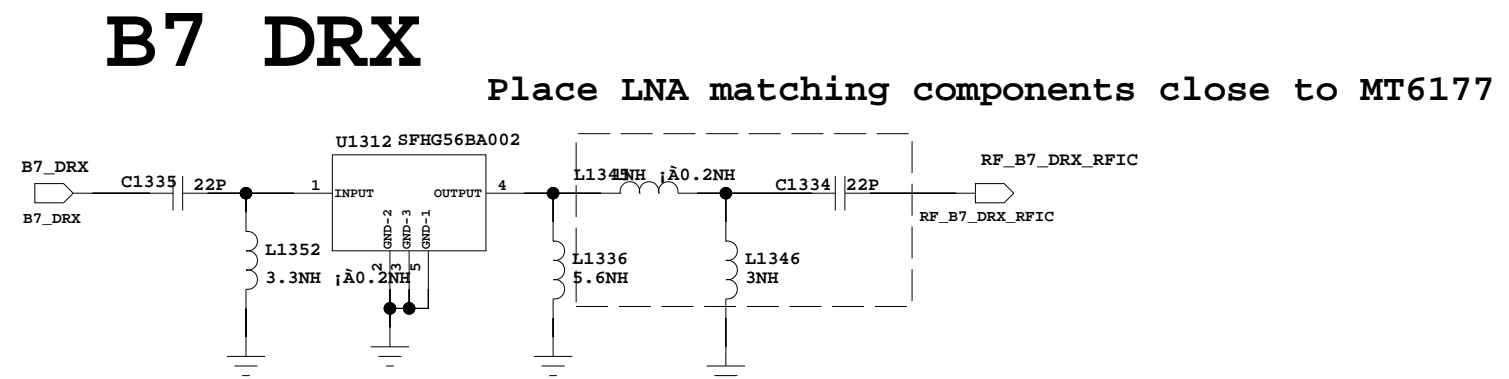
## B1 DRX

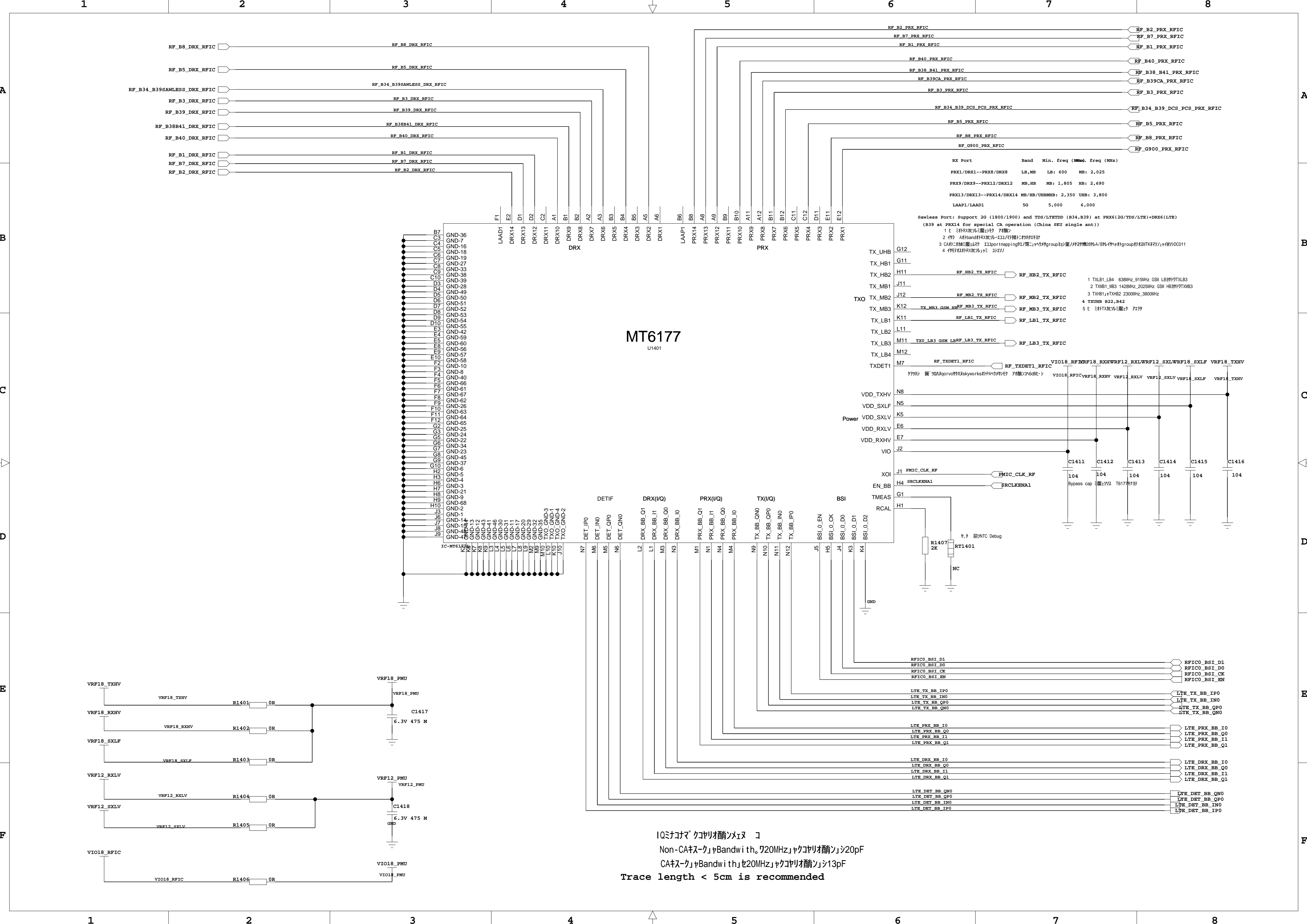


## B3 DRX



## B7 DRX





# 2.4G\_WIFI

## 5G\_WIFI

GPS\_AVDD18 20mA $\leq$ 10m

WB\_AVDD18 50mA $\leq$ 12m

VCN18\_PMU 80mA $\leq$ 15m

VCN33\_PMU 300mA $\leq$ 25m

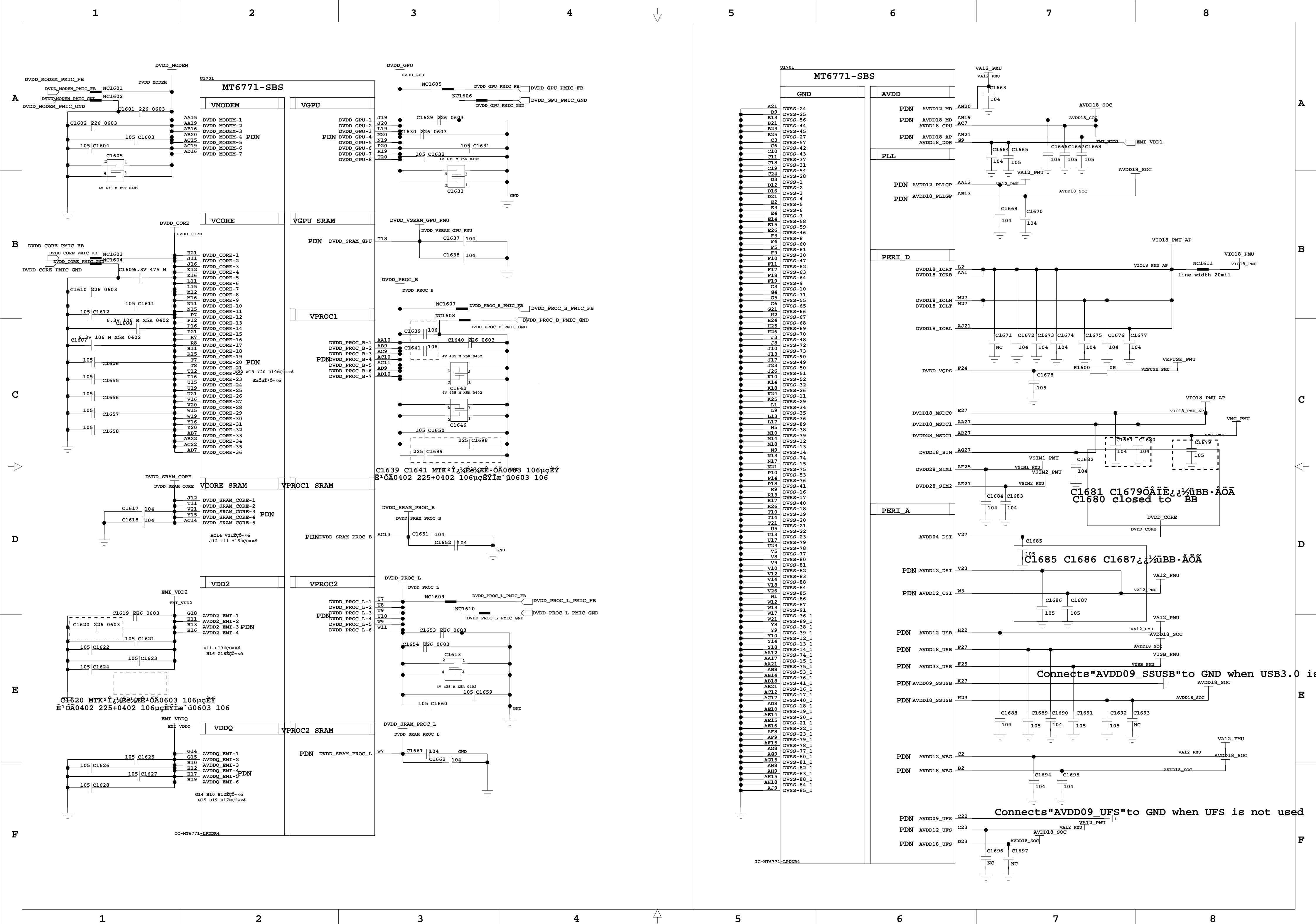
VCN28\_PMU 20mA $\leq$ 10m

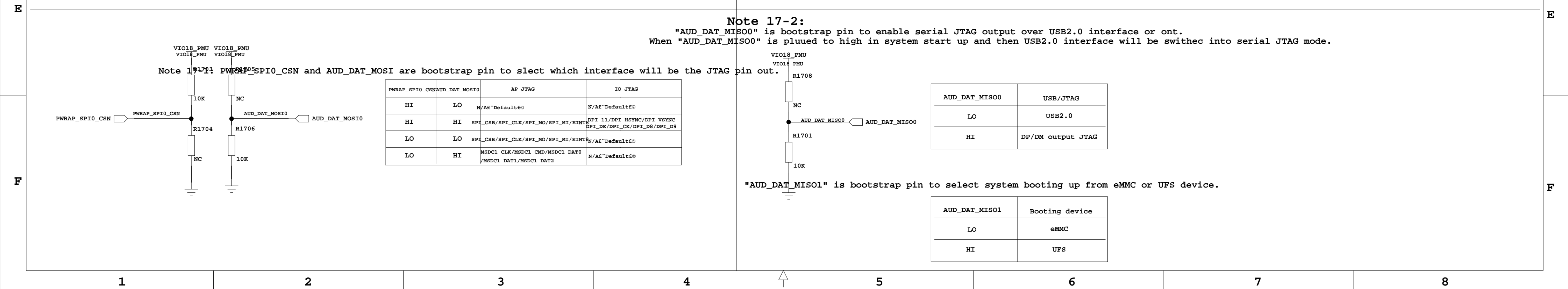
$I_{Q\dot{I}B^3\acute{E}\P\hat{O}^{\circ}\ddot{u}\mu\emptyset\acute{\neg}\acute{\neg}7\acute{\neg}40\acute{\neg}\textcircled{mm}}$

$VCN33\textcircled{D}\grave{e}\grave{O}^a20mil\grave{;}\acute{í}\acute{\neg}^3\alpha\grave{;}\acute{í}\pm\grave{E}\textcircled{D};\acute{O}\acute{U}310\acute{\neg}\neg^1\acute{y}\grave{;}\times\acute{A}^{\frac{1}{2}},\grave{o}\grave{O}\hat{O}\acute{E}\acute{I}$

$VCN33\textcircled{D}\grave{e}\grave{O}^a\circ^{\circ}\acute{O}\acute{O}300mA\text{ Peak}\mu\grave{\varsigma}\acute{A}\div\acute{A}^{\circ}\acute{A}\acute{A}^1\acute{A}$

$VCN33\mu\grave{\varsigma}\hat{O}^{\circ}\grave{;}\acute{í}\P\grave{E}^2\gg\acute{A}\acute{u}\times\grave{a}\grave{O}^a\grave{\varsigma}\acute{o}\gg\acute{a}\mu^{\frac{1}{4}}\acute{O}\acute{A}WiFi\text{ TX POWER}^2\gg\times^{\frac{1}{4}}\text{or EVM}\P\grave{n}\gg^{\circ}$

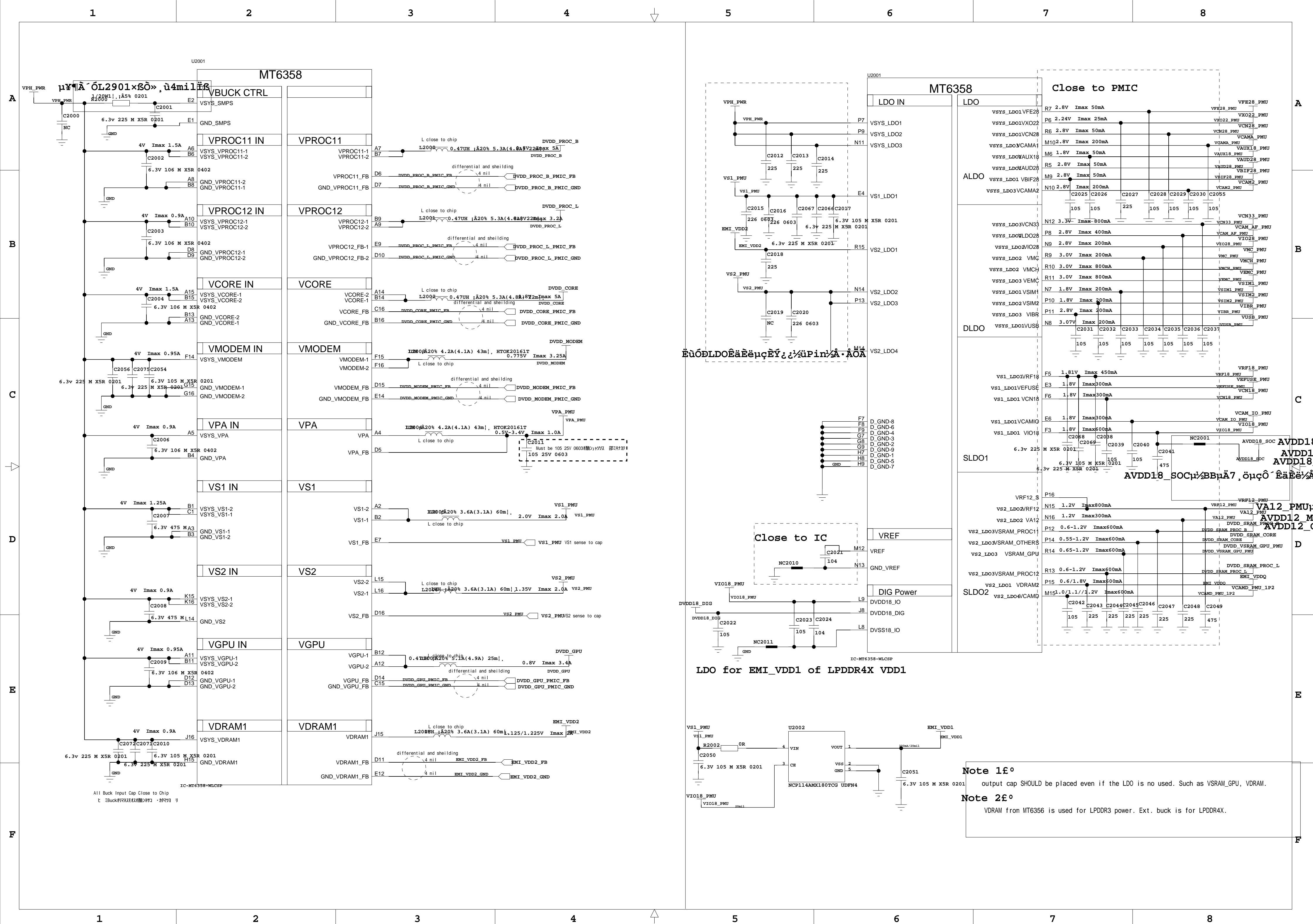


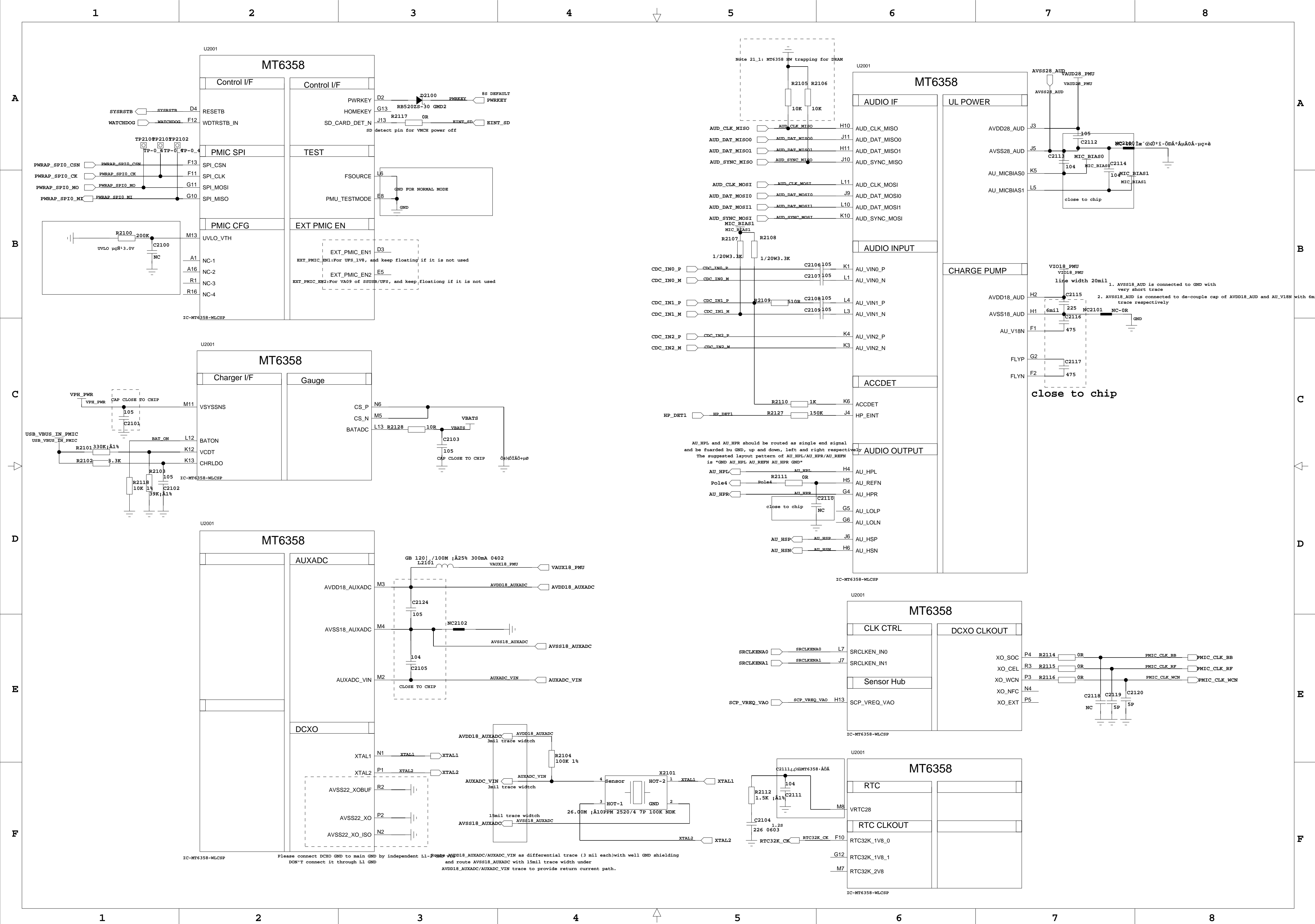




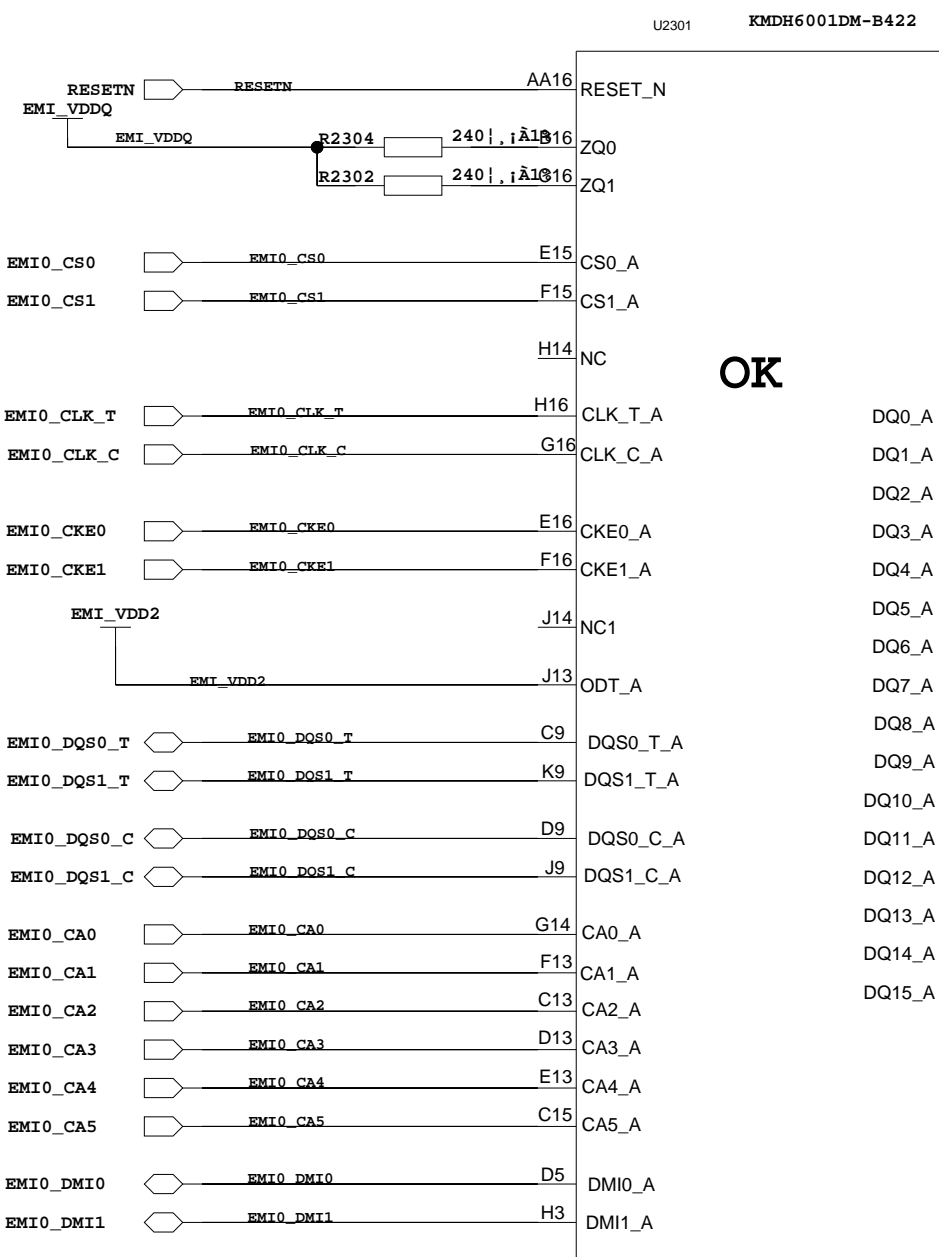




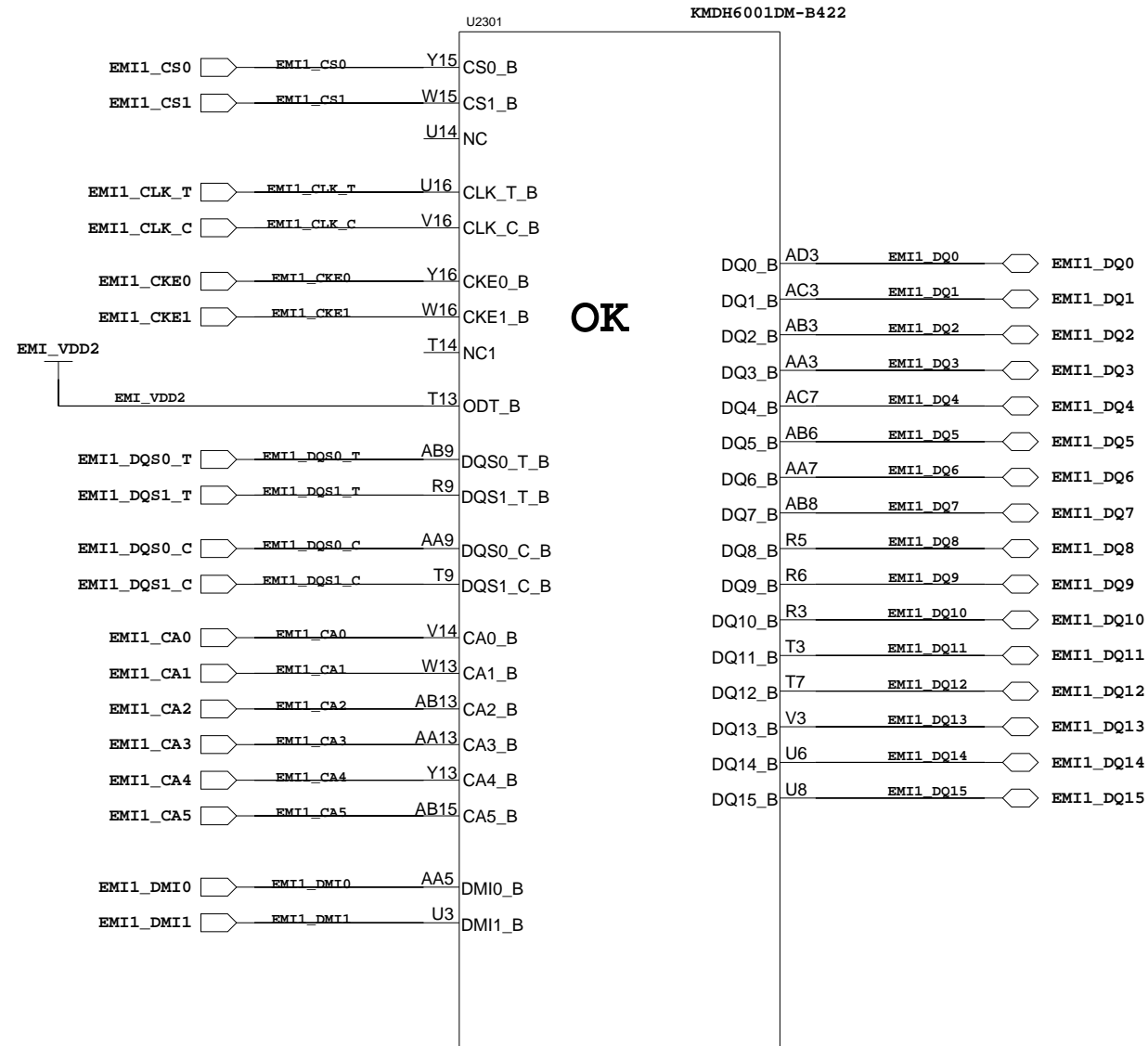




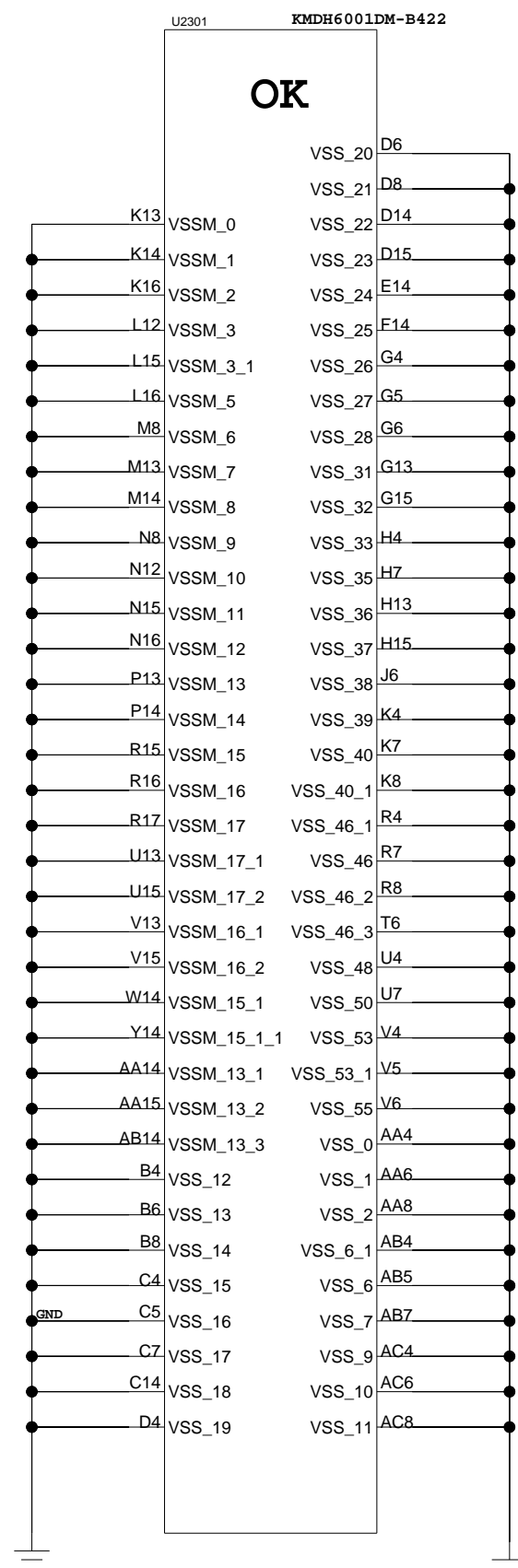
# EMCP\_EBI0



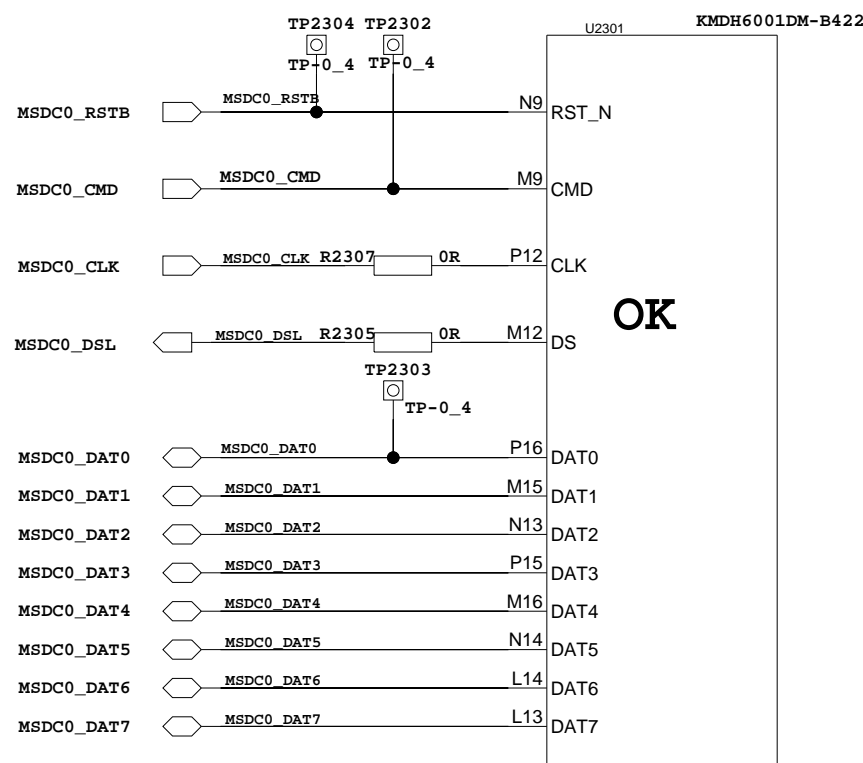
# EMCP\_EMI1



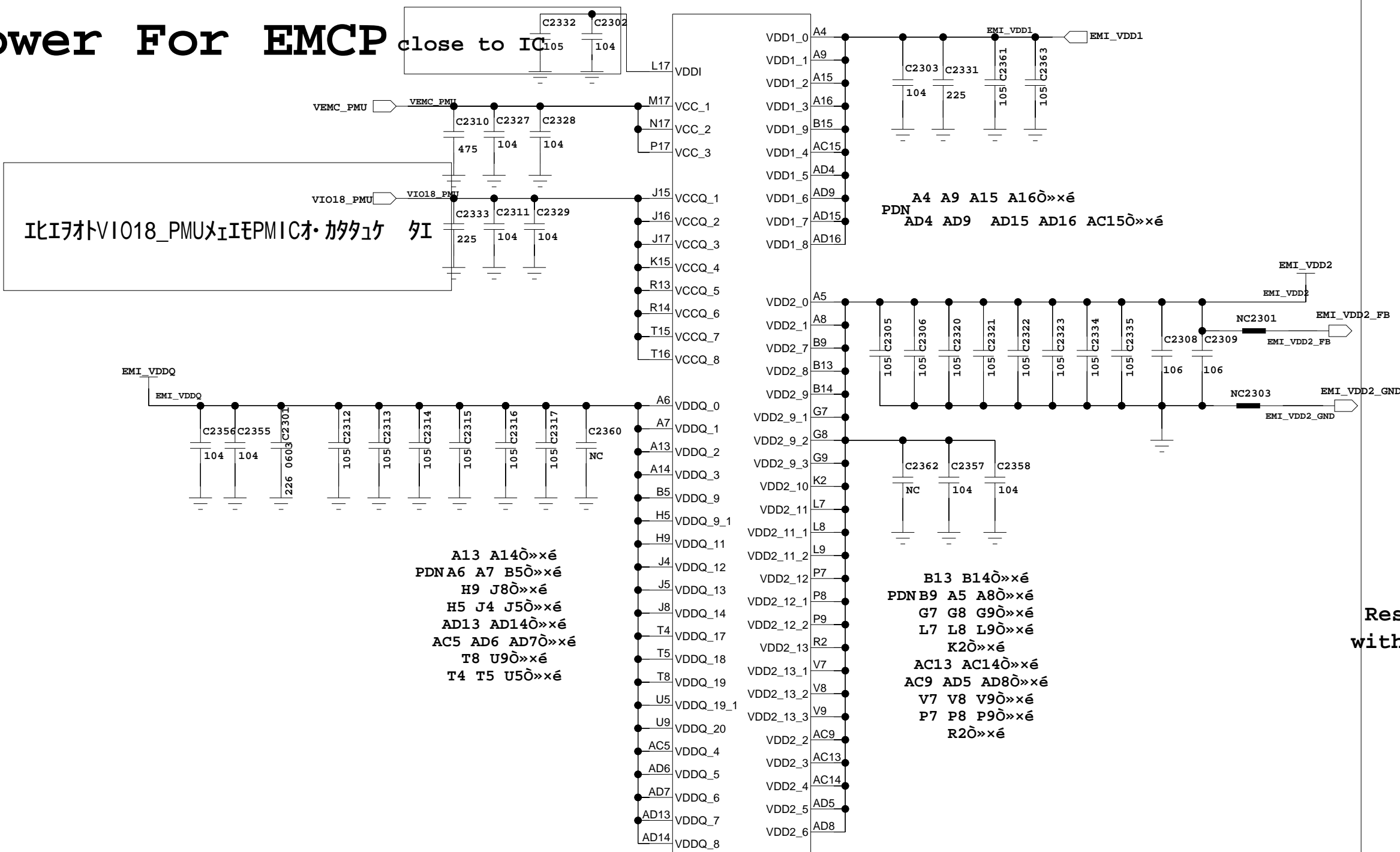
# EMCP\_GND



# EMCP\_SDC



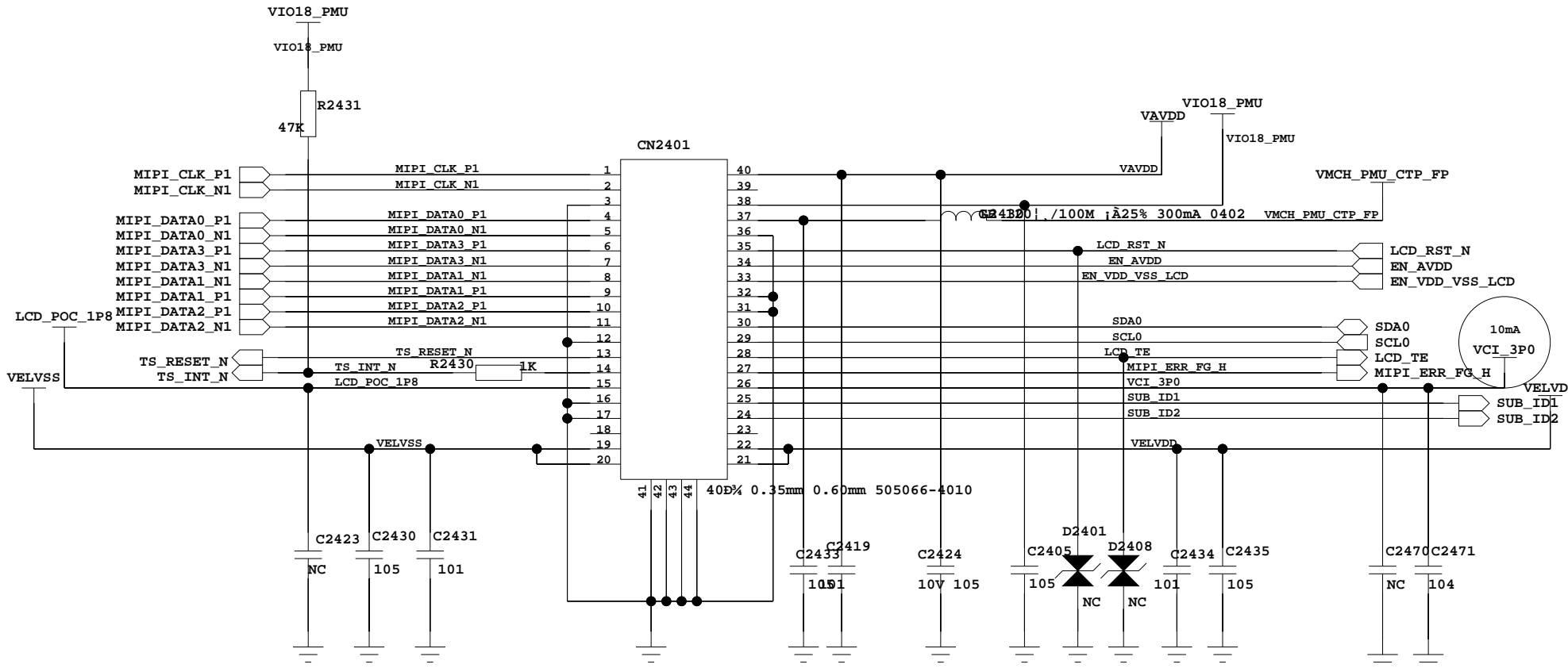
# Power For EMCP



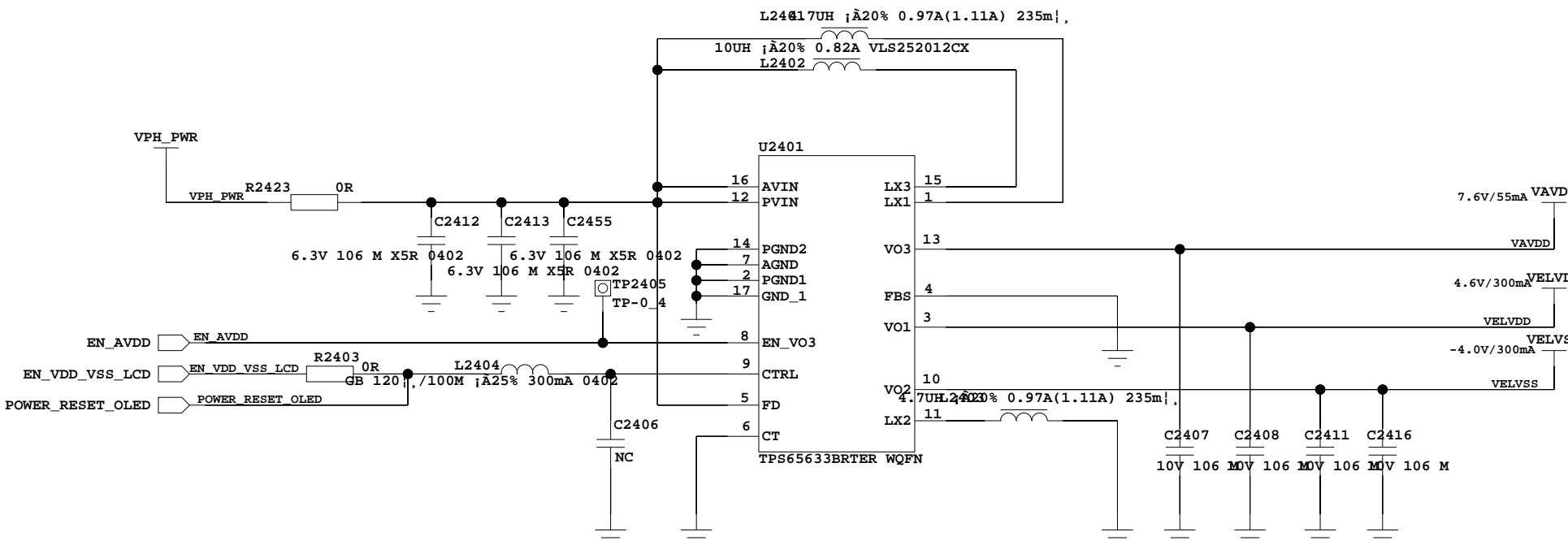
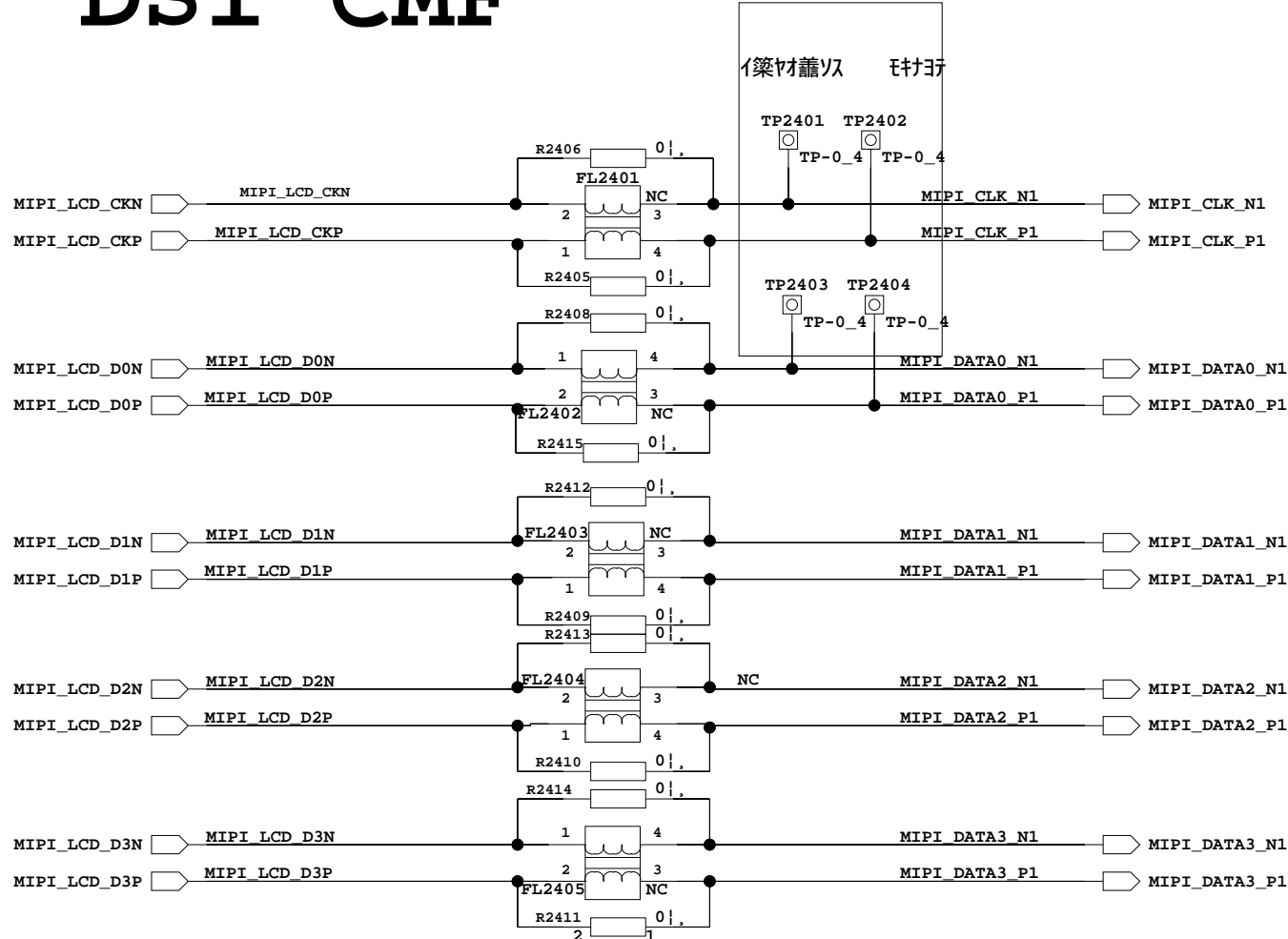
Reserved for DRMA  
with 3-die integrated

# LCM Connector

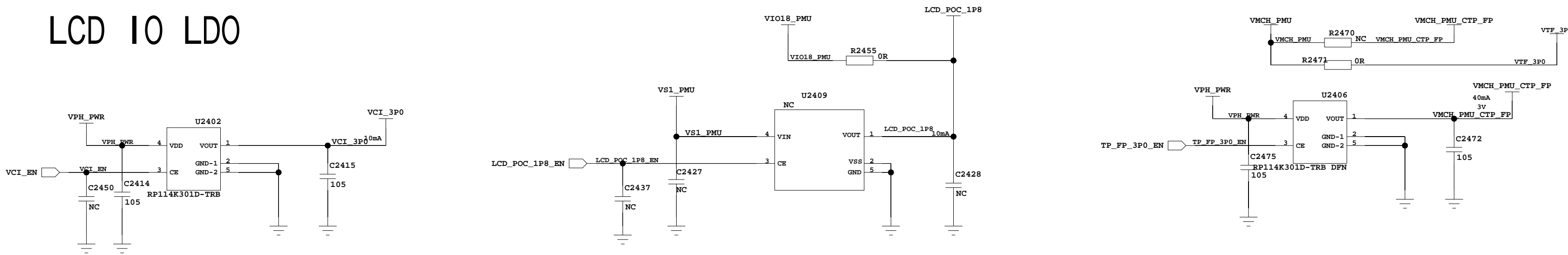
TD4310 IIC address「R:0x21; W:0x20」



# DSI CMF

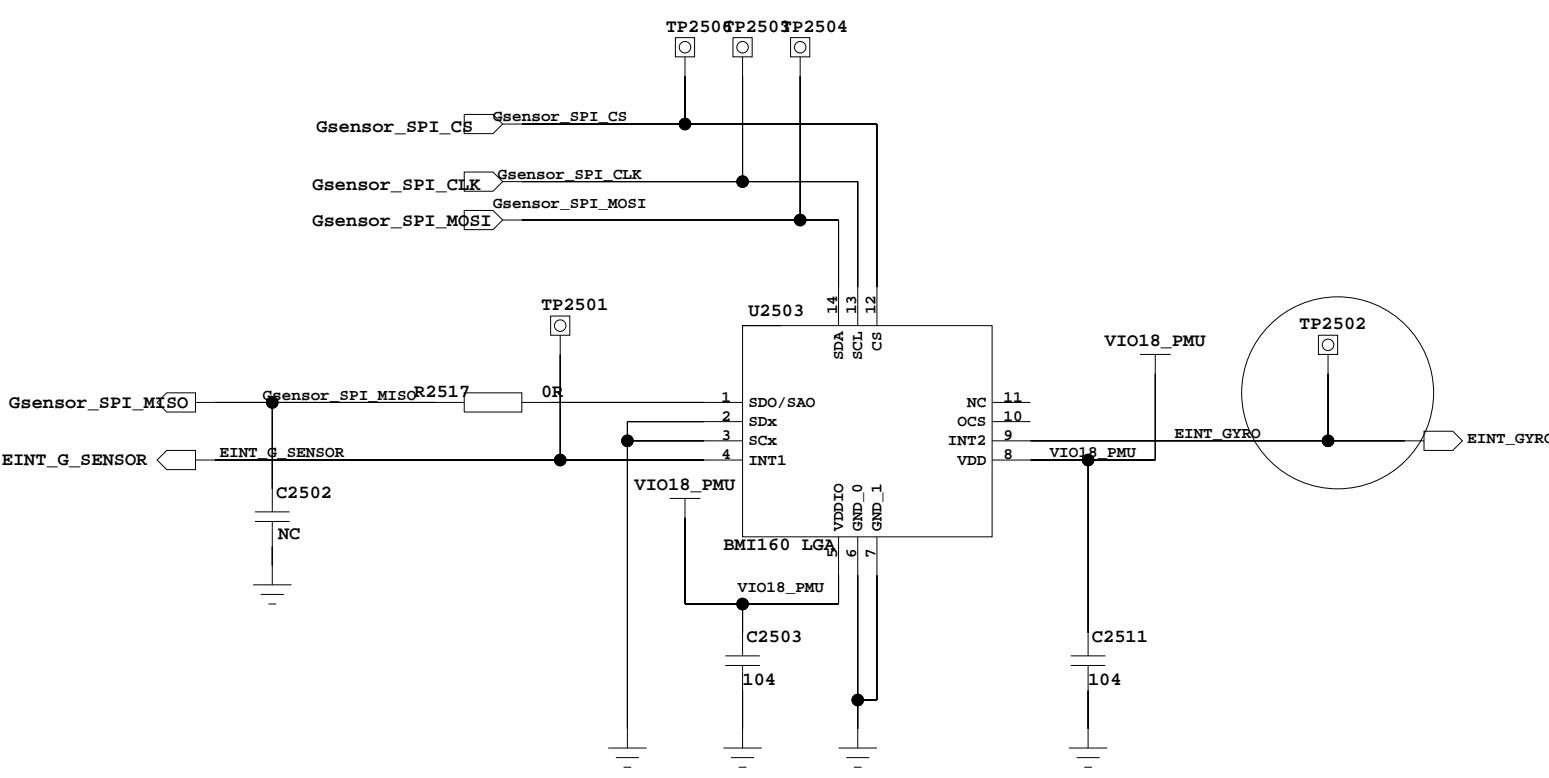


LCD IO LD0



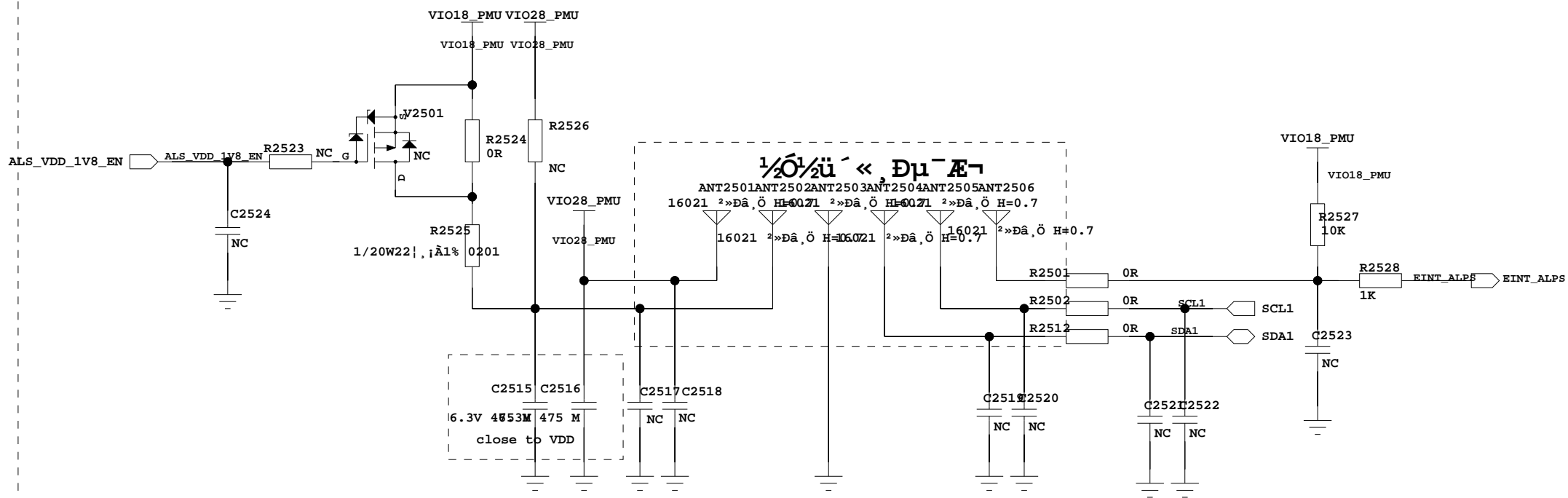
G-Sensor

LIS3DH I2C Address (R:0x33; W:0x32)



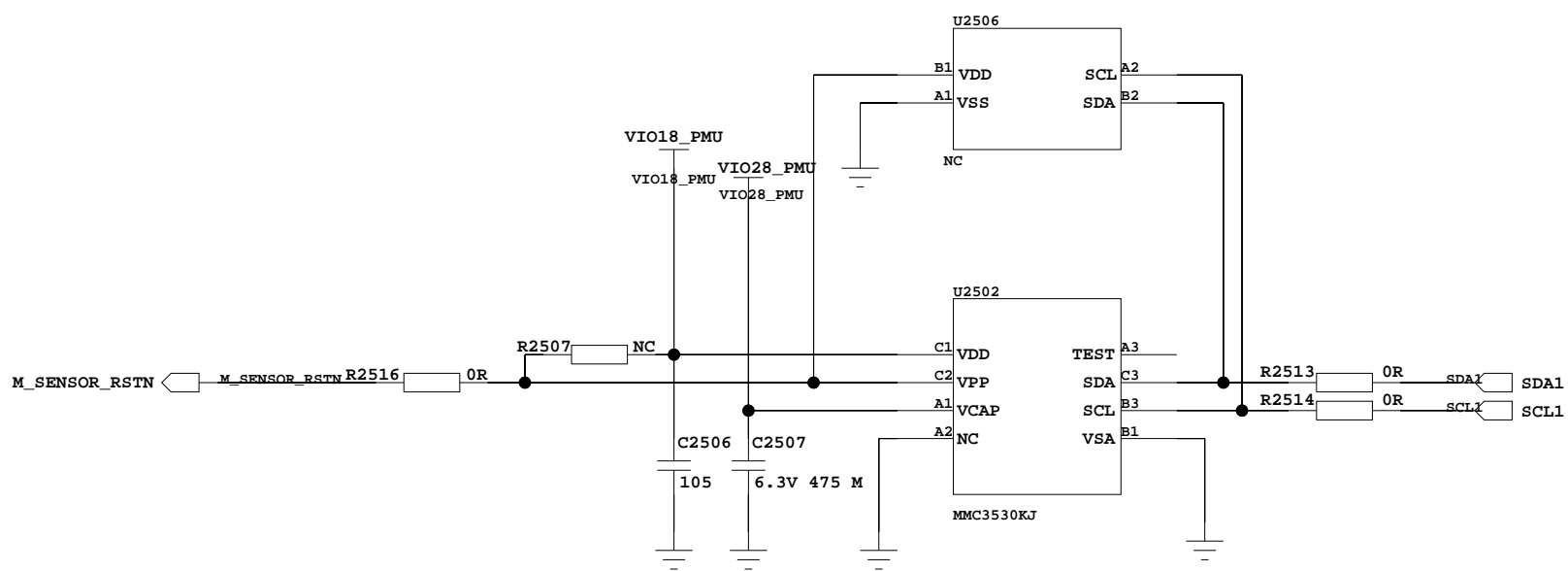
A.L.S.+P.S sensor

TMD2725 0X72(㇏)/0X73(㇏)

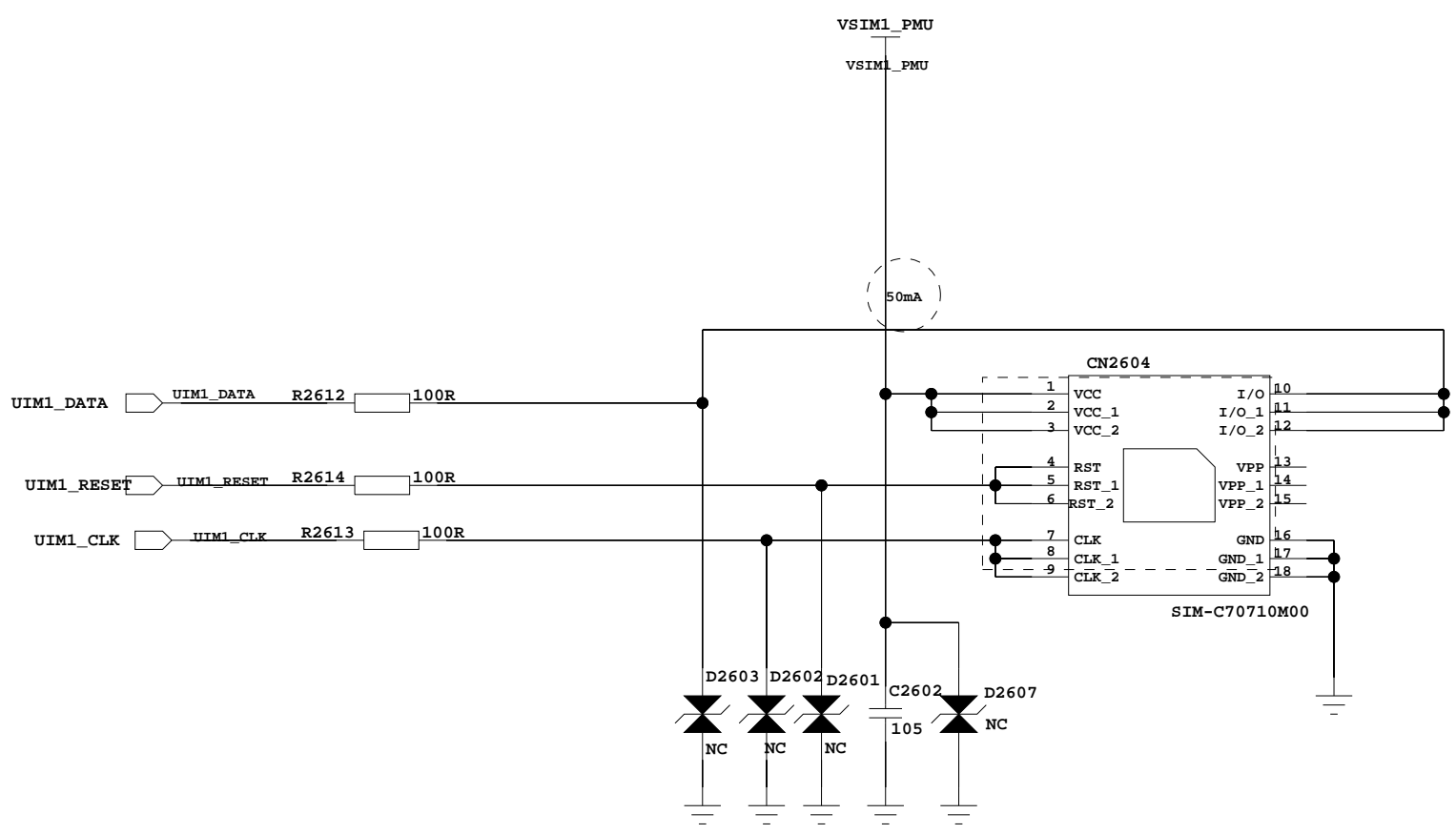


M Sensor

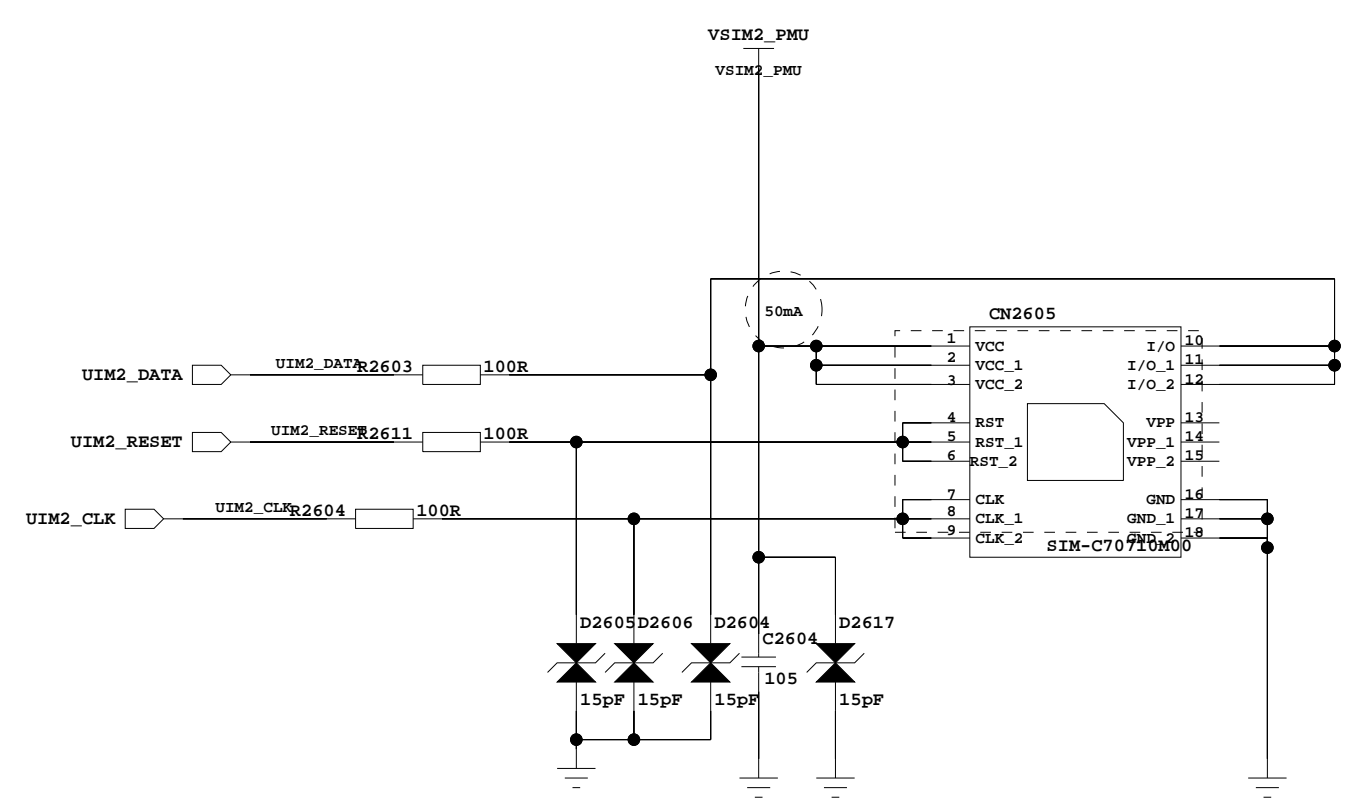
AK09911 I2C Address (R:0x19; W:0x18)



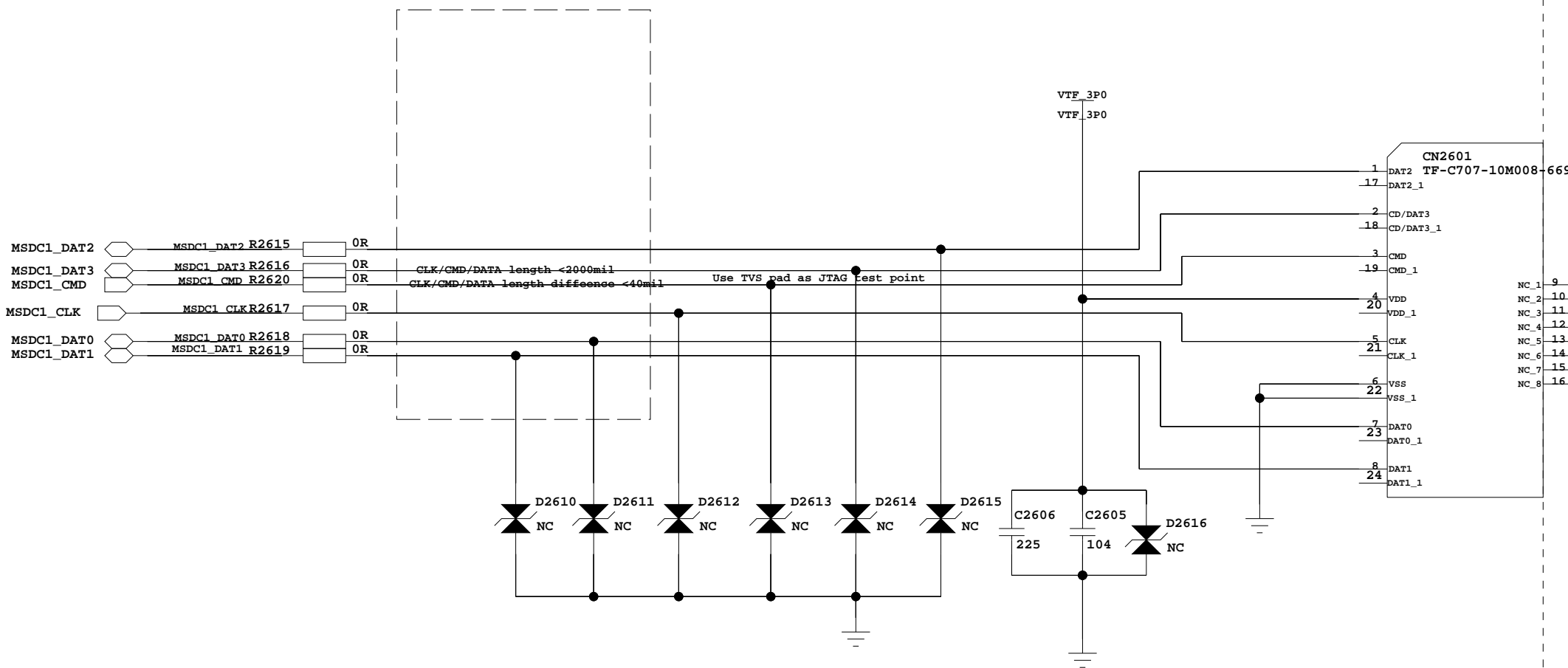
SIM1 Card



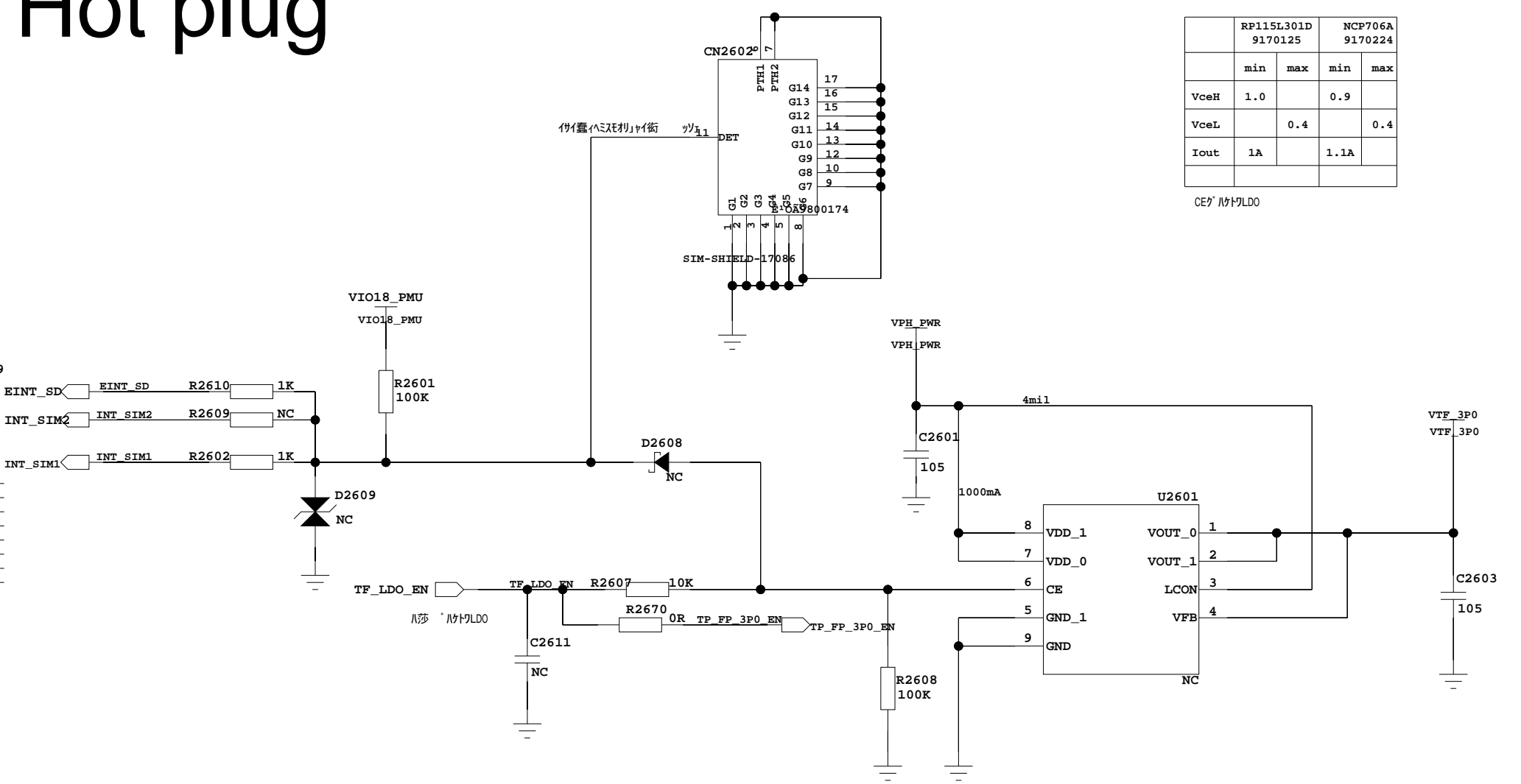
SIM2 Card



TF\_CARD



Hot plug Ext 2.95V Supply



	RP115L301D 9170125		NCP706A 9170224	
	min	max	min	max
VceH	1.0		0.9	
VceL		0.4		0.4
Iout	1A		1.1A	

CEP / 1019LDO

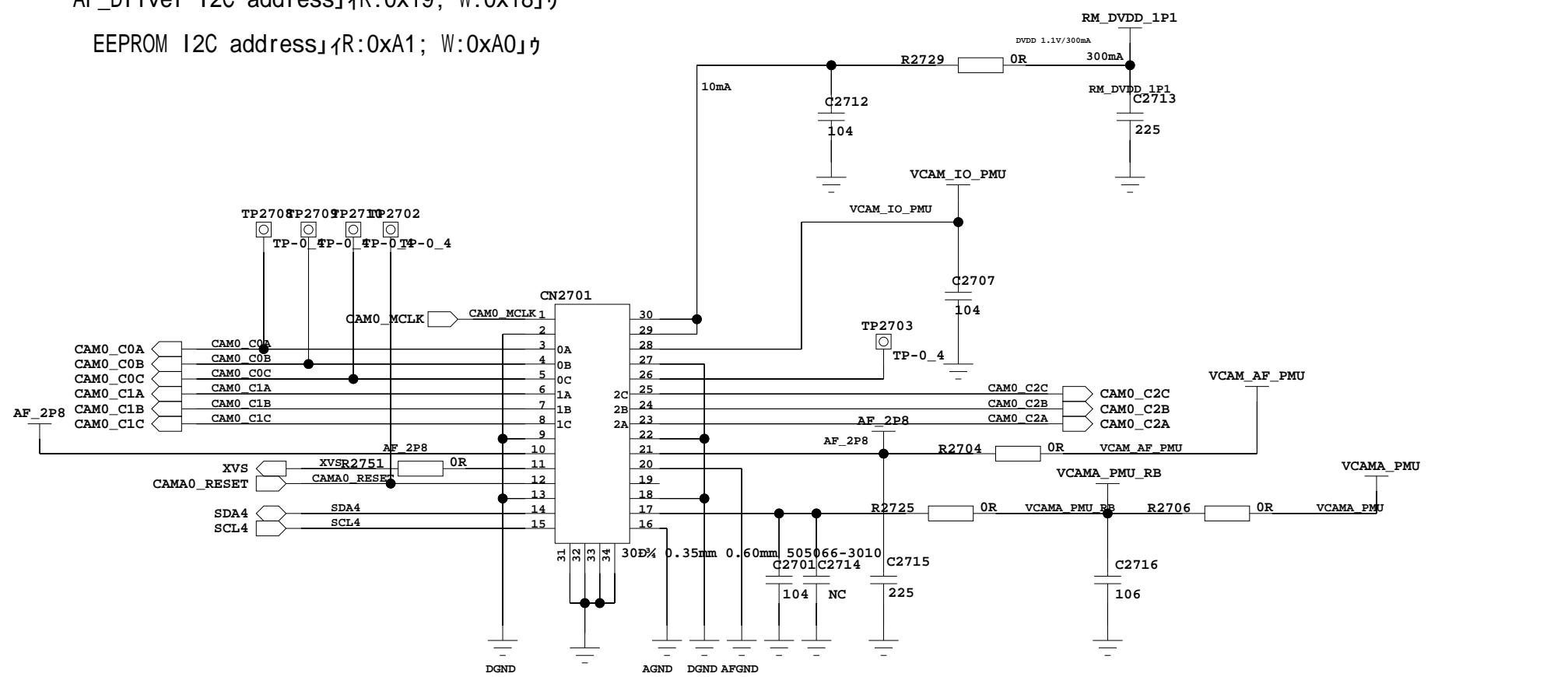


# Main CAMERA

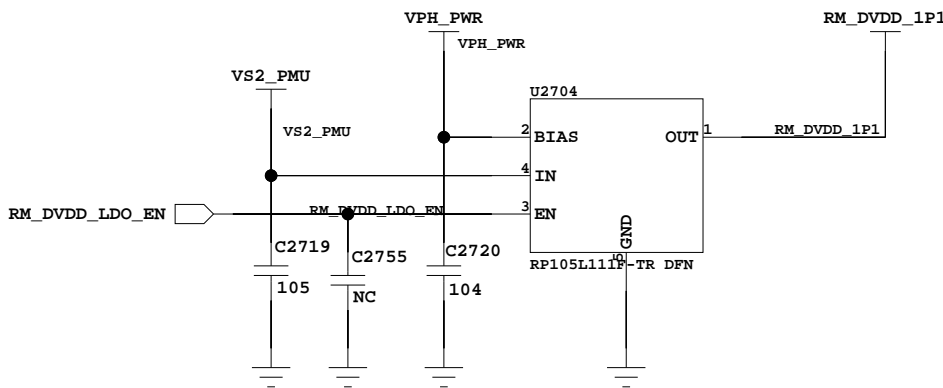
IMX398 I2C address<sub>1</sub>R:0x35; W:0x34<sub>1</sub>

AF\_Driver I2C address<sub>1</sub>R:0x19; W:0x18<sub>1</sub>

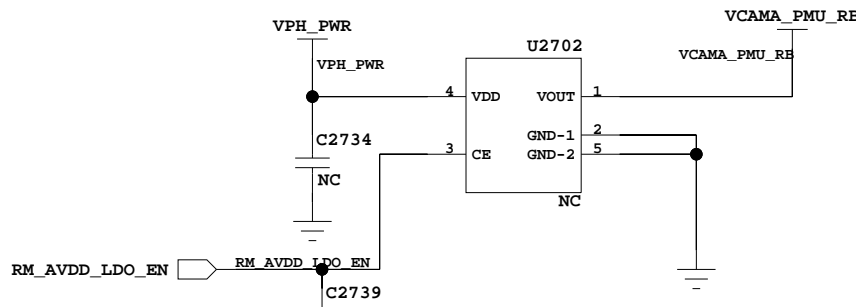
EEPROM I2C address<sub>1</sub>R:0xA1; W:0xA0<sub>1</sub>



## REAR DVDD

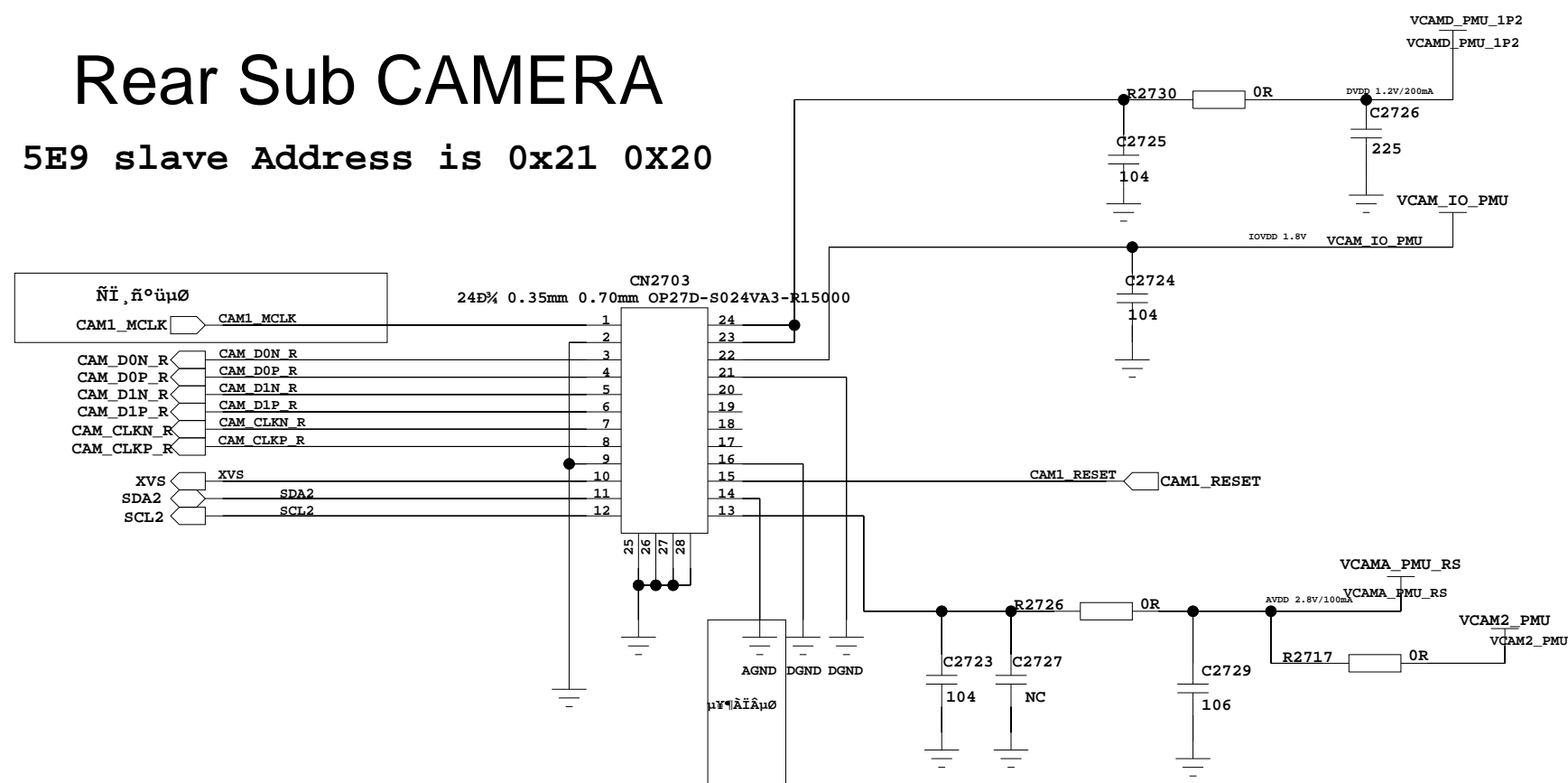


## REAR AVDD

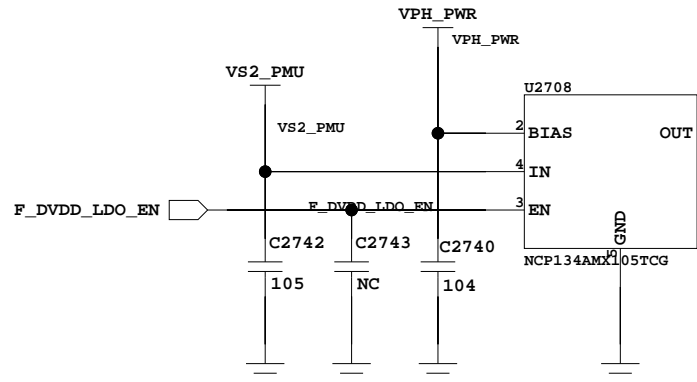


# Rear Sub CAMERA

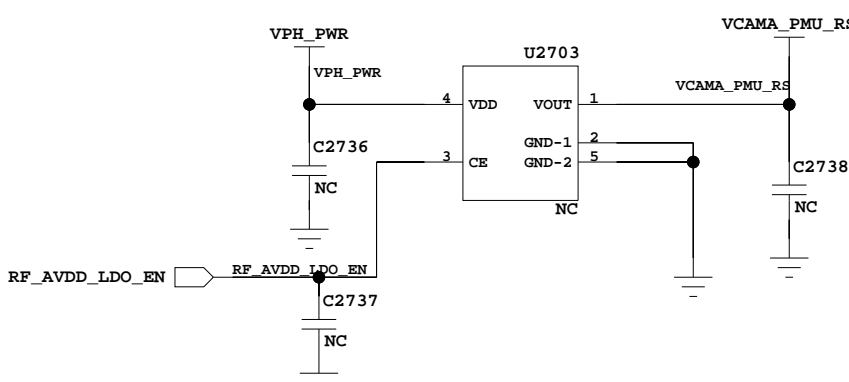
5E9 slave Address is 0x21 0x20



## FRONT DVDD

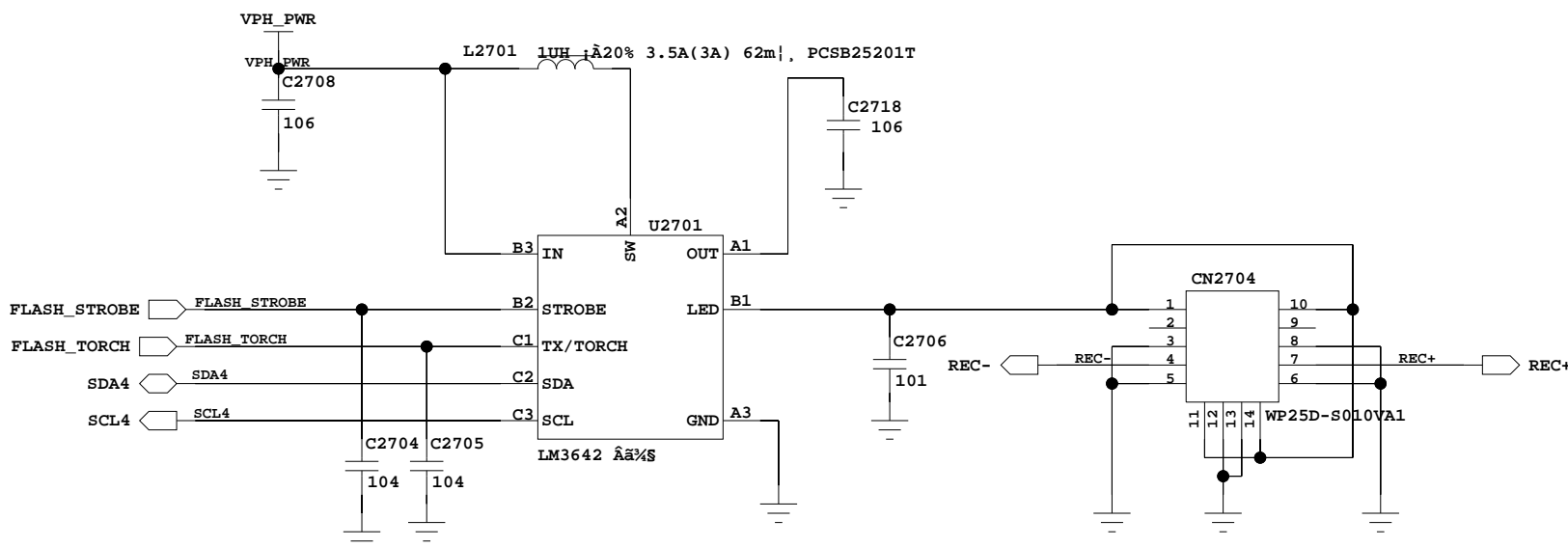


## REAR SUB&FRONT AVDD



# Flash LED Driver

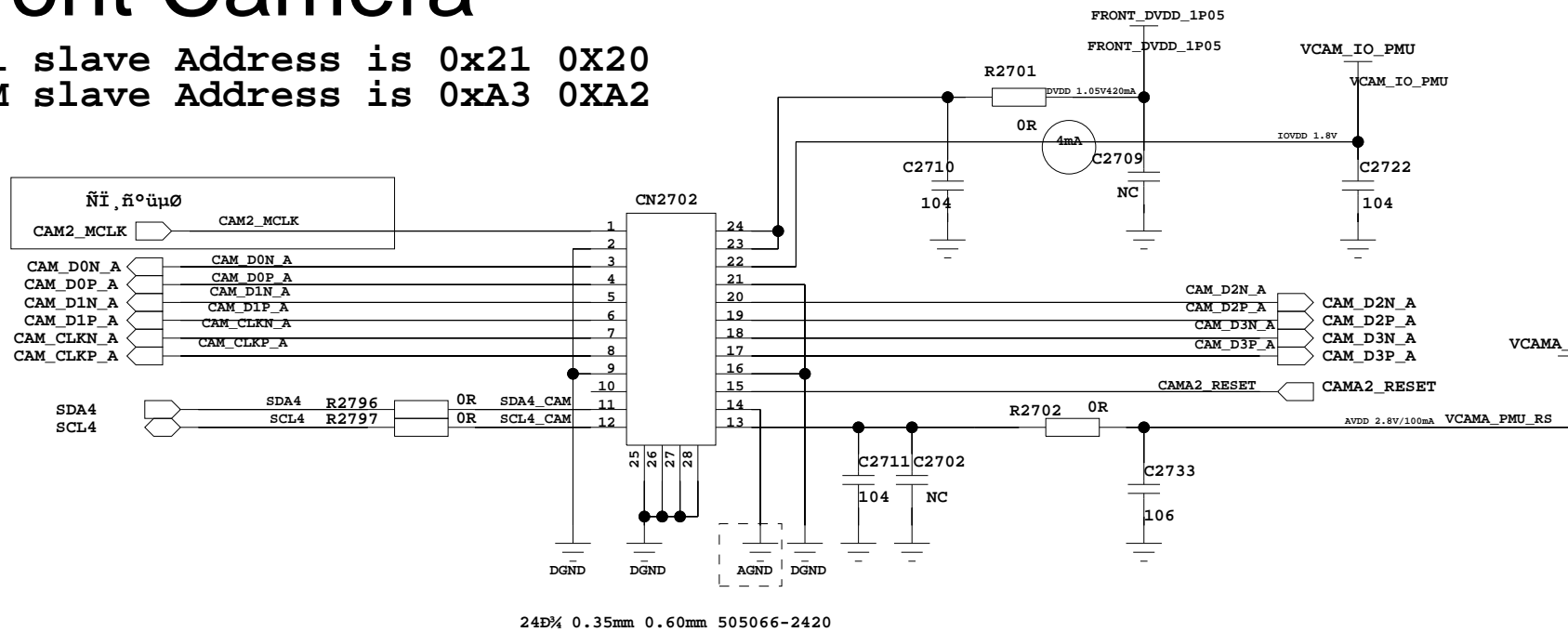
LM3642 slave Address is 0xC6 0XC7



# Front Camera

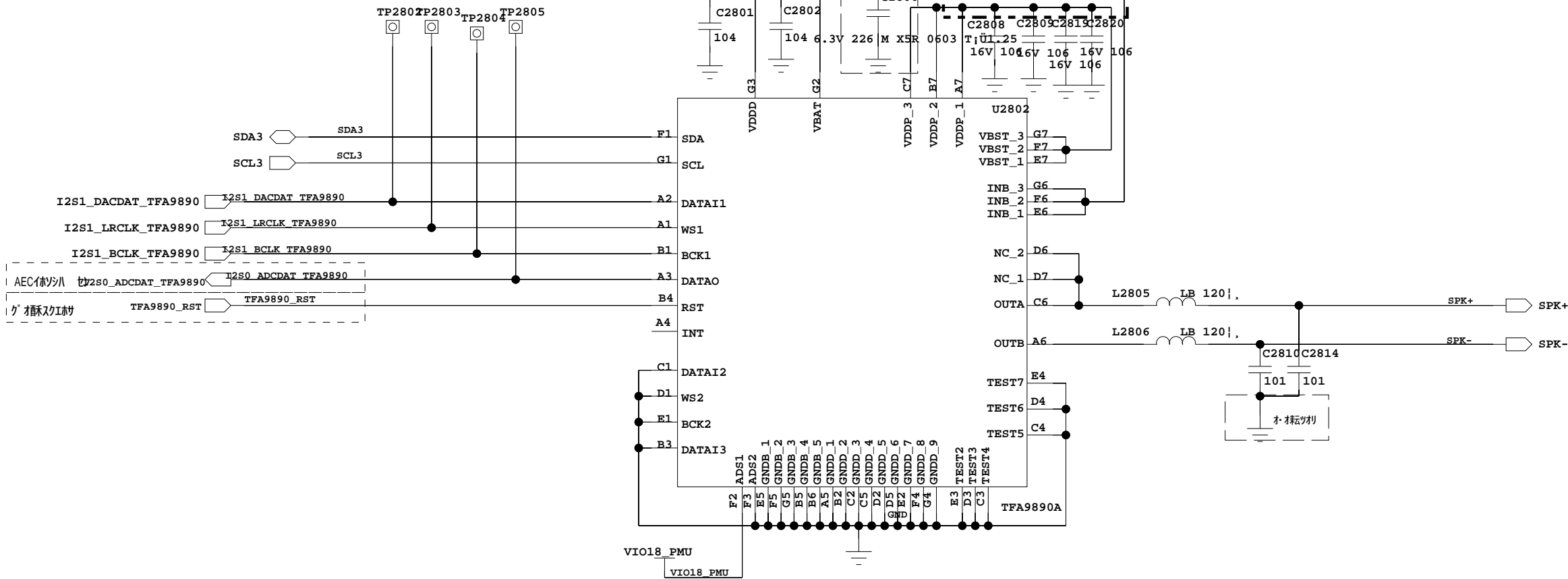
IMX371 slave Address is 0x21 0x20

EEPROM slave Address is 0xA3 0XA2

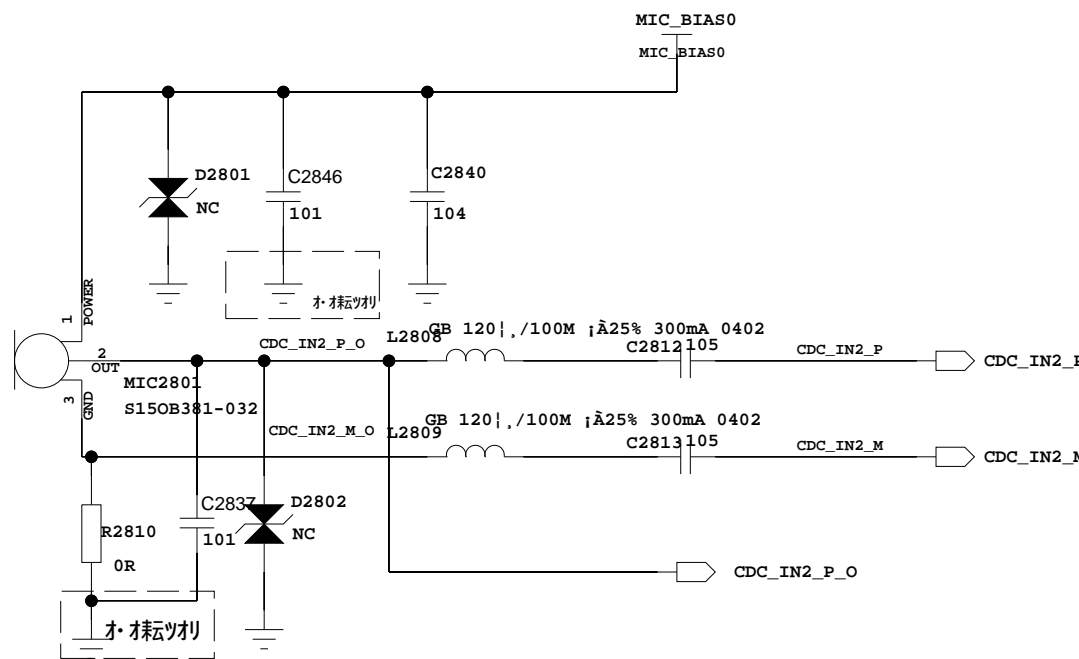


# SMART PA

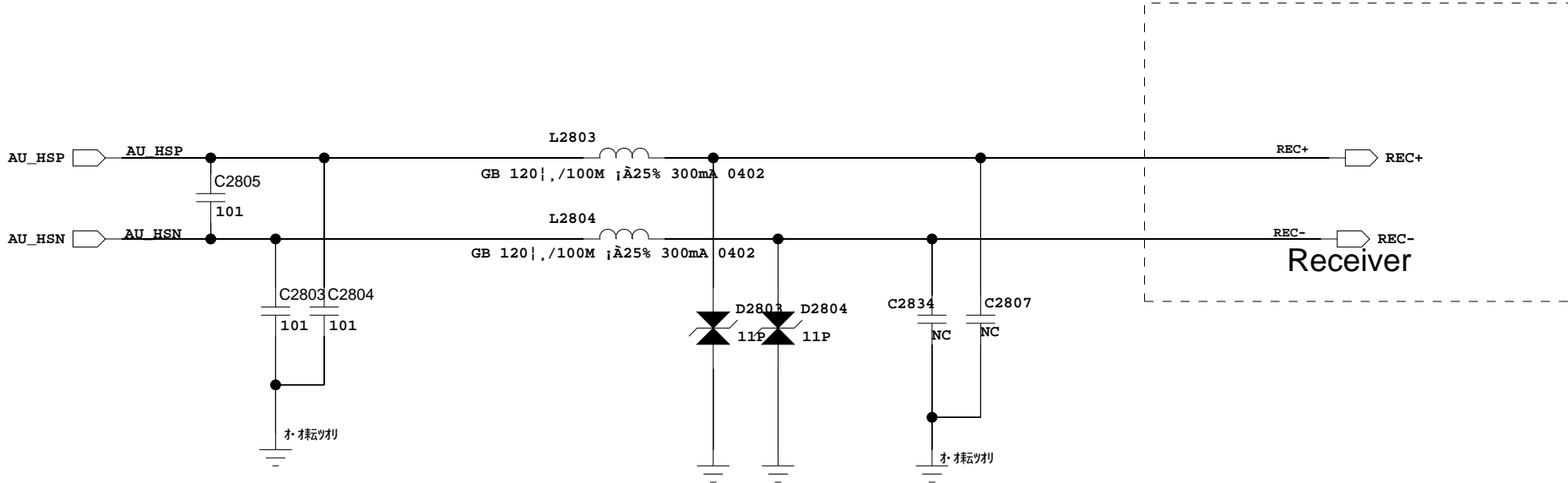
TFA9890A I2C Address (R:0x6B; W:0x6A)



# Ant-Noise MIC

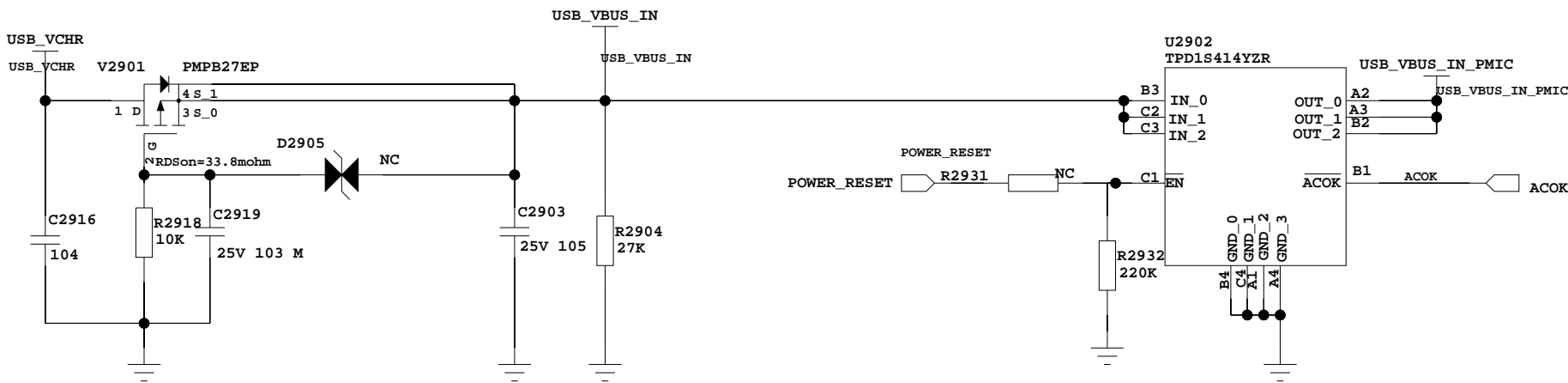
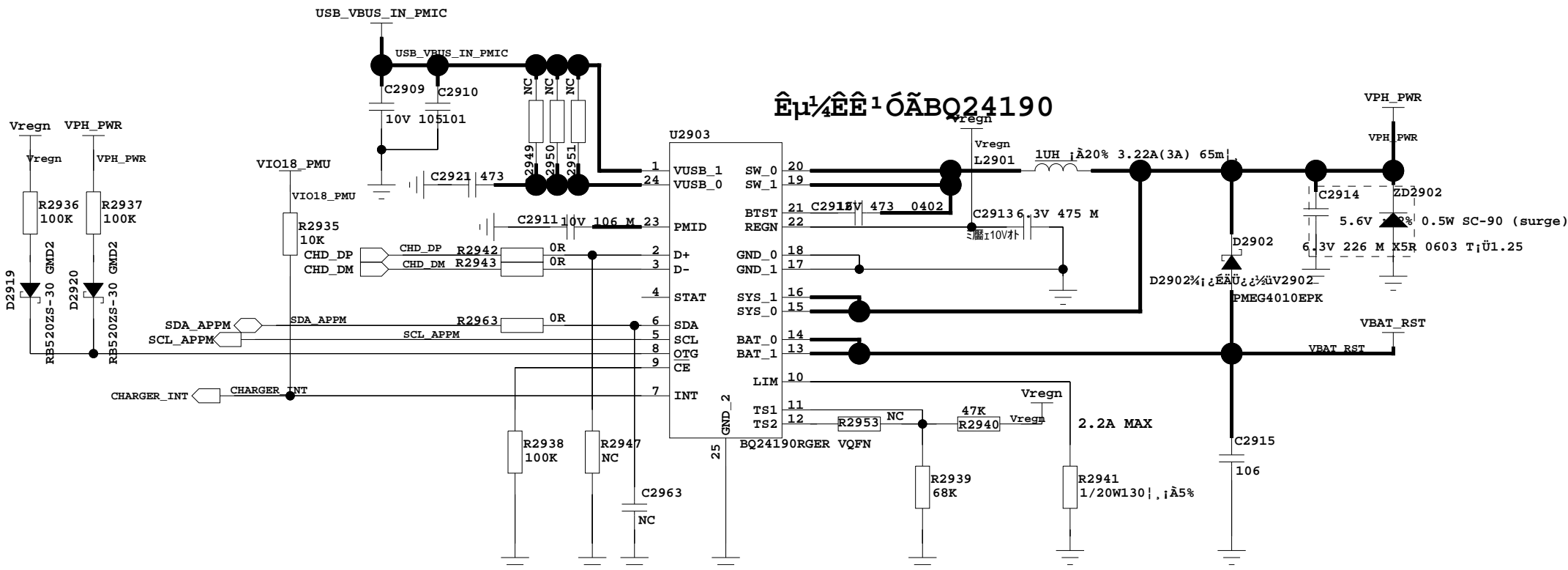


# Receiver



# Internal Switching Charge

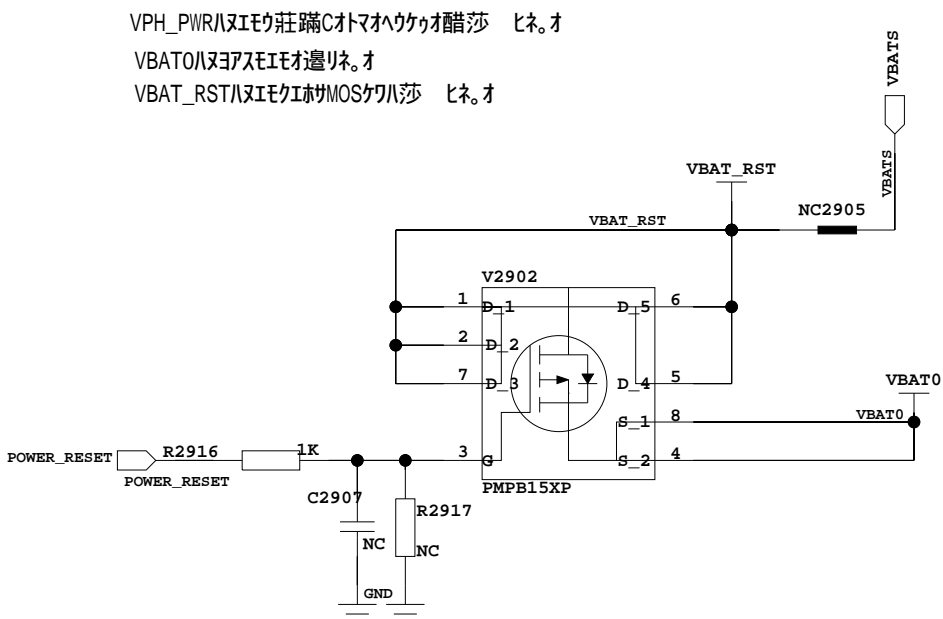
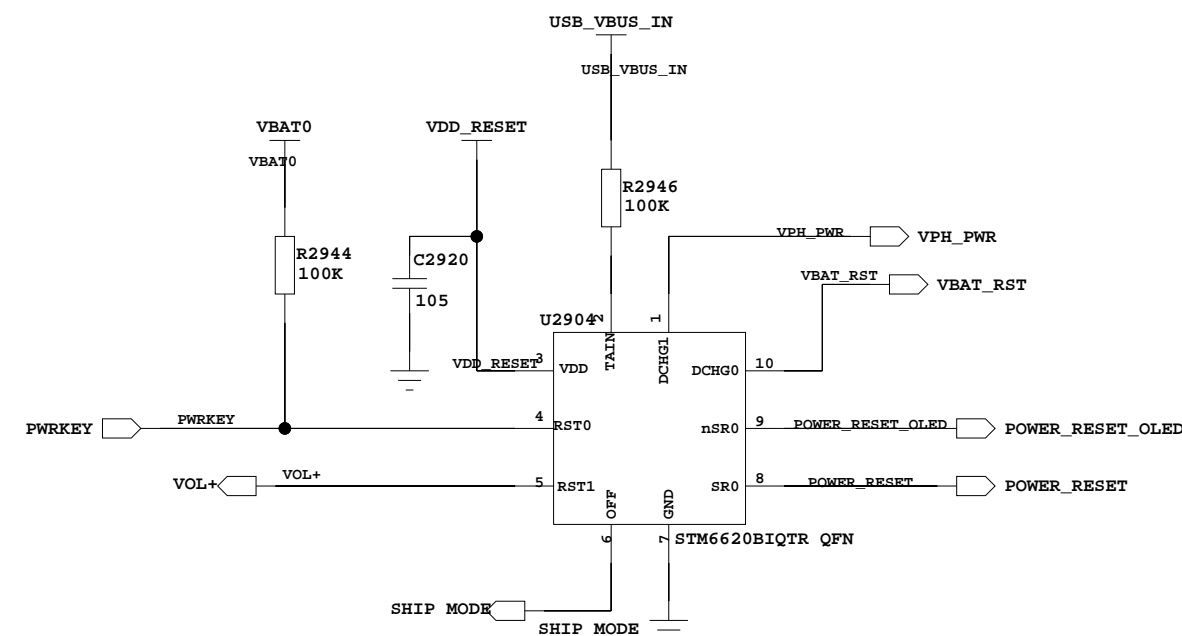
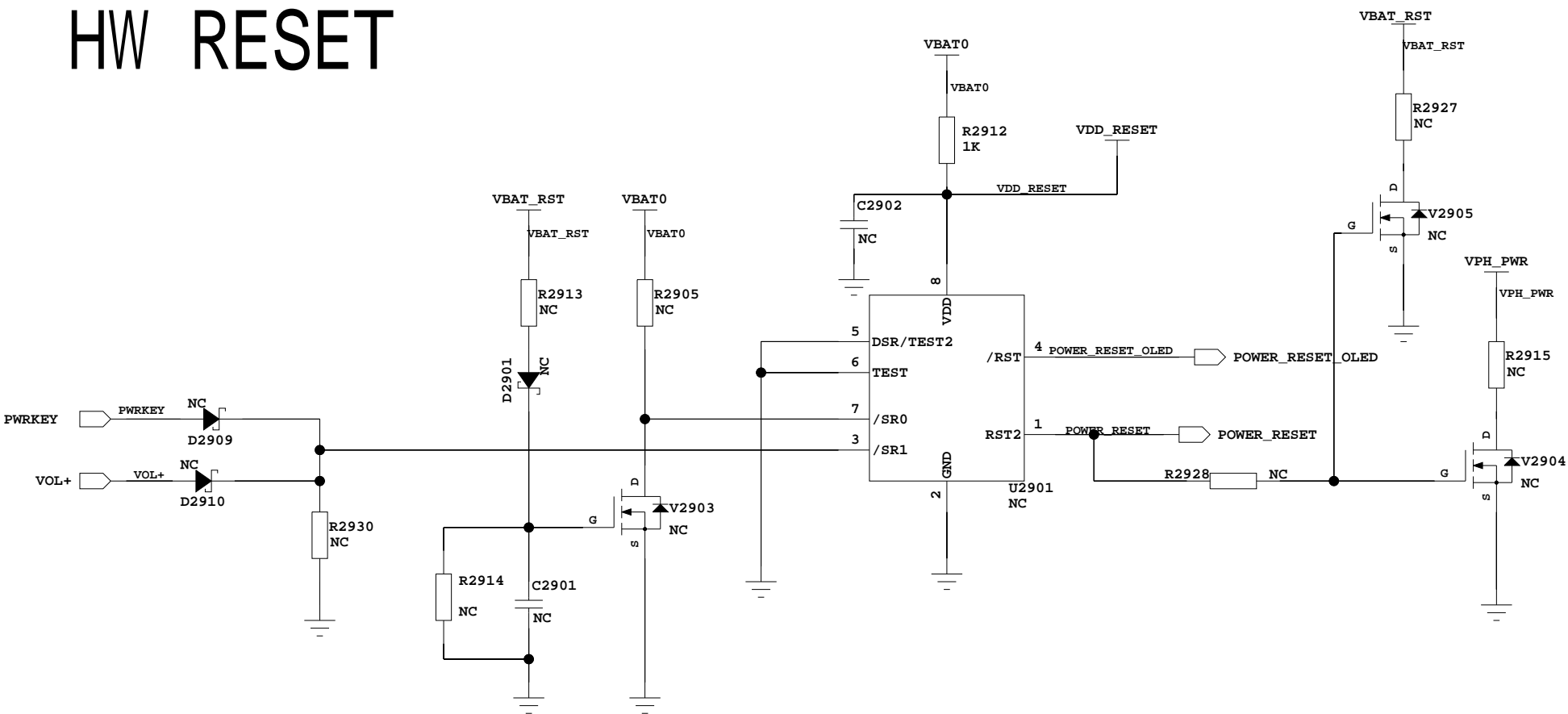
# NVP&OVP



BQ24196 I2C Address (R:0xD7;W:0xD6)

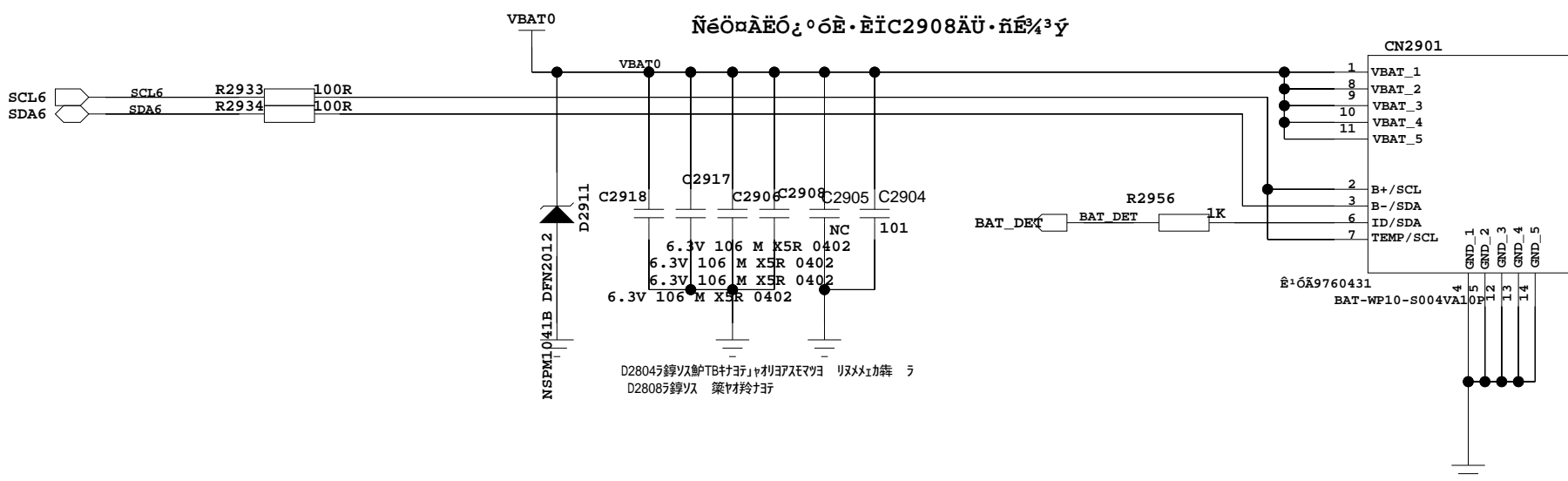
# HW RESET

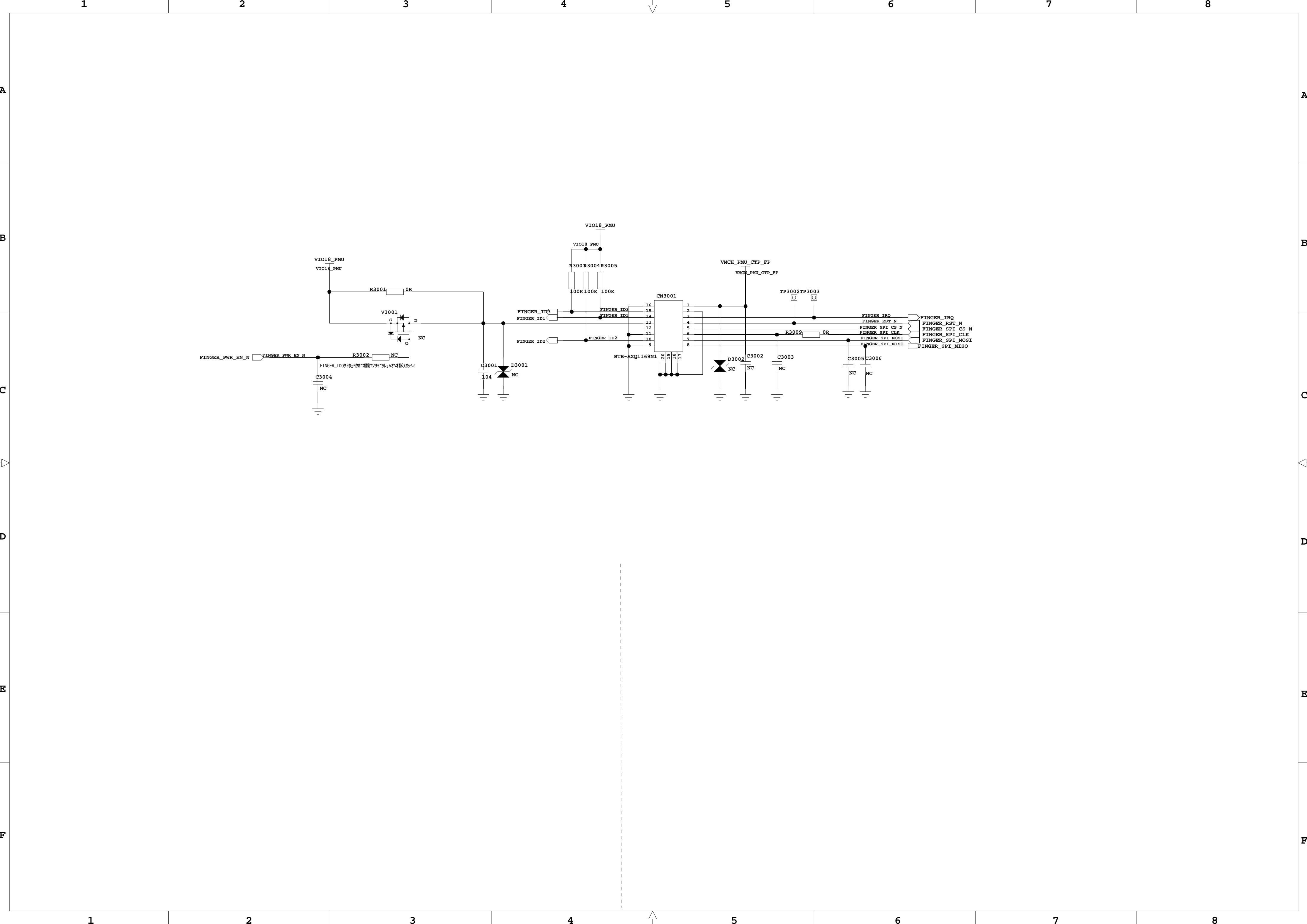
# SHIP MODE , BAT\_MOSFET



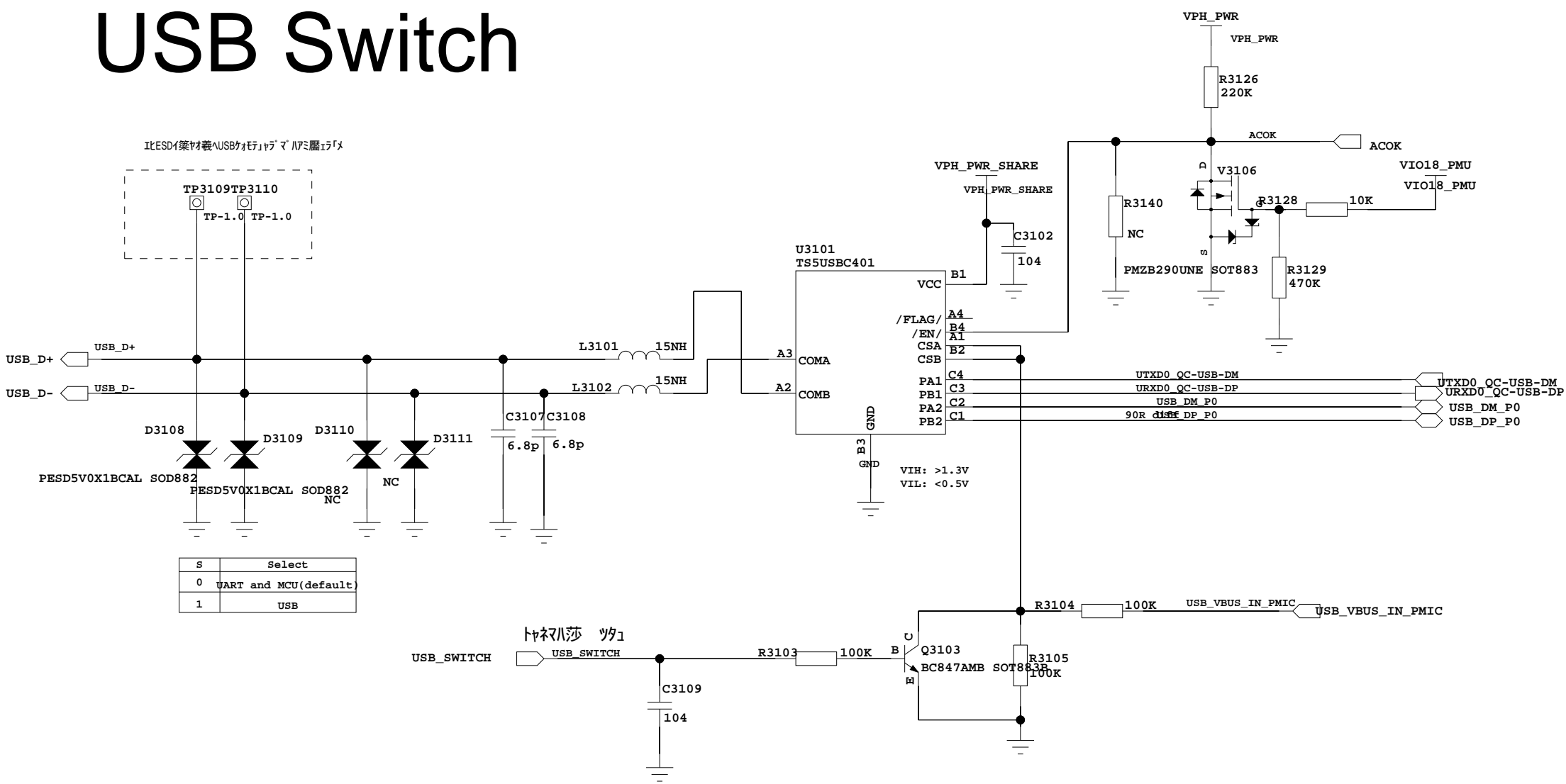
ネ テミツクIC「 $\mu$ C174R2816」外ホ11K,C2807 NC,R2817 NC  
ネ テセノクIC「 $\mu$ C174R2816」外ホ110K 8241005,C2807 ,R28177 マ

# Battery Connector



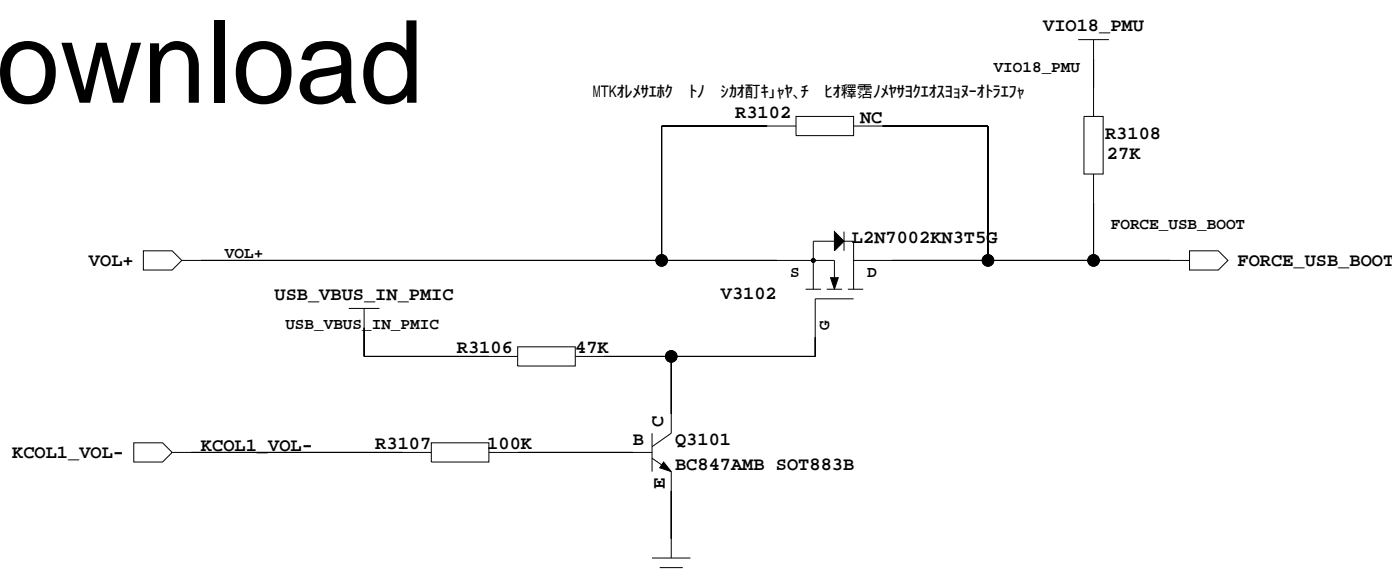


USB Switch

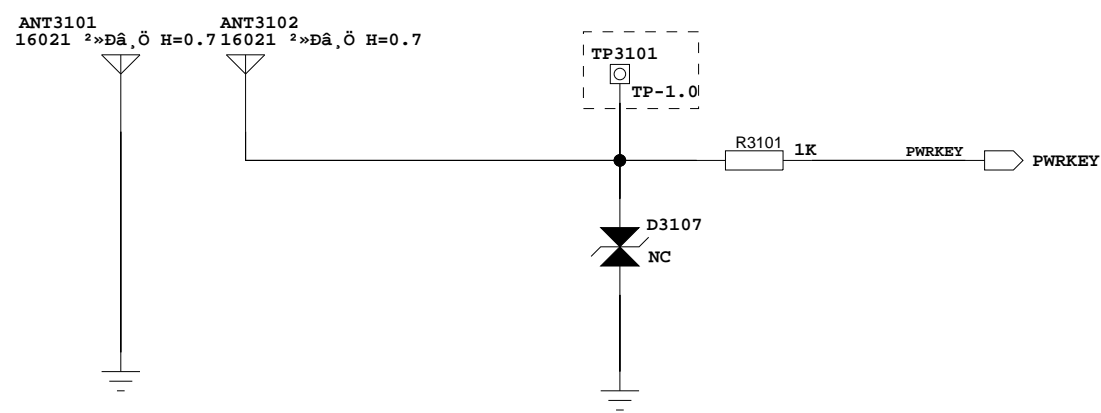


FORCE USB Download

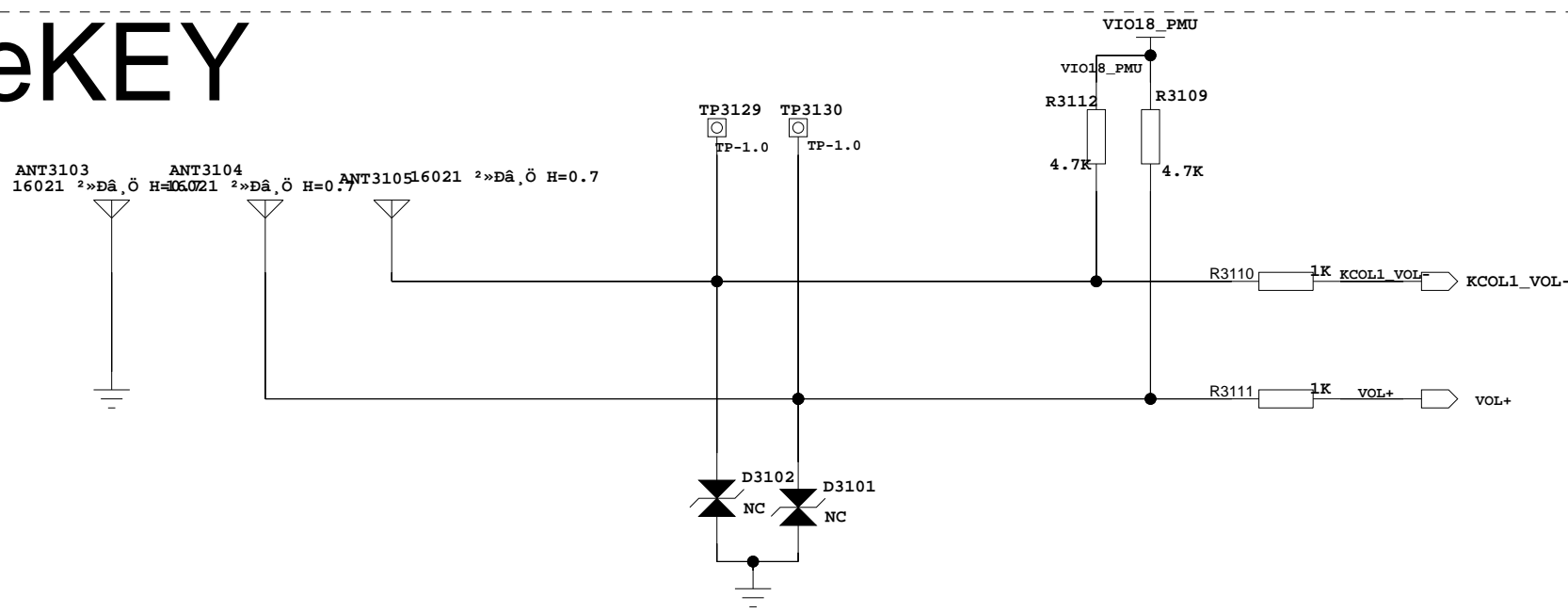
MTKニスフィヌソヨニマツヤリオ酏キ



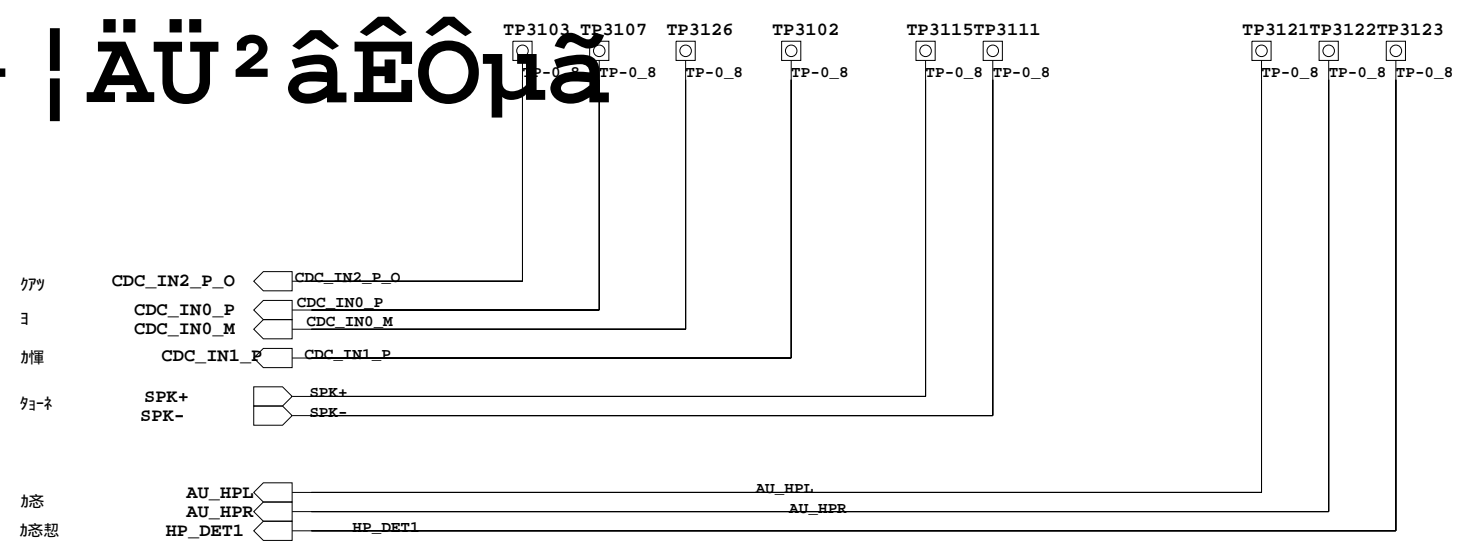
POWERKEY



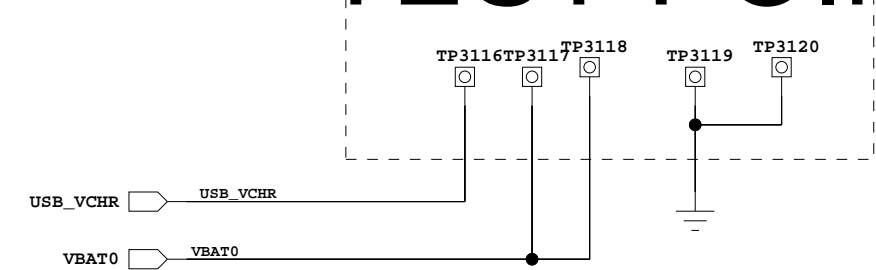
VolumeKEY



È«¹ | ÄÛ² âÊÔµã



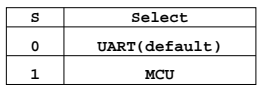
TEST POINT



A diagram showing a downward-pointing arrow representing a force vector. The arrow is positioned above a horizontal line, which represents the surface of the object.



## D



L

