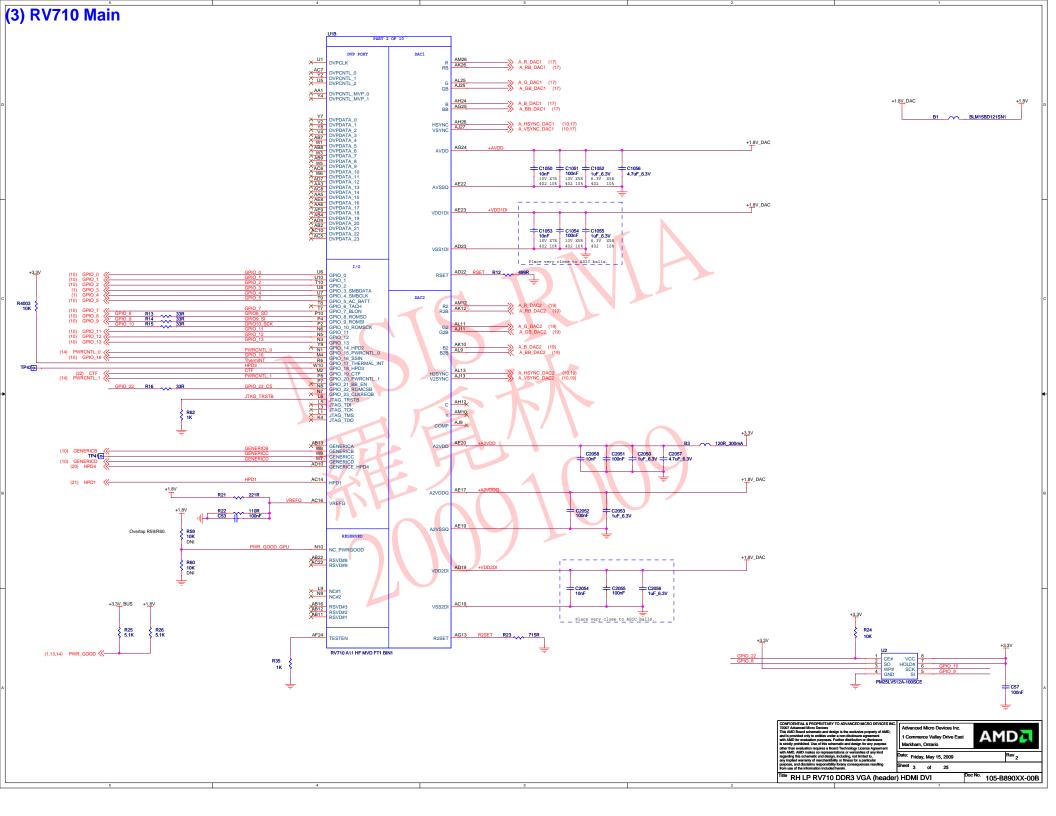
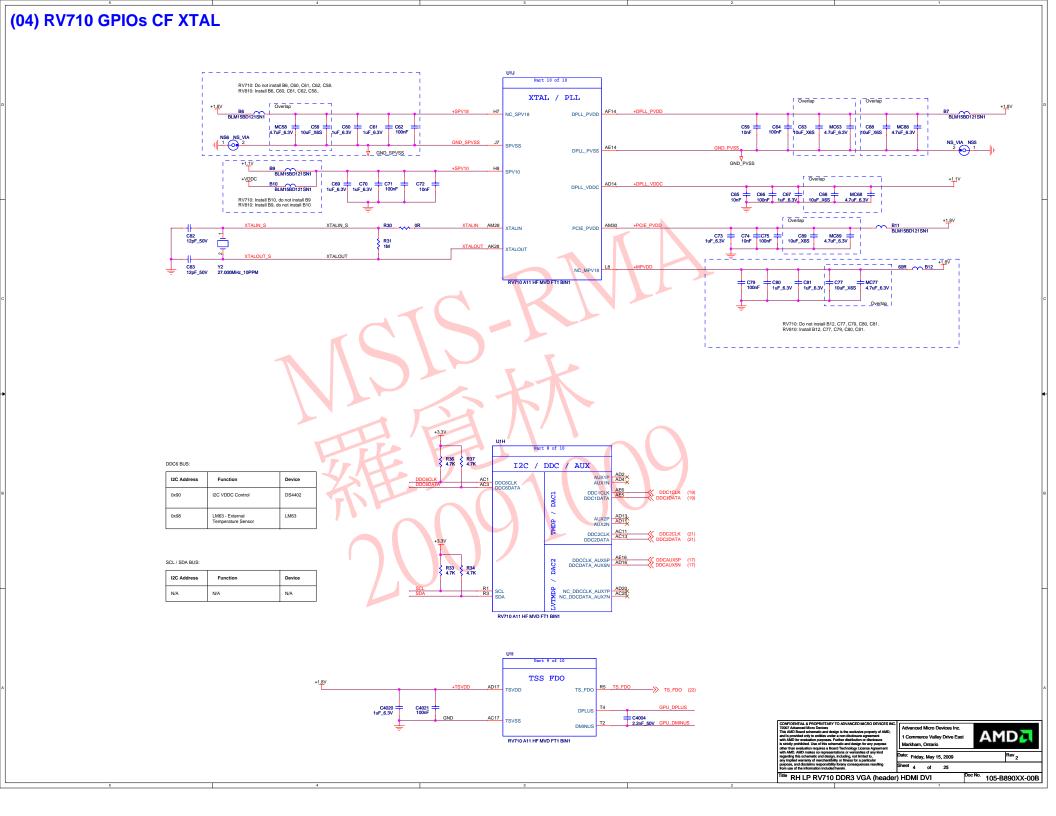


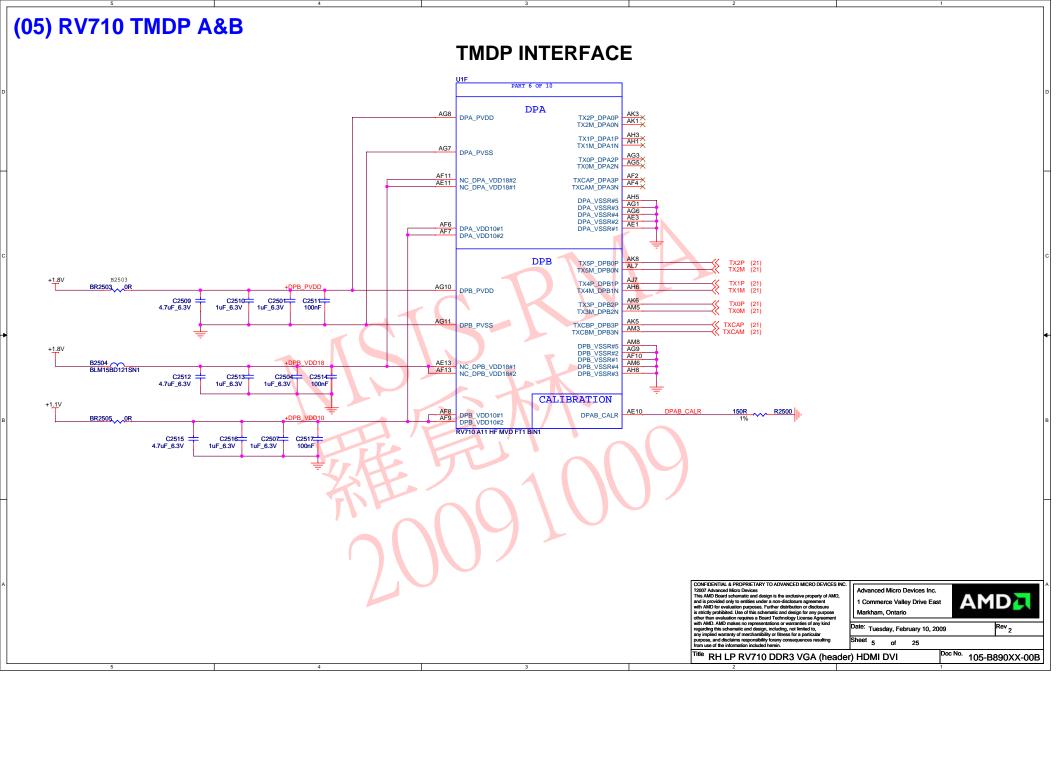
(2) RV710 PCIE Interface

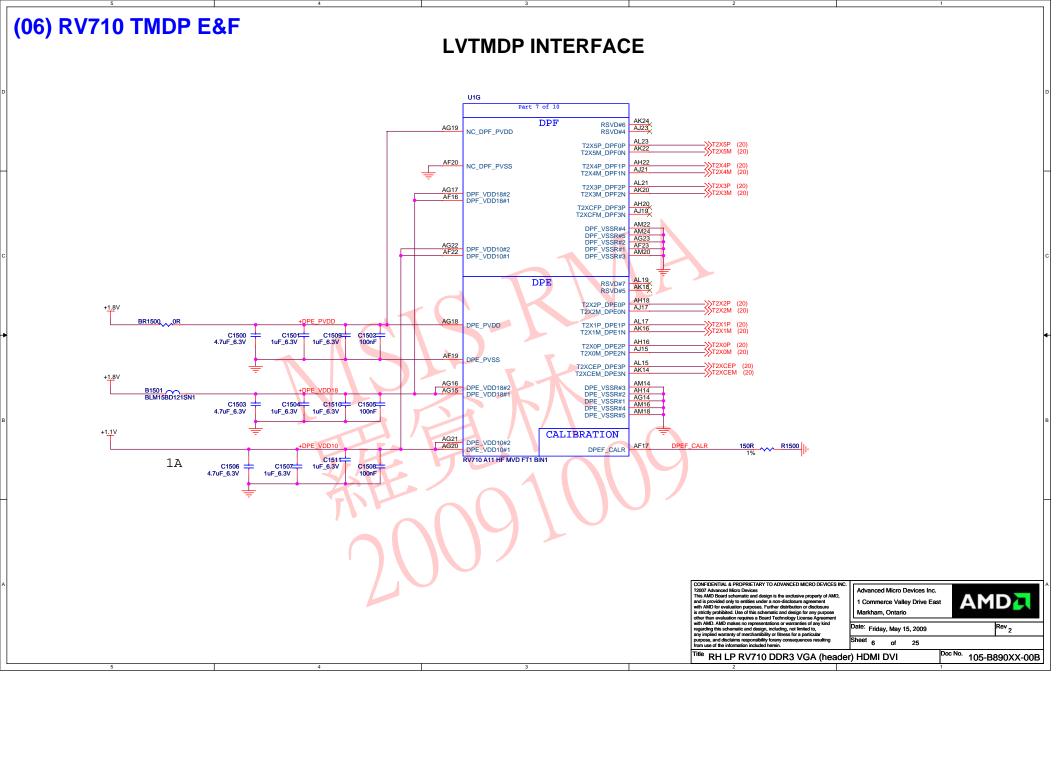


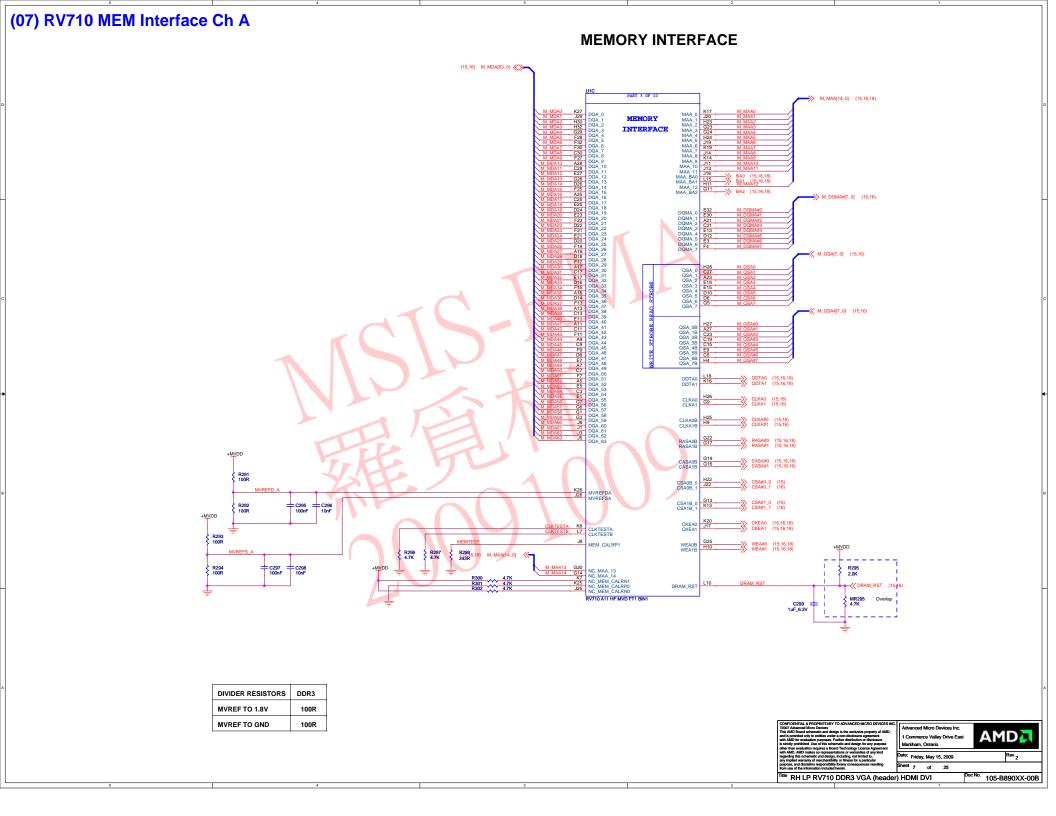
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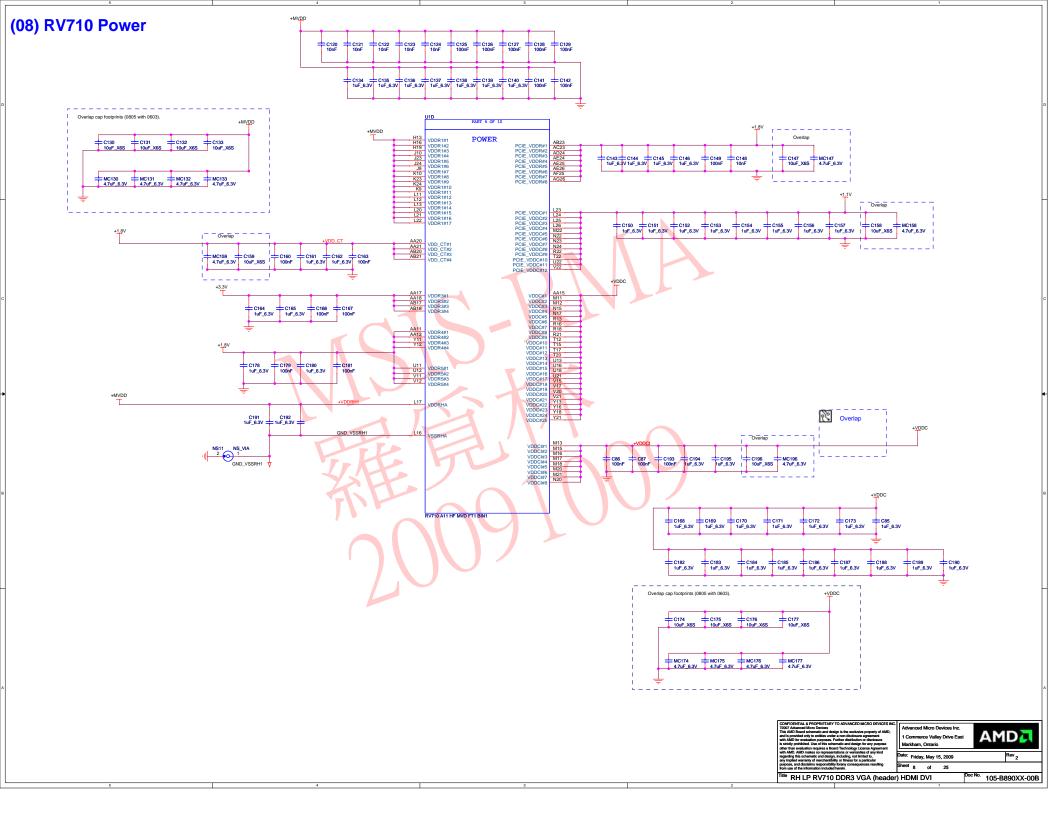


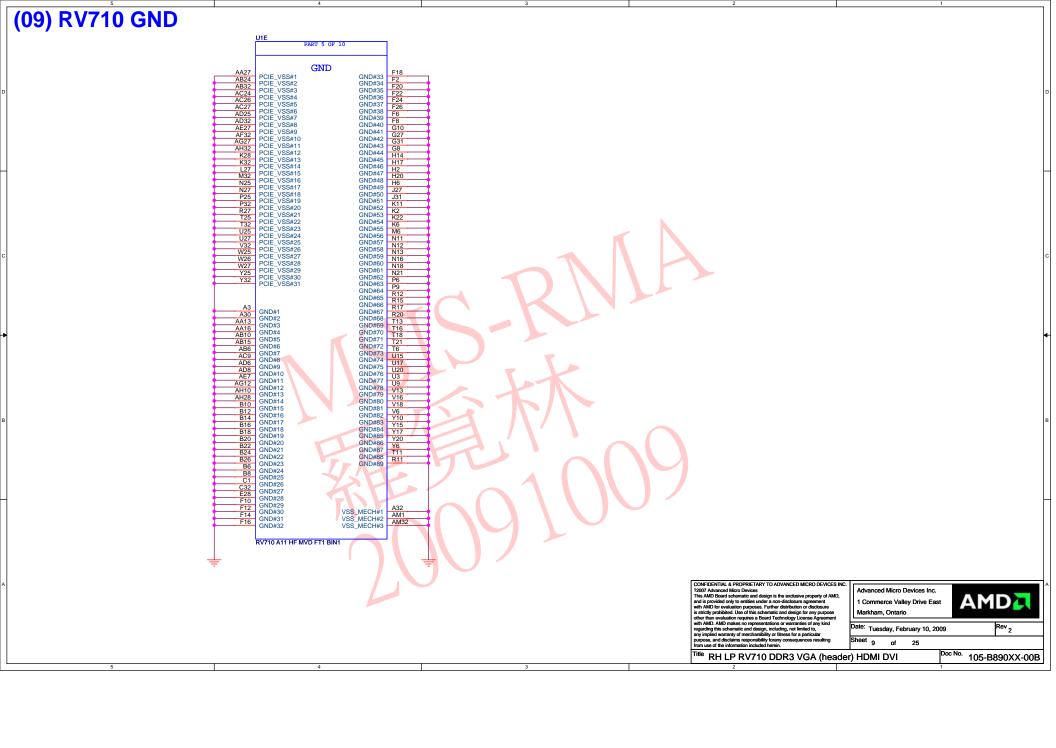




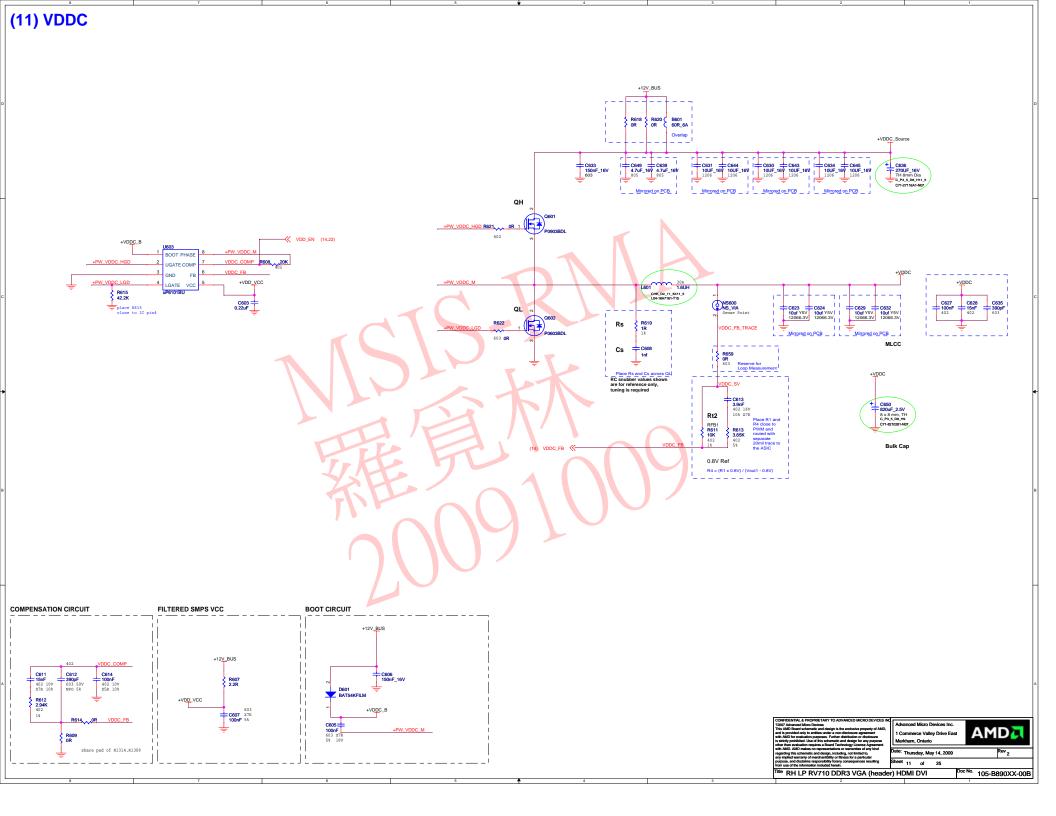


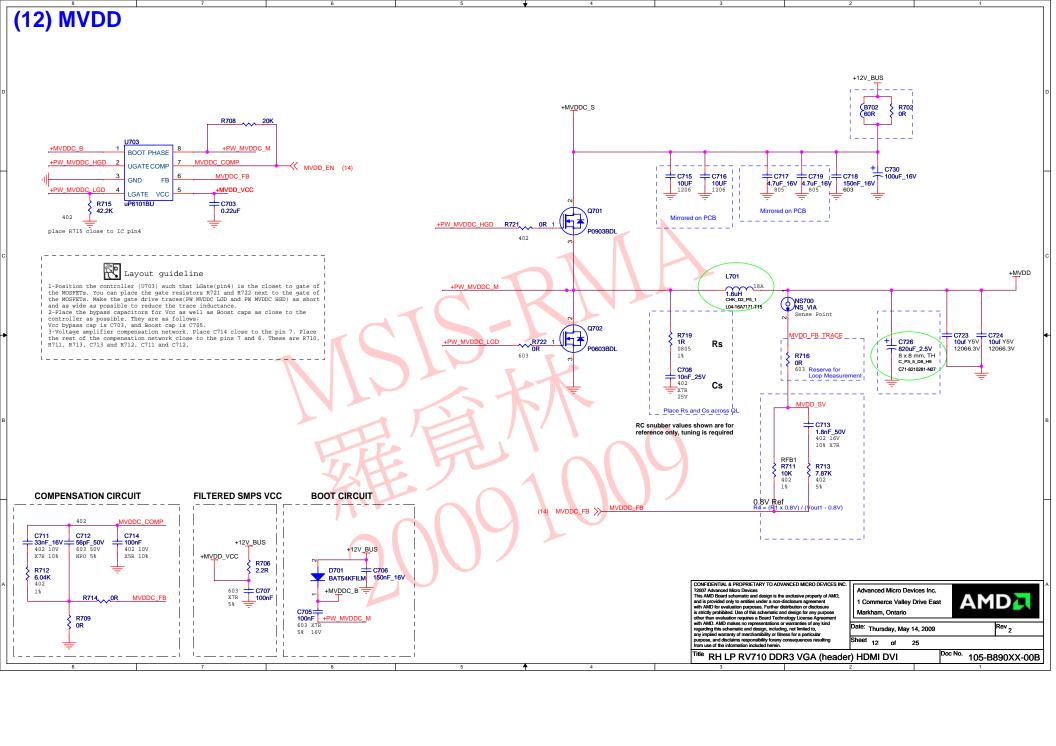


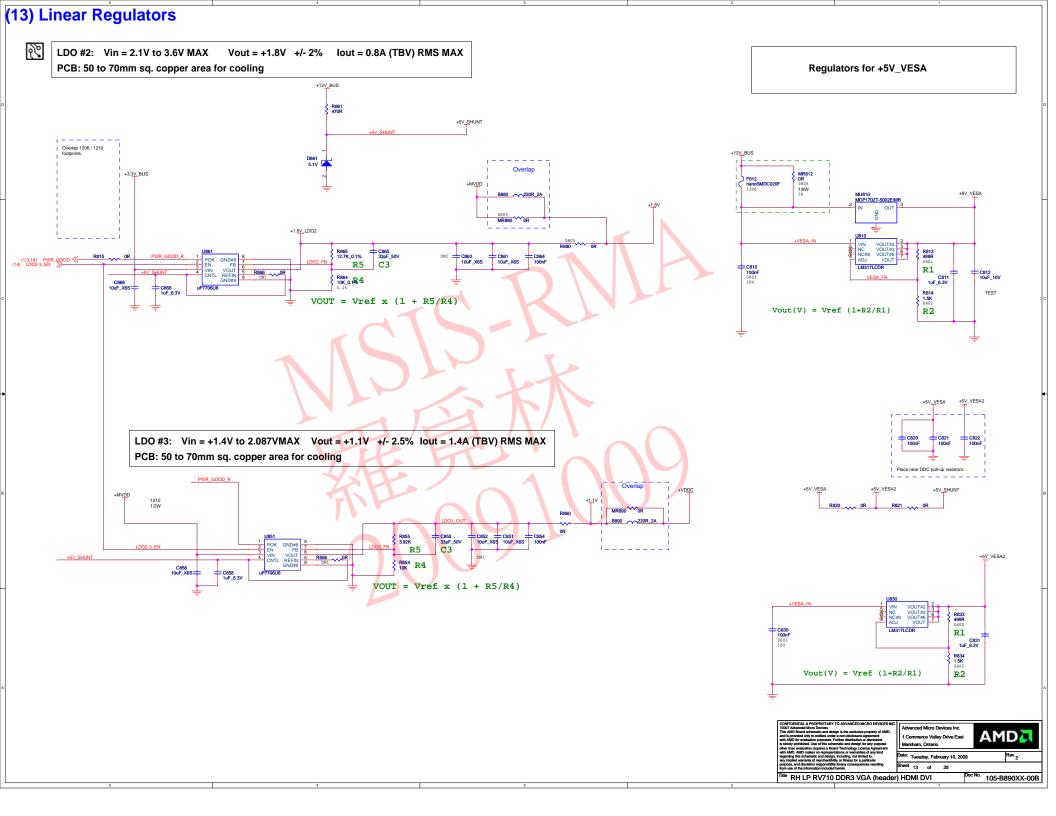


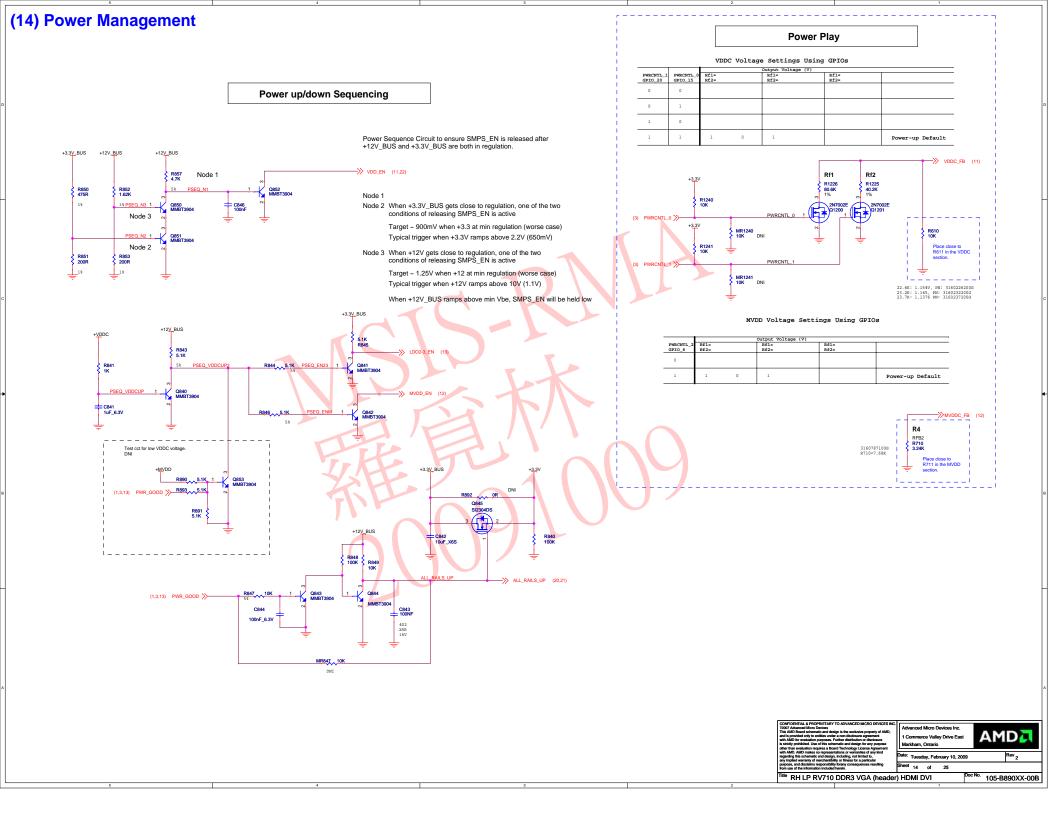


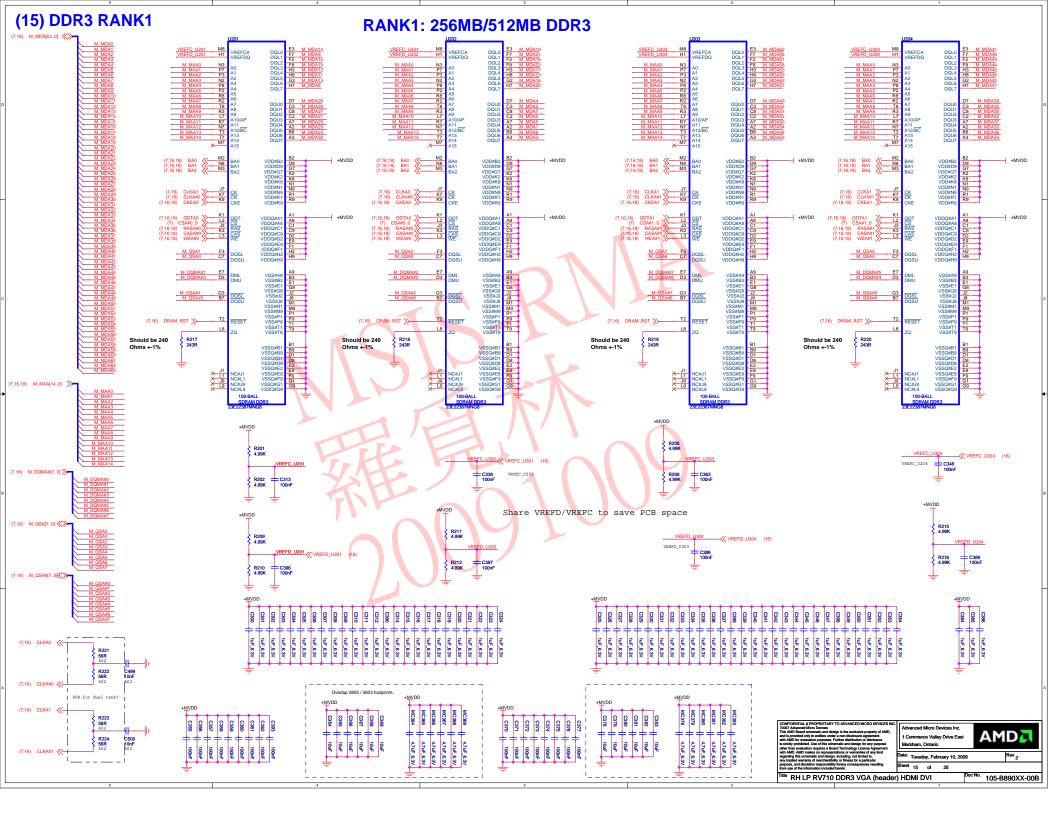
The State	5 4 4 A A A A A A A A A A A A A A A A A		3		2	1	
10	(10) RV710 STRAPS			O STRAPS			
150 184 193		DNI DNI	777 240 404	A_VSYNC_DAC2 (3,19)	O: Driver would ignore the value sampled on VHAD_0 during the value sampled at reset from VHAE whether or not a VIP slave device (e.g. Theater chip) is continuous.	0 to determine	
1			DNI R44 10K GPIO 9 SG	PIO_9 (3)	VGA DISABLE : 1 for disable (set to 0 for normal operation	n)	
15 15 15 15 15 15 15 15		DNI	R45 10K GPIO 0 SI	PIO_0 (3)	0: 50% Tx output swing for mobile mode	inable)	
### 1985 ###		 DNI 	R46 10K GPIO_1 >>> GI	PIO_1 (3)	0: Tx de-emphasis disabled for mobile mode	nable)	
18		DNI	R47 10K GPIO 2 >>> GI	PIO_2 (3)	GPIO(2) - BIF_GEN2_EN (5.0 GT/s Enable) 0 : Default. (Driver Controlled Gen2)		
### RES ### OPD ## OPD ## OPD ## OPD ### OPD #		DNI DNI I	DNI R49 10K GPIO 13	PIO_13 (3)	100 - 512Kbit M25P05A (ST)	 	
Process Proc		I DNI	R51 10K >>> /	A_VSYNC_DAC1 (3,17)	AUD[0]		
1 - Ended CLIRRICAL power management candidately (GPO_5 : GPO_16 GPO_16	T C	175	R52	A_HSYNC_DAC1 (3,17)	AUD[1]		
DET. BSS. 10K CPIO 16) GPIO 16 (3) Home girl Sammang (19)			DNI R53 10K GPIO 8 GPIO 8	PIO_8 (3)	0 - Disable CLKREQ# power management capability		
TV OUT STANDARD 0 - PAL TVO 1 · NTSC TVO INT. R70 10K GENERICS GENERICS (3) BIT R71 10K GENERICS GENERICS (3) CONFIDENTIAL A PROPRIETANT TO ADVINCED MICRO DENCES INC. To 2007 Advanced Micro Devices Inc. 1 Commerce Valley Drive East With AMAD Board exhausting proposes, Uniford infection or disclausers as in sub-problement. Uniford in a confidence of sub-proposes. Uniford infection or disclausers as in sub-problement. Uniford the infection of disclausers as in sub-problement and sub-problement. In the infection of disclausers as in sub-problement and sub-problement. In the infection of disclausers as in sub-problement and sub-problement. In the infection of disclausers as in sub-problement and sub-problement. In the infection of disclausers as in sub-problement and sub-problement. In the infection of disclausers are in the infection of disclausers as in the infection of disclausers. In the infection of disclausers are infection of disclausers as in sub-problement and sub-problement. In the infection of disclausers are in the infection of disclausers as in the infection of disclausers. In the infection of the infection of disclausers are infection of disclausers. In the infection of the infection of disclausers are infection of disclausers. In the infection of the infection of disclausers are infection of disclausers. In the infection of the infection of disclausers are infection of disclausers. In the infection of the infection of disclausers are infection of the in					Quimonda [0:0] Hynix [0:1]		
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CONFIDENTIAL & PROPRIETARY TO ADVANCED MICRO DEVICES INC. 72007 Advanced Micro Devices Inc. 172007 Advanced Micro Devices			NTSC R57 10K GPIO 7	GPIO_7 (3)	TV OUT STANDARD 0 - PAL TVO 1 - NTSC TVO		
72007 Advanced Micro Devices This AMD Board chematic and design is the exclusive property of AMD, and is provided by the efficient and design in the exclusive property of AMD, and is provided by the efficient propose. From the exclusive property of AMD, and is provided by the efficient propose. From the exclusive property of AMD, and is provided by the exclusive property of AMD, and is provided by the exclusive property of AMD, and					sed for additional memory vendor		
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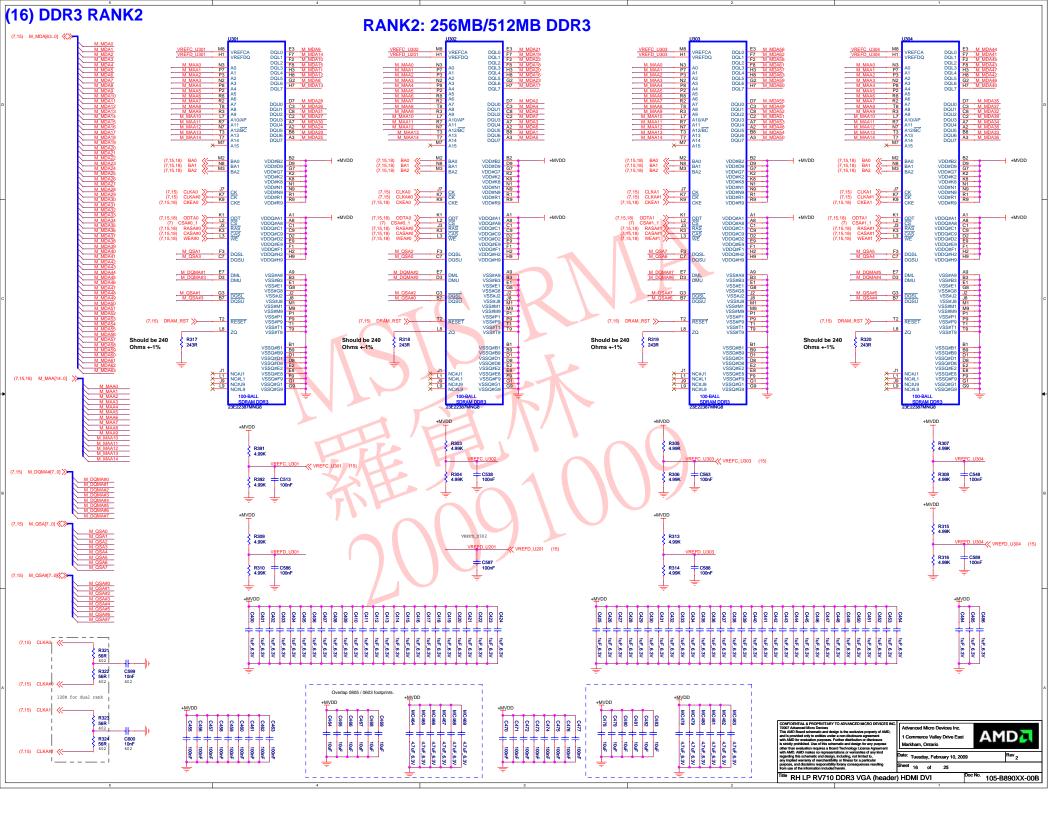






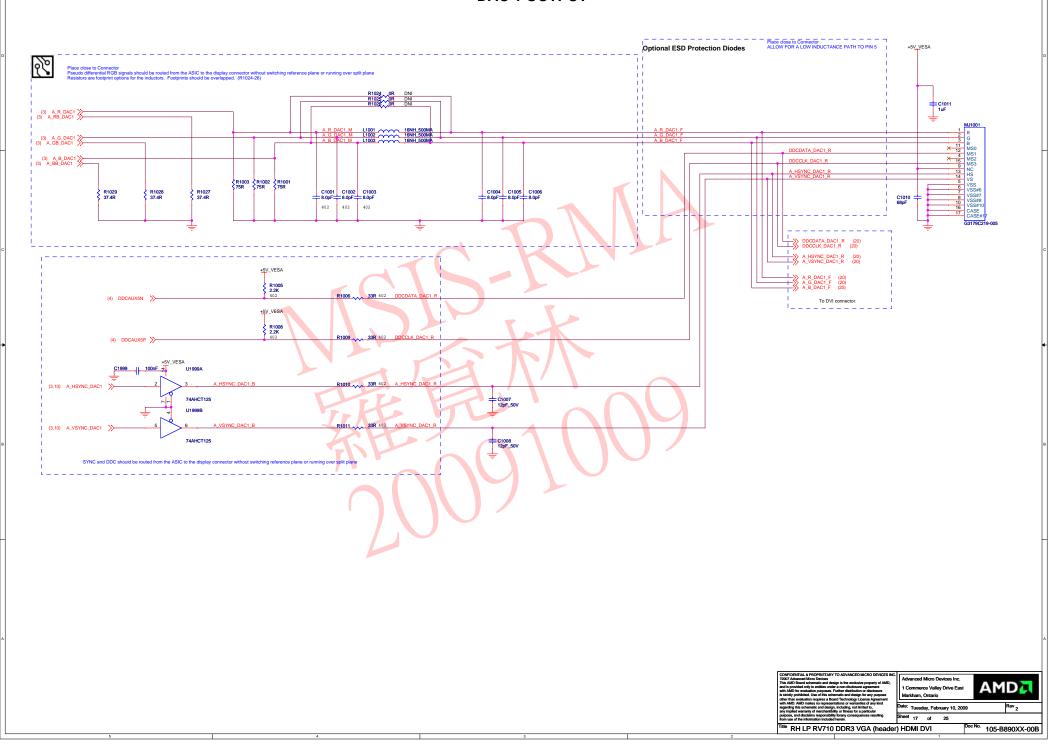


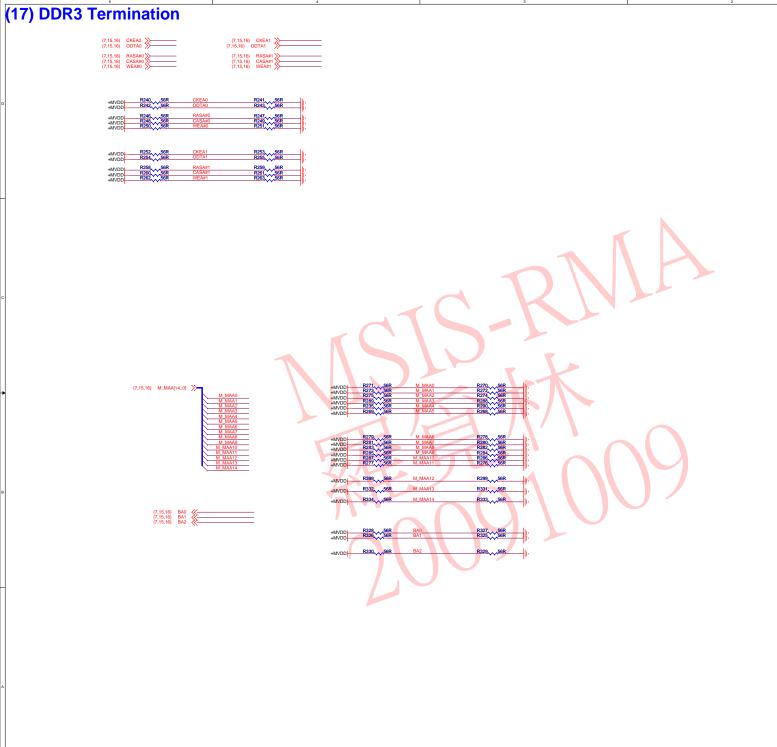




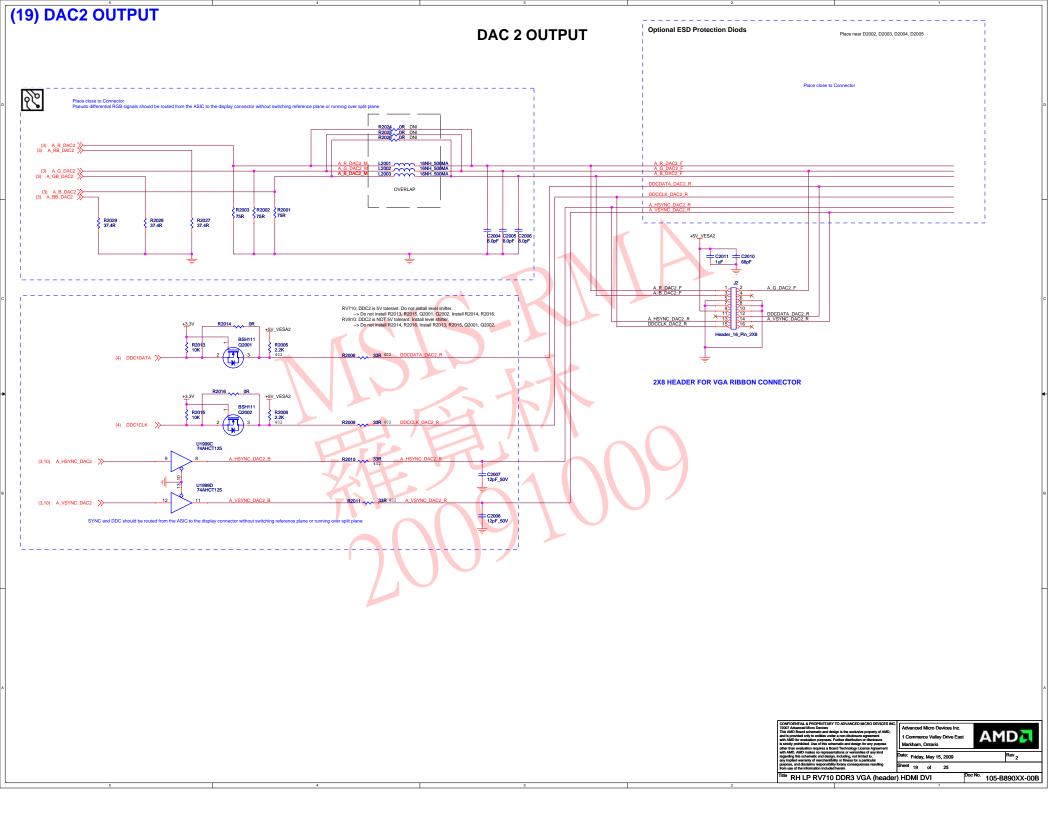
(18) DAC1 OUTPUT

DAC 1 OUTPUT





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(20) DVI OUTPUT **DPE / DPF OUTPUT** +5V_VESA 25 CASE Ze CASE

1 TMOS Data22 TMOS Data23 TMOS Data44 TMOS Data44 TMOS Data46 TMOS Data46 TMOS Data47 DOC Clock
7 TMOS Data47 DOC Clock
7 TMOS Data41 TMOS Data41 TMOS Data11 TMOS Data41 TMOS Data41 TMOS Data51 TMOS Data41 TMOS Data41 TMOS Data51 TMOS Data61 TMOS Data R1603 _____ 499R R1602 499R T2X4M_C T2X4P_C DDCCLK_DAC DDCDATA_DA R1604 499R C1605 | 100nF_6.3V C1606 | 100nF_6.3V R1607 499R R1608 _____499R (6) T2X0M (6) T2X0P C1609 100nF_6.3V C1610 100nF_6.3V R1611 499R (6) T2X5M (6) T2X5P C1611 100nF_6.3V C1612 100nF_6.3V R1613 499R R1614 499R C1 Analog Red Analog Green Analog Blue C5 Analog Hvnc C6 Analog GND Analog GND#C6 26 CASE#26 CASE#27 CASE#28 CASE#28 CASE#29 CASE#30 CASE#30 (14,21) ALL_RAILS_UP >> 1 HPD4 IN R1618 10K DVI_CONNECTOR

