

MS-V091 Base on V063-210-- G73GT, 128/256 Mb DDR3-136pin, VGA, DVI-I, SD/HDTV, VIVO

SUMMARY:

- 1. Base on V045-100 (P456-A02)to Modify
- 2. BGA-136 DDRIII

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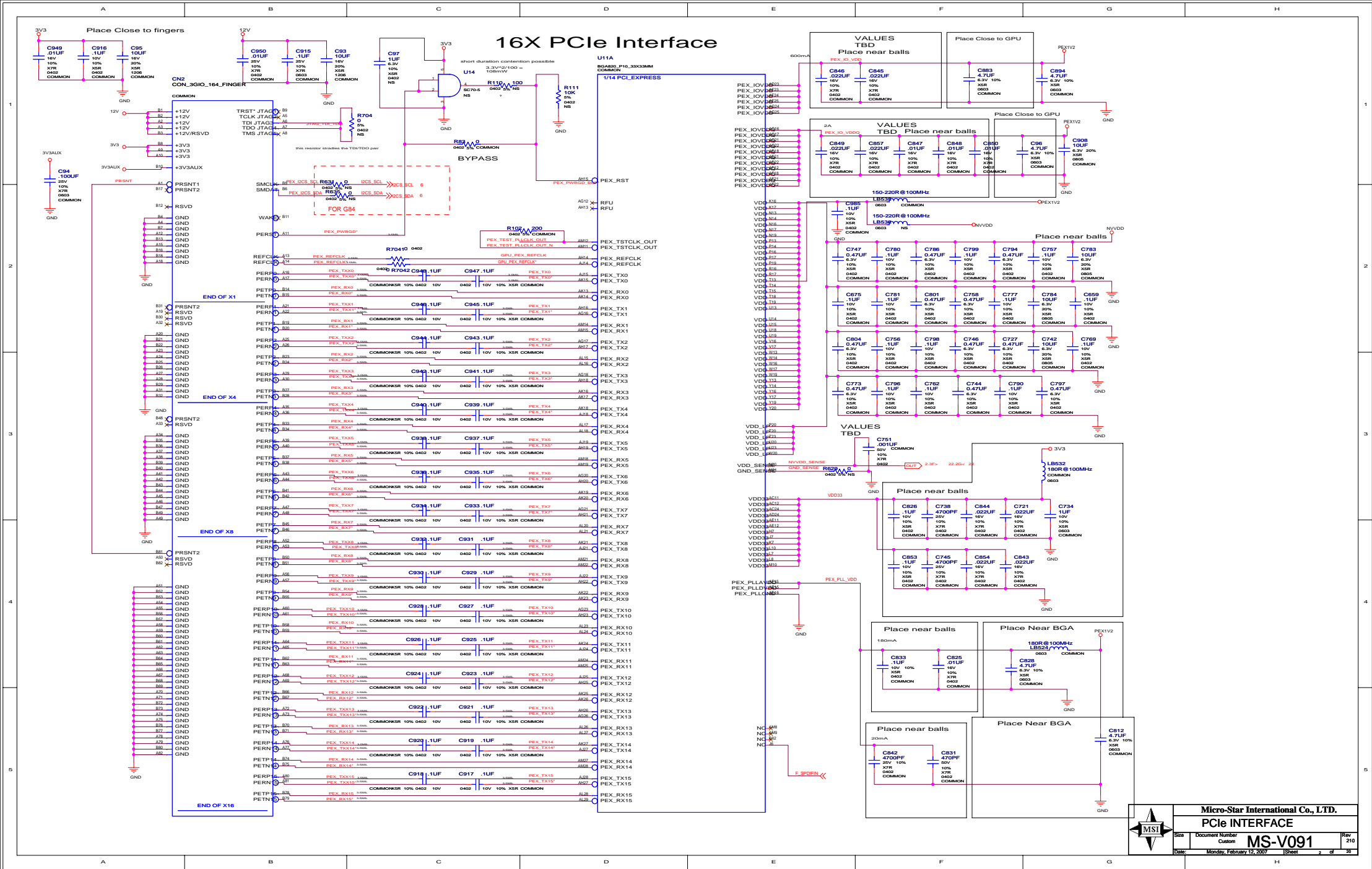
Page 1: TITLE
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Page 3: FBA INTERFACE
Page 4: FB A
Page 5: FBA DECOUPLING
Page 6: FBC INTERFACE
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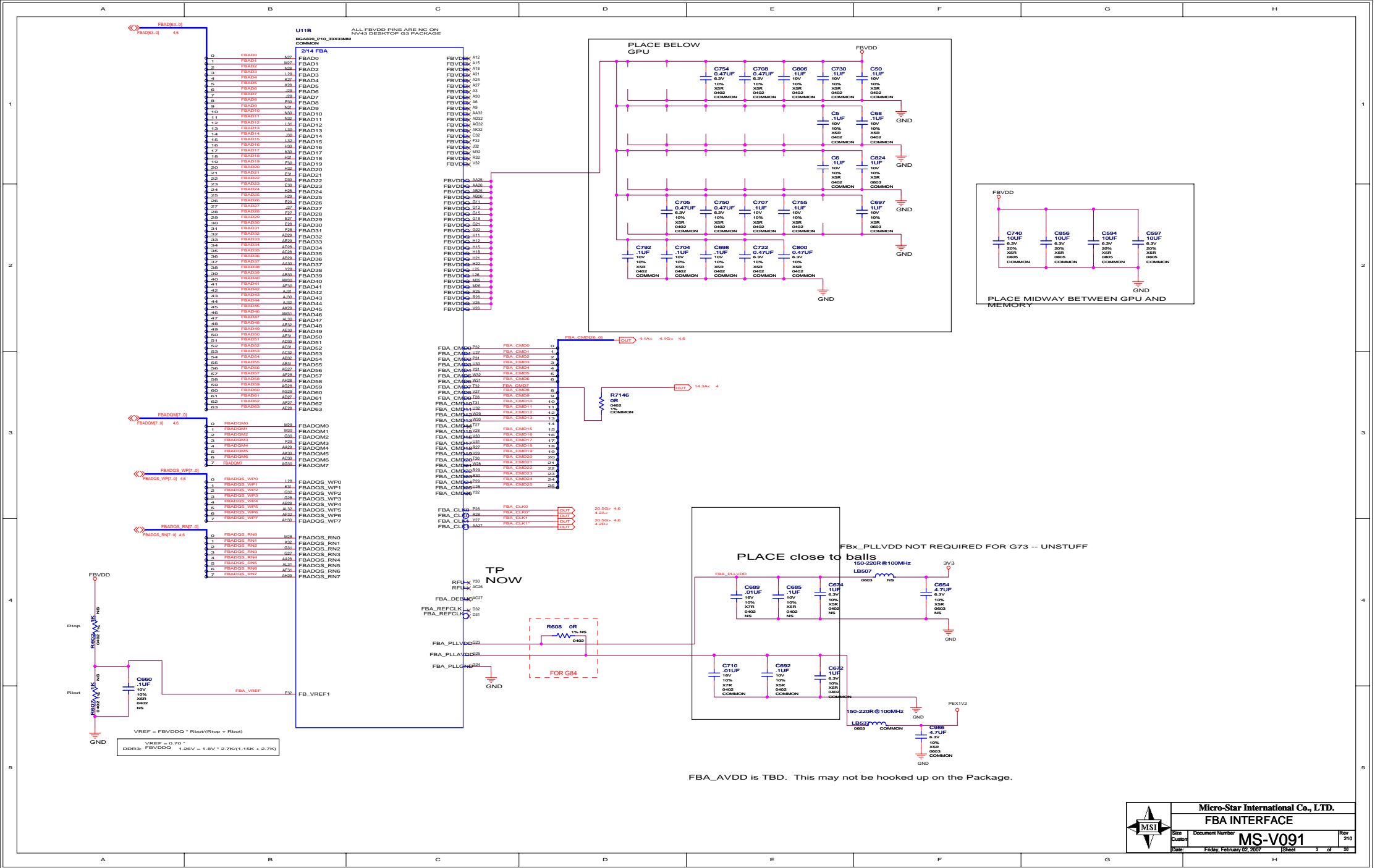
Rev History

- 1.Remove FBA2 and FBC2
 - 2.Modify Page 13 and 16 remove 4 pin VIDEO in circuit
 - 3.Modify Page 17 GPIO FAN control circuit
 - 4.Modify Page 20 NVVDD,FBVDD/Q,5V,A3V3 circuit
- 210
- 1.Modify A3V3 circuit all 3V3 use PEX3V3
 - 2. Add FBVDD POWER SEQUENCING
 - 3. Add L7001& L7002 BEAD and R7372 0ohm for NVVDD to PEX1V2 option
 - 4. Remove 5V and A3V3 OP/Mosfet circuit add 7805

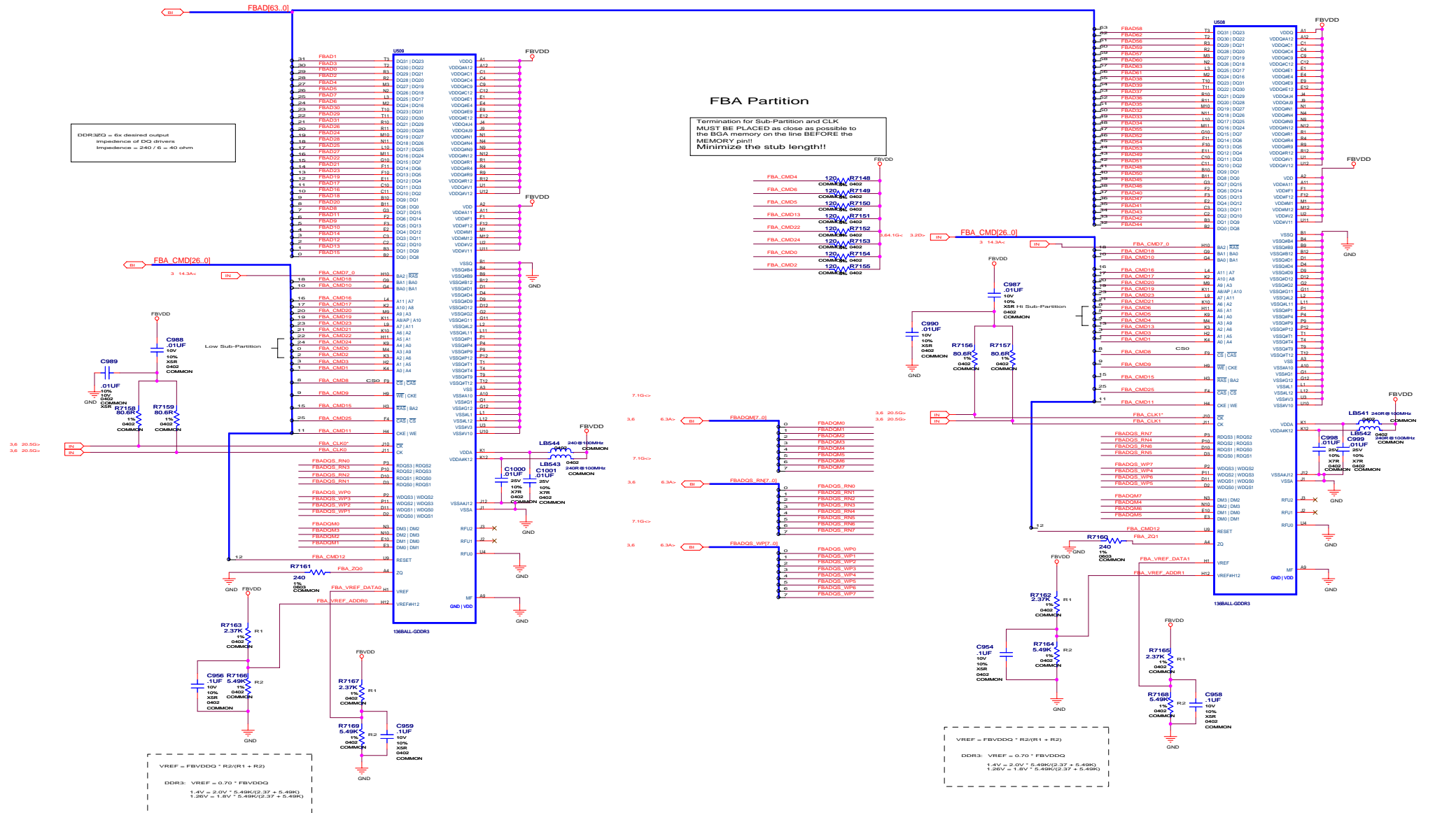
V091_ 0A-Base on V063-21 to Modify G84-400

- 1. PAGE 2,6 Add I2CS_SCL/SDA circuit
- 2. PAGE 3 Add R608 將 FBA_PLLVDD circuit 連接PEX1V2
- 3. PAGE 9 Add R92,R94,96,R105,R106,R107,R108 將 SLI circuit 連接
- 4. PAGE 9 Add SLI circuit
- 5. PAGE 10,11,29 Add DAC_VREF circuit
- 6. PAGE 13 Add 24.5M 49US pcb footprint
- 7. PAGE 14,15 Remove TMDS pull-up 電阻
- 8. PAGE 16 Add PEX1V2 circuit 連接PLLVDD and VID_PLLVDD
- 9. PAGE 17 Add GPIO11 R700 circuit 連接SLI
- 10. PAGE 18 Add Straps circuit
- 11. PAGE 19 Add Extrnal Power , DAC_VREF circuit
- 12. PAGE 20 Remove N201 I Power circuit , Add FBVDD APM7067N circuit , Extrnal 12V , 1.8V TMDS , NVVDD MOS ADD 上2下3
- 13 PAGE 14,15 Add 1.8V circuit



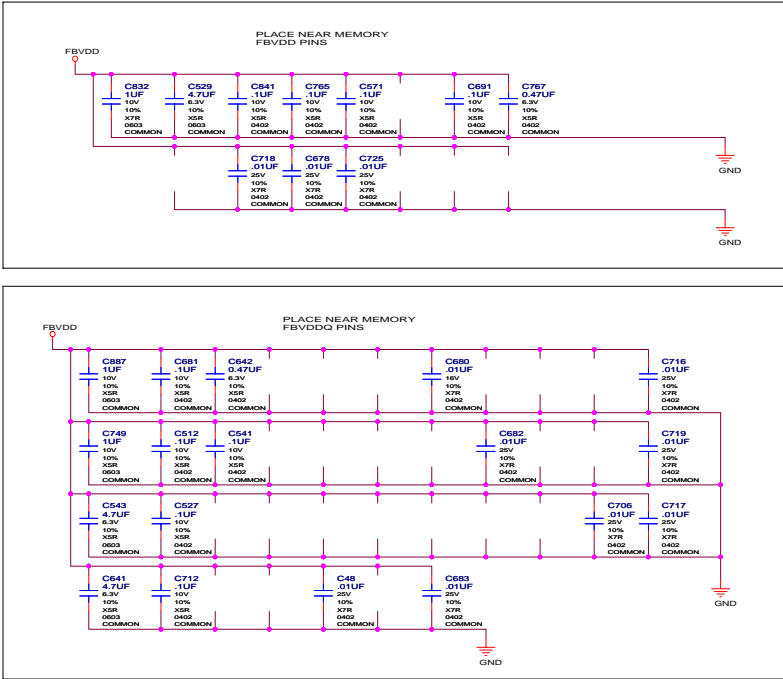


Framebuffer: Partition A
16Mx32 BGA136 DDR3

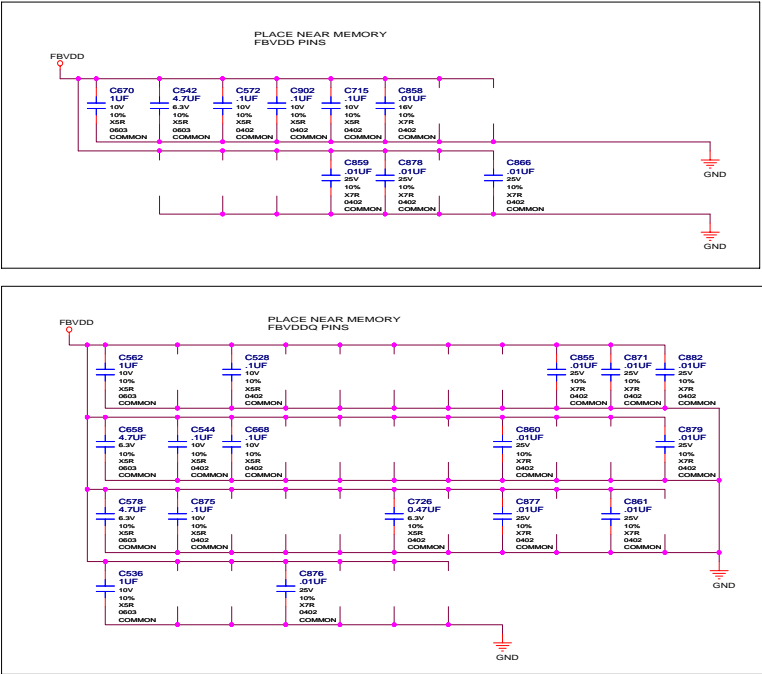


FRAME BUFFER: PARTITION A
DECOUPLING

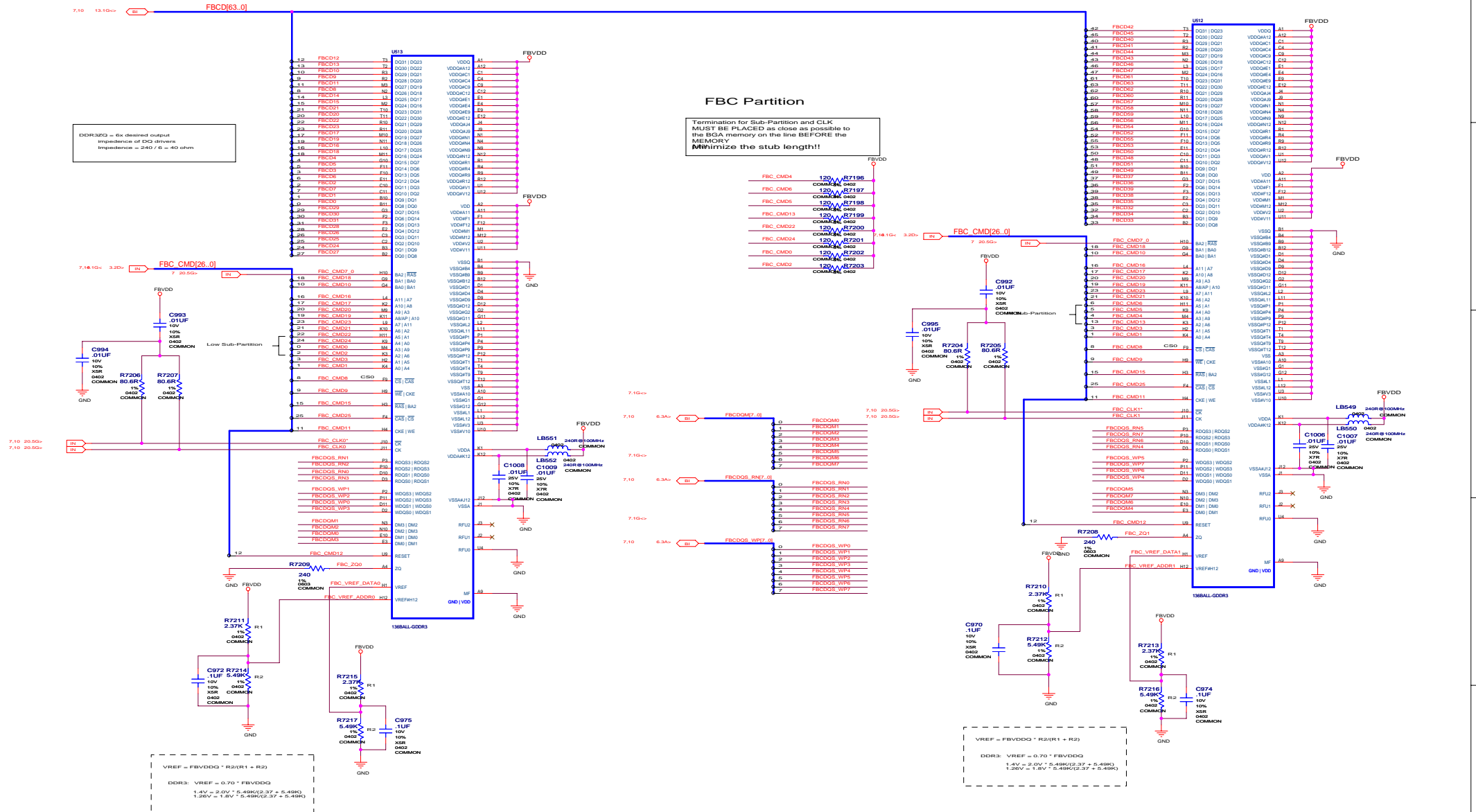
Decoupling for FBA 0..31



Decoupling for FBA 32..63

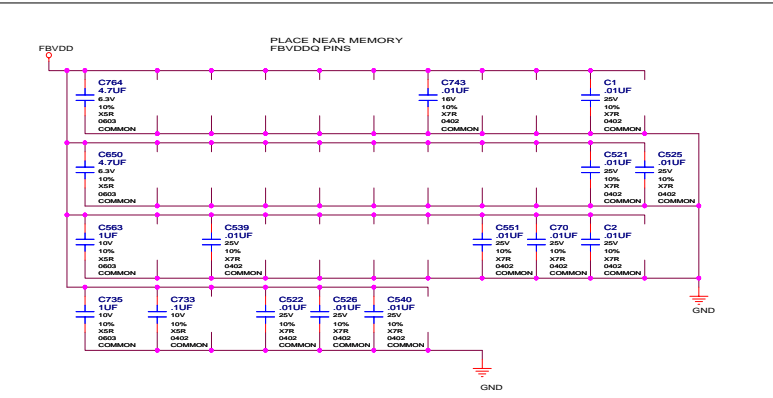
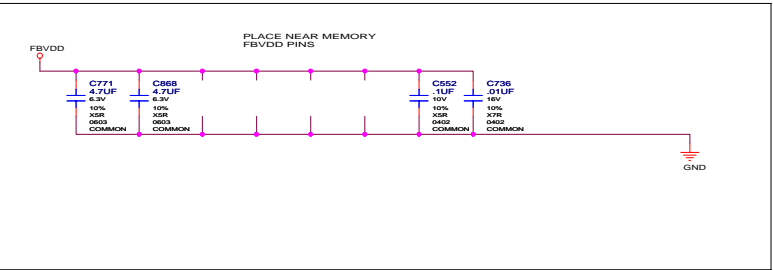


FRAMEBUFFER: PARTITION
C 16Mx32 BGA136 DDR3

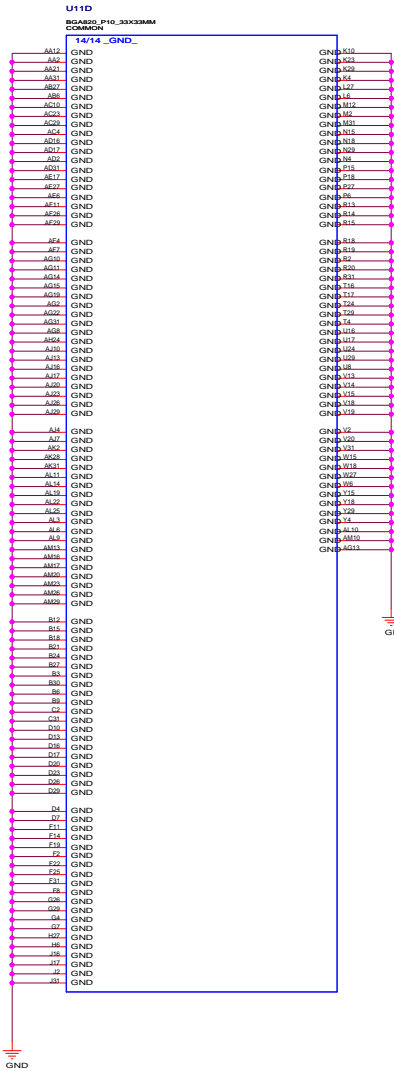
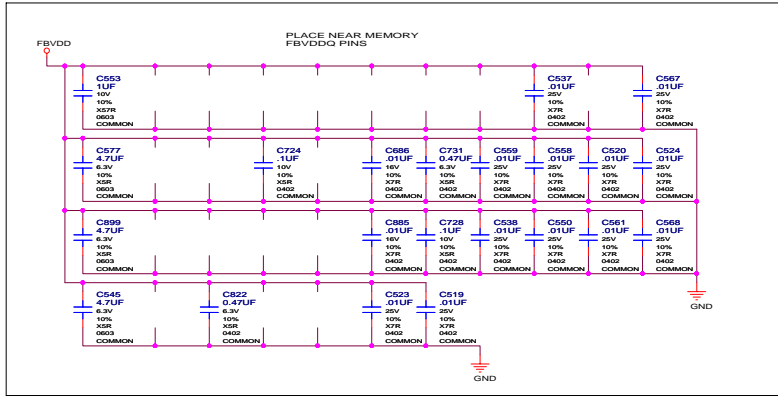
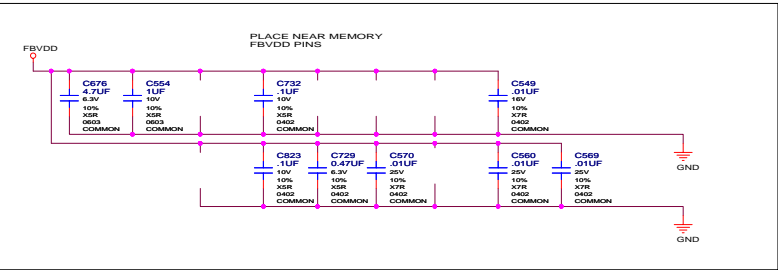


FRAMEBUFFER: PARTITION C
DECOUPLING

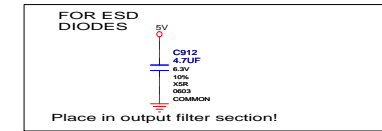
Decoupling for FBC 0..31



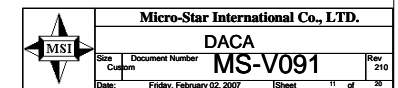
Decoupling for FBC 32..63



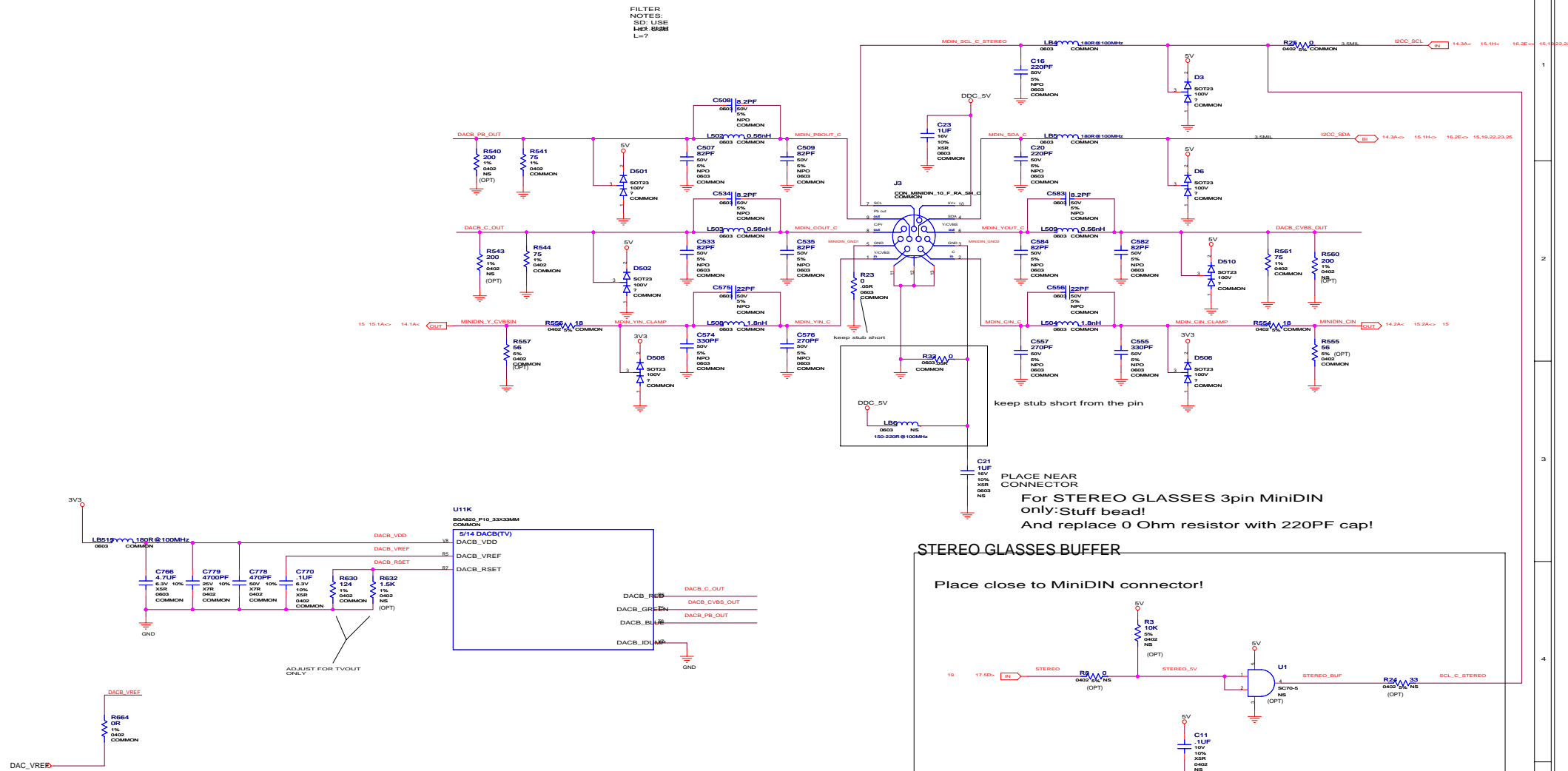
DACC
RGB-FILTER



DACA
RGB-FILTER

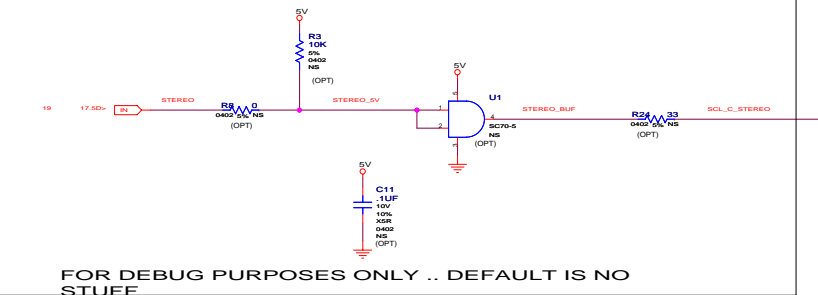


DACB .. MiniDIN VIDEO IN/OUT CONNECTOR /STEREO GLASSES



STEREO GLASSES BUFFER

Place close to MiniDIN connector!





U11G

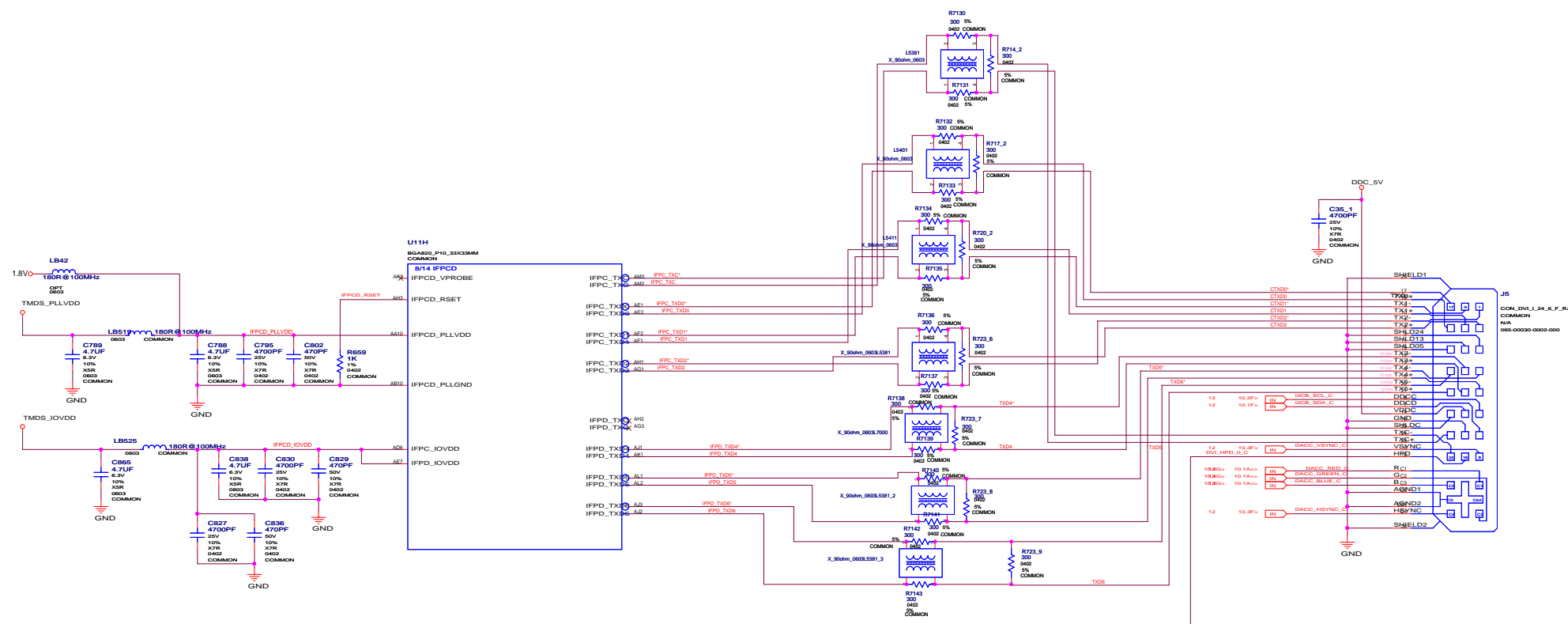


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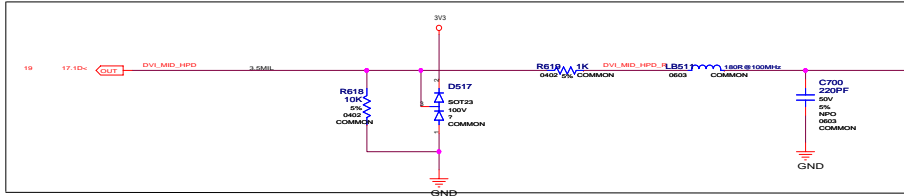
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INTERNAL TMDS .. LINK C & D

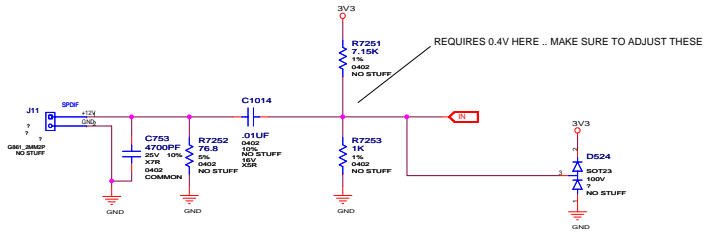


Hotplug Detection

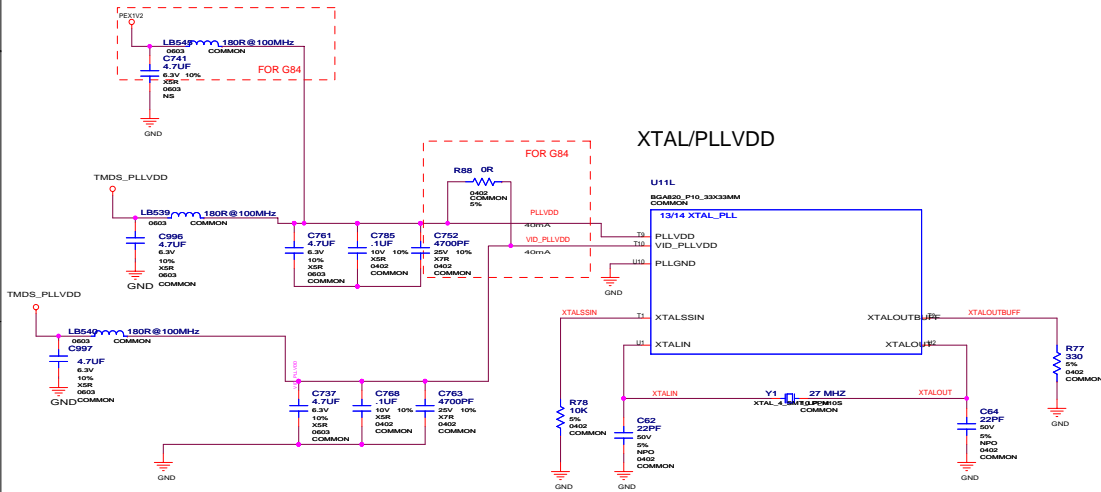


XTAL/PLLVDD/SPDIF IN

SPDIF IN

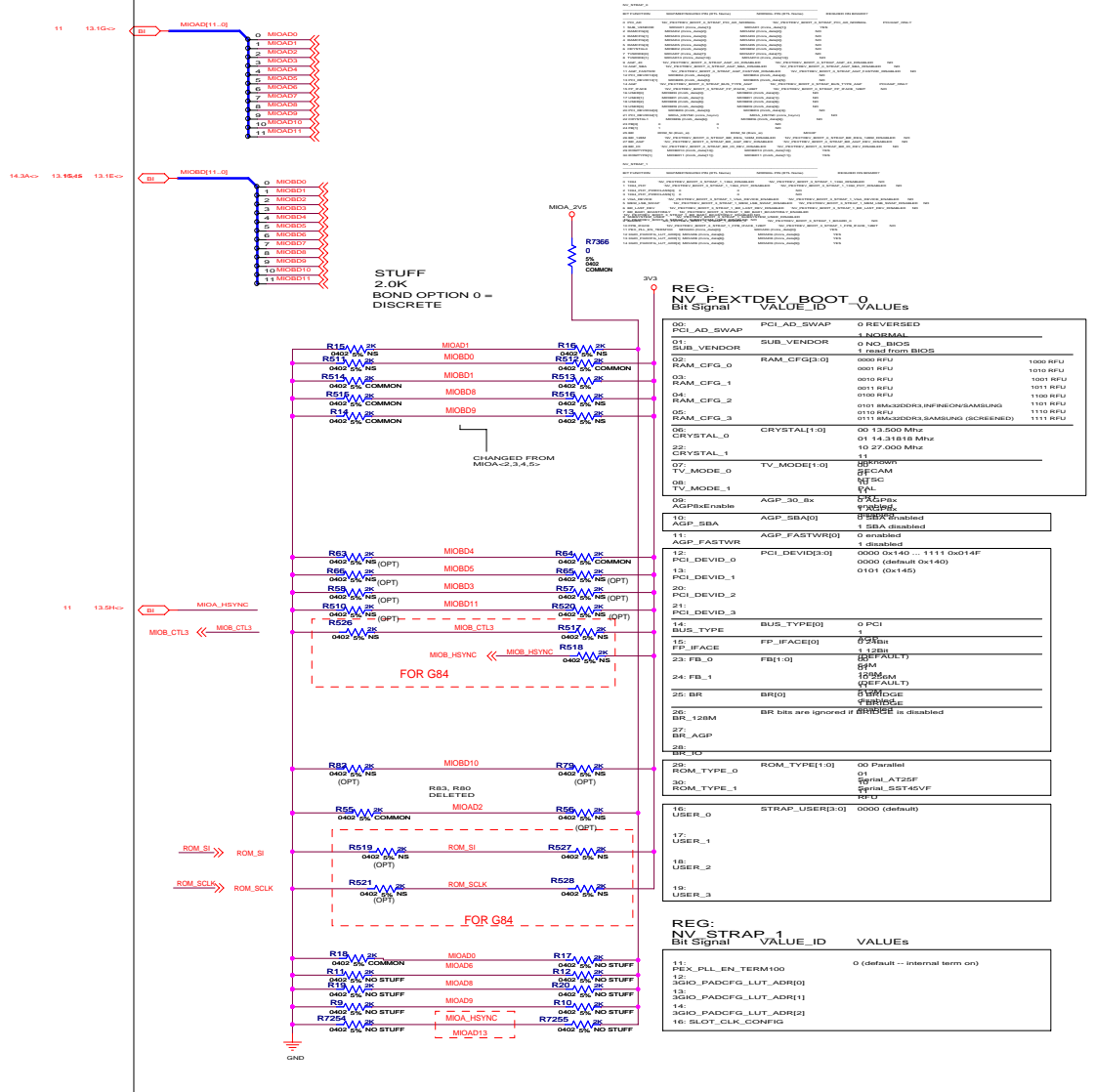


XTAL/PLLVDD

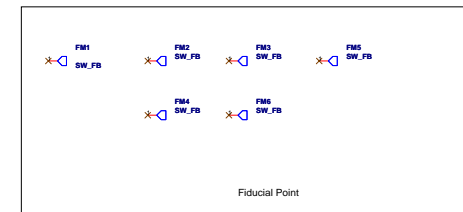
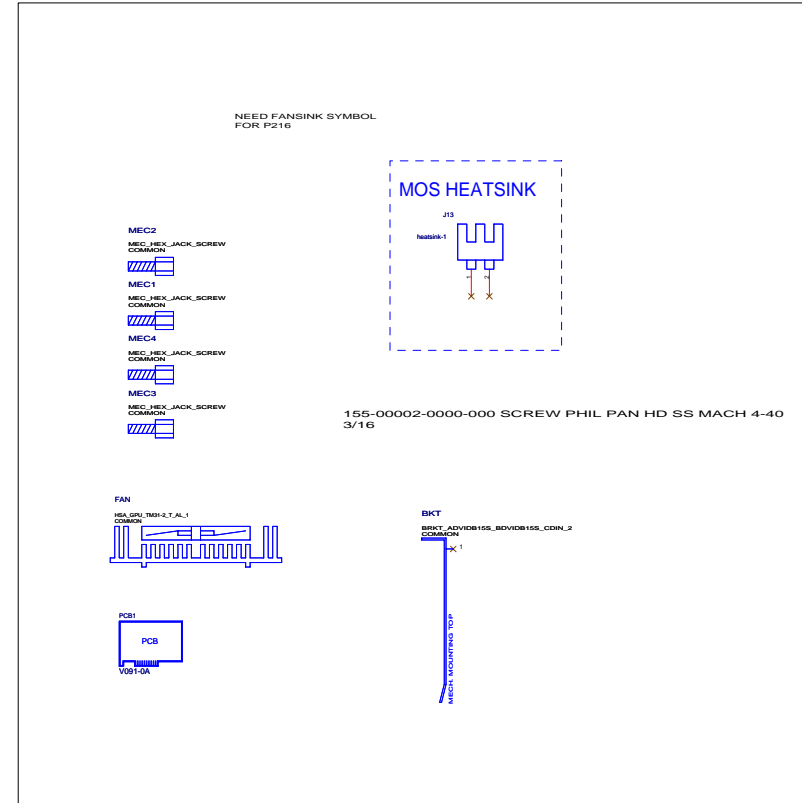


BIOS, Straps, Misc

Assembly:
BIOS



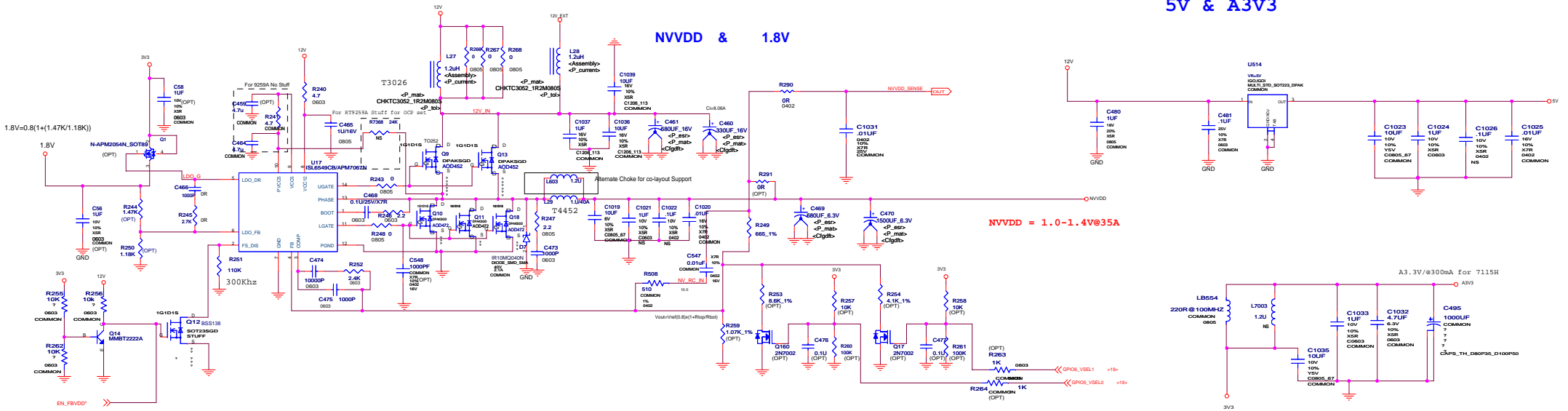
Mechanical parts



NVVDD ,FBVDD, 5V,A3V3 POWER SUPPLY

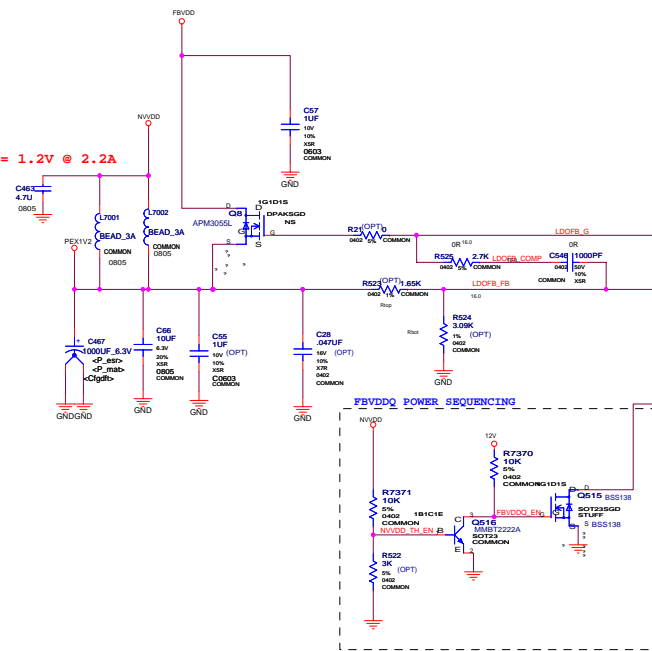
5V & A3V3

NVVDD & 1.8V



PEX1V2

PEX1V2 = 1.2V @ 2.2A



FBVDD

FBVDDQ: 1.8-2.2V@10.6A

