

V084: G72, DDR2 MEMORY 32Mx8


base on V056 - 1.0

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REV	VARIANT	M/PN	ASSEMBLY
0	BASE	600-10381-0000-000	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO. STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU01	600-10381-0000-100	P081: G72C3-6A 32 BIT DDR2 MEMORY, VGA/SDTV
2	SKU01	600-10381-0001-100	P081: G72C3-6A 64 BIT DDR2 MEMORY, VGA/SDTV
3	SKU02	600-10381-0000-100	P081: G72C3-6A 32 BIT DDR2 MEMORY, DVI/SDTV
4	SKU02	600-10381-0000-100	P081: G72C3-6A 64 BIT DDR2 MEMORY, DVI/SDTV
5	SKU04	600-10381-0004-100	P081: G72C3-6A 64 BIT DDR2 MEMORY, VGA/DVI/SDTV
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO. STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	P081 Overview



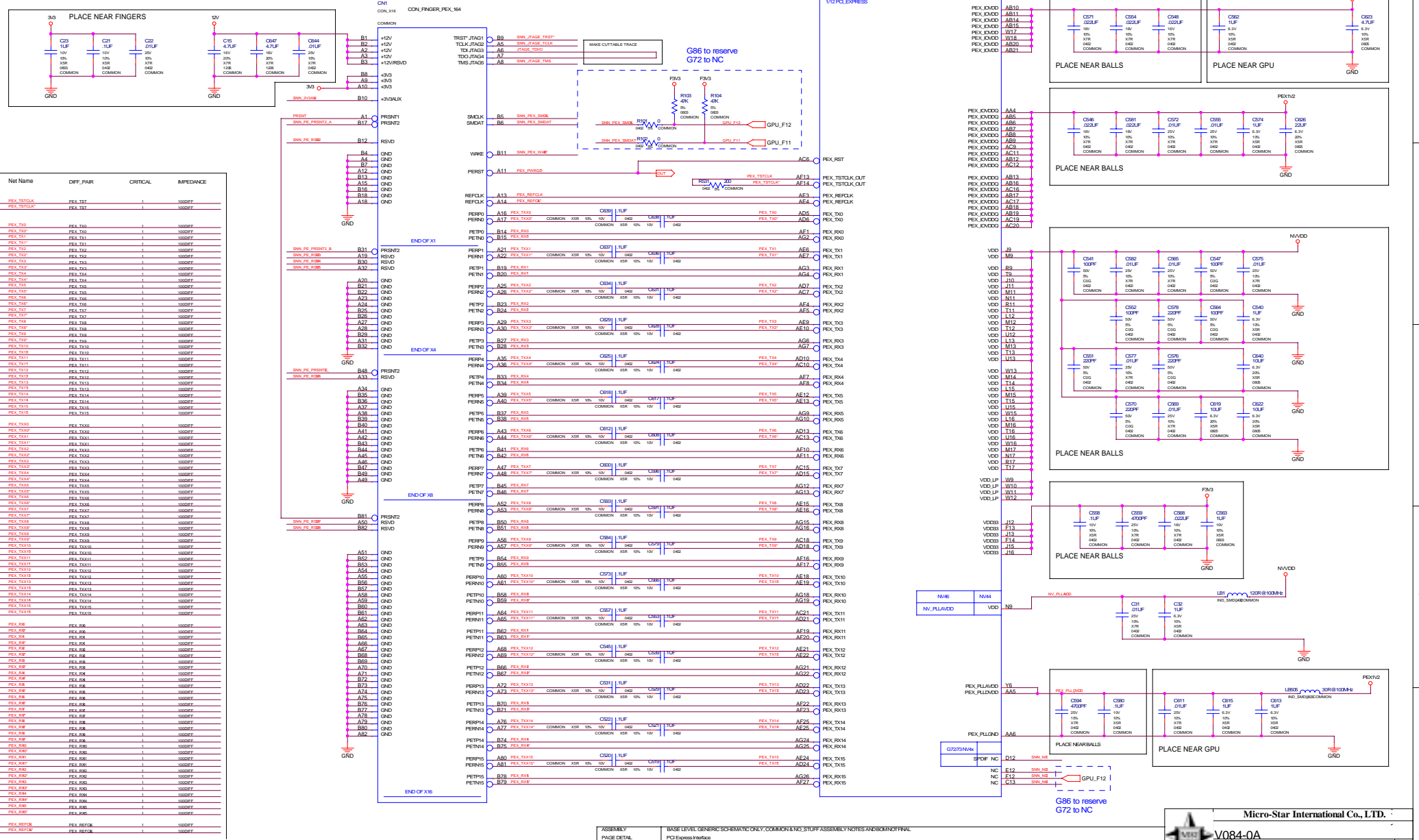
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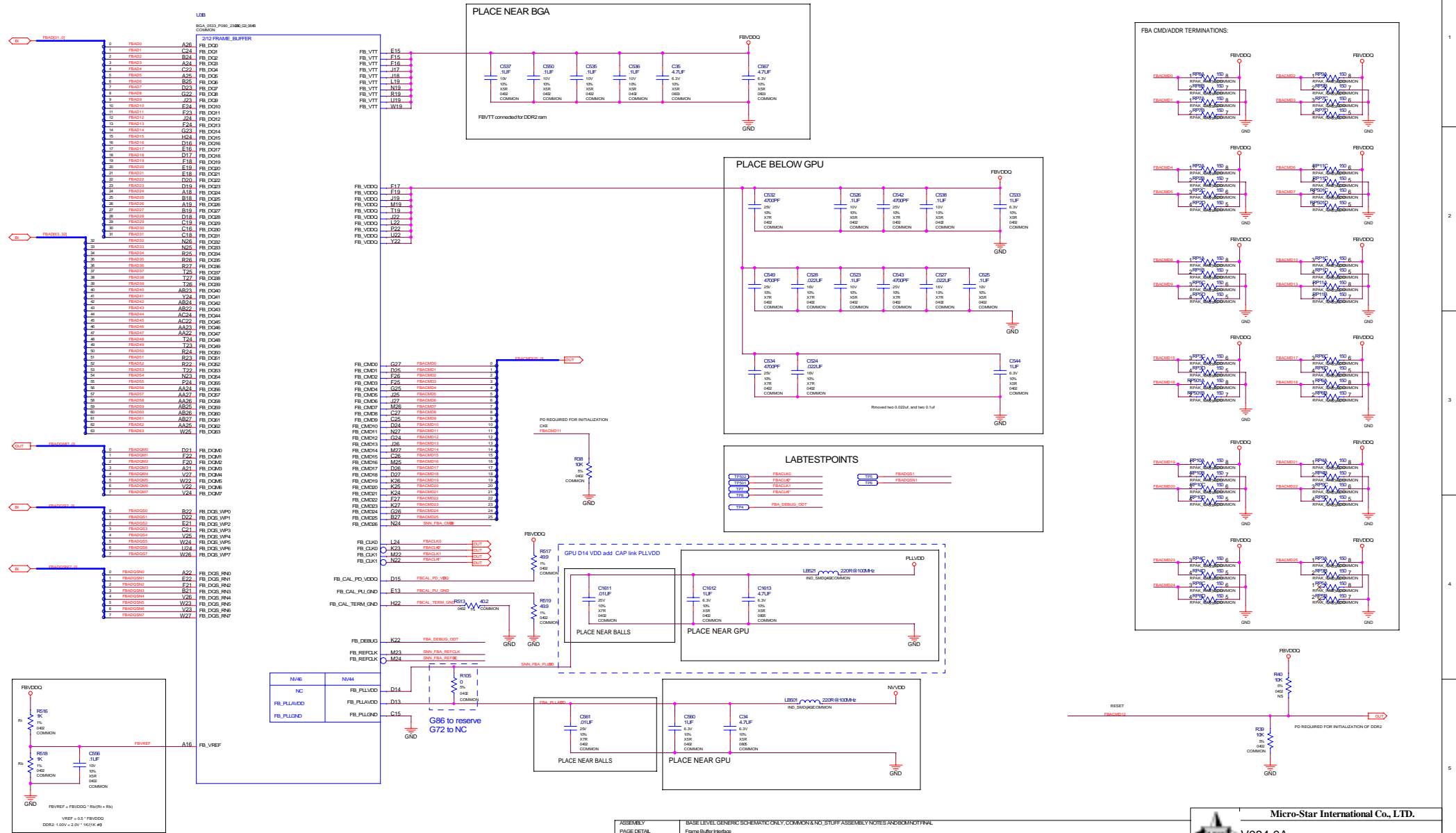
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PEX-Interface

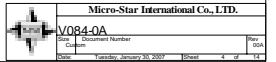


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GPU: FB-Interface

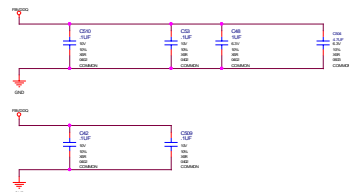
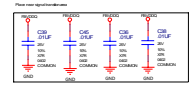
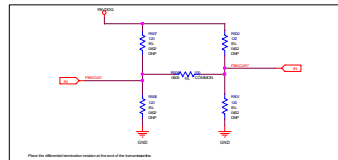
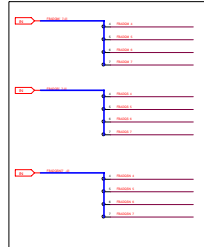
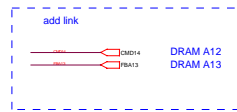
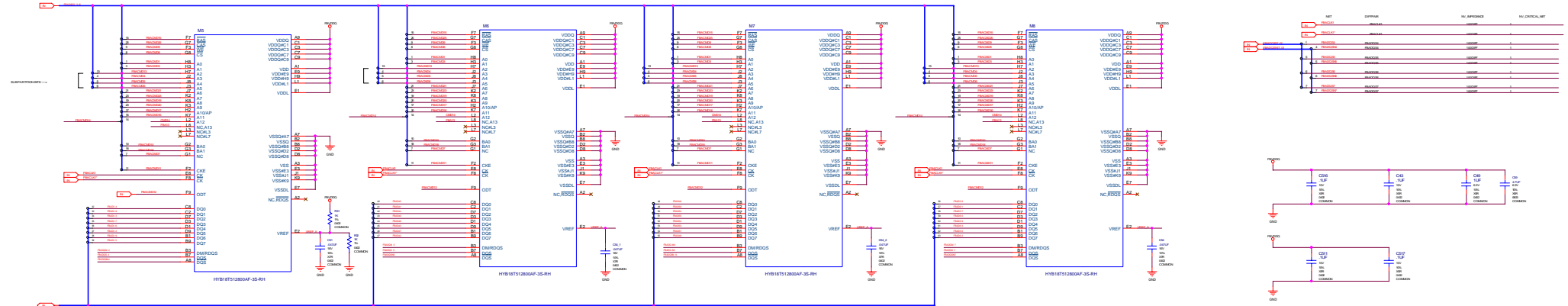


PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY



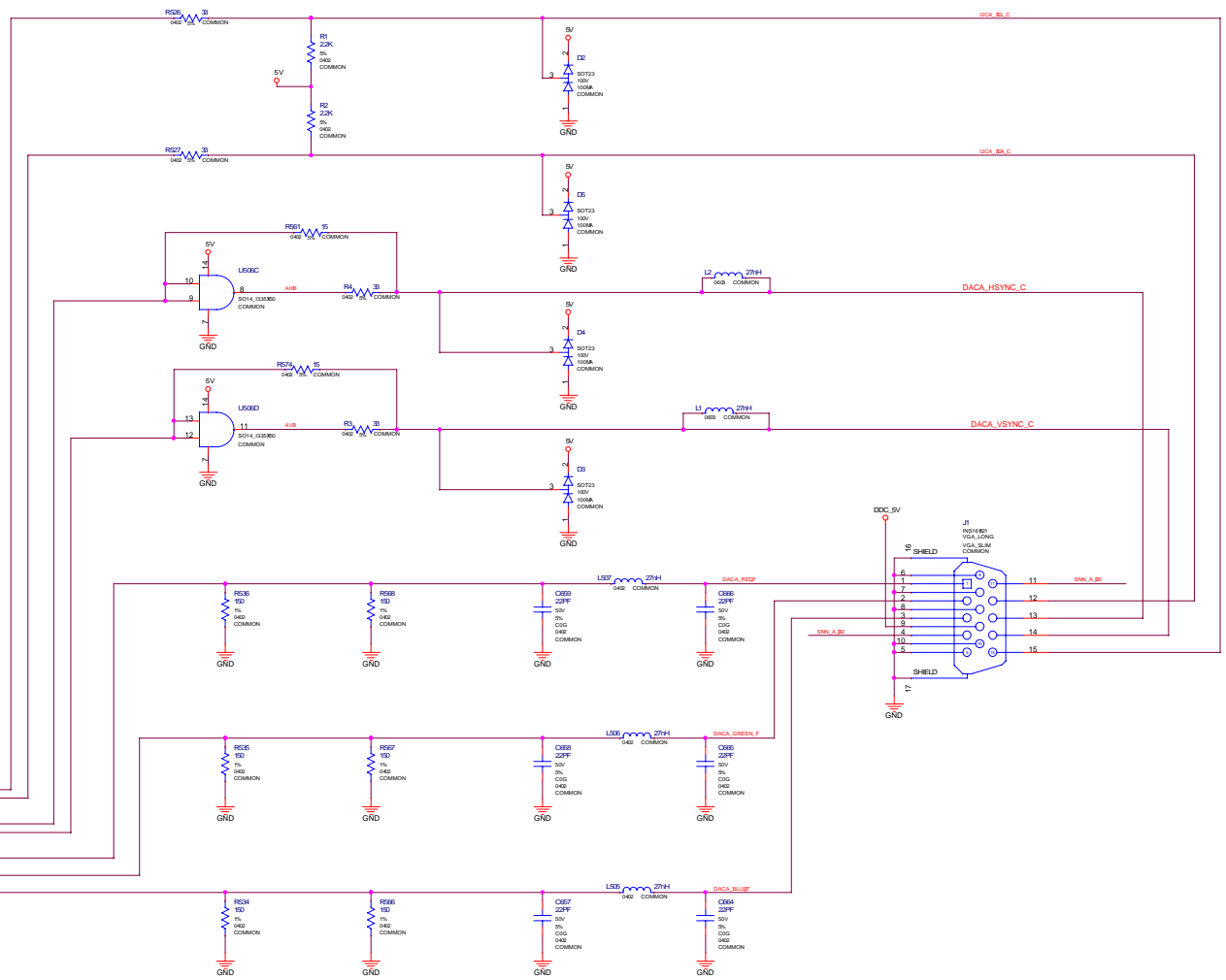
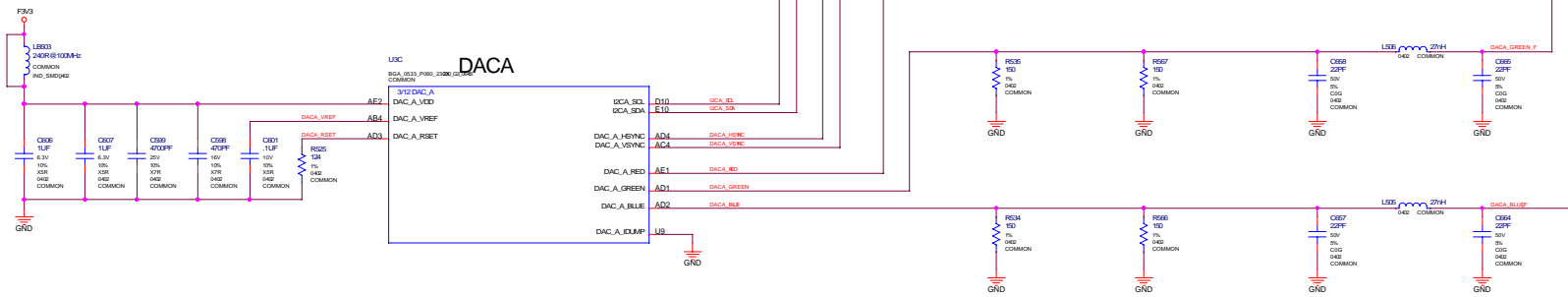
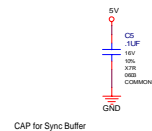
Memory Bit 32..63

PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY



DAC-A, DB15 Connector

	Net Name	IMP_LINE_WIDTH	CRITICAL_NET	IMPEDANCE
IN	DACA_VIN0	2		50OHM
IN	DACA_VIN0	2		50OHM
IN	DACA_VIN0_C	2		50OHM
IN	DACA_VIN0_C	2		50OHM
IN	DACA_VIN1	1		50OHM
IN	DACA_VIN1	1		50OHM
IN	DACA_VIN2	1		50OHM
IN	DACA_VIN2	1		50OHM



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACA, DACB, DACC, Sync Buffers

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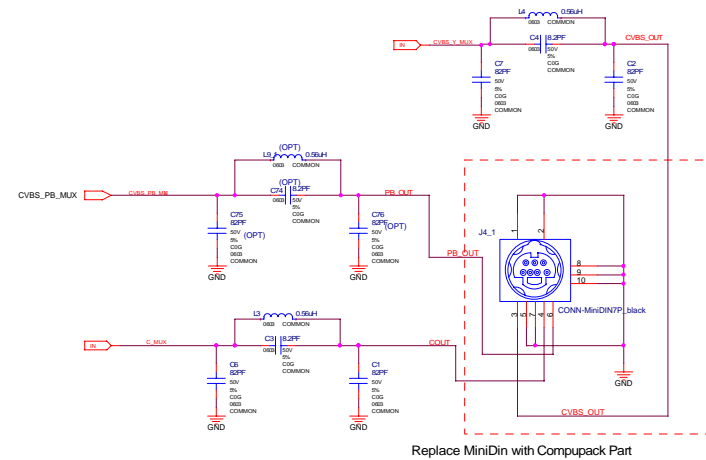
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VIDEO CONNECTORS: MiniDIN

Name	LINE_WIDTH	CRITICAL	IMPEDANCE
 C_MAX		2	5000M
 C_VS_V_WB		2	5000M
 COUT		1	5000M
 C_VS_OUT		1	5000M



Replace MiniDin with Compupack Part

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PAGE DETAIL	VIDEO CONNECTORS: MiniDIN, 2x6 HDR



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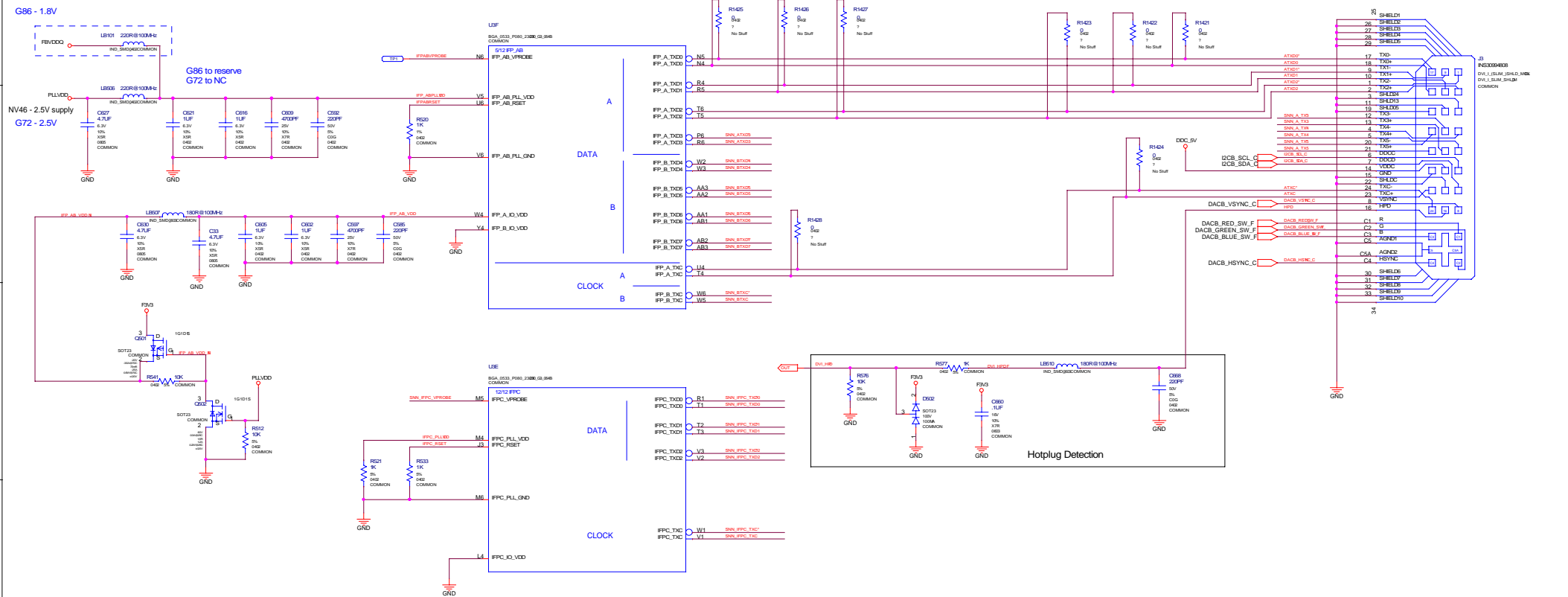
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Internal TMDS, DVI-Connector



Net Name		Diff	CRITICAL	IMPEDANCE
N1	ATXIO	ATXIC	1	100DIFF
N2	ATXIO	ATXIC	1	100DIFF
N3	ATXIO	ATXIO	1	100DIFF
N4	ATXIO	ATXIO	1	100DIFF
N5	ATXIO	ATXIO	1	100DIFF
N6	ATXIO	ATXIO	1	100DIFF
N7	ATXIO	ATXIO	1	100DIFF
N8	ATXIO	ATXIO	1	100DIFF
N9	ATXIO	ATXIO	1	100DIFF
N10	ATXIO	ATXIO	1	100DIFF

Net Name		Min Line Width	Voltage
N1	PP_ABLIBD	100M	3.3V
N2	PP_ABLIBD	100M	3.3V
N3	PP_ABLIBD	100M	3.3V
N4	PP_ABLIBD	100M	3.3V
N5	PP_ABLIBD	100M	3.3V

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BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
TMD5 Interface

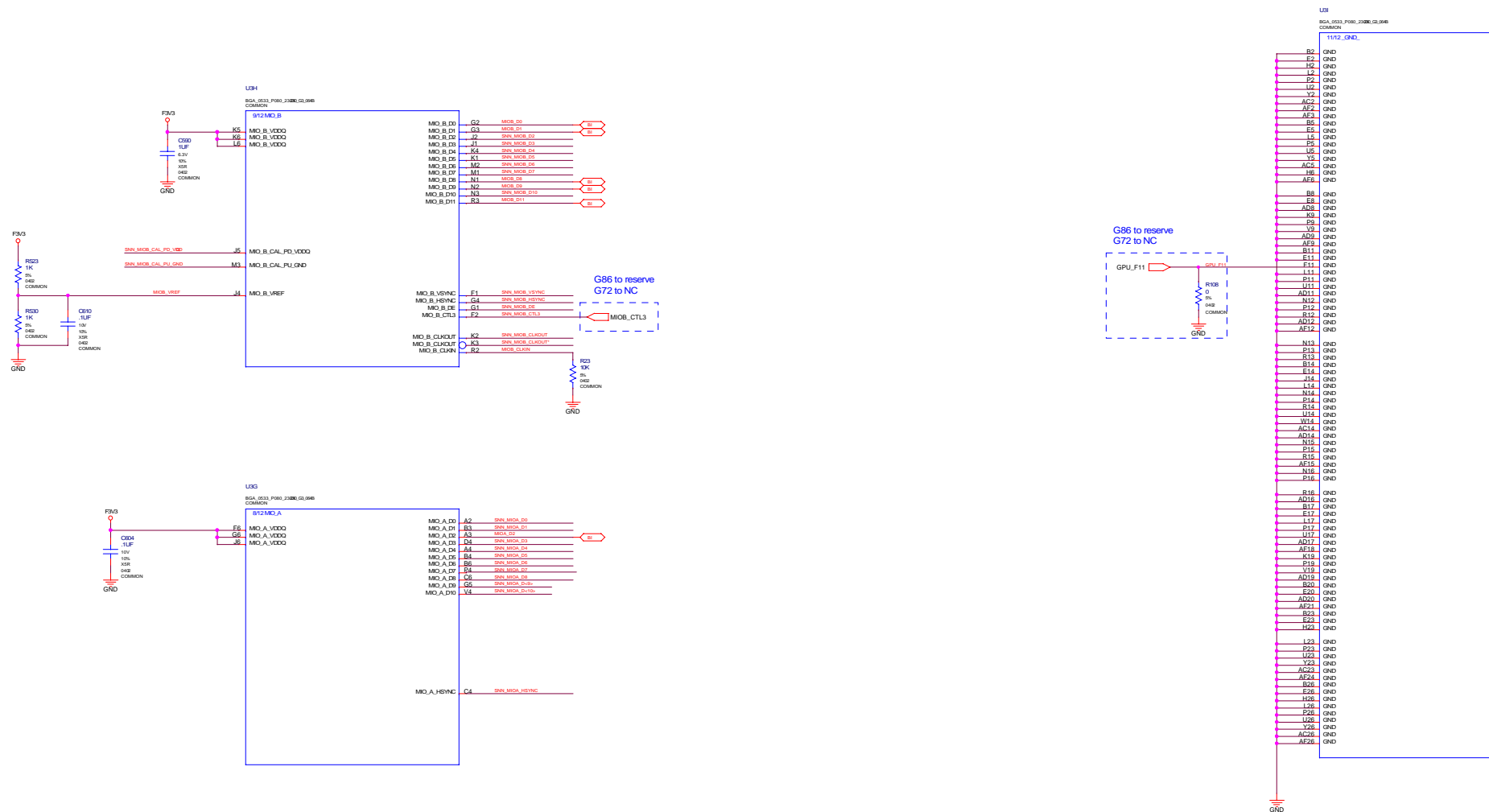


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MIOA, MIOB Interface



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PAGE DETAIL	MIOA, MIOB Interface, LPC-ROM



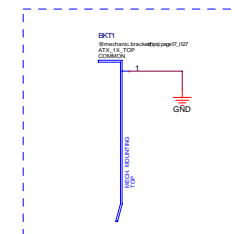
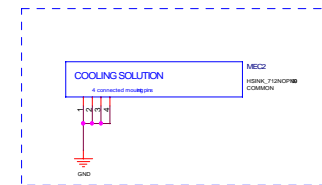
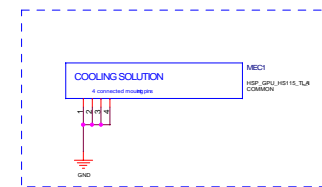
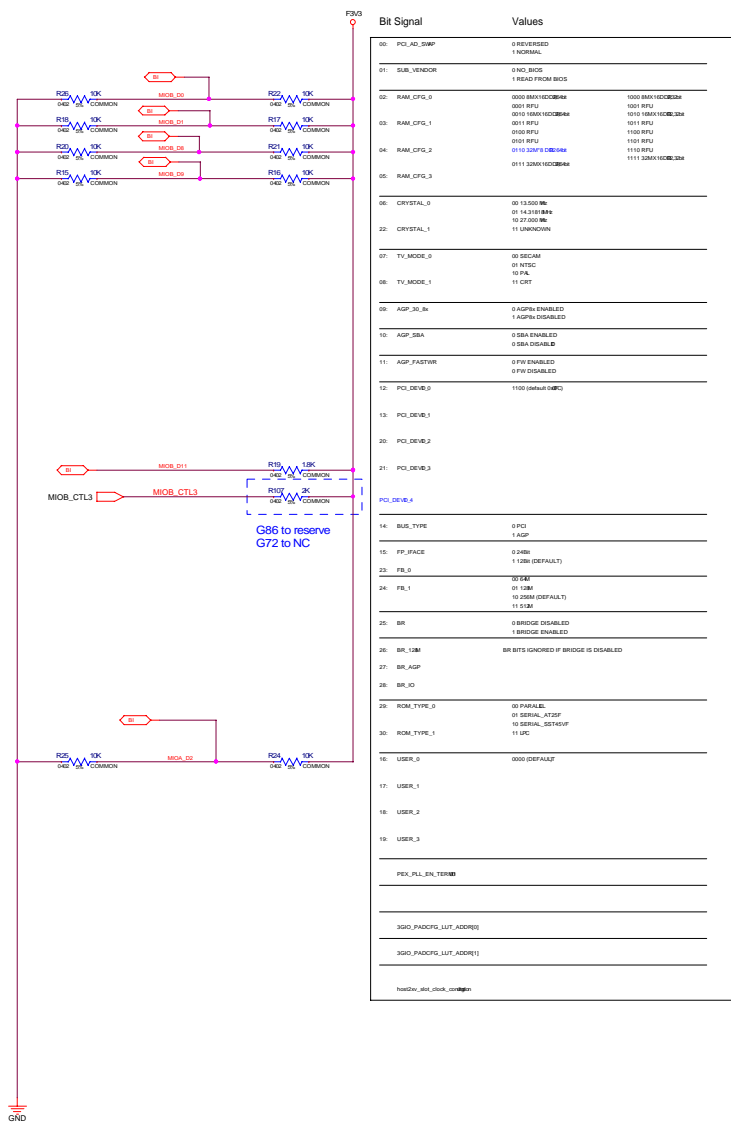
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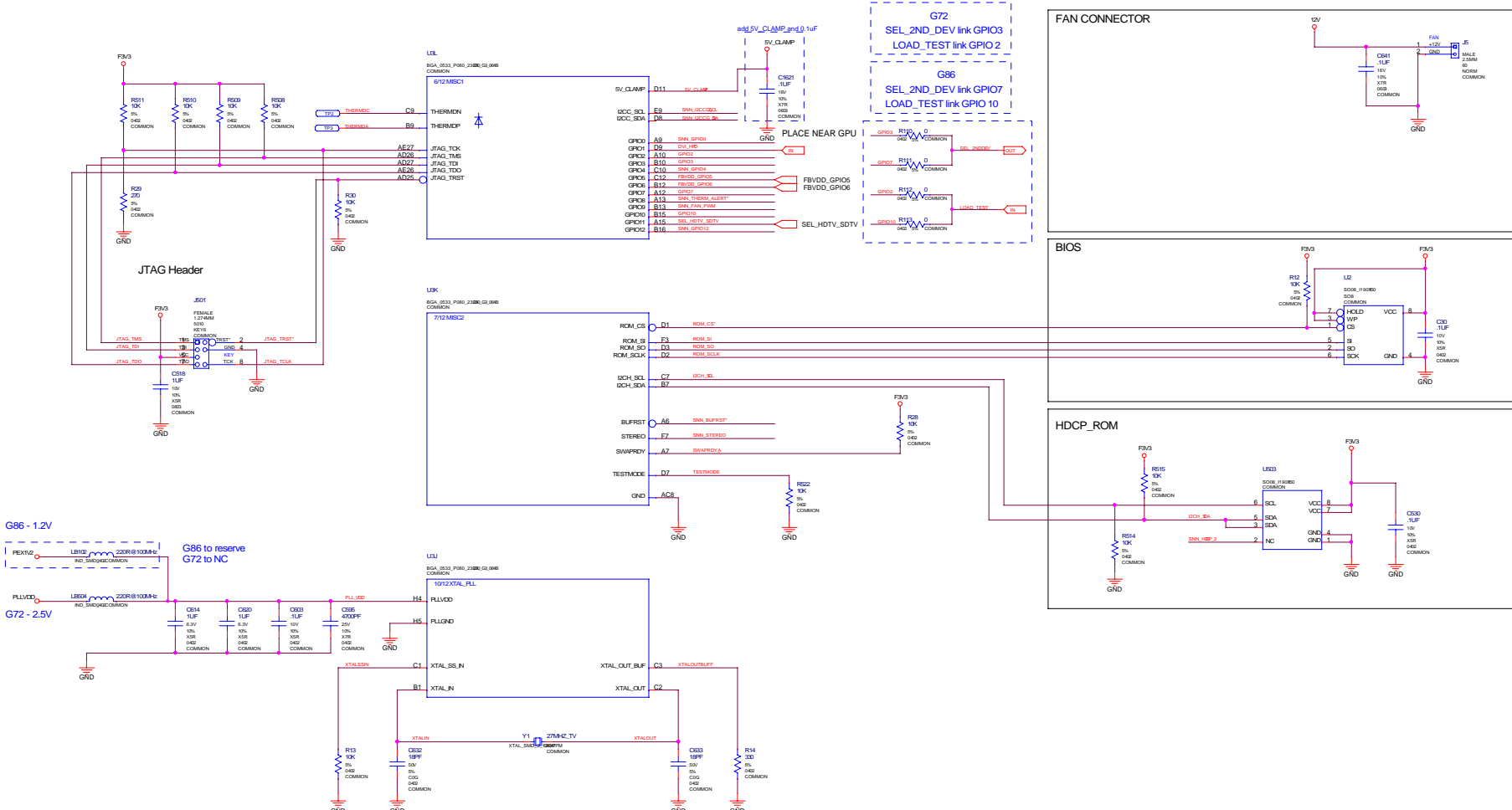
STRAPS, Mechanical Parts



- MEC7
MEC_SCREW_RRI
COMMON
- MEC4
MEC_SCREW_HEI&C
COMMON
- MEC5
MEC_SCREW_HEI&C
COMMON
- MEC6
MEC_SCREW_HEI&C
COMMON
- MEC3
MEC_SCREW_HEI&C
COMMON



XTAL, GPIO, BIOS, FAN, JTAG, THERMAL



PowerSupplyI: NVVDD, PLLVDD

Net Name	LINE_WIDTH	Current	Voltage
12V_G	12V	3.0000	1.2000
12V_F	12V	3.0000	1.2000
NVVDD	NVVDD	1.0000	1.2000
PEX1V2	PEX1V2	1.0000	1.2000
GND	GND	0.0000	0.0000

