

P691: G98/GT218, DDR2 MEMORY 64MX16/32MX16

Page 1: P691 Overview

Page 2: PCI Express Interface

Page 3: Frame Buffer Interface

Page 4: DDR2 Memories

Page 5: DAC A Slim VGA

Page 6: DAC B VGA Header

Page 7: TMDS Interface

Page 8: DisplayPort Connector

Page 9: IFPC, IFPE Interface, Mechanical, SPDIF

Page 10: XTAL, ROM, JTAG

Page 11: Thermal Protection, Protected 3V3, Straps

Page 12: Power Supply I: FBVDD/Q, PEX\_VDD, 5V, 3V3\_F

Page 13: Power Supply II: PEX\_PLLVDD, NVVDD

| REV | VARIANT     | NVPN               | ASSEMBLY  |
|-----|-------------|--------------------|---|
| B   | BASE        | 600-10691-BASE-100 | BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| 1   | SKU0001     | 600-10691-0001-100 | GT218-300, 550/1375/500, 256MB/448, 32Mx16 DDR2, DVI-DL+DP+VGA, DT                    |
| 2   | SKU0002     | 600-10691-0002-100 | GT218-300, 550/1375/500, 512MB/648, 64Mx16 DDR2, DVI-DL+DP+VGA, DT                    |
| 3   | SKU0010     | 600-10691-0010-100 | G98-400, 550/1375/500, 256MB/448, 32Mx16 DDR2, DVI-DL+DP+VGA, DT                      |
| 4   | SKU0011     | 600-10691-0011-100 | G98-400, 550/1375/500, 512MB/448, 64Mx16 DDR2, DVI-DL+DP+VGA, DT                      |
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| PAGE DETAIL | P691 Overview   |

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3V3\_F

10uF

6.2V

C49

C50

D+

D-

GND

12V\_PEX

4.7uF

16V

C24

C25

D+

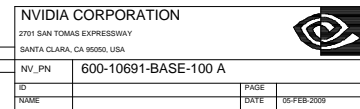
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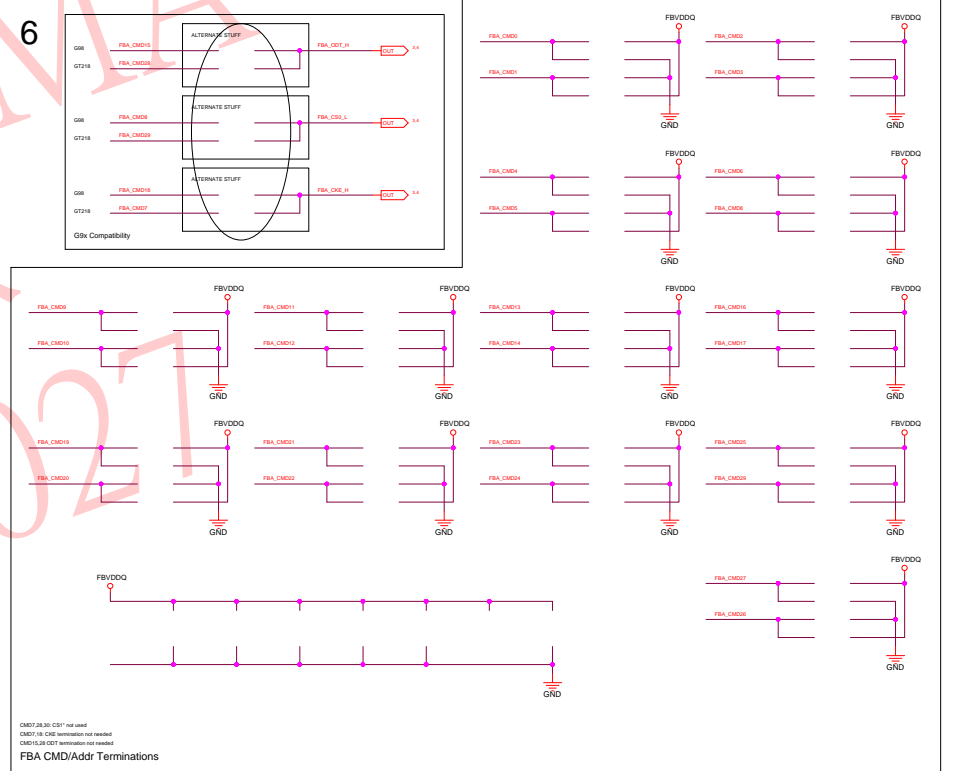
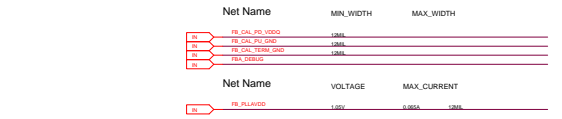
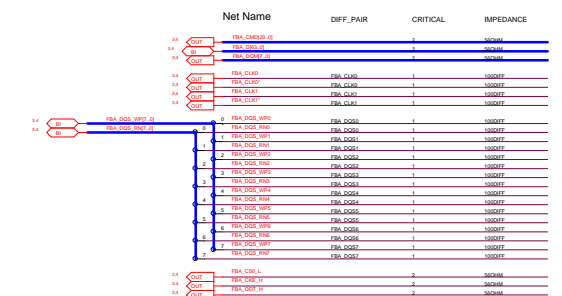
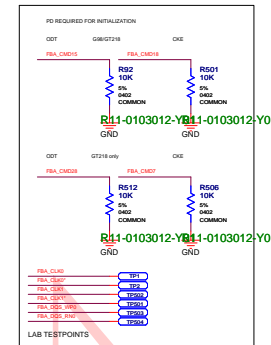
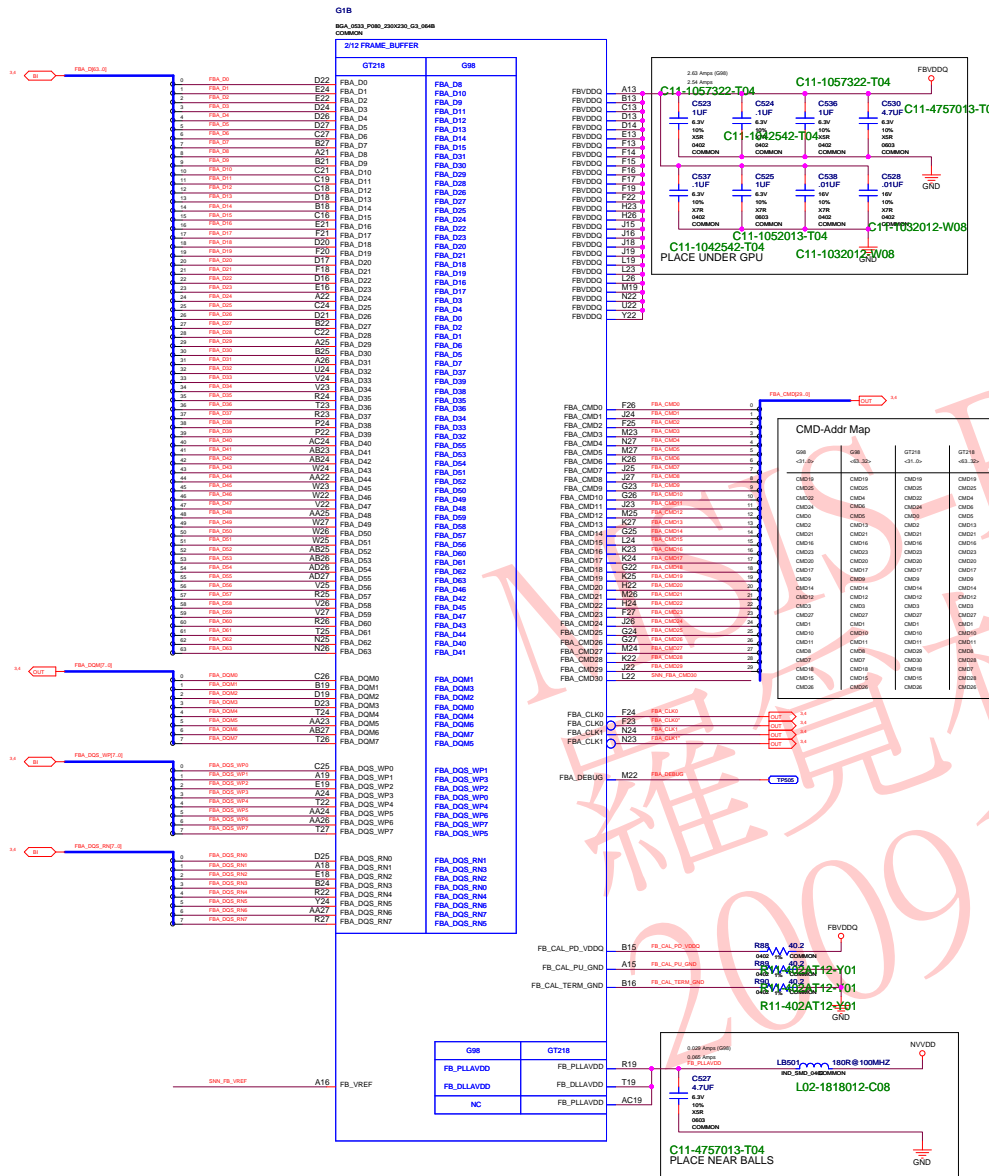
COMMON

PLACE NEAR FINGERS

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## Frame Buffer Interface

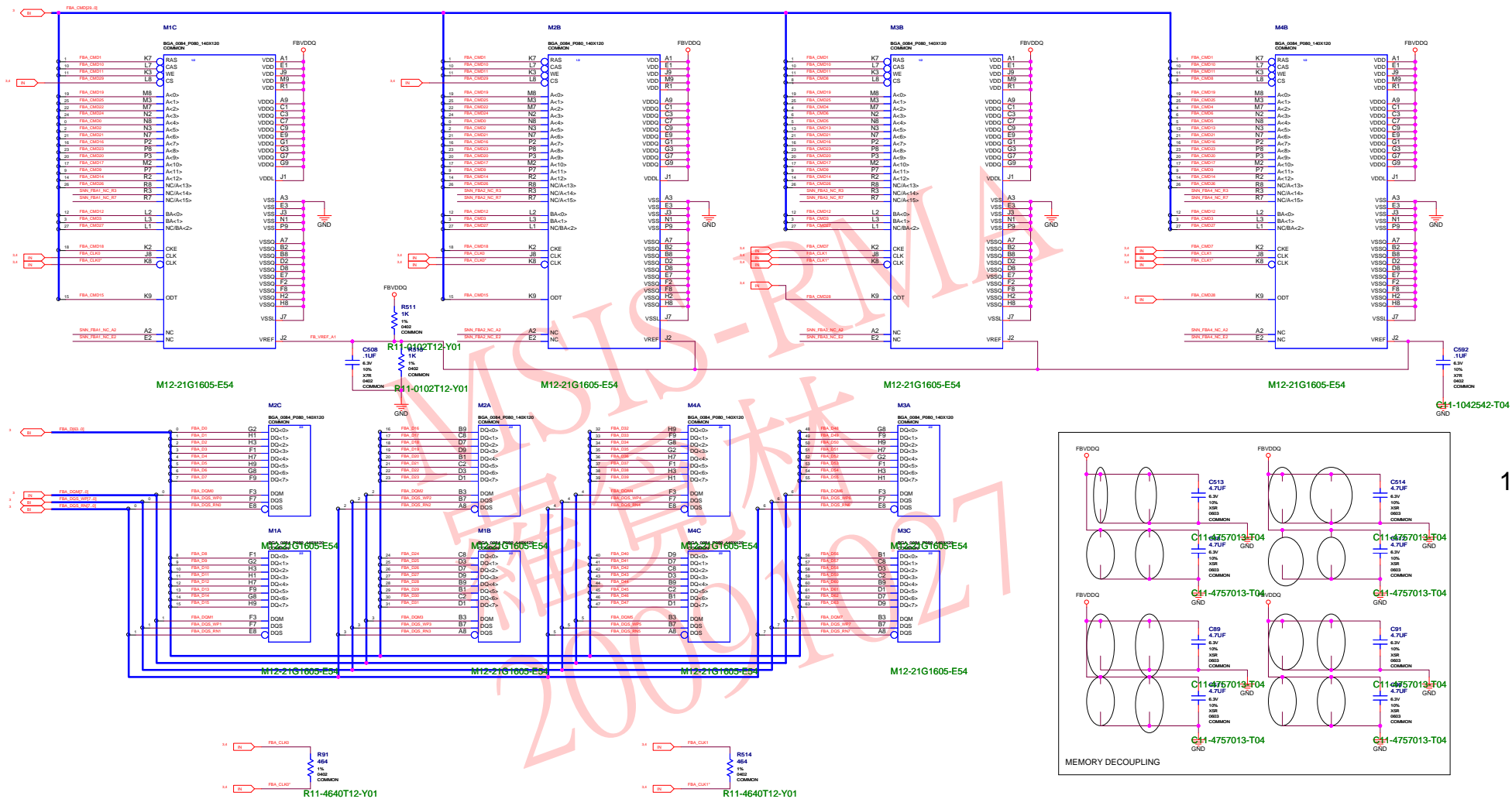


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## DDR2 Memories



16

## MEMORY DECOUPLING

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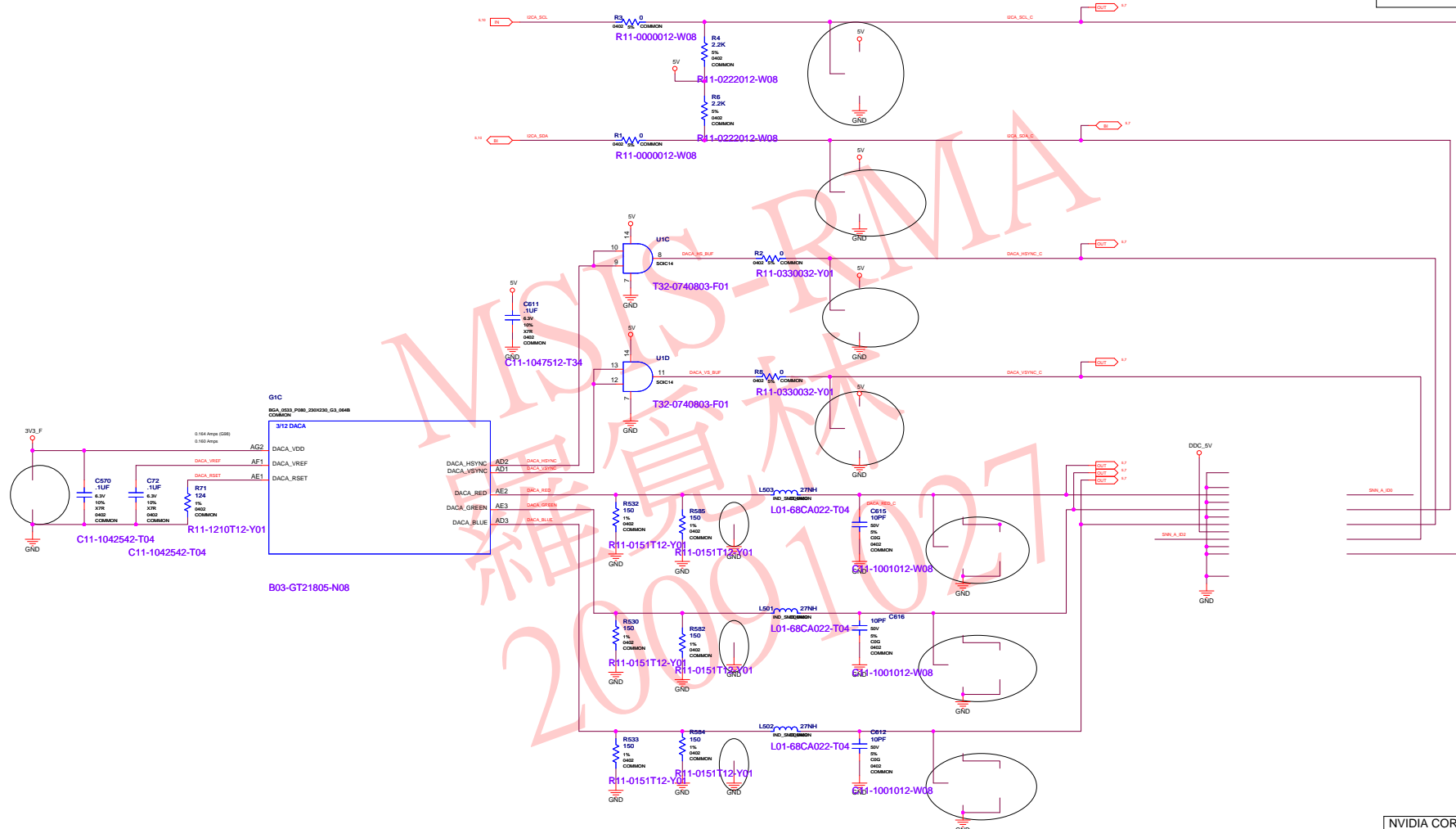
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| PAGE DETAIL | DDR2 Memories   |

## DAC A Slim VGA


11



| Net Name |                  | CRITICAL | IMPEDANCE |
|----------|------------------|----------|-----------|
| 0.7      | IN DQCA_W00      | 1        | 50OHM     |
|          | IN DQCA_W00EN    | 1        | 50OHM     |
|          | IN DQCA_W01      | 1        | 50OHM     |
|          | IN DQCA_W01EN    | 1        | 50OHM     |
|          | OUT DQCA_W00EN_C | 1        | 50OHM     |
| 0.7      | OUT DQCA_W01_C   | 1        | 50OHM     |
|          | IN DQCA_W02EN    | 2        | 50OHM     |
|          | IN DQCA_W02      | 2        | 50OHM     |
|          | IN DQCA_W02EN_C  | 2        | 50OHM     |
|          | OUT DQCA_W02_C   | 2        | 50OHM     |
| 0.7      | IN DQCA_W03EN    | 2        | 50OHM     |
|          | IN DQCA_W03      | 2        | 50OHM     |
|          | IN DQCA_W03EN_C  | 2        | 50OHM     |
|          | OUT DQCA_W03_C   | 2        | 50OHM     |
|          | OUT DQCA_W03EN_C | 2        | 50OHM     |

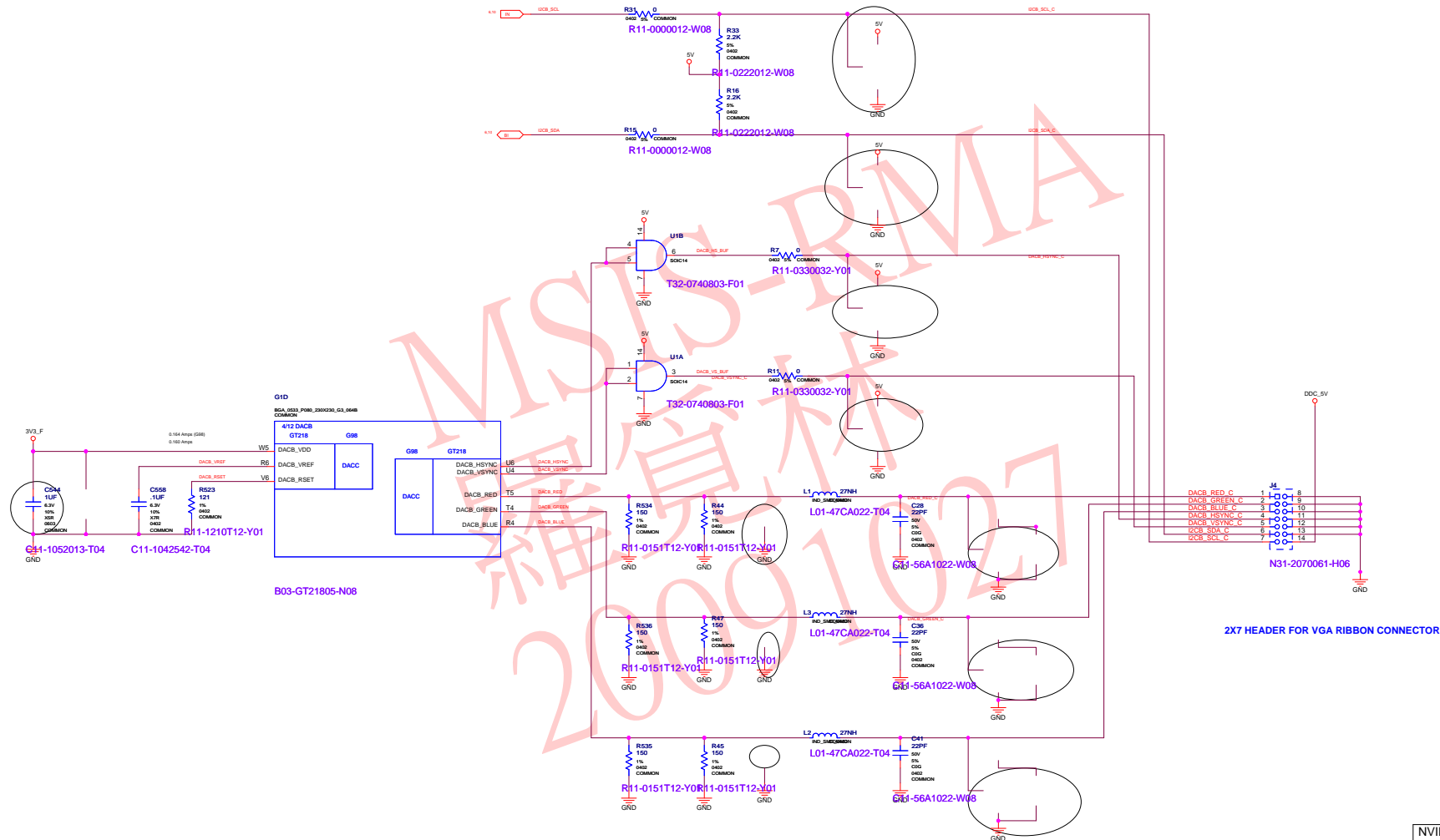
  

| Net Name |                | MIN_WIDTH | MAX_WIDTH |
|----------|----------------|-----------|-----------|
| 0.0      | IN DQCA_W00    |           |           |
|          | IN DQCA_W00_C  |           |           |
| 0.7      | OUT DQCA_W00_C |           |           |
|          | IN DQCA_W00EN  | 128UM     |           |
| 0.7      | IN DQCA_W01    |           |           |
|          | IN DQCA_W01EN  | 128UM     |           |

|                            |                      |   |             |
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## DAC B VGA Header




| Net Name |               | CRITICAL | IMPEDANCE |
|----------|---------------|----------|-----------|
| 15       | DACK_RED      | 1        | 50OHM     |
| 15       | DACK_GREEN    | 1        | 50OHM     |
| 15       | DACK_BLUE     | 1        | 50OHM     |
| 15       | DACK_RED_0    | 1        | 50OHM     |
| 15       | DACK_GREEN_0  | 1        | 50OHM     |
| 15       | DACK_BLUE_0   | 1        | 50OHM     |
| 15       | DACK_VHNC     | 2        | 50OHM     |
| 15       | DACK_VHNC_0   | 2        | 50OHM     |
| 15       | DACK_VHNC_1   | 2        | 50OHM     |
| 15       | DACK_VH_0_BUF | 2        | 50OHM     |
| 15       | DACK_VH_1_BUF | 2        | 50OHM     |
| 15       | DACK_VH_2_BUF | 2        | 50OHM     |
| 15       | DACK_VH_3_BUF | 2        | 50OHM     |

| Net Name |            | MIN_WIDTH | MAX_WIDTH |
|----------|------------|-----------|-----------|
| 0.10     | HCB_SCL    |           |           |
| 0.10     | HCB_SDA    |           |           |
| 0.10     | HCB_SCL_0  |           |           |
| 0.10     | HCB_SDA_0  |           |           |
| 0.10     | HCB_VDDP   | 120MIL    |           |
| 0.10     | HCB_VDDP_0 | 120MIL    |           |
| 0.10     | HCB_RESET  | 120MIL    |           |

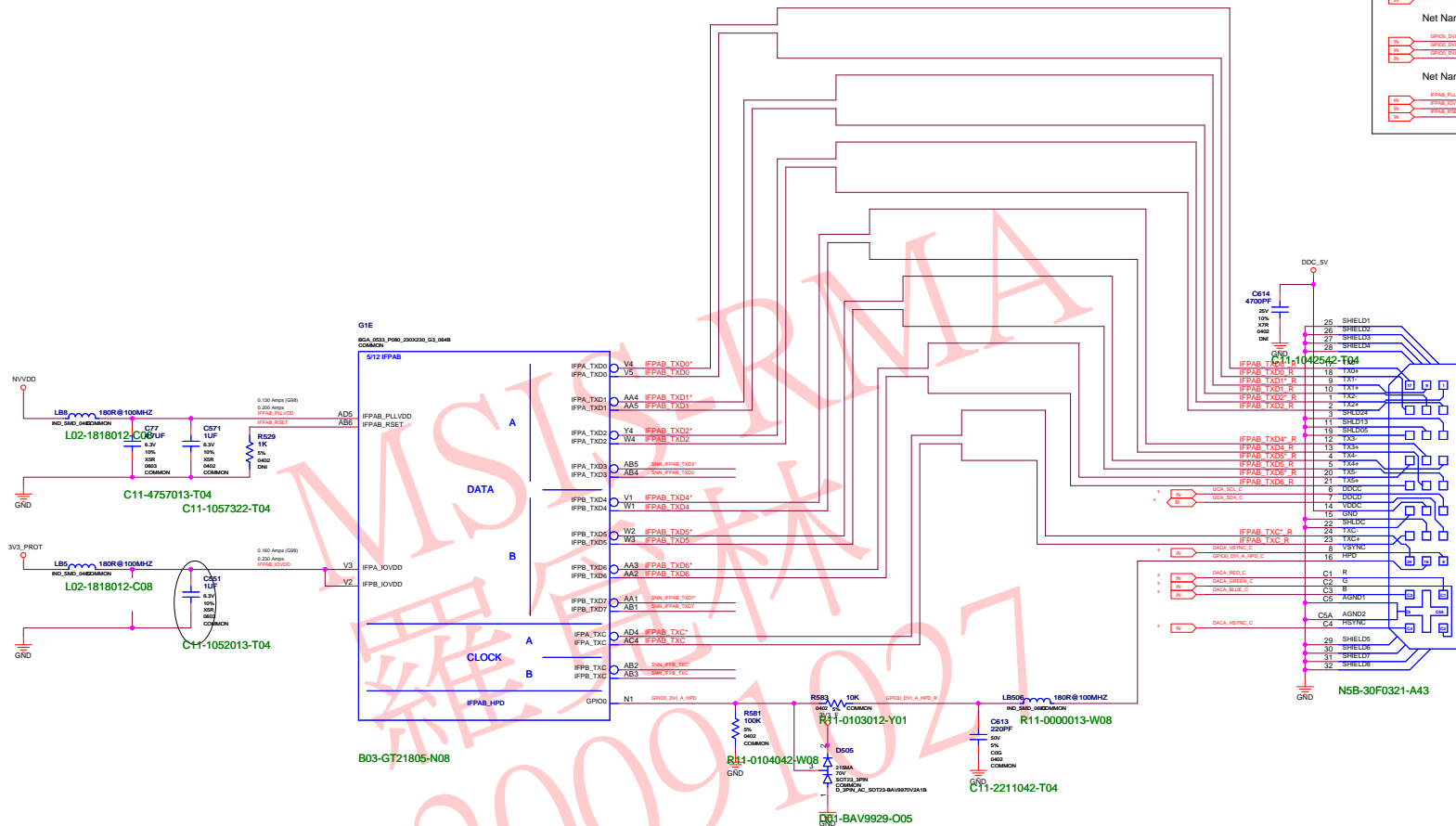
11

### 2X7 HEADER FOR VGA RIBBON CONNECTOR

|  |  |   |             |
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## TMDS Interface



| Net Name |           | DIFF_PAIR | CRITICAL | IMPEDANCE |
|----------|-----------|-----------|----------|-----------|
| IN       | FFIN_120P | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120I | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120Q | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120J | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120K | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120L | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120M | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120H | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120P | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120S | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120T | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120V | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120W | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120X | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120Y | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120Z | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120  | FFIN_120N | 1        | 50OHM     |
| IN       | FFIN_120  | FFIN_120N | 1        | 50OHM     |

| Net Name |                 | MIN_WIDTH | MAX_WIDTH |
|----------|-----------------|-----------|-----------|
| IN       | GGIN_50A_10P5   |           |           |
| IN       | GGIN_50A_10P5 B |           |           |
| IN       | GGIN_50A_10P5 C |           |           |

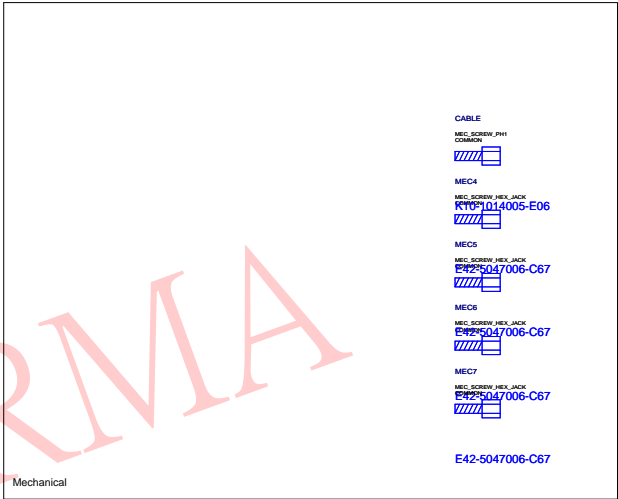
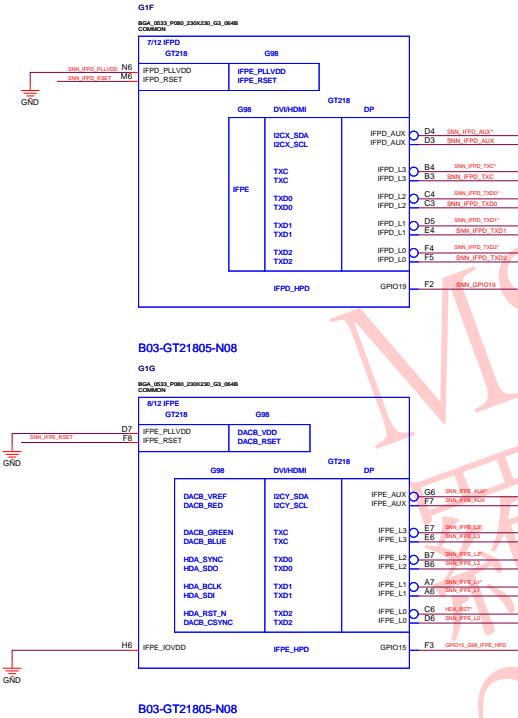
| Net Name |            | VOLTAGE | MAX_CURRENT |
|----------|------------|---------|-------------|
| IN       | FFIN_PLVDD | 1.8V    | 0.300A 100A |
| IN       | FFIN_PLVDD | 3.3V    | 0.300A 100A |
| IN       | FFIN_PLVDD |         | 100A        |

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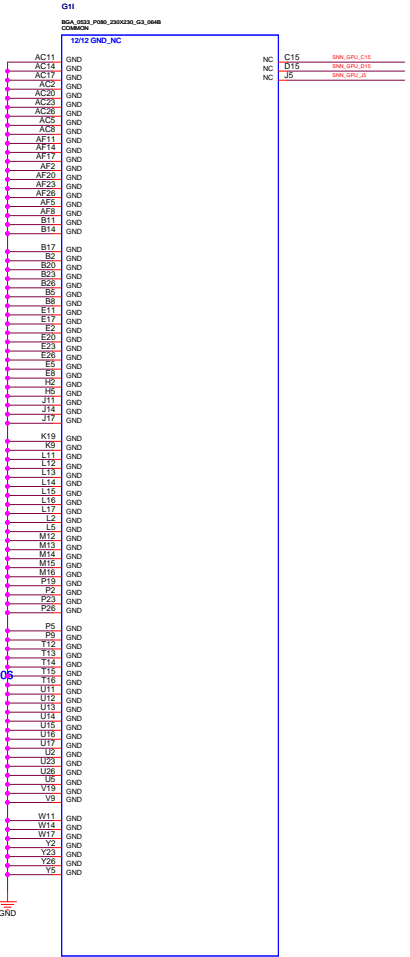




IFPC, IFPE Interface, Mechanical, SPDIF



| Net Name     | MIN_WIDTH | MAX_WIDTH |
|--------------|-----------|-----------|
| HDA_RST      | 128M      |           |
| GND_FAN_PWM  |           |           |
| SPDIF_IN     |           |           |
| SPDIF_OUT    |           |           |
| SPDIF_IN_1   |           |           |
| SPDIF_IN_2   |           |           |
| SPDIF_IN_3   |           |           |
| SPDIF_IN_4   |           |           |
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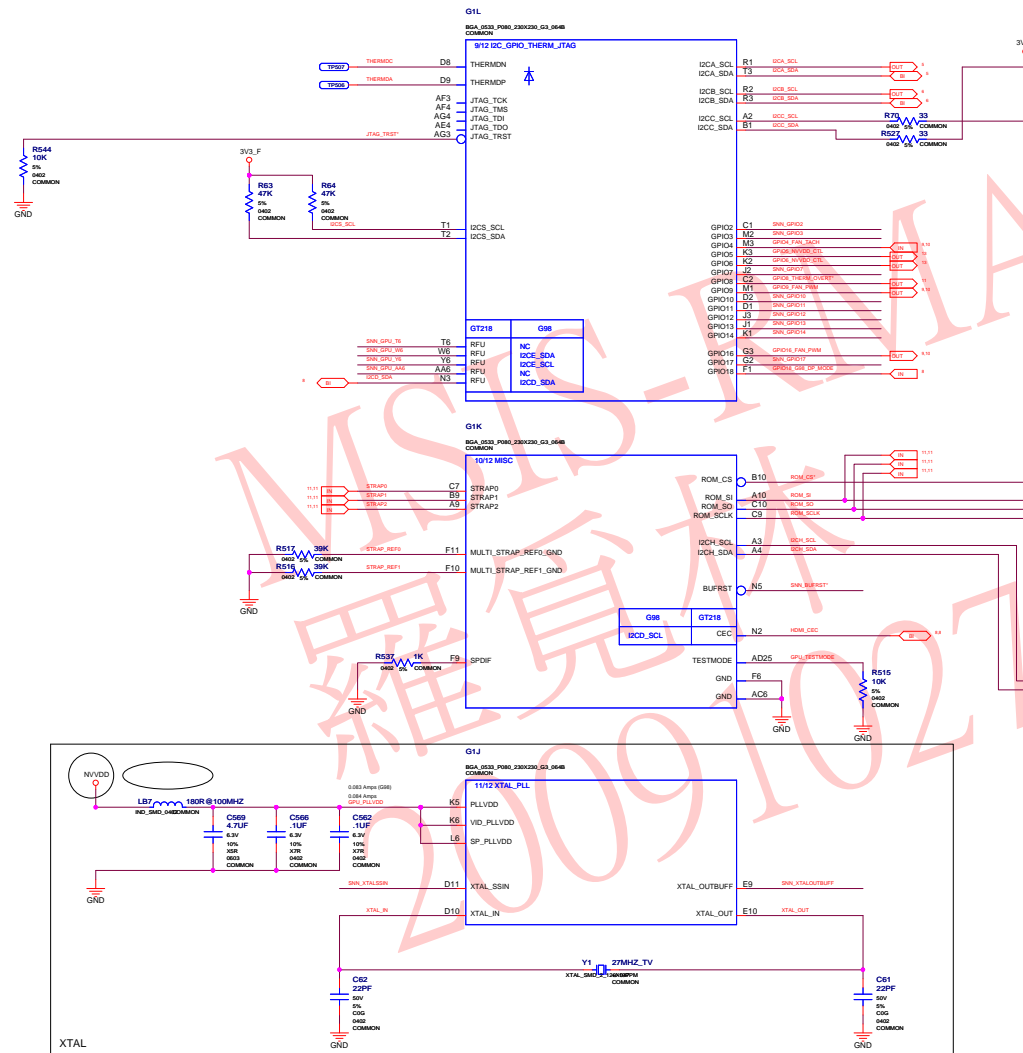
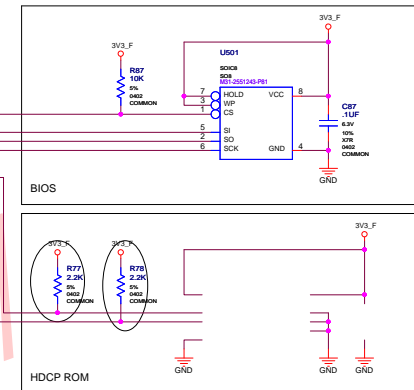


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| PAGE DETAIL | IFPC, IFPE Interface, Mechanical, SPDIF  |


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XTAL, ROM, JTAG

[illegible]

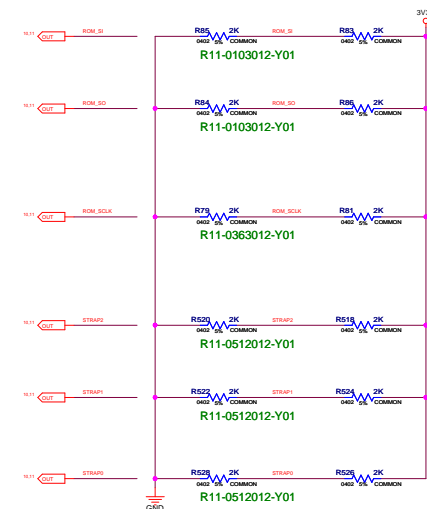
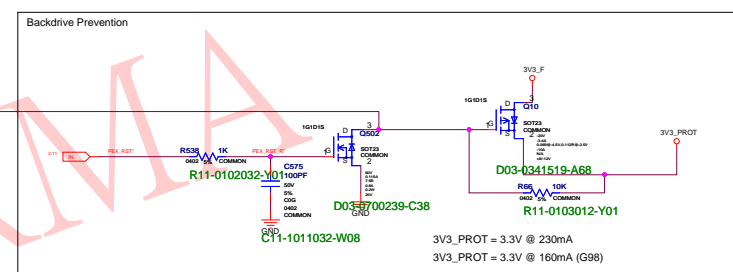
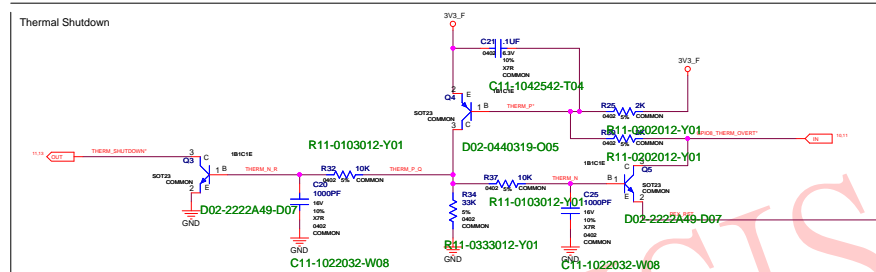
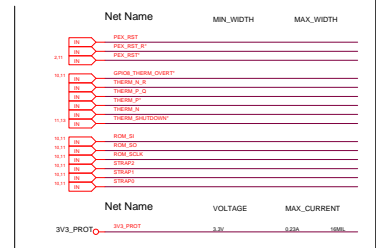
XTAL

|             |   |
|-------------|---|
| ASSEMBLY    | BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| PAGE DETAIL | XTAL, ROM, JTAG   |

|  |                             |   |             |
|--|-----------------------------|---|-------------|
| <b>NVIDIA CORPORATION</b><br>2701 SAN TOMAS EXPRESSWAY<br>SANTA CLARA, CA 95050, USA |                             |  |             |
| <b>NV_PN</b>   | <b>600-10691-BASE-100 A</b> |   |             |
| <b>ID</b>  |                             | <b>PAGE</b>   |             |
| <b>NAME</b>  |                             | <b>DATE</b>   | 05-FEB-2009 |

ALL WIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, SOURCE BOOKS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. WIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

## Thermal Protection, Protected 3V3, Straps



| G718 Straps |                      |      | GLTS Mode            |      |                      |
|-------------|----------------------|------|----------------------|------|----------------------|
| Bit Signal  |                      |      | Values               |      |                      |
| 03          | RAMCFG1              | 0000 | Enable               |      |                      |
|             |                      | 0001 | Demultiplex Memory   |      |                      |
|             |                      | 0010 | Overwrite            |      |                      |
| 01          | RAMCFG2              | 0001 | Hydra                |      |                      |
|             |                      | 0000 | None                 |      |                      |
| 03          | ACCEL_X17            | 0    | 277 (Default)        |      |                      |
| 03          | FBUS                 | 0    | 256M (Default)       |      |                      |
| 01          | SMB_ALT_ADDR         | 0    | Local                |      |                      |
|             |                      | 1    | Remote               |      |                      |
| 03          | VGA_DEVICE           | 0    | Class code 000       |      |                      |
|             |                      | 1    | Class code 300       |      |                      |
| 03          | PCI_DEVICE_EXT       | 0    | GT218-B00-A1         |      |                      |
| 03          | SUB_VENDOR           | 0    | No BIOS              |      |                      |
|             |                      | 1    | BIOS                 |      |                      |
| 01          | SLOT_CFG_CFG0        | 0    | Disable              |      |                      |
|             |                      | 1    | Enable               |      |                      |
| 03          | PEK_LIN_EN_TERMINIO  | 0    | Disable              |      |                      |
|             |                      | 1    | Enable               |      |                      |
| 03          | PCI_DEVICE1          | 0000 | GT218-B00-A1         |      |                      |
| 03          | PCI_DEVICE2          |      |                      |      |                      |
| 01          | PCI_DEVICE3          |      |                      |      |                      |
| 03          | PCI_DEVICE4          |      |                      |      |                      |
| 03          | SGSC_PADCFG_LUT_ACR0 | 0000 | DISPORT_DEFAULT      | 1000 | DISPORT_0THS_HS      |
|             |                      | 0001 | MOBILE_DEFAULT       | 1001 | MOBILE_0THS_HSAMP    |
|             |                      | 0002 | MOBILE_0THS_LAMP     | 1002 | MOBILE_0THS_LAMPAMP  |
|             |                      | 0003 | MOBILE_0THS_LAMP     | 1003 | MOBILE_0THS_LAMP     |
|             |                      | 0010 | MOBILE_0THS_HSAMP    | 1100 | MOBILE_0THS_HSAMP    |
|             |                      | 0011 | MOBILE_0THS_HSAMP    | 1101 | MOBILE_0THS_HSAMP    |
| 03          | SGSC_PADCFG_LUT_ACR2 | 0010 | MOBILE_0THS_HSAMPAMP | 1110 | MOBILE_0THS_HSAMPAMP |
|             |                      | 0011 | MOBILE_0THS_HSAMPAMP | 1111 | MOBILE_0THS_HSAMPAMP |
| 01          | USESG0               | 0000 | Default              |      |                      |
| 01          | USESG2               |      |                      |      |                      |
| 01          | USESG1               |      |                      |      |                      |
| 01          | USESG3               |      |                      |      |                      |

| BT18 Straps             |      | BU Mode             |  |
|-------------------------|------|---------------------|--|
| Bit Signal              |      | Values              |  |
| POL.DEV15_KAT           | 0    | CT519-308-A1        |  |
| KCAL_417                | 0    | 277 (default)       |  |
|                         |      |                     |  |
| XSIO_PACKETS_LUT_ASR[0] | 0000 | CSP/TP_DEFAULT      |  |
|                         | 0001 | MOBLE_ASTRS_DEFAULT |  |
|                         | 0003 | MOBLE_ASTRS_LAMP    |  |
|                         | 0011 | MOBLE_ASTRS_LAMP    |  |
|                         | 0100 | MOBLE_ASTRS_LAMP    |  |
|                         | 0101 | MOBLE_ASTRS_LAMP    |  |
|                         | 0110 | MOBLE_ASTRS_LAMP    |  |
|                         | 0111 | MOBLE_ASTRS_LAMP    |  |
|                         | 1000 | CSP/TP_ASTRS        |  |
|                         | 1001 | MOBLE_ASTRS_LAMP    |  |
|                         | 1010 | MOBLE_ASTRS_LAMP    |  |
|                         | 1011 | MOBLE_ASTRS_LAMP    |  |
| XSIO_PACKETS_LUT_ASR[2] | 1100 | MOBLE_ASTRS_LAMP    |  |
|                         | 1101 | MOBLE_ASTRS_LAMP    |  |
|                         | 1110 | MOBLE_ASTRS_LAMP    |  |
|                         | 1111 | MOBLE_ASTRS_LAMP    |  |
|                         |      |                     |  |
| XSIO_PACKETS_LUT_ASR[3] |      |                     |  |
|                         |      |                     |  |
| XSIO_PACKETS_LUT_ASR[6] |      |                     |  |

| GT218 Straps   |        |                    |
|----------------|--------|--------------------|
| PM Mode        |        |                    |
| Bit Signal     | Values |                    |
| POL_DEVICE_EXT | 0      | GT218-300-A1       |
| ACSC_4107      | 0      | 2703271 QR-4514167 |
| POL_DEVICE2    | 0      | GT218-300-A1       |
| RANKCFG2       | 0000   | Epistle            |
|                | 0001   | Samuelson Mirror   |
|                | 0003   | Comrade            |
|                | 0011   | Phelix             |
|                | 0100   | Nazgale            |
| RANKCFG1       |        |                    |
|                | ML5    | Druff              |
|                | BZ     | No stuff           |
|                | PM     | Druff one          |
| RANKCFG3       |        |                    |
|                | Mode   | REFORM1            |
|                | ML5    | Druff              |
|                | BZ     | No stuff           |
|                | PM     | Druff one          |

| G98 Straps |                         | MLS Mode |                 |
|------------|-------------------------|----------|-----------------|
| Bit Signal |                         | Values   |                 |
| (0)        | MMVC[02]                | 0000     | Empty           |
| (1)        | MMVC[02]                | 0100     | Normal          |
| (2)        | MMVC[02]                | 0101     | Swarming        |
| (3)        | MMVC[02]                | 0110     | Overload        |
| (4)        | MMVC[02]                | 0111     | Ways            |
| (5)        | MMVC[02]                |          |                 |
| (6)        | ACCU_377                | 1        | Enabled         |
| (7)        | TUMODE[02]              | 001      | NTSC_I          |
| (8)        | TUMODE[02]              |          |                 |
| (9)        | TUMODE[02]              |          |                 |
| (10)       | TUMODE[02]              |          |                 |
| (11)       | TUMODE[02]              |          |                 |
| (12)       | PCU_DEVIO_817           | 0        | G98-403-U2      |
| (13)       | PCU_DEVIO_817           | 0        | G98-403-A2      |
| (14)       | PCU_DEVIO_817           | 0        | G98-403-U2      |
| (15)       | SUB_VENDOR              | 1        | BCOS            |
| (16)       | SUB_VENDOR              |          |                 |
| (17)       | SLOT_CXA_CFG            | 0        | Disable         |
| (18)       | SLOT_CXA_CFG            | 1        | Enable          |
| (19)       | PCU_DEVIO_817           | 0        | Disable         |
| (20)       | PCU_DEVIO_817           |          |                 |
| (21)       | PCU_DEVIO[05]           | 0000     | RTU             |
| (22)       | PCU_DEVIO[05]           | 0100     | G98-403-U2      |
| (23)       | PCU_DEVIO[05]           | 0101     | G98-403-A2      |
| (24)       | PCU_DEVIO[05]           | 0102     | G98-403-U2      |
| (25)       | PCU_DEVIO[05]           |          |                 |
| (26)       | PCU_DEVIO[05]           |          |                 |
| (27)       | PCU_DEVIO[05]           |          |                 |
| (28)       | SGIO_PNDGPG_LUT_ADR[02] | 0000     | G987TOP_020A1.1 |
| (29)       | SGIO_PNDGPG_LUT_ADR[02] |          |                 |
| (30)       | SGIO_PNDGPG_LUT_ADR[02] |          |                 |
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| Multilevel |      |
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| Straps     |      |
| 5K to GND  | 0000 |
| 10K to GND | 0001 |
| 15K to GND | 0010 |
| 20K to GND | 0011 |
| 25K to GND | 0100 |
| 30K to GND | 0101 |
| 35K to GND | 0110 |
| 45K to GND | 0111 |
| 5K to VCC  | 1000 |
| 10K to VCC | 1001 |
| 15K to VCC | 1010 |
| 20K to VCC | 1011 |
| 25K to VCC | 1100 |
| 30K to VCC | 1101 |
| 35K to VCC | 1110 |
| 45K to VCC | 1111 |

|      |           |
|------|-----------|
| Mode | REF0/REF1 |
| MLS  | Stuff     |
| BU   | No stuff  |

**NVIDIA CORPORATION**  
2701 SAN TOMAS EXPRESSWAY  
SANTA CLARA, CA 95050, USA



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| NV_PN | 600-10691-BASE-100 A |
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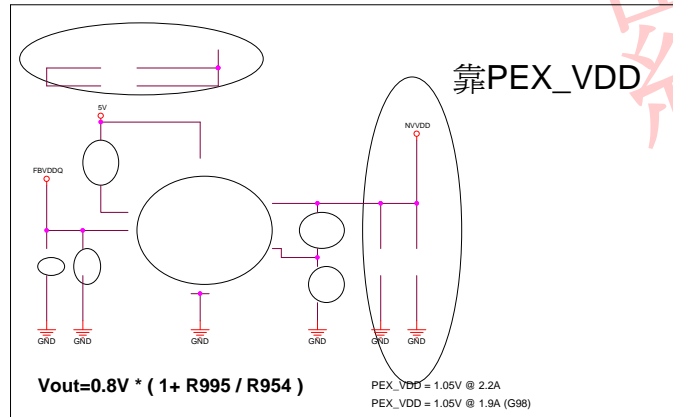
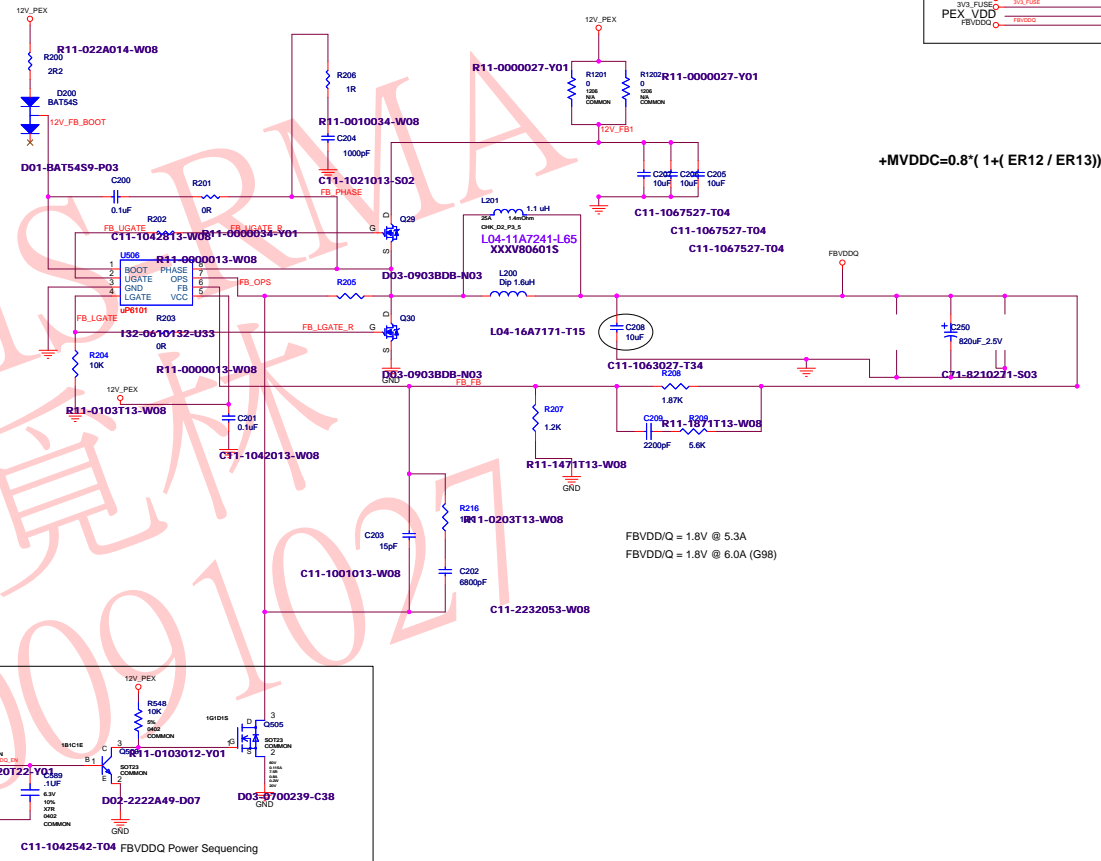
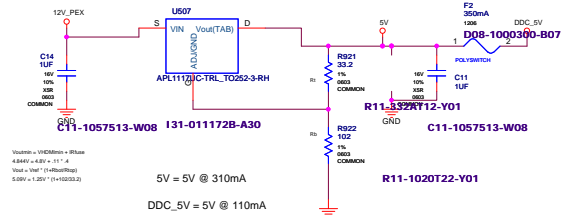
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|             |   |
|-------------|---|
| ASSEMBLY    | BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FIN |
| PAGE DETAIL | Thermal Protection, Protected 3V3, Straps   |

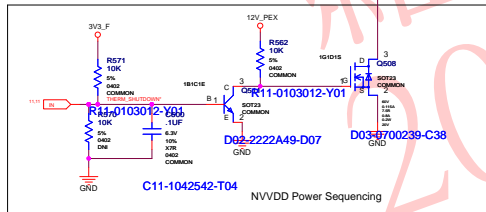
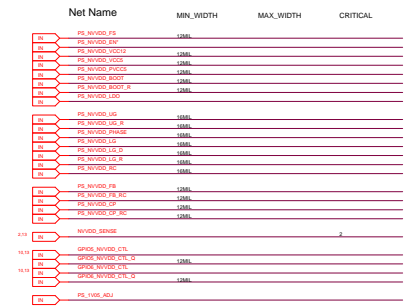
Power Supply I: FBVDD/Q, PEX\_VDD, 5V, 3V3\_F



| Net Name |  | MIN_WIDTH | MAX_WIDTH |
|----------|--|-----------|-----------|
| 15       | PS_V0_A00                                    |           |           |
| 16       | PS_V0_V00                                    | 128bit    |           |
| 17       | PEVEX_V0                                     | 128bit    |           |
| 18       | PS_V0_V01                                    | 128bit    |           |
| 19       | PS_V0V00_0V                                  |           |           |
| 20       | PS_V0V00_0V                                  | 128bit    |           |
| 21       | PS_V0V00_0V                                  | 128bit    |           |
| 22       | PS_V0V00_0V00                                | 128bit    |           |
| 23       | PS_V0V00_0V000                               | 128bit    |           |
| 24       | PS_V0V00_0V0000                              | 128bit    |           |
| 25       | PS_V0V00_0V00000                             | 128bit    |           |
| 26       | PS_V0V00_0V000000                            | 128bit    |           |
| 27       | PS_V0V00_0V0000000                           | 128bit    |           |
| 28       | PS_V0V00_0V00000000                          | 128bit    |           |
| 29       | PS_V0V00_0V000000000                         | 128bit    |           |
| 30       | PS_V0V00_0V0000000000                        | 128bit    |           |
| 31       | PS_V0V00_0V00000000000                       | 128bit    |           |
| 32       | PS_V0V00_0V000000000000                      | 128bit    |           |
| 33       | PS_V0V00_0V0000000000000                     | 128bit    |           |
| 34       | PS_V0V00_0V00000000000000                    | 128bit    |           |
| 35       | PS_V0V00_0V000000000000000                   | 128bit    |           |
| 36       | PS_V0V00_0V0000000000000000                  | 128bit    |           |
| 37       | PS_V0V00_0V00000000000000000                 | 128bit    |           |
| 38       | PS_V0V00_0V000000000000000000                | 128bit    |           |
| 39       | PS_V0V00_0V0000000000000000000               | 128bit    |           |
| 40       | PS_V0V00_0V00000000000000000000              | 128bit    |           |
| 41       | PS_V0V00_0V000000000000000000000             | 128bit    |           |
| 42       | PS_V0V00_0V0000000000000000000000            | 128bit    |           |
| 43       | PS_V0V00_0V00000000000000000000000           | 128bit    |           |
| 44       | PS_V0V00_0V000000000000000000000000          | 128bit    |           |
| 45       | PS_V0V00_0V0000000000000000000000000         | 128bit    |           |
| 46       | PS_V0V00_0V00000000000000000000000000        | 128bit    |           |
| 47       | PS_V0V00_0V000000000000000000000000000       | 128bit    |           |
| 48       | PS_V0V00_0V0000000000000000000000000000      | 128bit    |           |
| 49       | PS_V0V00_0V00000000000000000000000000000     | 128bit    |           |
| 50       | PS_V0V00_0V000000000000000000000000000000    | 128bit    |           |
| 51       | PS_V0V00_0V0000000000000000000000000000000   | 128bit    |           |
| 52       | PS_V0V00_0V00000000000000000000000000000000  | 128bit    |           |
| 53       | PS_V0V00_0V000000000000000000000000000000000 | 1         |           |

| Net Name |         | VOLTAGE | MAX_CURRENT  |
|----------|---------|---------|--------------|
| 5V       | D0C_V0  | 5V      | 2.00A 128bit |
| D0C_V0   | D0C_V0  | 5V      | 2.00A 128bit |
| V0_V0    | V0_V0   | 3.3V    | 2.00A 128bit |
| V0_V0    | V0_V0   | 3.3V    | 2.00A 128bit |
| PEX_VDD  | PEX_VDD | 1.00V   | 2.00A 128bit |
| PEVDD0   | PEVDD0  | 1.00V   | 2.00A 128bit |

## 6



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| NV_PN | 600-10691-BASE-100 A |      |             |
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| NAME  |                      | DATE | 05-FEB-2009 |

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| A  |  | B                             |  | C                                 |  | D                                 |  | E                                 |  | F                                 |  | G                                 |  | H                                 |  |
|--|--|-------------------------------|--|-----------------------------------|--|-----------------------------------|--|-----------------------------------|--|-----------------------------------|--|-----------------------------------|--|-----------------------------------|--|
| Title: Baseline Report                   |  | 4.2E 4.2D                     |  | FBA_D08_WF4< 3.10x 3.2E 3.4B 4.4B |  | FBA_D08_WF4< 3.10x 3.2E 3.4B 4.4B |  | FBA_D08_WF4< 3.10x 3.2E 3.4B 4.4B |  | FBA_D08_WF4< 3.10x 3.2E 3.4B 4.4B |  | FBA_D08_WF4< 3.10x 3.2E 3.4B 4.4B |  | FBA_D08_WF4< 3.10x 3.2E 3.4B 4.4B |  |
| Design: j9901_A01                        |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| Date: Jan 27 23:02:52 2009               |  | 4.2E 4.2D                     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| Base note and synonyms for               |  | 4.2E 4.2D                     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| j9901_A01_B1.PW01_AD01(B9901_A01_B1.PW01 |  | 4.2E 4.2D                     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| _AD01_B1.PW01)                           |  | 4.2E 4.2D                     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| Base Signal Location(Zone900)            |  | 4.2E 4.2D                     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| 3V3_F 12.2H                              |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| 3V3_FUSE 12.2H                           |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| 3V3_PEX 12.2D                            |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| SV 12.2H                                 |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_BLUE 5.10x 5.4C                     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_BLUE_C 5.10x 5.4F+ 7.3F+            |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_GREEN 5.10x 5.4C                    |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_GREEN_C 5.10x 5.4F+ 7.3F+           |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_HPMC 5.10x 5.4C                     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_HPMC_C 5.10x 5.3F+ 7.4F+            |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_HS_BUF 5.10x 5.3D                   |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_RED 5.10x 5.4C                      |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_RED_C 5.10x 5.4F+ 7.3F+             |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_RST 5.10x 5.4B                      |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_VREF 5.10x 5.4B                     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_VPMC 5.10x 5.4C                     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_VPMC_C 5.10x 5.3F+ 7.3F+            |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_VS_BUF 5.10x 5.3D                   |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_BLUE_C 5.10x 5.4C                   |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_BLUE_C 5.10x 5.4C                   |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_GREEN 5.10x 5.4C                    |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_GREEN_C 5.10x 5.4C                  |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_HPMC 5.10x 5.4C                     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_HPMC_C 5.10x 5.3F+ 7.4F+            |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_HS_BUF 5.10x 5.3D                   |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_RED 5.10x 5.4C                      |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_RED_C 5.10x 5.4F+ 7.3F+             |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_RST 5.10x 5.4B                      |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_VREF 5.10x 5.4B                     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_VPMC 5.10x 5.4C                     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_VPMC_C 5.10x 5.3F+ 7.3F+            |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_VS_BUF 5.10x 5.3D                   |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_BLUE_C 5.10x 5.4C                   |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_BLUE_C 5.10x 5.4C                   |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_GREEN 5.10x 5.4C                    |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_GREEN_C 5.10x 5.4C                  |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_HPMC 5.10x 5.4C                     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_HPMC_C 5.10x 5.3F+ 7.4F+            |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |
| DACA_HS_BUF 5.10x 5.3D                   |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  | FBA_CMD4< 3.3C 3.4E 4.2A 4.2C     |  |                                   |  |                                   |  |                                   |  |                                   |  |

