

P2002

2GB GDDR5, 256b, 64Mx32

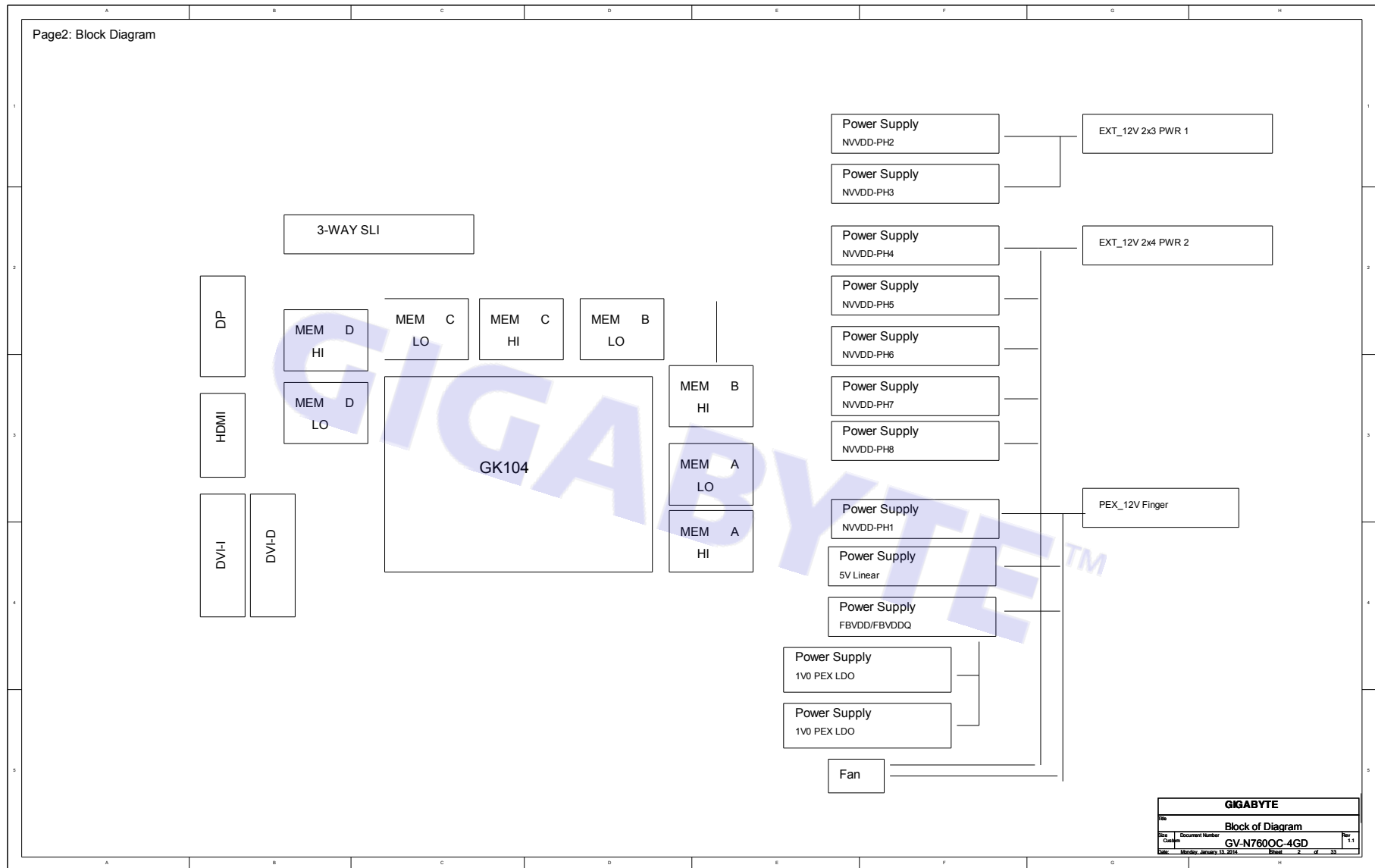
Stacked DVI-I/DVI-D + HDMI + DP

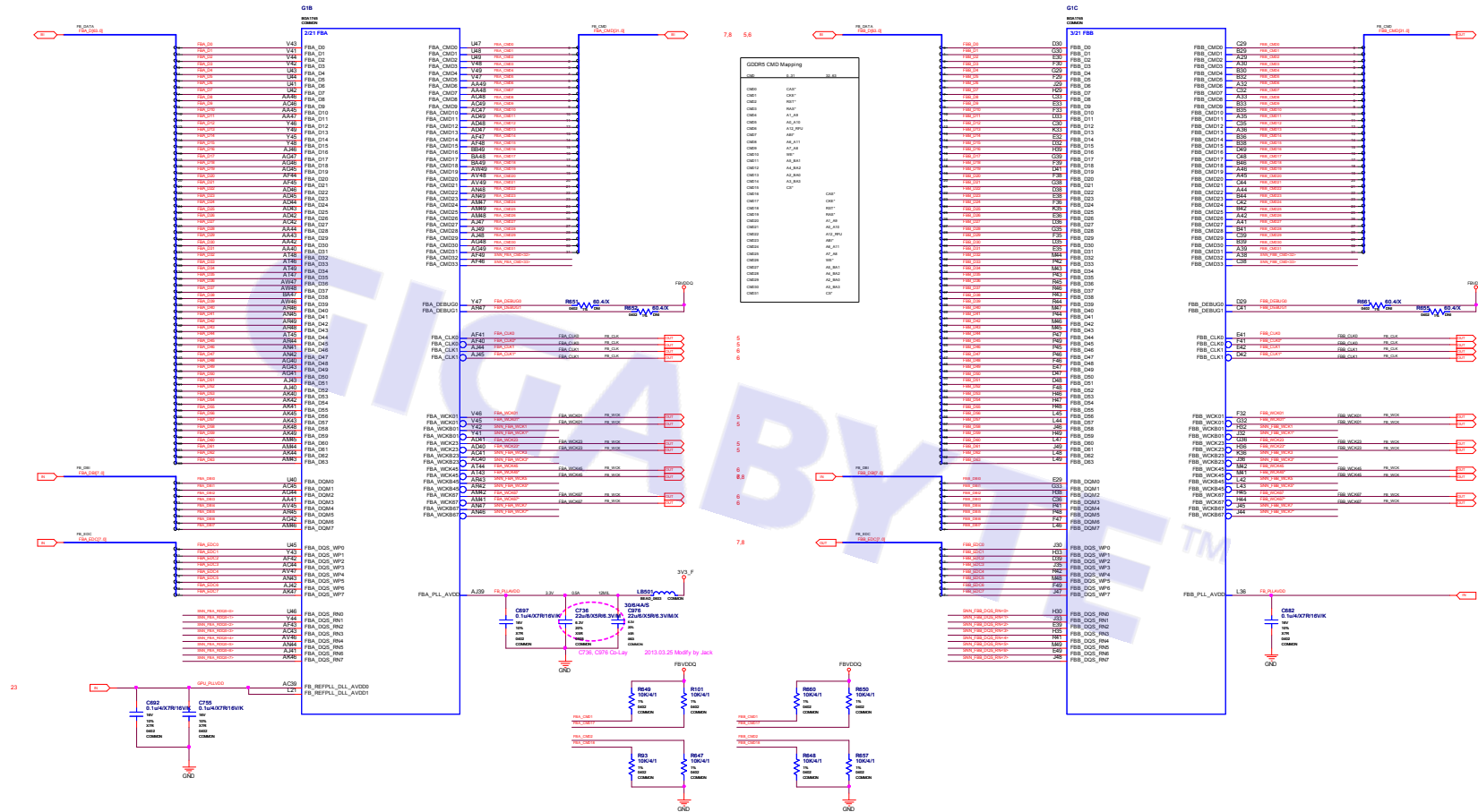
TABLE OF CONTENTS

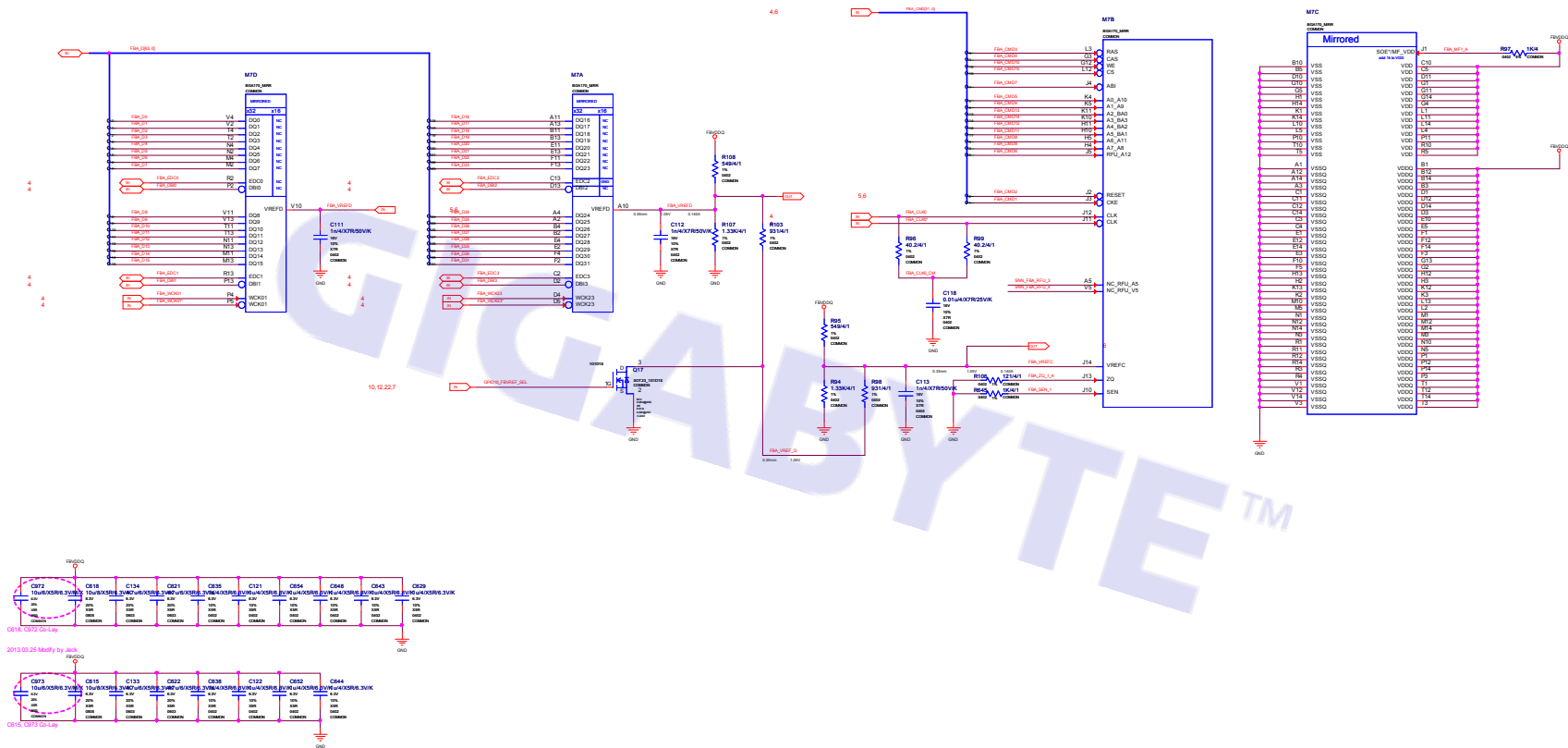
Page	Description
1	Table of Contents
2	Block Diagram
3	PCI Express
4	MEMORY: GPU Partition A/B
5	MEMORY: FBA[31:0]
6	MEMORY: FBA[63:32]
7	MEMORY: FBB[31:0]
8	MEMORY: FBB[63:32]
9	MEMORY: GPU Partition C/D
10	MEMORY: FBC[31:0]
11	MEMORY: FBC[63:32]
12	MEMORY: FBD[31:0]
13	MEMORY: FBD[63:32]
14	GPU PWR and GND
15	GPU Decoupling
16	DACA Interface
17	IFPAB DVI-I-DL
18	IFPEF DVI-D-DL
19	IFPC HDMI
20	IFPD DP
21	MIOA/B Interface
22	MISC1: Fan, Thermal, JTAG, GPIO
23	MISC2: ROM, XTAL, Straps
24	PS: 5V, PEX_VDD
25	PS: FBVDD/Q

Page	Description
26	PS: NVVDD Controller
27	PS: NVVDD Phase 1,3
28	PS: NVVDD Phase 2,5
29	PS: NVVDD Phase 4
30	PS: Inputs, Filtering, and Monitoring
31	PS: Shutdown
32	PS: 12V Current Steering PSI Control and LED
33	MECH: Bracket/Thermal

GIGABYTE			
Table of Contents			
Doc	Document Number	Rev	
GV-N760OC-4GD		1.1	
Date	Monday, January 14, 2014	Sheet	1 of 33



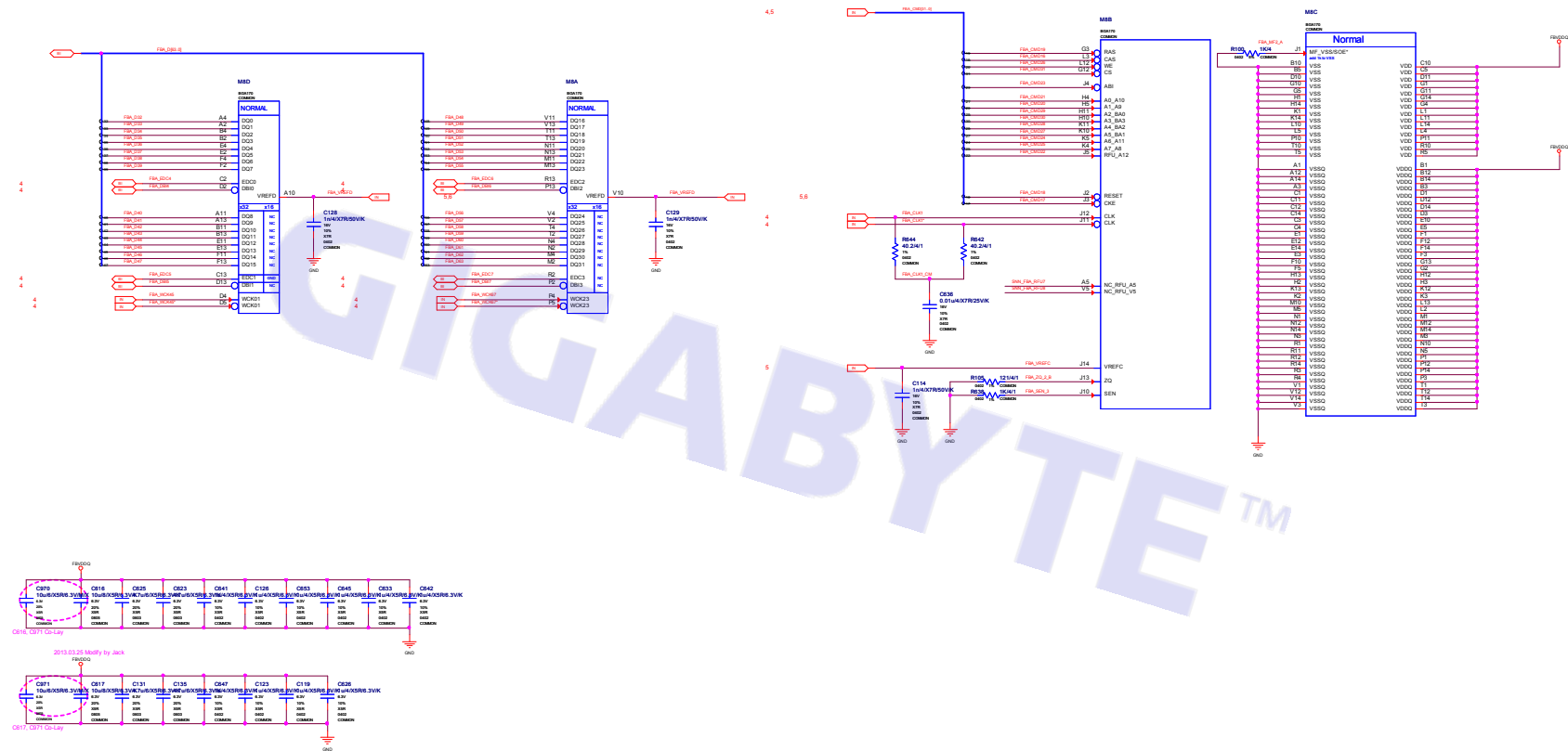




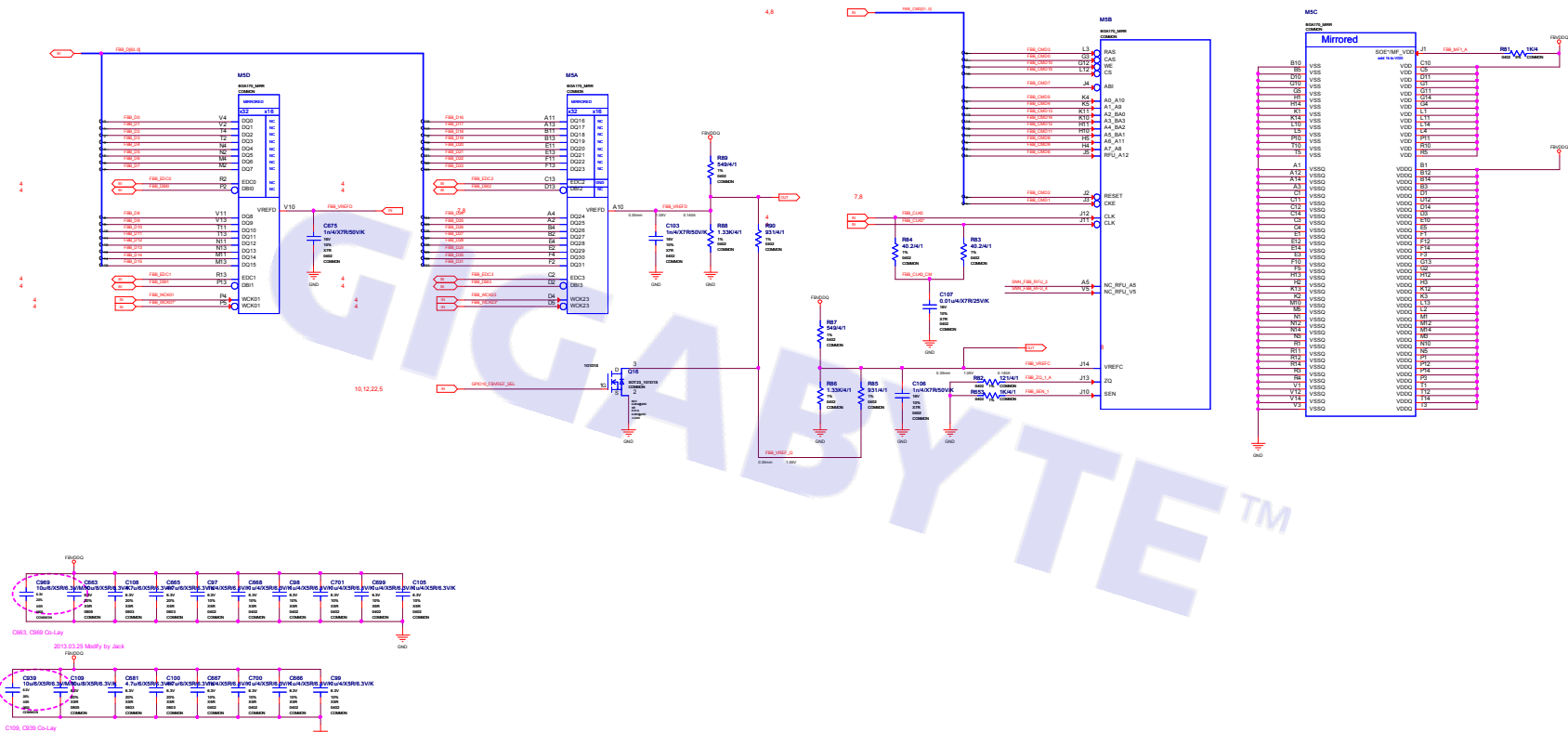
<p>ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS: THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.</p>	<p>ASSEMBLY PAGE DETAIL</p>
--	---------------------------------

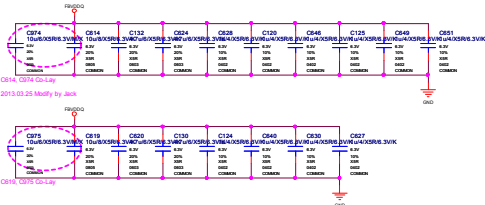
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	MEMORY: FBA[31:0]

GIGABYTE			
File			
Memory FBA{0..31}			
Size	Document Number		Rev
Custom	GV-N760OC-4GD		1.1
Date:	Monday, January 13, 2014	Sheet	of 33



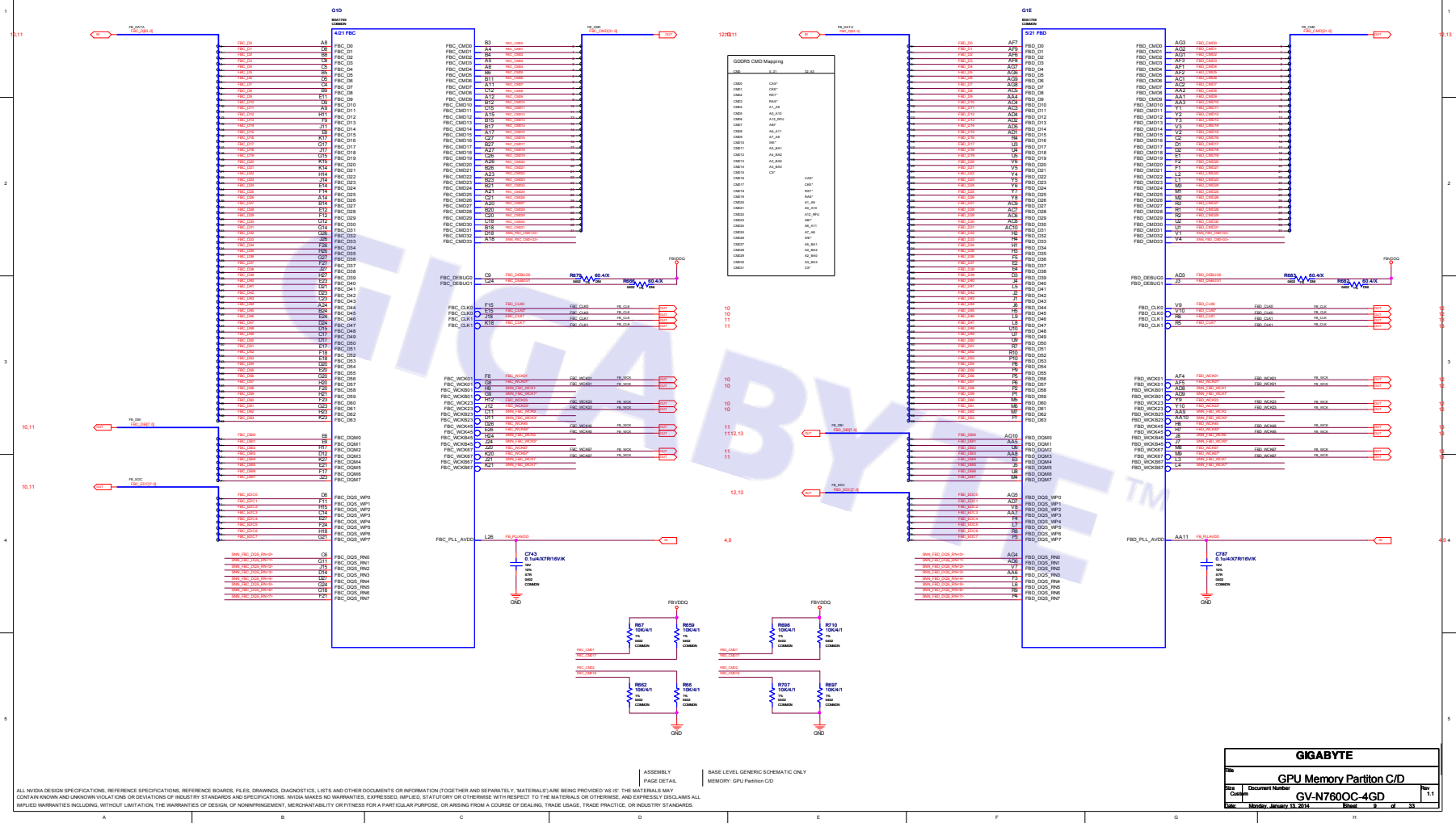
GIGABYTE			
Title			
Memory FBA{32..63}			
Size	Document Number	Rev	
Custom	GV-N760OC-4GD	1.1	
Date	Monday, January 13, 2014	Sheet	6 of 13

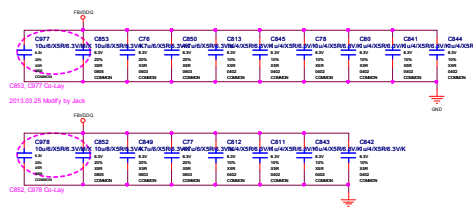




ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

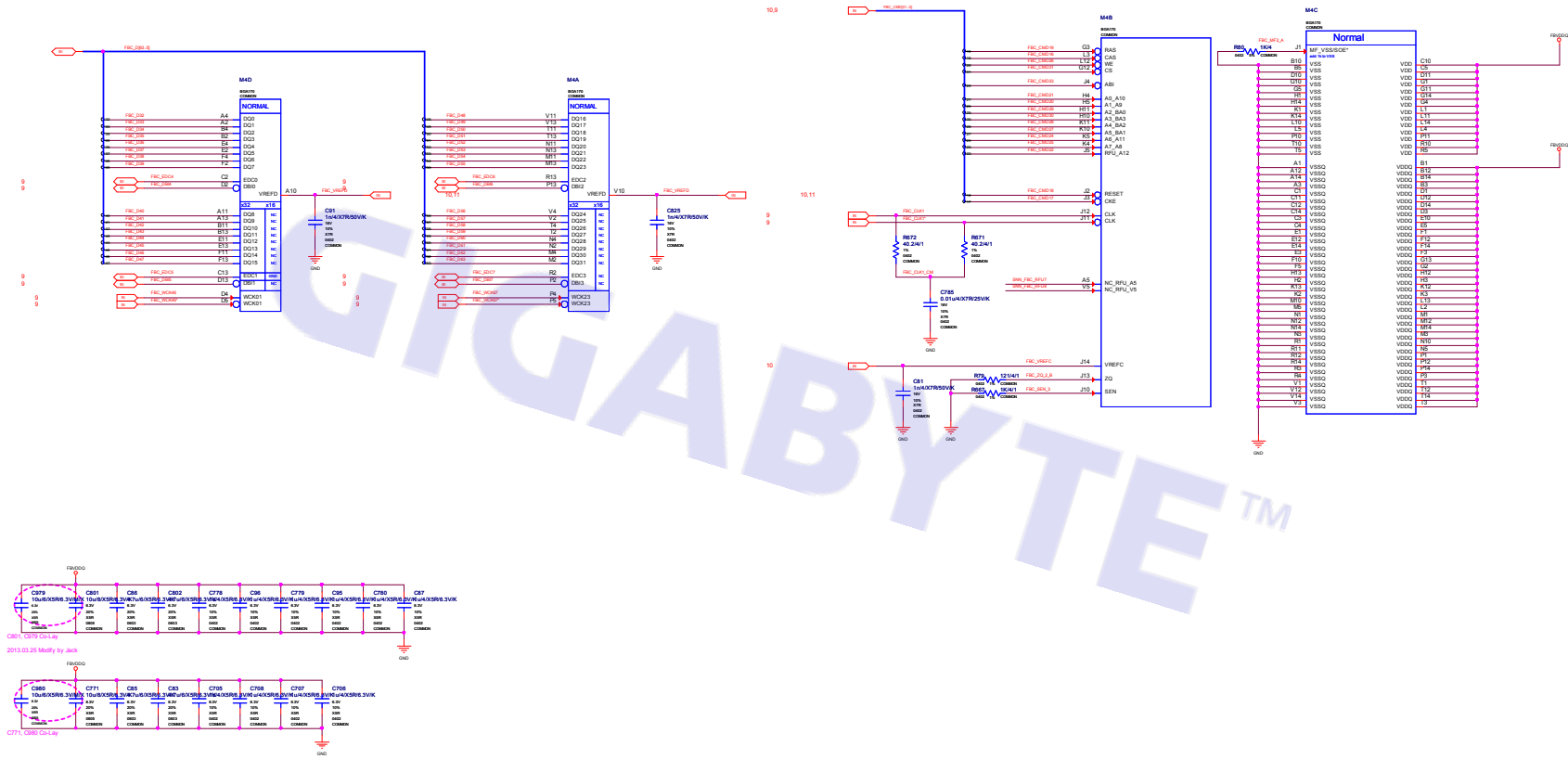
GIGABYTE			
File			
Memory FBB{32..63}			
Size	Document Number		Rev
Custom	GV-N760OC-4GD		1.1
Date:	Monday, January 13, 2014	Sheet	8 of 33





ALL WINDA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, STANDARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS: THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. WINDA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

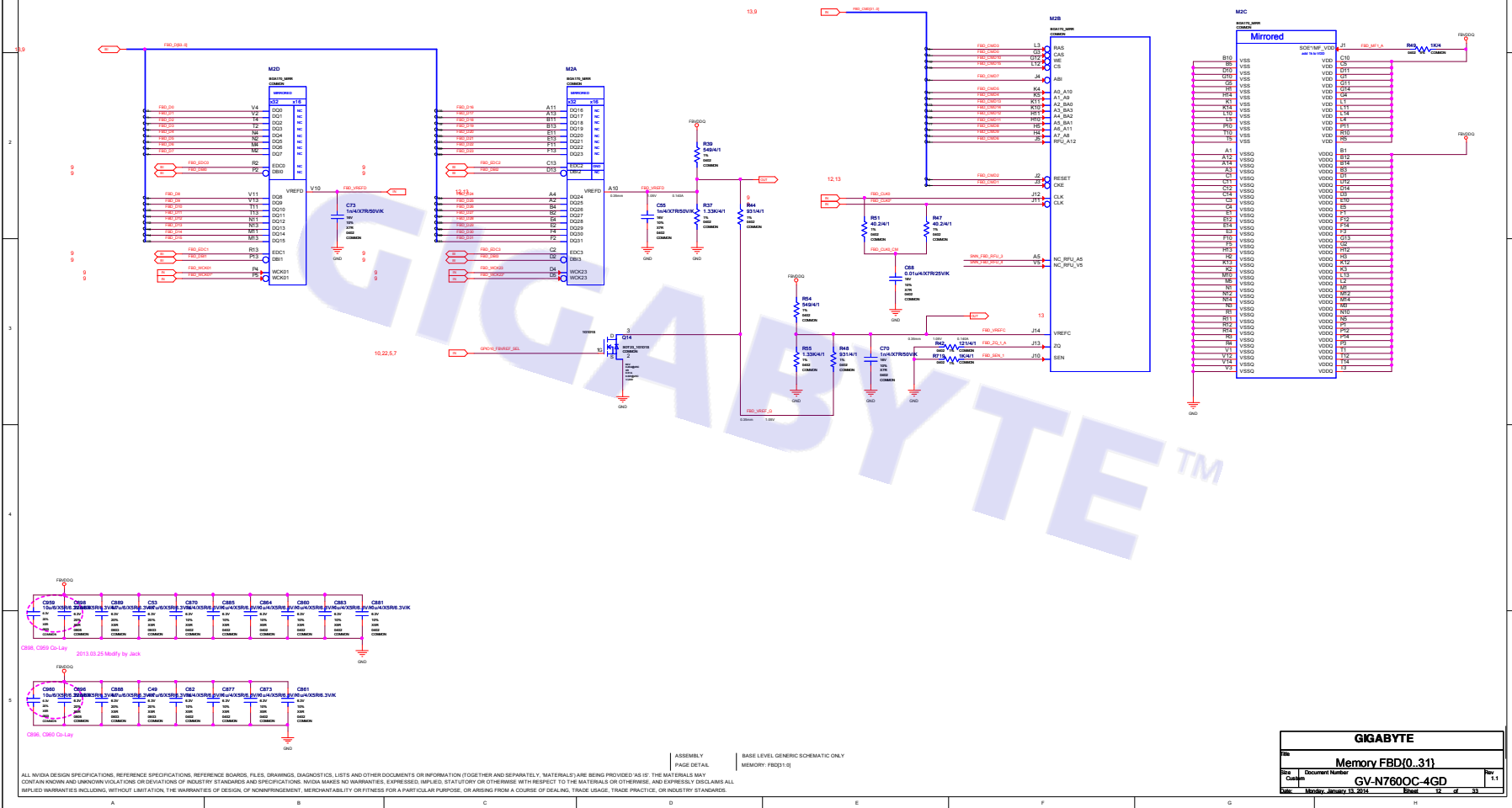
GIGABYTE			
File			
Memory FBC{0..31}			
Size	Document Number	Rev	
Custom	GV-N760OC-4GD	1.1	
Date:	Monday, January 13, 2014	Sheet	10 of 33

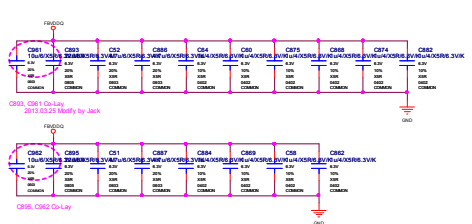
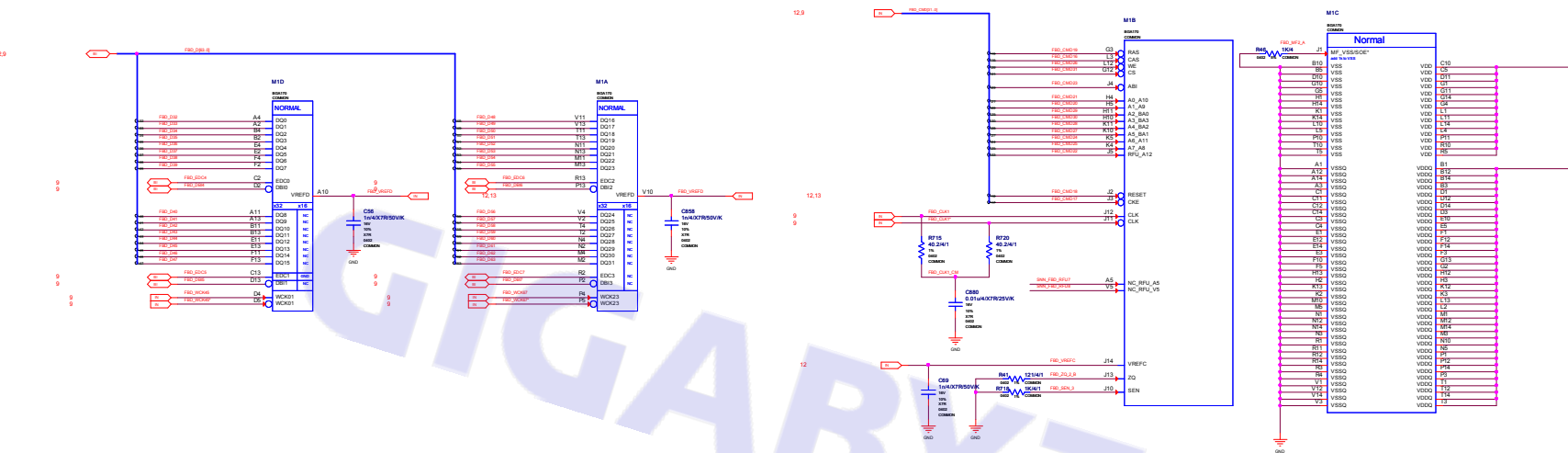


ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTIC LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE, WITH RESPECT TO THE MATERIALS AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARD.

ASSEMBLY
PAGE DETAIL
BASE LEVEL GENERIC SCHEMATIC ONLY
MEMORY: FBC(32..63)

GIGABYTE			
Doc	Memory FBC(32..63)		
Rev	Document Number	Rev	1.1
Ctrl	GV-N760OC-4GD	Sheet	11 of 33
Date: Monday, January 13, 2014			

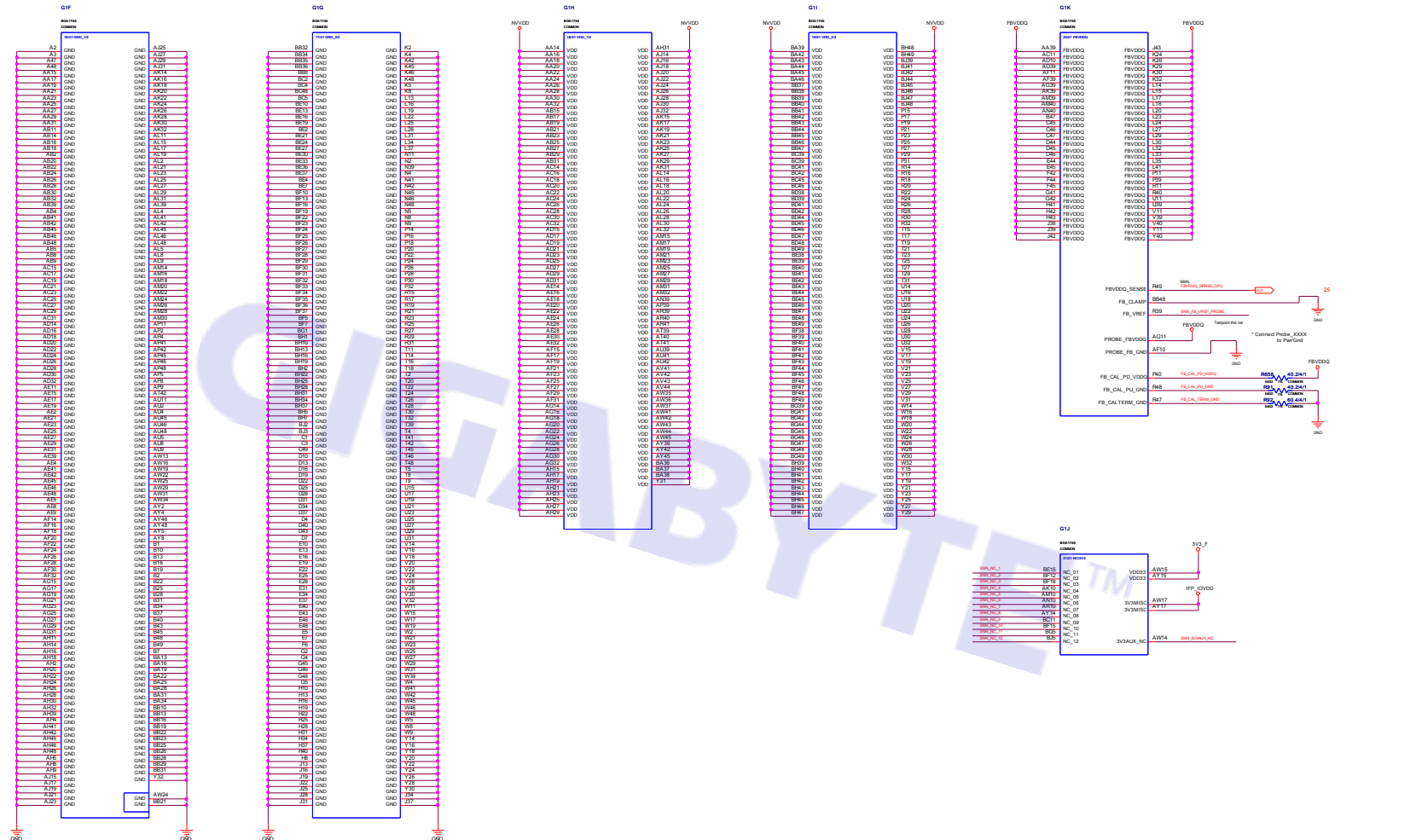




ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, OR EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	MEMORY: FBD[63.32]

GIGABYTE			
File			
Memory FBD{32..63}			
Size	Document Number	Rev	
Custom	GV-N760OC-4GD	1.1	
Date:	Monday, January 13, 2014	Sheet	13 of 33



Based on GB2-X GDDR5 FBVDDQ Decap Guideline

FBVDDQ

0.1uF, 0.47uF & 1uF, 0402 (Place Under GPU)

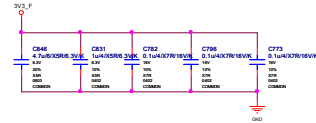
4.7uF, 0603 (Place Near GPU)

10uF, 0805 (Place Near GPU)

22uF, 0805 (Place Near GPU)

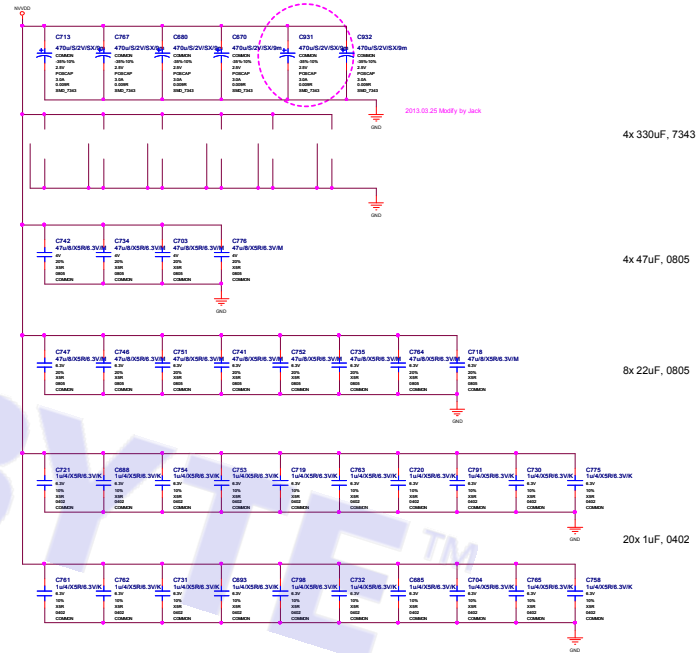


VDD33



NVVD Decoupling caps. Place under GPU.

NVDD

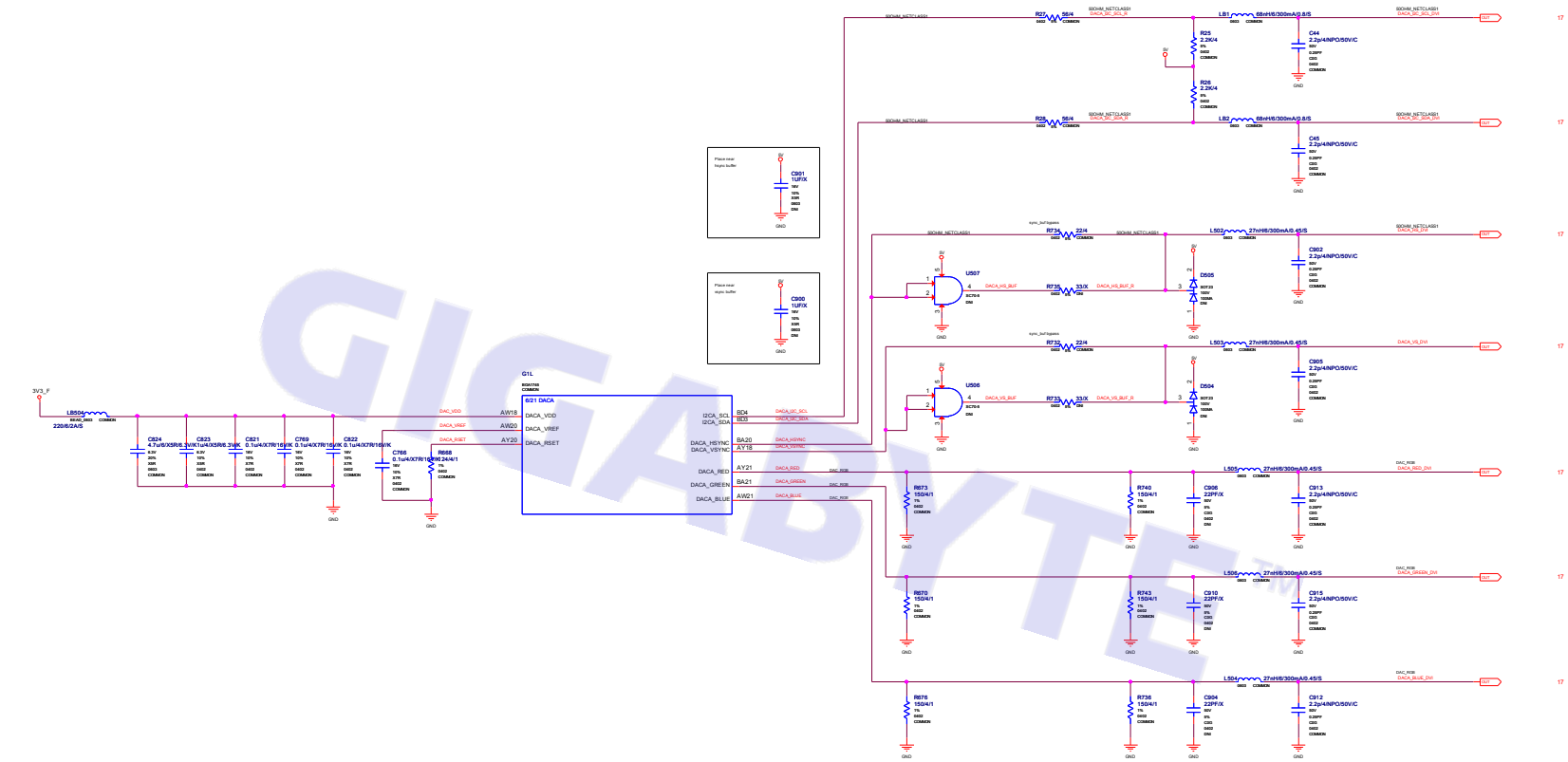


ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARD.

ASSEMBLY
PAGE DETAIL

BASE LEVEL GENERIC SCHEMATIC ONLY
GPU Decoupling

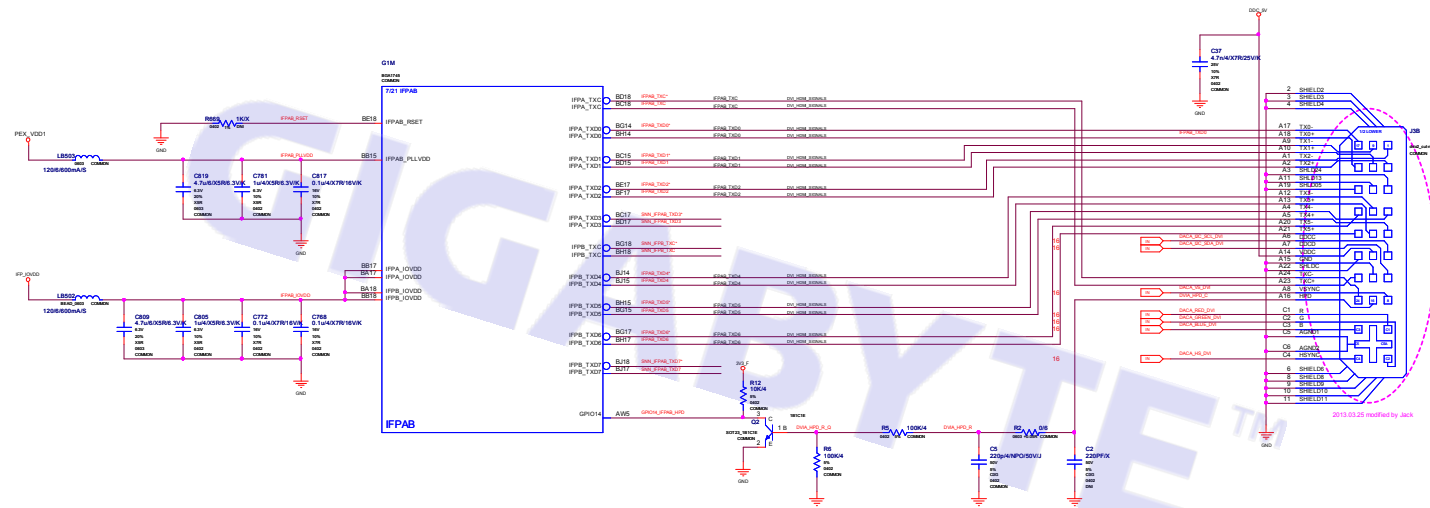
GIGABYTE			
GPU Decoupling			
Doc	Document Number	Rev	1.1
GV-N760OC-4GD			
Date: Monday, January 13, 2014 Sheet 16 of 33			



ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS, AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE, WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY
PAGE DETAIL
DACA Interface

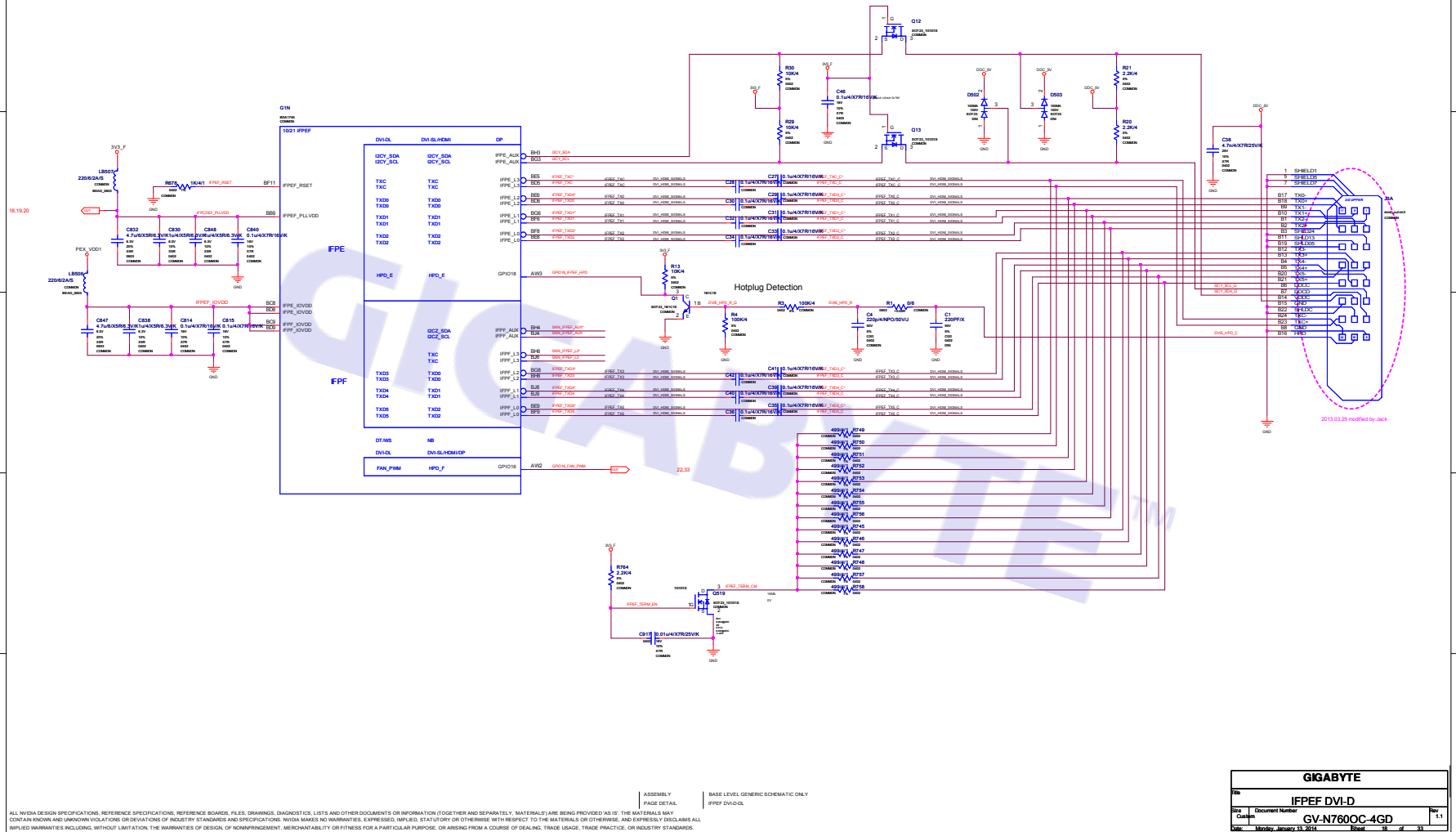
GIGABYTE			
DACA Interface			
Doc	Document Number	Rev	1.1
GV-N760OC-4GD			
Date	Monday, January 14, 2013	Sheet	16 of 33

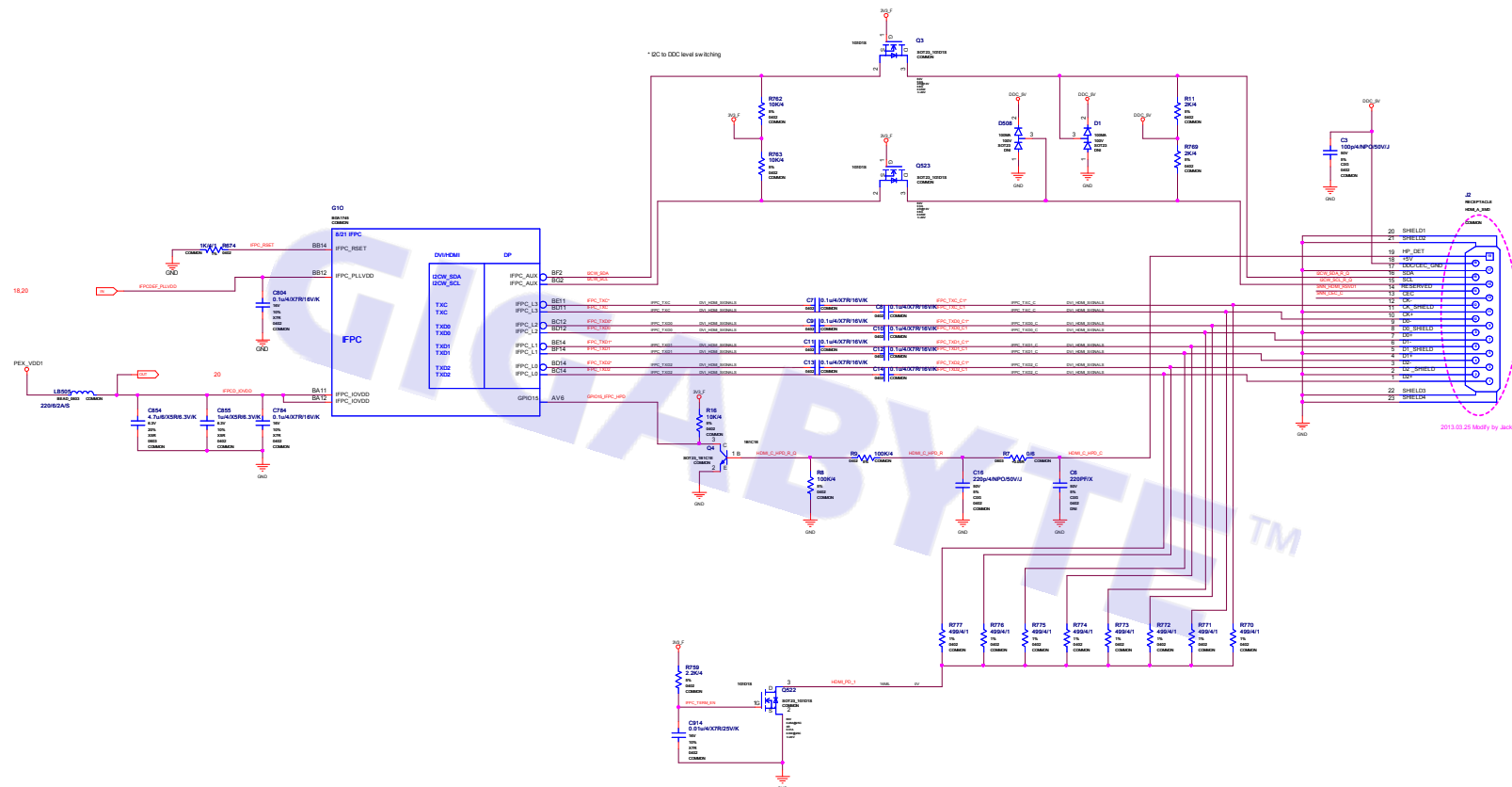


ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTIC LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, OR INDUSTRY STANDARD.

ASSEMBLY
PAGE DETAIL
BASE LEVEL GENERIC SCHEMATIC ONLY
IFPAB DVI-I-DL

GIGABYTE			
IFPAB DVI-DL			
Doc	Document Number	Rev	1.1
GV-N760OC-4GD			
Date: Monday, January 14, 2013 Sheet 17 of 33			





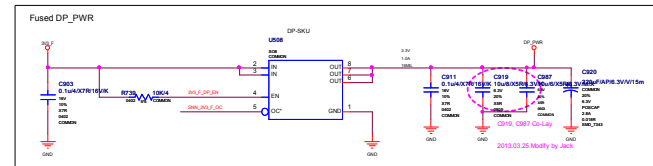
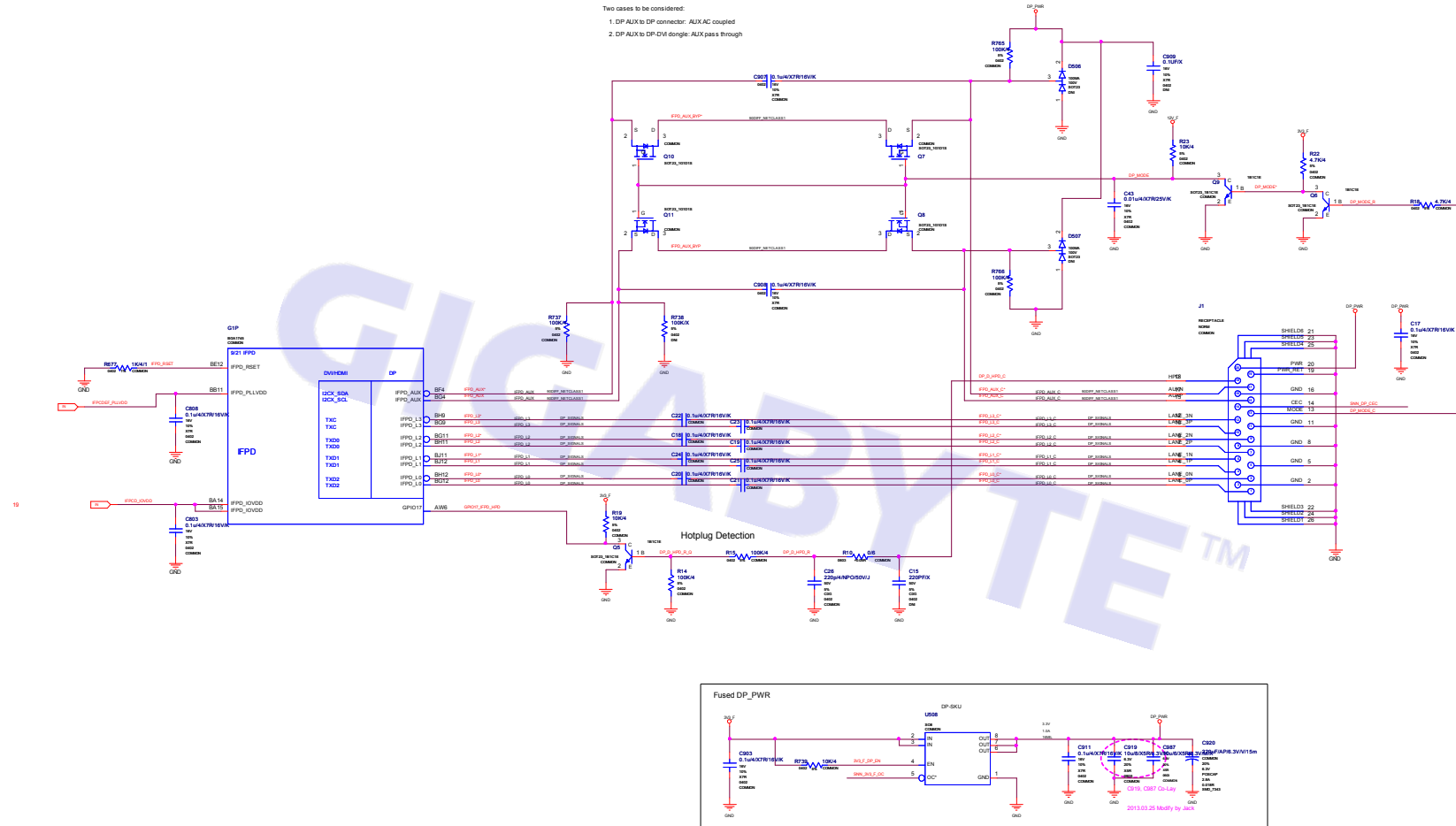
GIGABYTE

IFPC HDMI

GV-N760OC-4GD

May 13, 2014	Brest	19
--------------	-------	----

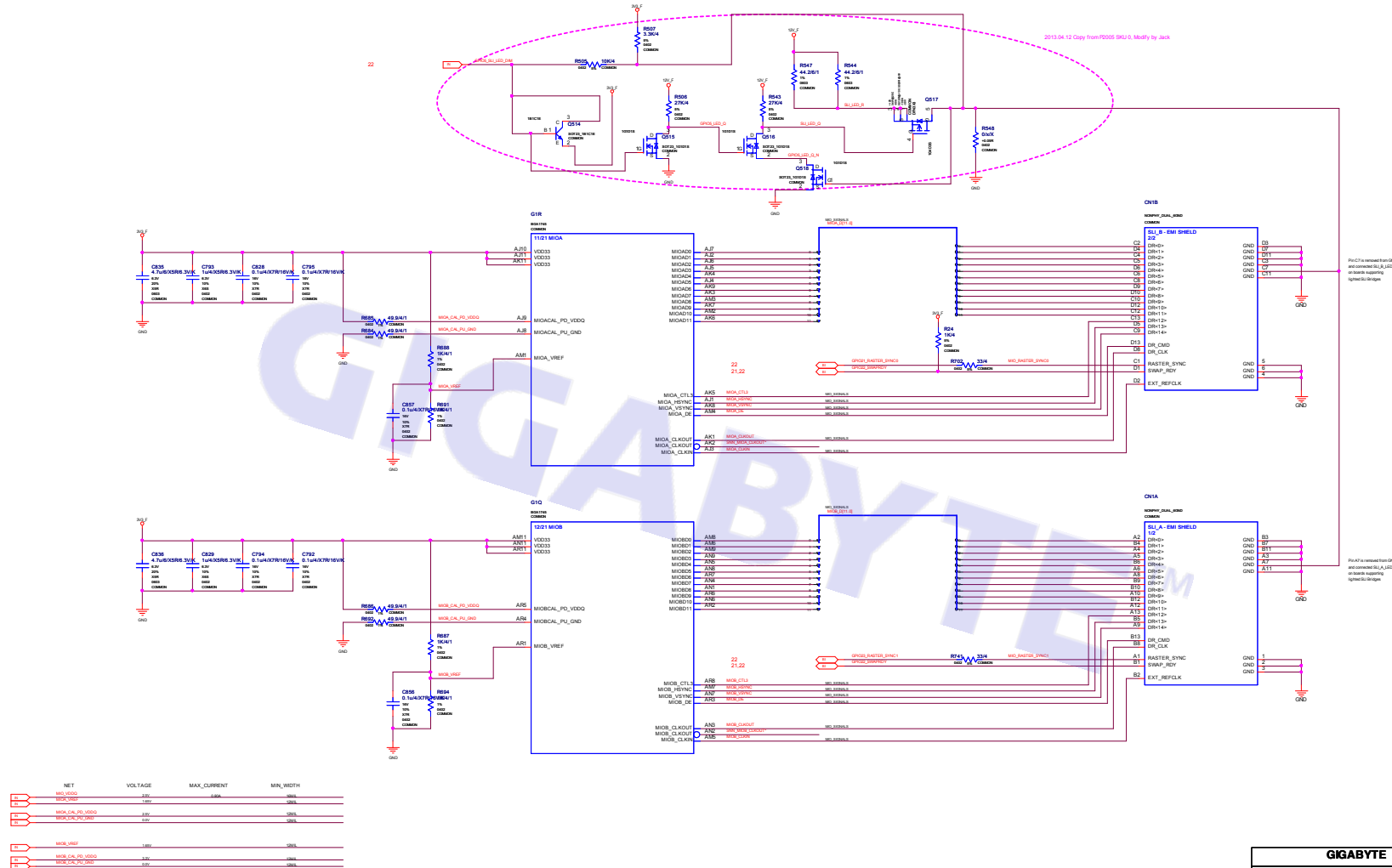
Two cases to be considered:
 1. DP AUX to DP connector: AUX AC coupled
 2. DP AUX to DP-DW dongle: AUX pass through



ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, INQUIRY STANDARDS AND SPECIFICATIONS, NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS BEING PROVIDED "AS IS". THE MATERIALS BEING PROVIDED "AS IS" ARE NOT TO BE USED IN ANY MANNER THAT MAY IMPLY WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARD.

ASSEMBLY PAGE DETAIL
 BASE LEVEL GENERIC SCHEMATIC ONLY
 IFPD DP

GIGABYTE			
IFPD DP			
Doc	Document Number	GV-N760OC-4GD	
Rev	1.1		
Date	Monday, January 14, 2014	Sheet	20 of 33

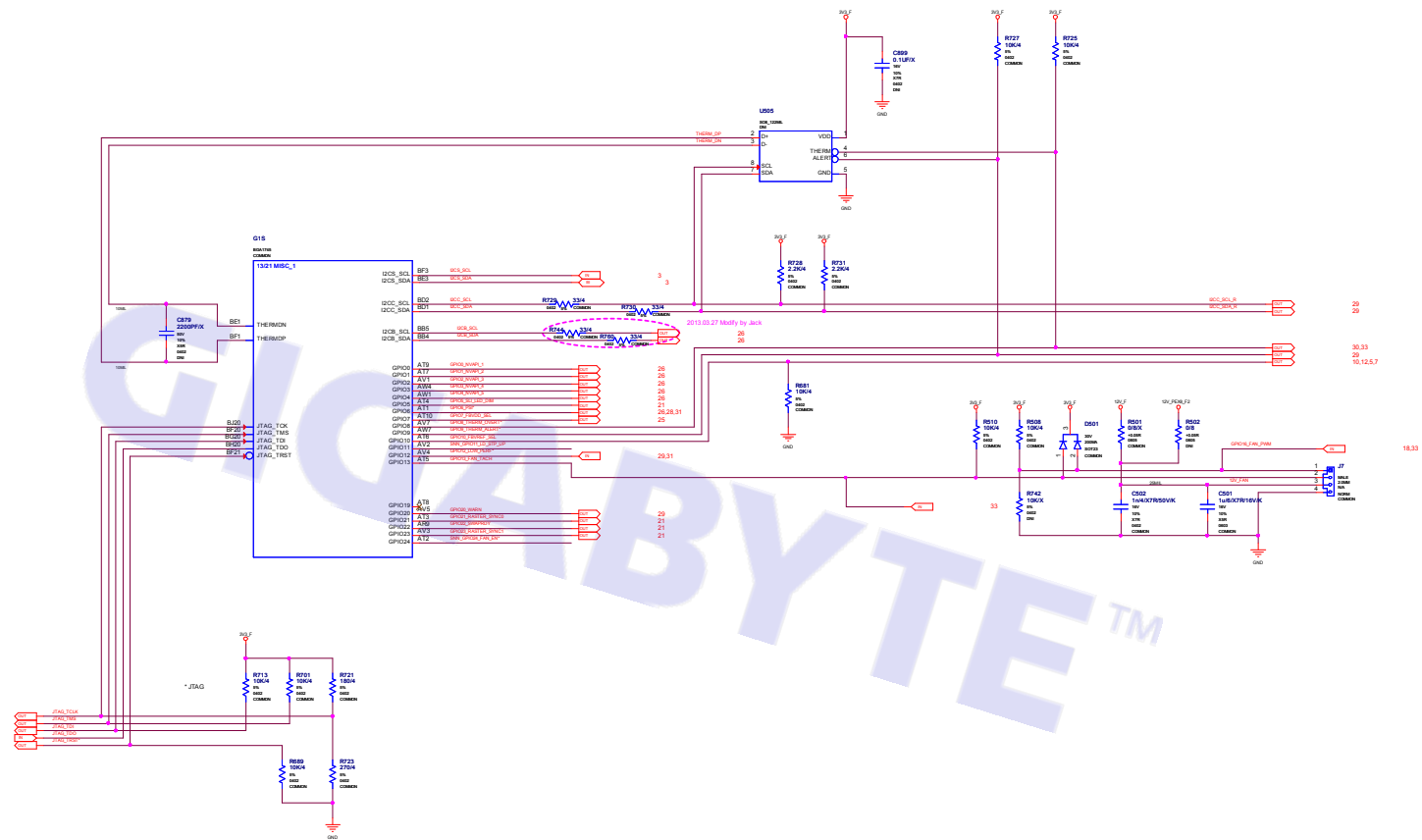


ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS". THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARD.

ASSEMBLY
PAGE DETAIL
MIOA/B Interface

BASE LEVEL GENERIC SCHEMATIC ONLY
MIOA/B Interface

GIGABYTE			
GV-N760OC-4GD			
Doc	Doc Number	Rev	1.1
Rev	Doc Number	Rev	1.1
Rev	Doc Number	Rev	1.1



<p>ALL INVIDA DESIGN SPECIFICATIONS, REFERENCE BOARDS, FILLS, DRAWINGS, CALCULATIONS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS" TO THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. INVIDA MAKES NO WARRANTIES, EXPRESS, IMPLIED, STATUTORY OR OTHERWISE, WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL SUCH WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE PRACTICE, OR INDUSTRY STANDARDS.</p>	<p>ASSEMBLY PAGE DETAIL</p>	<p>BASE FILE: GENERIC SCHEMATIC ONLY MISC1: Fan, Thermal, JTAG, GPIO</p>
--	---------------------------------	--

GIGABYTE			
File			
FAN Thermal JTAG			
Size	Document Number		Rev
Custom	GV-N760OC-4GD		1.1
Date:	Monday, January 13, 2014	Sheet	22 of 33

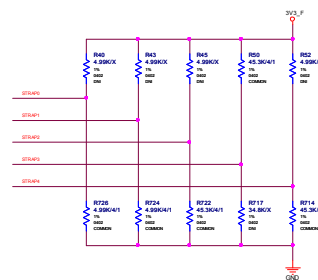
Page23: MISC2: ROM, XTAL, Straps

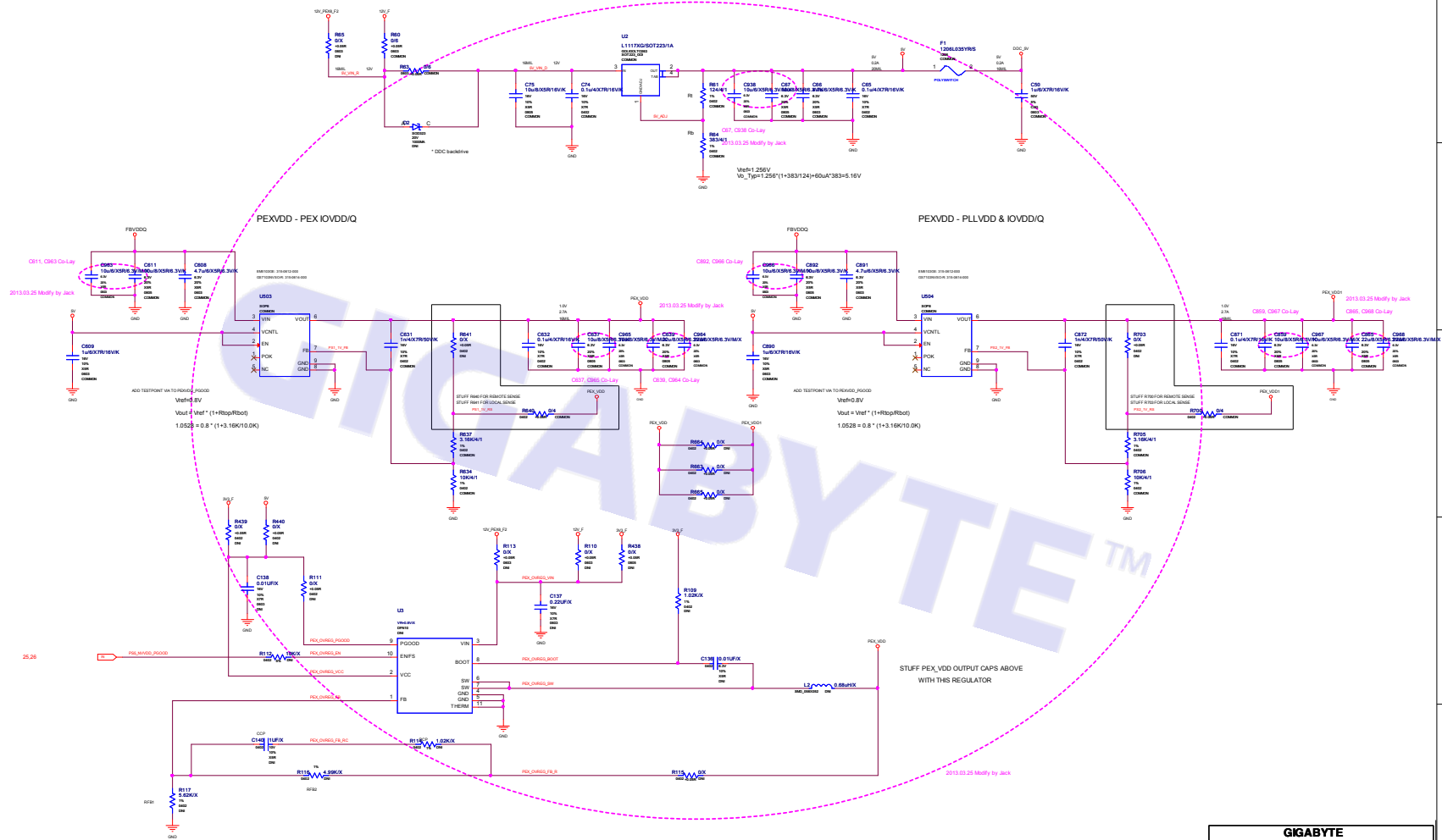
STRAP0	USER_BIT [3:0]	0000	5K PD*
STRAP1	3GIO_PADCFG_LUT_ADDR*	0000	5K PD Desktop*
STRAP2	PCI_DEVID [3:0]	0000 - (0x1180)	5K PD -400 GPU*
STRAP3	SOR_EXPOSED [3:0]	1111	45K PU*
STRAP4	DP_PLL_VDD_33V*	1* FOR 3_3V*	
	PEX_MAX_SPEED*	1* FOR GEN2/3*	45K PD*
	PEX_SPD_CHANGE_GEN3*	1* ENABLED*	
	RAMCFGQ[0]	0*	
ROM_SI	RAMCFGQ[1]	1*	35K PD*
	RAMCFGQ[2]	1*	
	RAMCFGQ[3]	0*	
	VGA_DEVICE*	1*	
ROM_SO	SMB_ALT_ADDR*	0*	10K PU*
	FB[0]_APERTURE_SIZE*	0* For 256MB*	
	FB[1]_APERTURE_SIZE*	1* For 256MB*	
	PEX_PLL_EN_TERM100*	0* DISABLED*	
ROM_SCLK	PCI_DEVID_EXT[5]	0* For 0x1180*	25K PD*
	SUB_VENDOR*	1* Dedicated BIOS*	
	PCI_DEVID_EXT[4]	0* For 0x1180*	

	GND	3V3
3K	0000	1000
10K	0001	1001
15K	0010	1010
20K	0011	1011
25K	0100	1100
30K	0101	1101
35K	0110	1110
45K	0111	1111

CFGQ[3:0] Config With Vendor
0000 Reserved
0001 32Mx2 256-bit Elpida
0010 32Mx2 256-bit Hynix
0011 32Mx2 256-bit Samsung
0100 Reserved
0101 64Mx2 256-bit Elpida
0110 64Mx2 256-bit Hynix
0111 64Mx2 256-bit Samsung
1000 Reserved
1001 32Mx2 192-bit Elpida
1010 32Mx2 192-bit Hynix
1011 32Mx2 192-bit Samsung
1100 Reserved
1101 64Mx2 192-bit Elpida
1110 64Mx2 192-bit Hynix
1111 64Mx2 192-bit Samsung

	MULTI_STRAP_REF0_GND
BINARY PRODUCTION	NO
BINARY BRINGUP	NO
MULTILEVEL	NO (N/A TO GND)

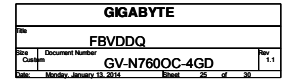




ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

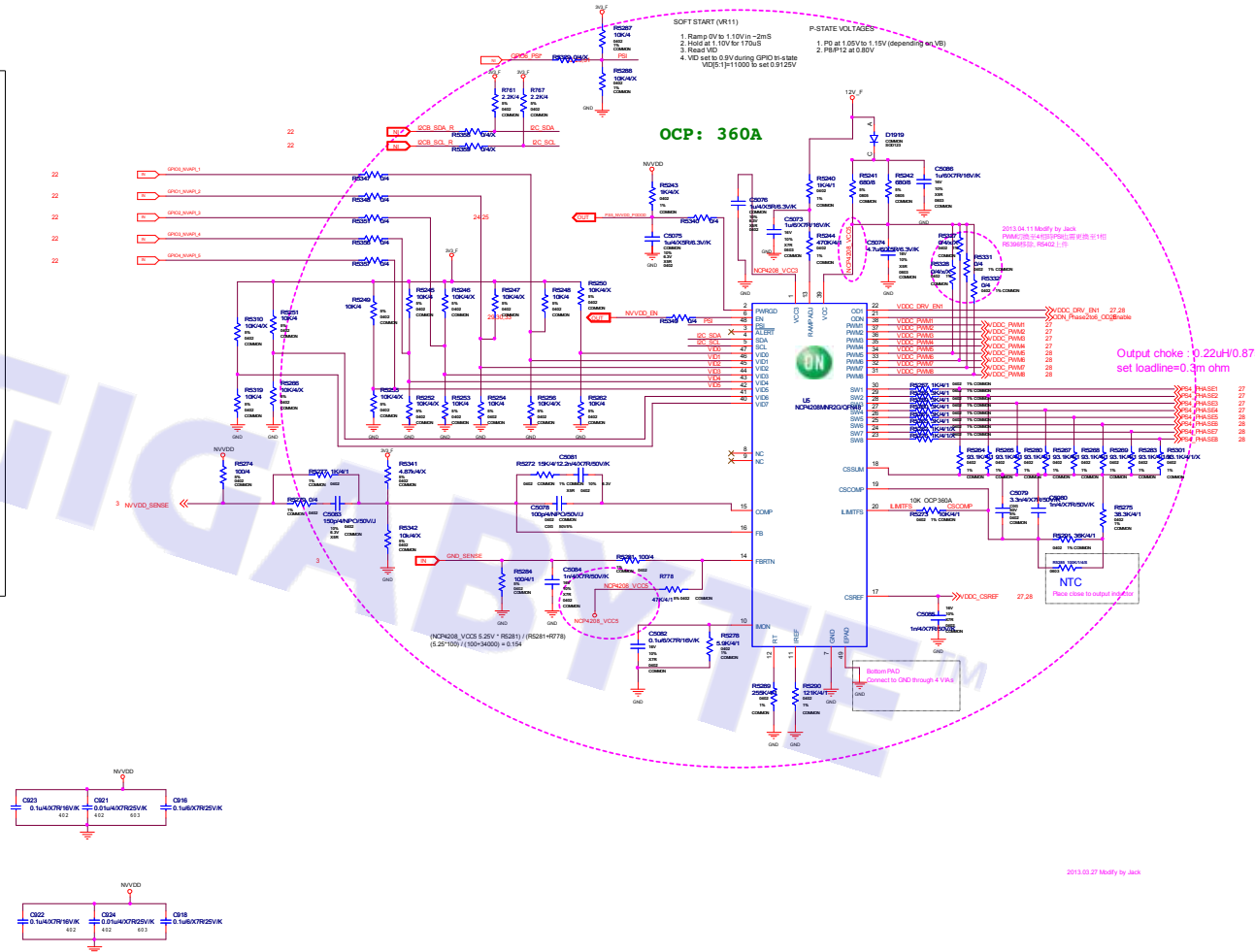
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	PS: 5V, PEX_VDD

GIGABYTE			
File			
5V PEX VDD			
Size	Document Number		Rev
Custom	GV-N7600C-4GD		1.1
Date:	Monday, January 13, 2014	Sheet	24 of 30



VID Table

GPI07	GPI08	GPI05	GPI02	GPI017	VOUT
VID_5	VID_4	VID_3	VID_2	VID_1	
0	0	0	0	0	1.2100V
0	0	0	0	1	1.2000V
0	0	0	1	0	1.1875V
0	0	0	1	1	1.1750V
0	0	1	0	0	1.1625V
0	0	1	0	1	1.1500V
0	0	1	1	0	1.1375V
0	0	1	1	1	1.1250V
0	1	0	0	0	1.1125V
0	1	0	0	1	1.1000V
0	1	0	1	0	1.0875V
0	1	0	1	1	1.0750V
0	1	1	0	0	1.0625V
0	1	1	0	1	1.0500V
0	1	1	1	0	1.0375V
0	1	1	1	1	1.0250V
1	0	0	0	0	1.0125V
1	0	0	0	1	1.0000V
1	0	0	1	0	0.9875V
1	0	0	1	1	0.9750V
1	0	1	0	0	0.9625V
1	0	1	0	1	0.9500V
1	0	1	1	0	0.9375V
1	0	1	1	1	0.9250V
1	1	0	0	0	0.9125V
1	1	0	0	1	0.9000V
1	1	0	1	0	0.8875V
1	1	0	1	1	0.8750V
1	1	1	0	0	0.8625V
1	1	1	0	1	0.8500V
1	1	1	1	0	0.8375V
1	1	1	1	1	0.8250V

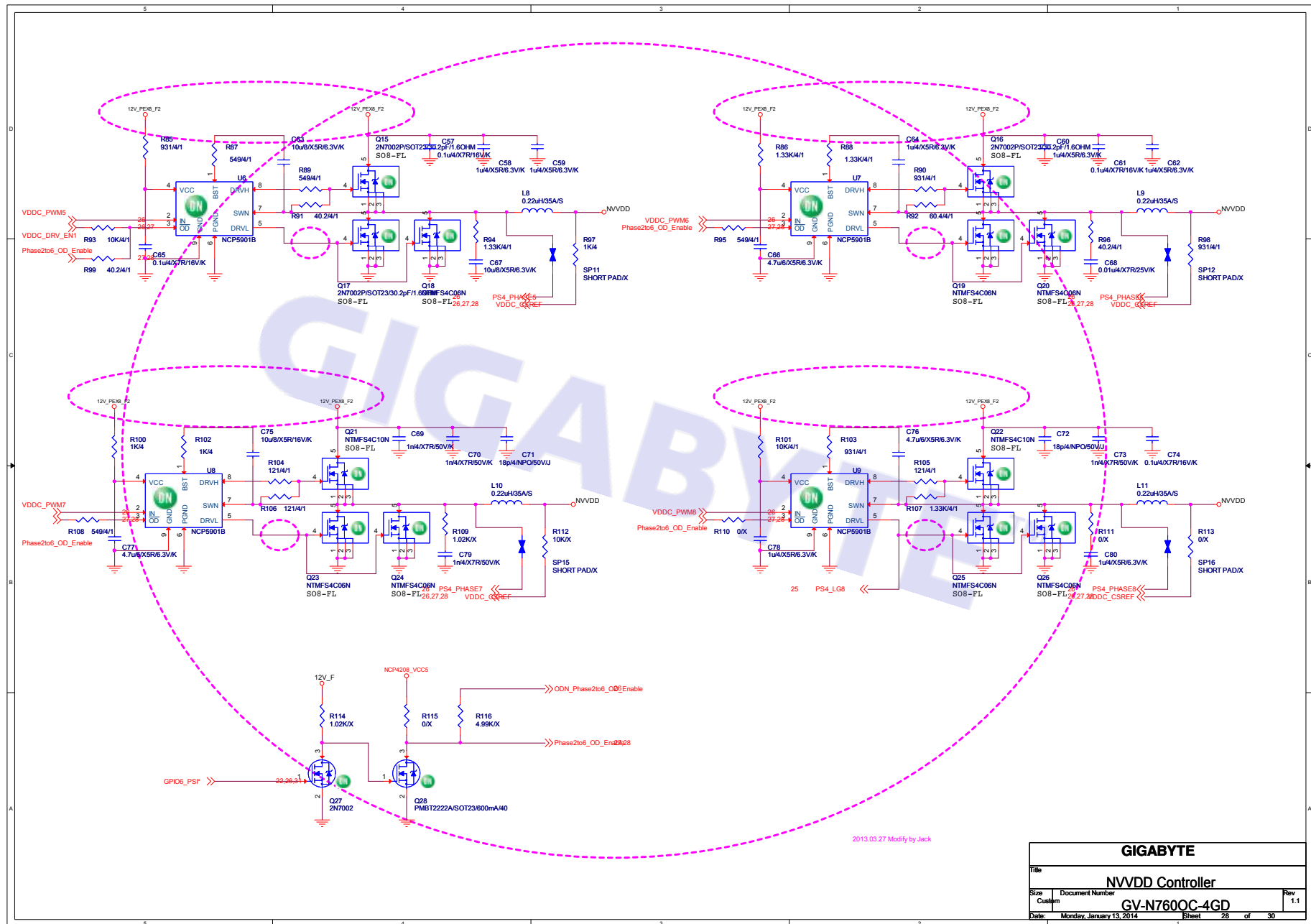


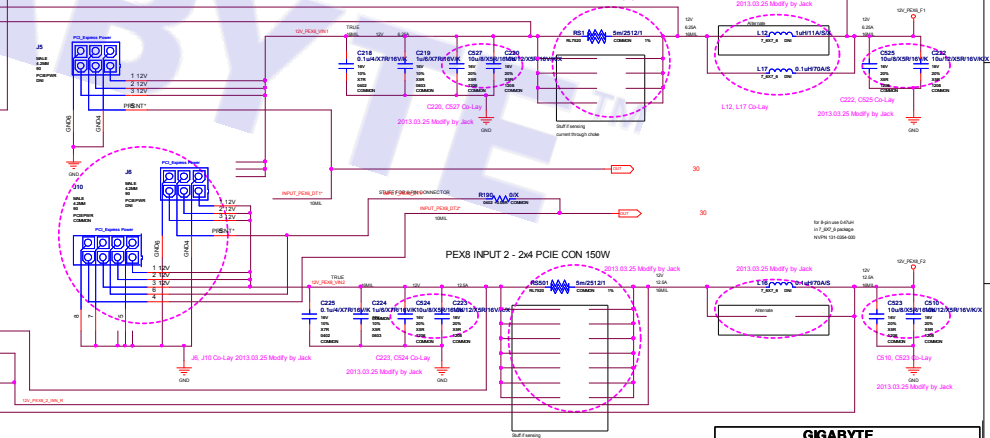
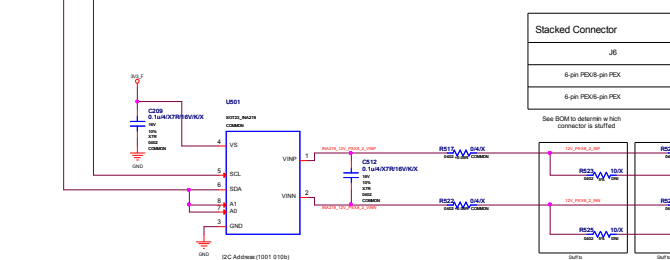
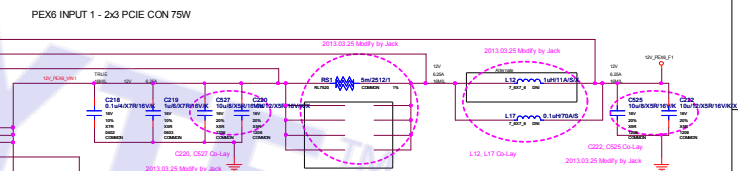
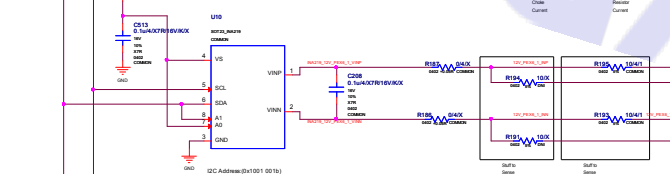
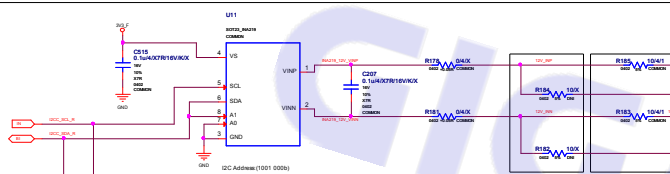
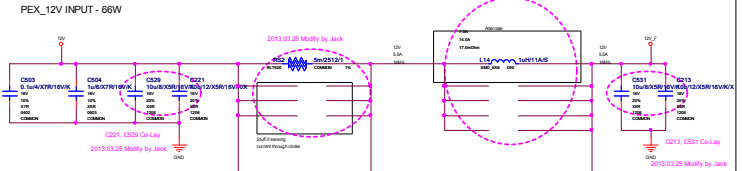
GIGABYTE

NVVDD Controller

GV-N760OC-4GD

Date: Monday, January 14, 2014





Stacked Connector
J6
6-pin PEX/6-pin PEX
6-pin PEX/6-pin PEX

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS: THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

BASE LEVEL GENERIC SCHEMATIC ONLY
PS: Inputs, Filtering, and Monitoring

GIGABYTE

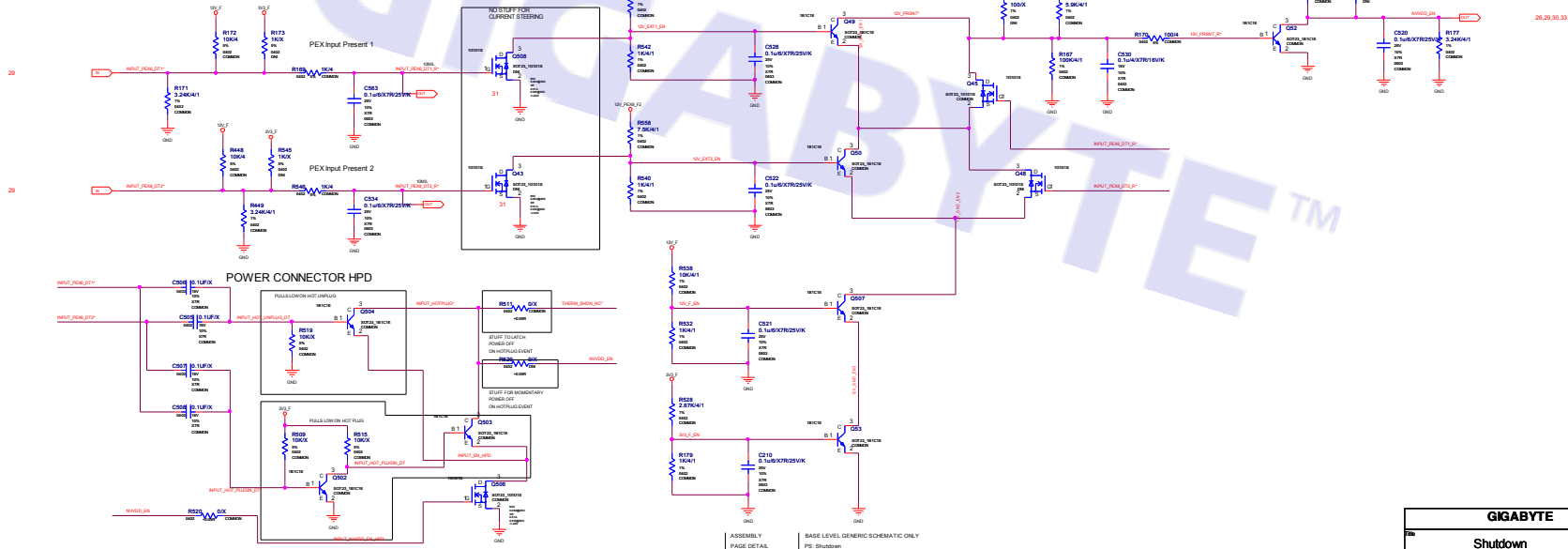
Input Filtering Monitoring

GV-N760OC-4GD

THERM OVERT SHUTDOWN LATCH

IFP_IOVDD (backdrive prevention)

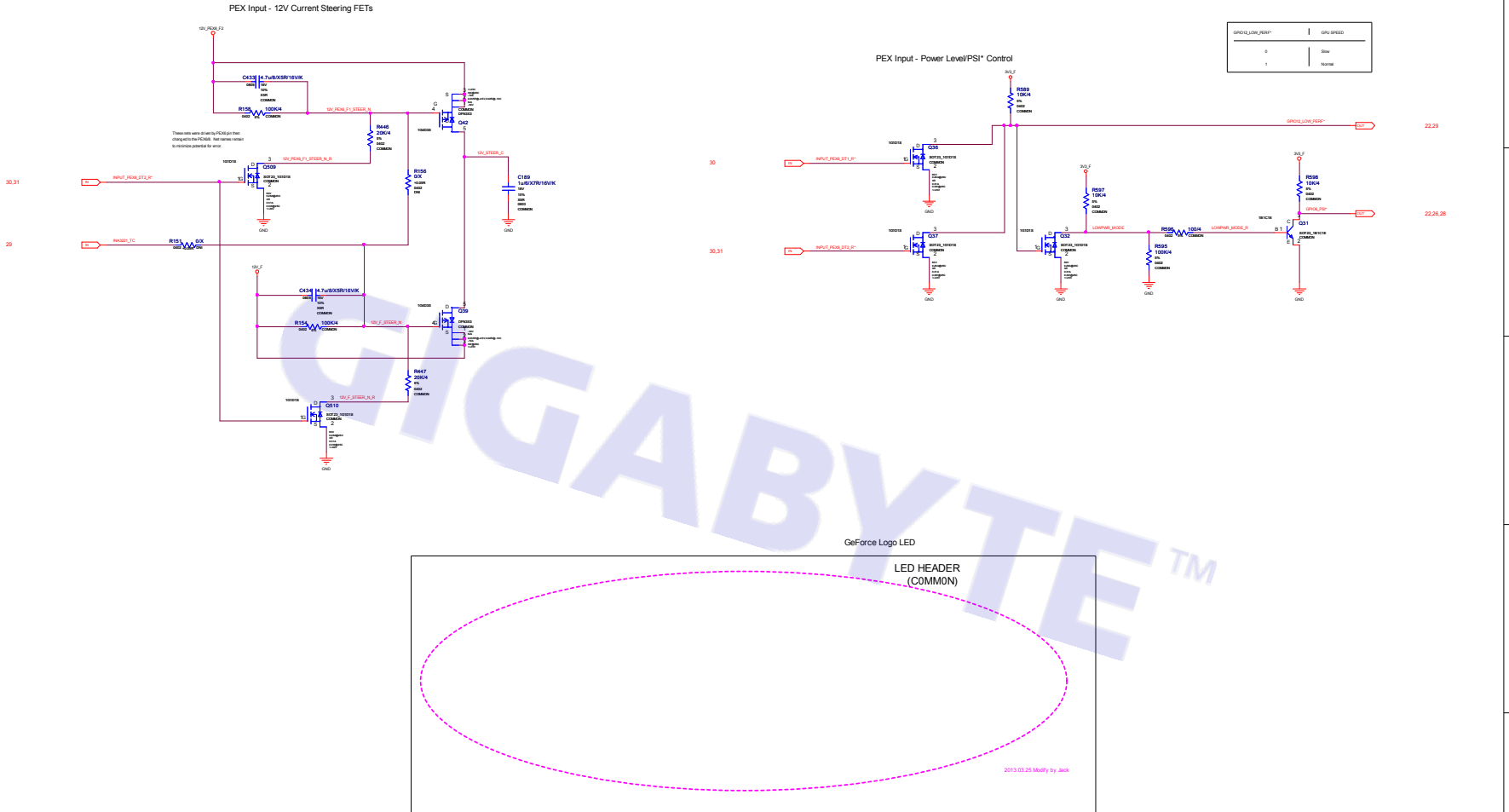
PEX Input - NVDD Power Sequence



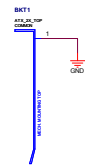
ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARD.

ASSEMBLY PAGE DETAIL
PS: Shutdown

GIGABYTE			
Shutdown			
Doc	Document Number	GV-N760OC-4GD	
Rev	1.1		
Date	Monday, January 13, 2014	Sheet	30 of 31



Brackets:



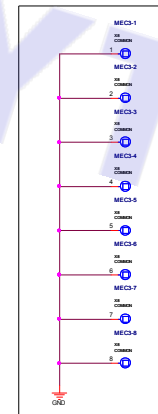
Bracket Screw



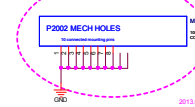
Hockey Stick



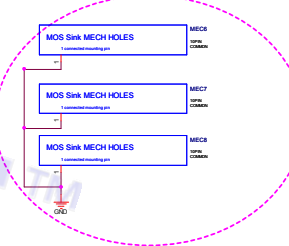
GPU Stiffener



Mechanical Holes Symbol



2013.04.02 Remove old MOSFET Sink, Modify by Jack



2013.04.02 Add new MOSFET Sink, Modify by Jack

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS". THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARD.

ASSEMBLY
PAGE DETAIL

BASE LEVEL GENERIC SCHEMATIC ONLY
MECH: Bracket/Thermal

GIGABYTE			
Thermal Bk			
Doc	Document Number	Rev	1.1
Ctrl	GV-N760OC-4GD		
Date	Monday, January 14, 2014	Sheet	32 of 51

Thermal 3 Pin Fan



ASSEMBLY
PAGE DETAIL
MATERIALS MAY
EXPRESSLY DISCLAIMS ALL
INDUSTRY STANDARDS.

BASE LEVEL GENERIC SCHEMATIC ONLY
Straps, Thermal, Mechanical

GIGABYTE			
File 33_ Thermal_3 Pin Fan			
Size	Document Number		Rev
Custom	GV-N760OC-4GD		1.1
Date:	Monday, January 13, 2014	Sheet	33 of 33