P1071: GF108, 1024MB x16 DDR3, DVI+HDMI+VGA

SKU	VARIANT	NVPN	ASSEMBLY
B	BASE	600-11071-BASE-SCH	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU0001	600-11071-0001-100	GF108-300-A1 625/1250/900MHz 1024MB/128bit DDR3, DVI-DL+HDMI+VGA
2	SKU0002	600-11071-0002-100	GF108-200-A1 650/1300/900MHz 1024MB/128bit DDR3, DVI-DL+HDMI+VGA
3	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
4	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
5	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
6	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
7	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
8	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
9	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
10	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
11	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
12	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
13	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
14	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
15	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>

ASSEMBLY: BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL

PAGE DETAIL: Overview

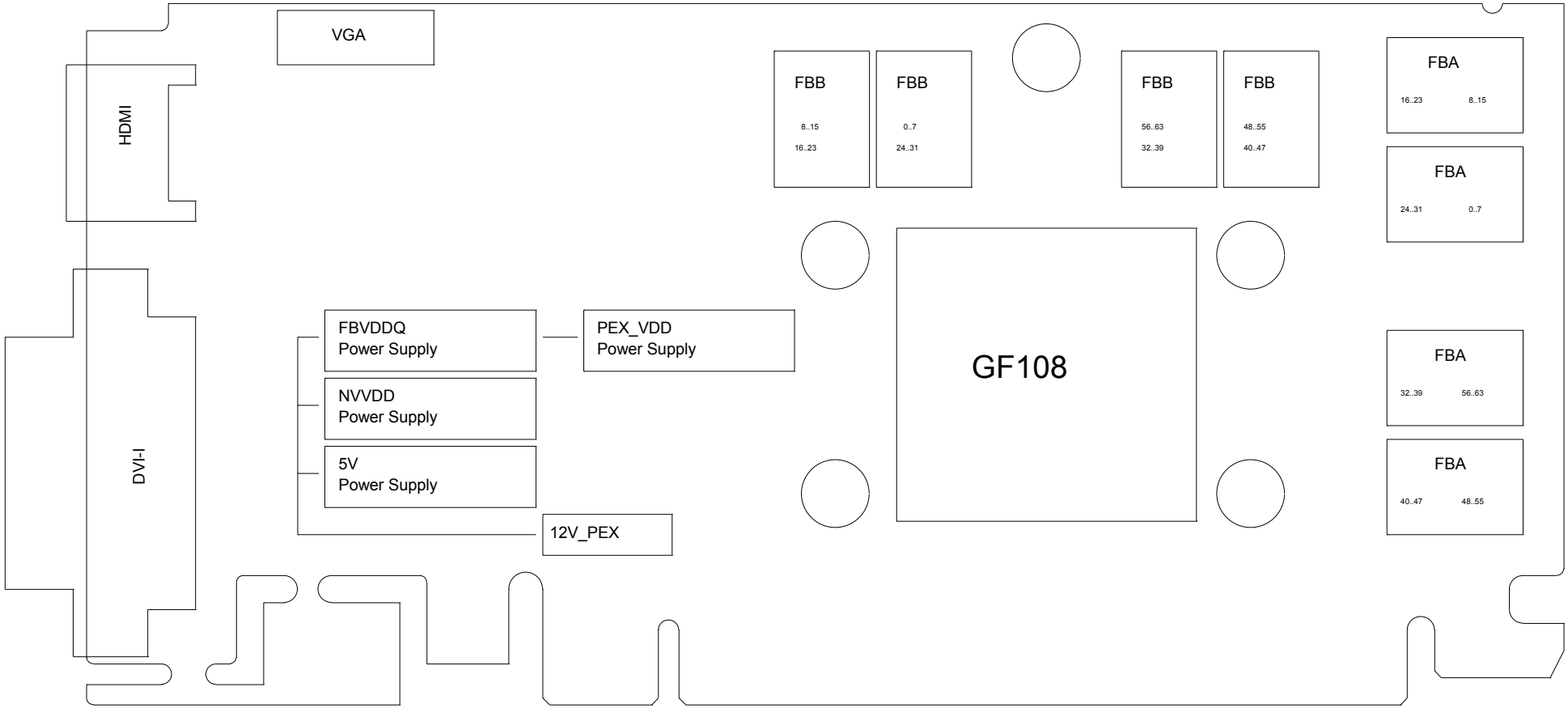
GIGABYTE

Title: OVER VIEW

Size: Custom | Document Number: GV-N730-2GI | Rev: 1.0

Date: Tuesday, May 20, 2014 | Sheet 1 of 20

Block Diagram



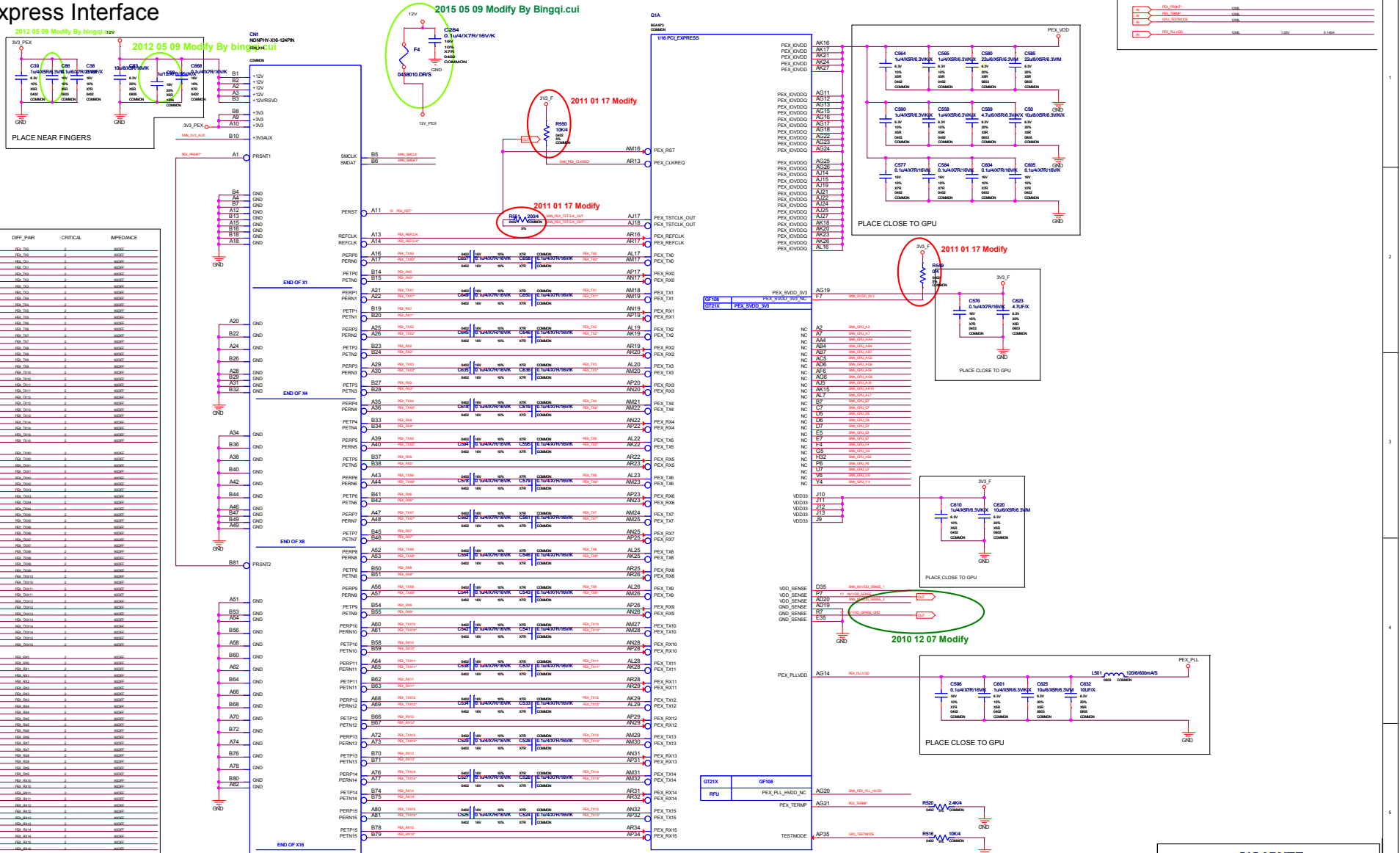
ASSEMBLY
PAGE DETAIL

BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
Board Block Diagram

GIGABYTE

Title	Block Diagram	
Size: Custom	Document Number: GV-N730-2GI	Rev: 1.0
Date: Tuesday, May 20, 2014	Sheet 2 of 20	

PCI Express Interface



ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, 'MATERIALS') ARE BEING PROVIDED 'AS IS'. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

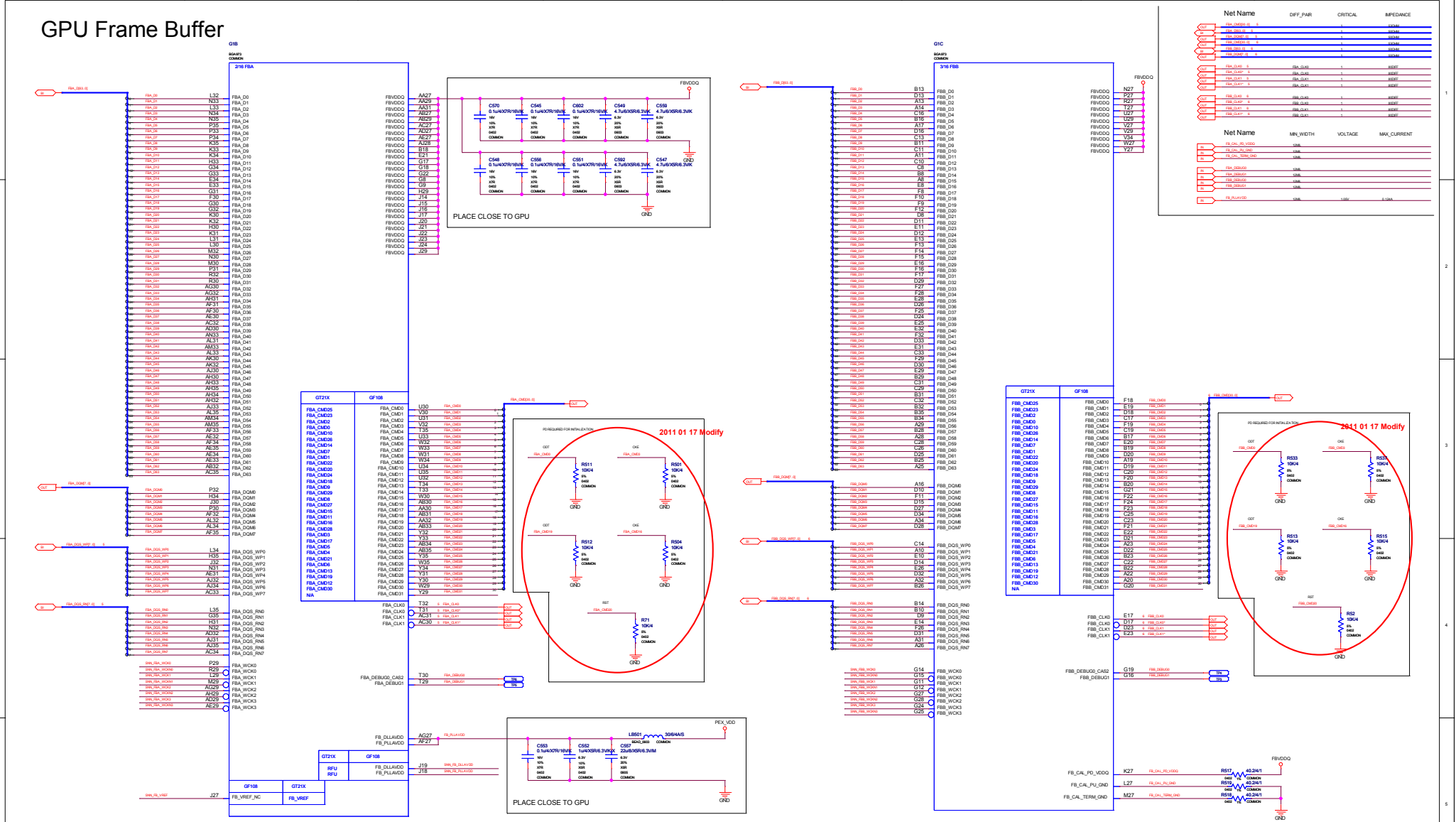
ASSEMBLY
PAGE DETAIL

BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PCI Express Interface

GIGABYTE

Title	PCI Express Interface		
Size	Document Number		Rev
Custom	GV-N730-2GI		1.0
Date:	Tuesday, May 20, 2014	Sheet 3 of 20	

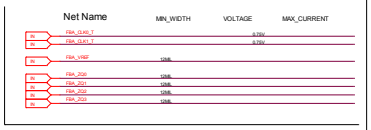
GPU Frame Buffer



GIGABYTE

Title	GPU Frame Buffer
Size: Custom	Document Number: GV-N730-2GI
Rev	1.0
Date: Tuesday, May 20, 2014	Sheet 4 of 20

FBA Partition

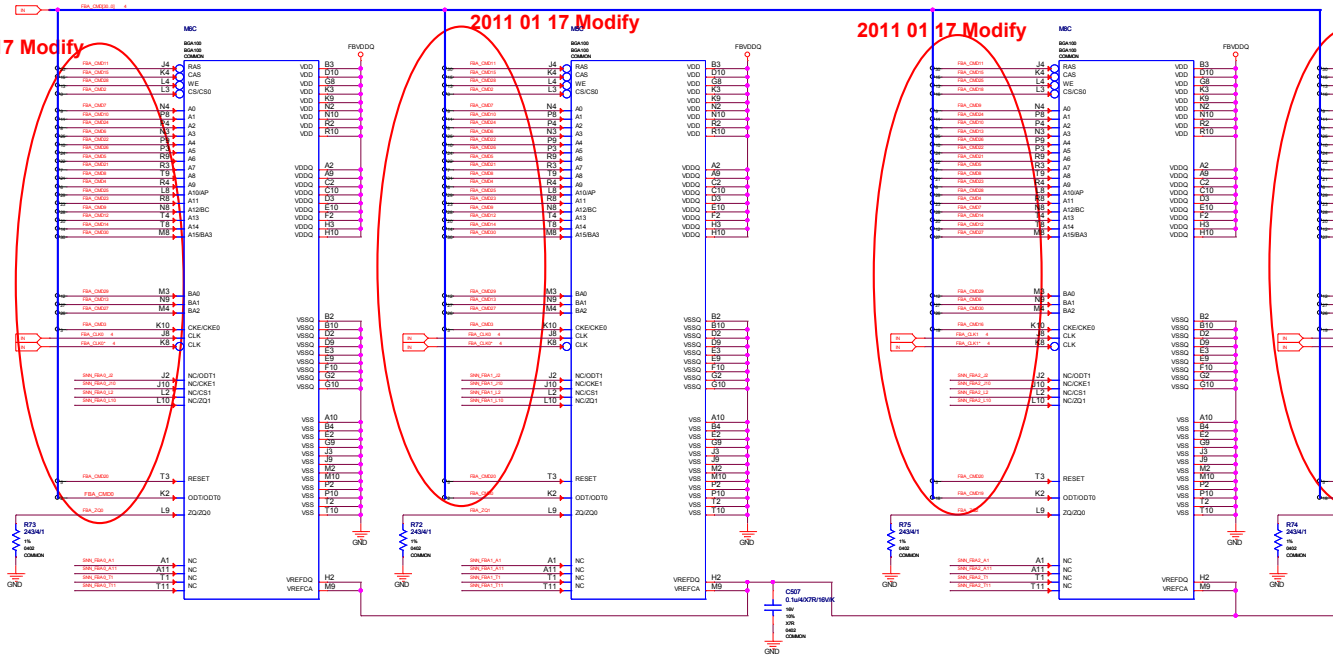


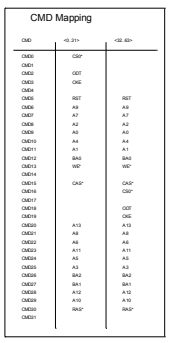
2011 01 17 Modify

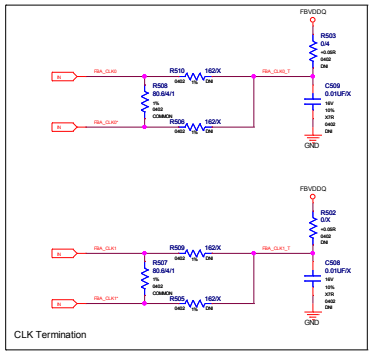
2011 01 17 Modify

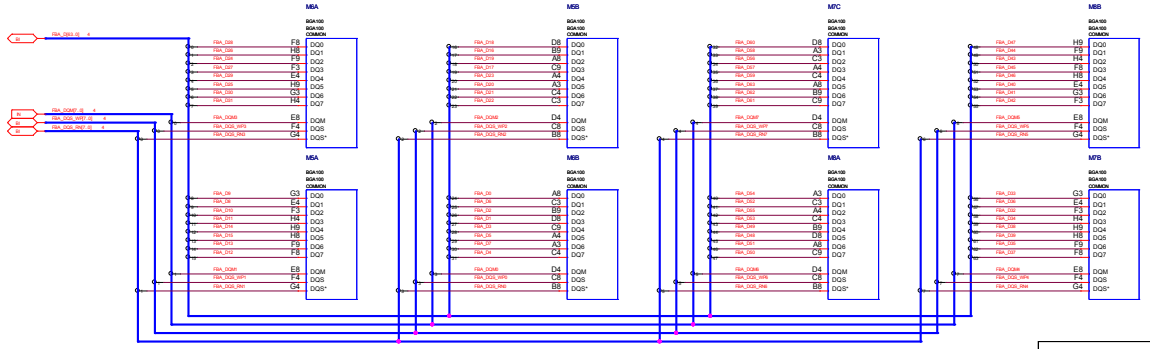
2011 01 17 Modify

2011 01 17 Modify









ASSEMBLY
PAGE DETAIL

BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
FBA Partition

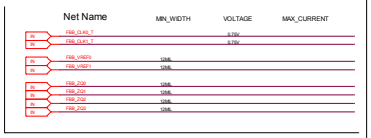
GIGABYTE

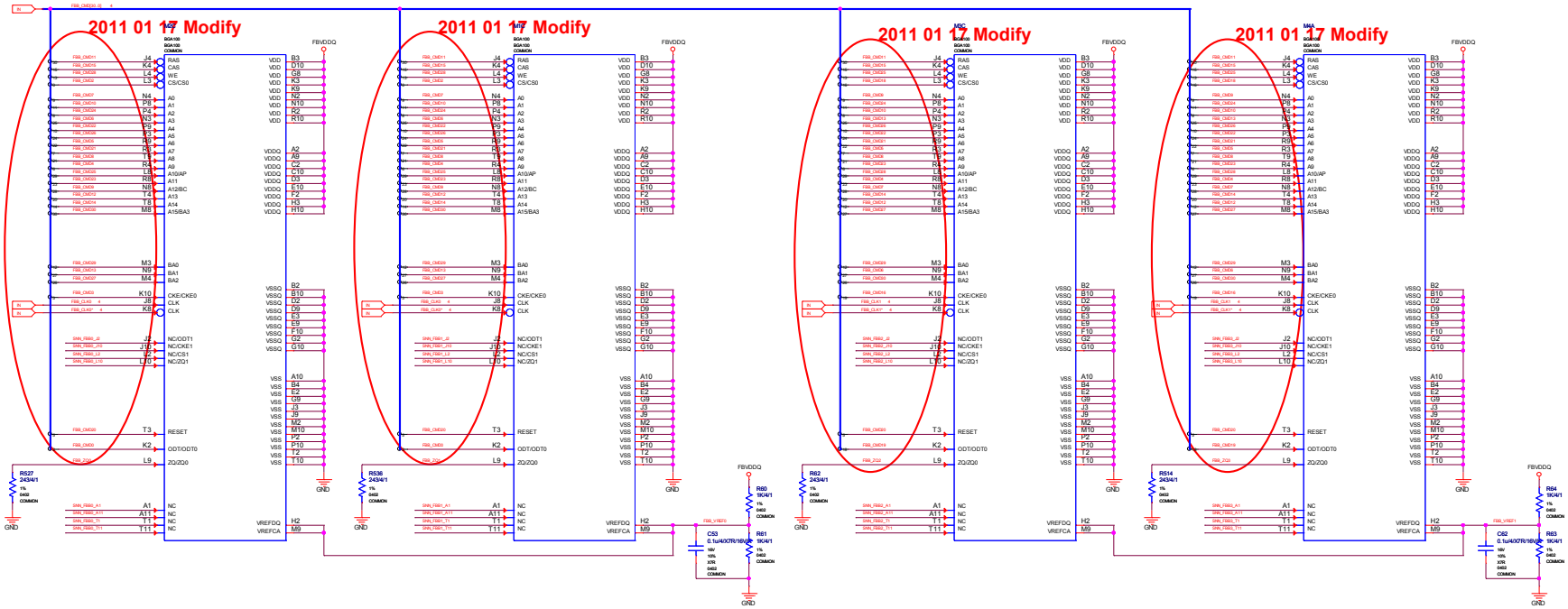
Title: FBA Partition

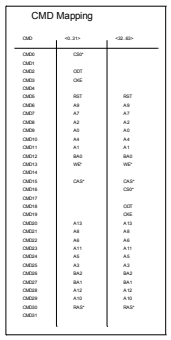
Size: Custom | Document Number: GV-N730-2GI | Rev: 1.0

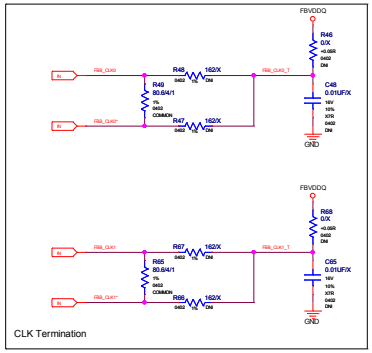
Date: Tuesday, May 20, 2014 | Sheet 5 of 20

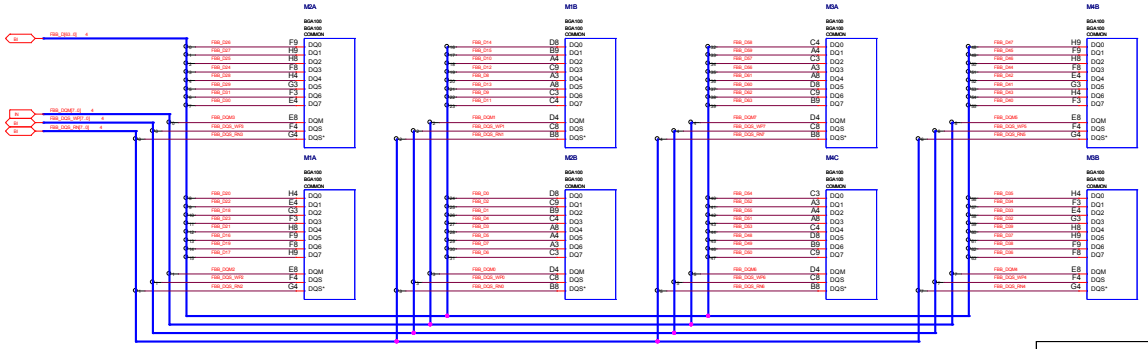
FBB Partition











ASSEMBLY
PAGE DETAIL

BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
FBB Partition

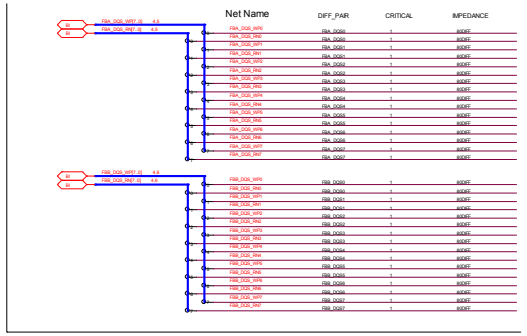
GIGABYTE

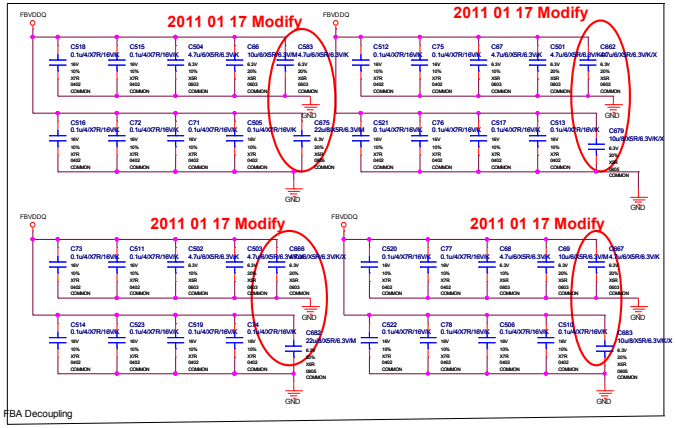
Title: FBB Partition

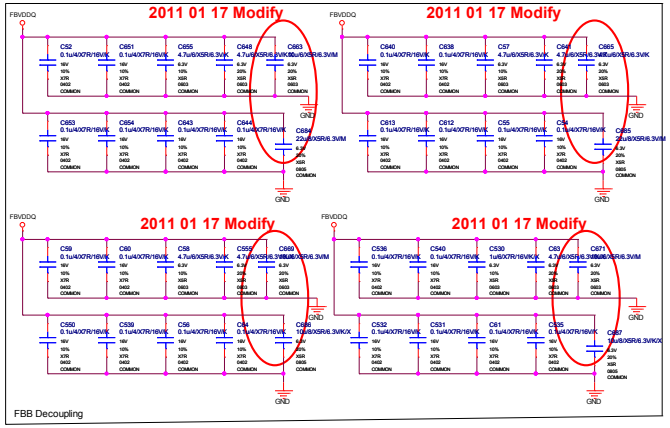
Size: Custom | Document Number: GV-N730-2GI | Rev: 1.0

Date: Tuesday, May 20, 2014 | Sheet 6 of 20

Memory Decoupling







ASSEMBLY
PAGE DETAIL

BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
Memory Decoupling

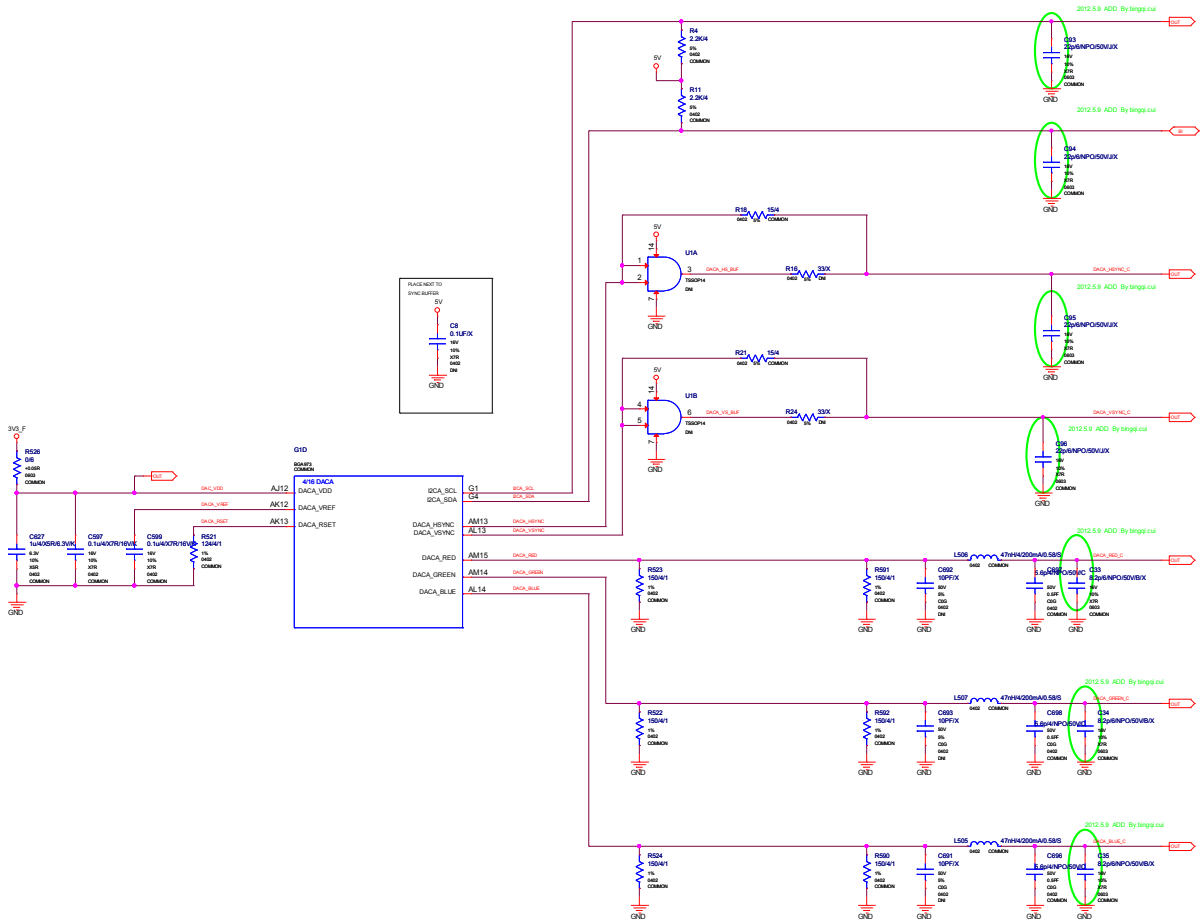
GIGABYTE

Title: Memory Decoupling

Size: Custom | Document Number: GV-N730-2GI | Rev: 1.0

Date: Tuesday, May 20, 2014 | Sheet 7 of 20

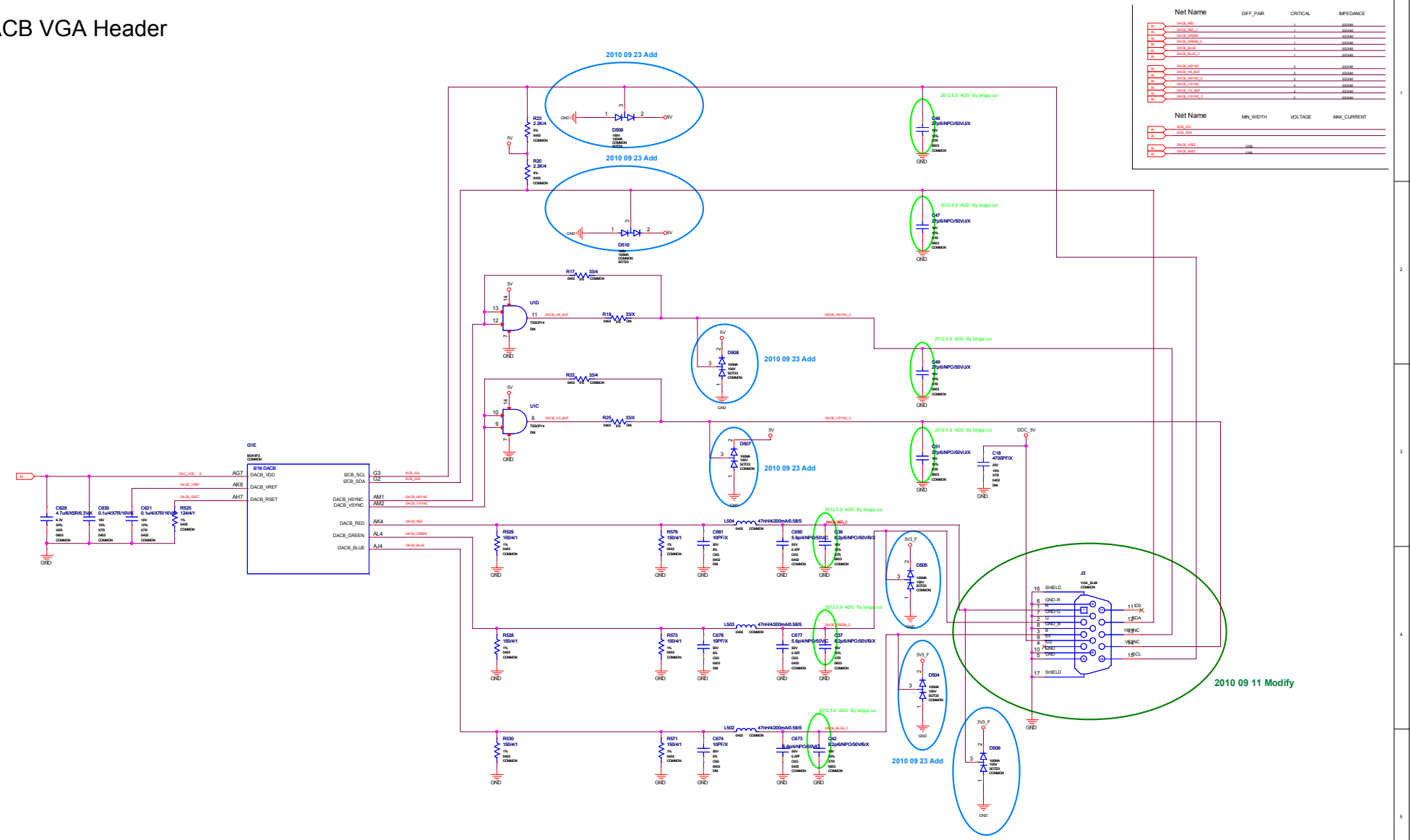
DACA VGA



Title	DACA VGA
Size	Custom
Document Number	GV-N730-2GI
Rev	1.0
Date:	Tuesday, May 20, 2014
Sheet	8 of 20

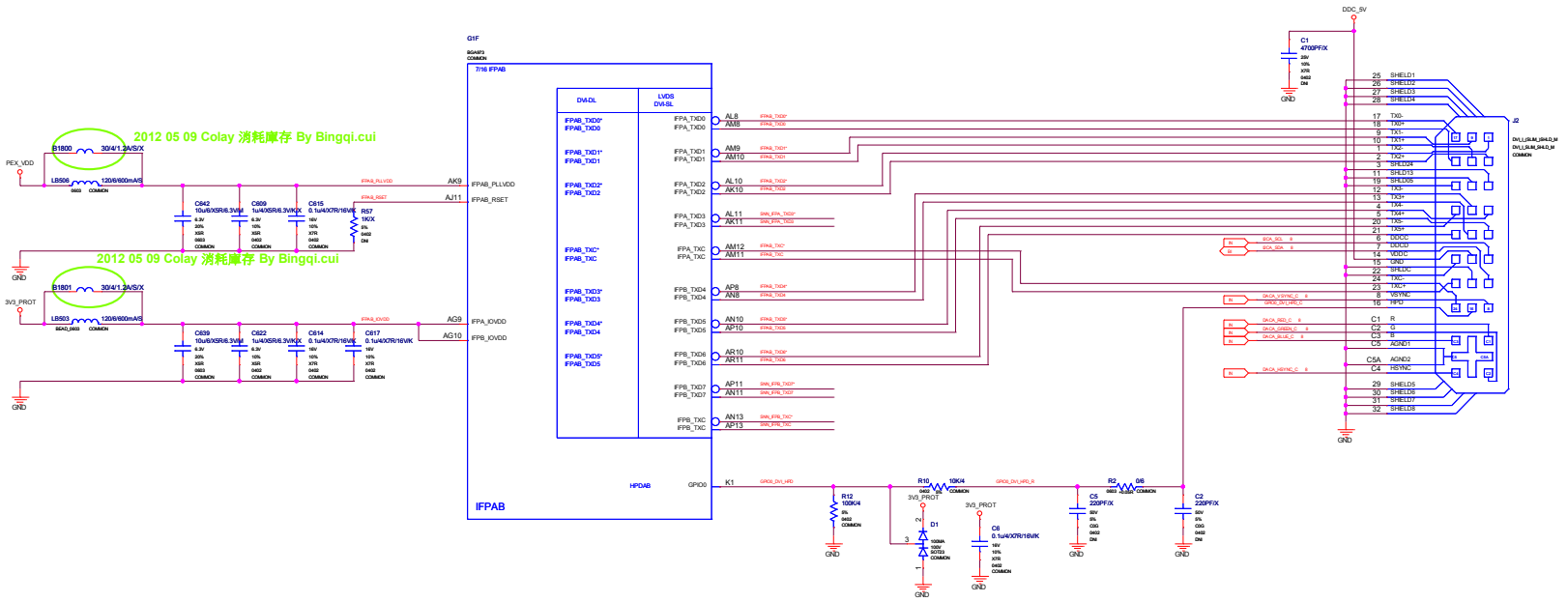
GIGABYTE

DACB VGA Header



GIGABYTE			
Title: DACB VGA Header			
Size: Custom	Document Number: GV-N730-2GI		Rev: 1.0
Date: Tuesday, May 20, 2014	Sheet 9 of 20		

TMDS Interface



Net Name	DIFF_PAIR	CRITICAL	IMPEDANCE
IFPAB_TXD0	IFPAB_TXD0	1	90DIFF
IFPAB_TXD0*	IFPAB_TXD0	1	90DIFF
IFPAB_TXD1	IFPAB_TXD1	1	90DIFF
IFPAB_TXD1*	IFPAB_TXD1	1	90DIFF
IFPAB_TXD2	IFPAB_TXD2	1	90DIFF
IFPAB_TXD2*	IFPAB_TXD2	1	90DIFF
IFPAB_TXD3	IFPAB_TXD3	1	90DIFF
IFPAB_TXD3*	IFPAB_TXD3	1	90DIFF
IFPAB_TXD4	IFPAB_TXD4	1	90DIFF
IFPAB_TXD4*	IFPAB_TXD4	1	90DIFF
IFPAB_TXD5	IFPAB_TXD5	1	90DIFF
IFPAB_TXD5*	IFPAB_TXD5	1	90DIFF
IFPAB_TXD6	IFPAB_TXD6	1	90DIFF
IFPAB_TXD6*	IFPAB_TXD6	1	90DIFF
IFPAB_TXC	IFPAB_TXC	1	90DIFF
IFPAB_TXC*	IFPAB_TXC	1	90DIFF

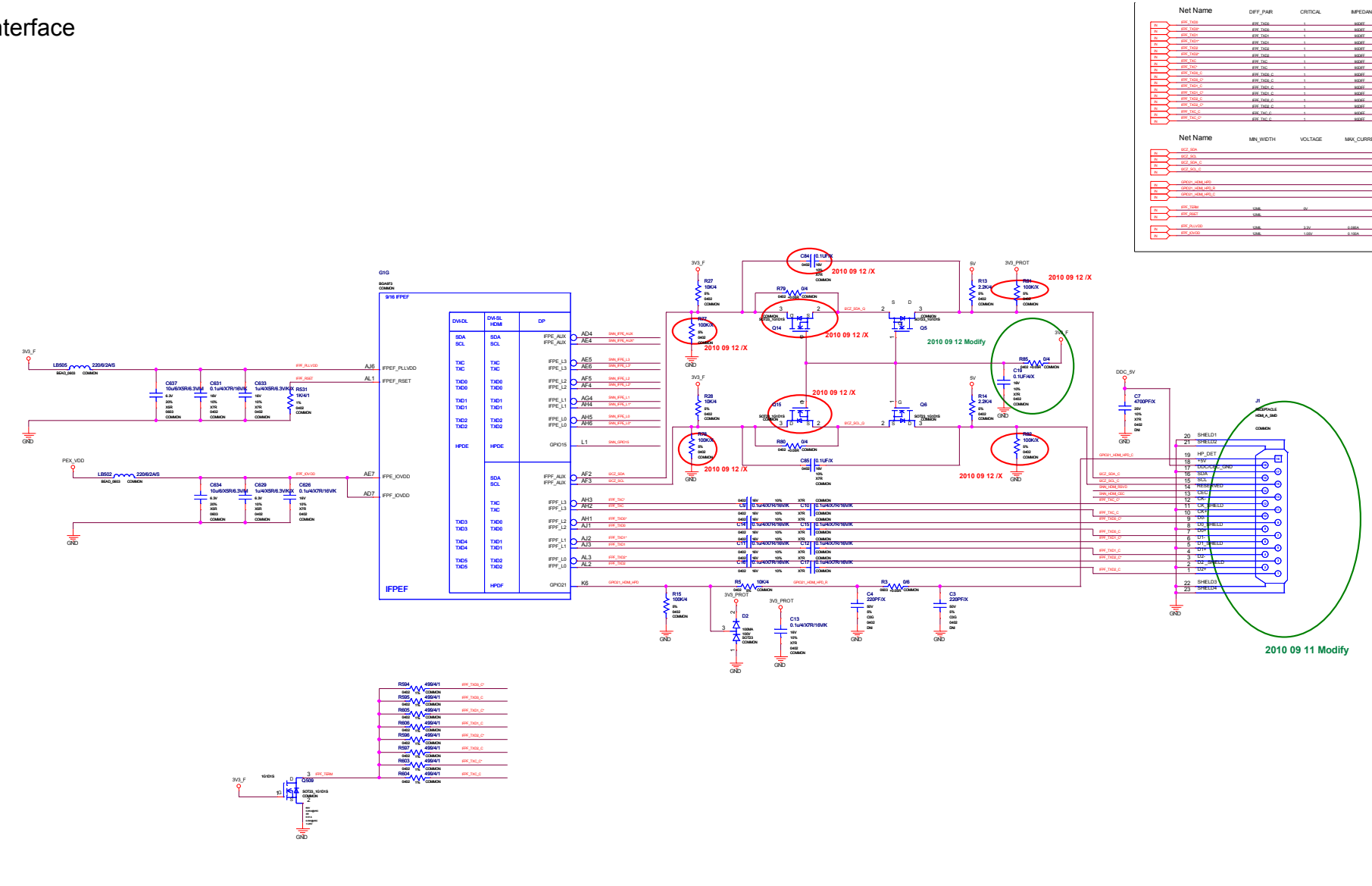
Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
GPIO0_DVI_HPD			
GPIO0_DVI_HPD_R			
GPIO0_DVI_HPD_C			
IFPAB_RSET	12MIL		
IFPAB_PLLVDD	12MIL	1.05V	0.135A
IFPAB_IOVDD	12MIL	3.3V	0.330A

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	TMDS Interface

GIGABYTE

Title	TMDS Interface	
Size: Custom	Document Number: GV-N730-2GI	Rev: 1.0
Date: Tuesday, May 20, 2014	Sheet 10 of 20	

HDMI Interface

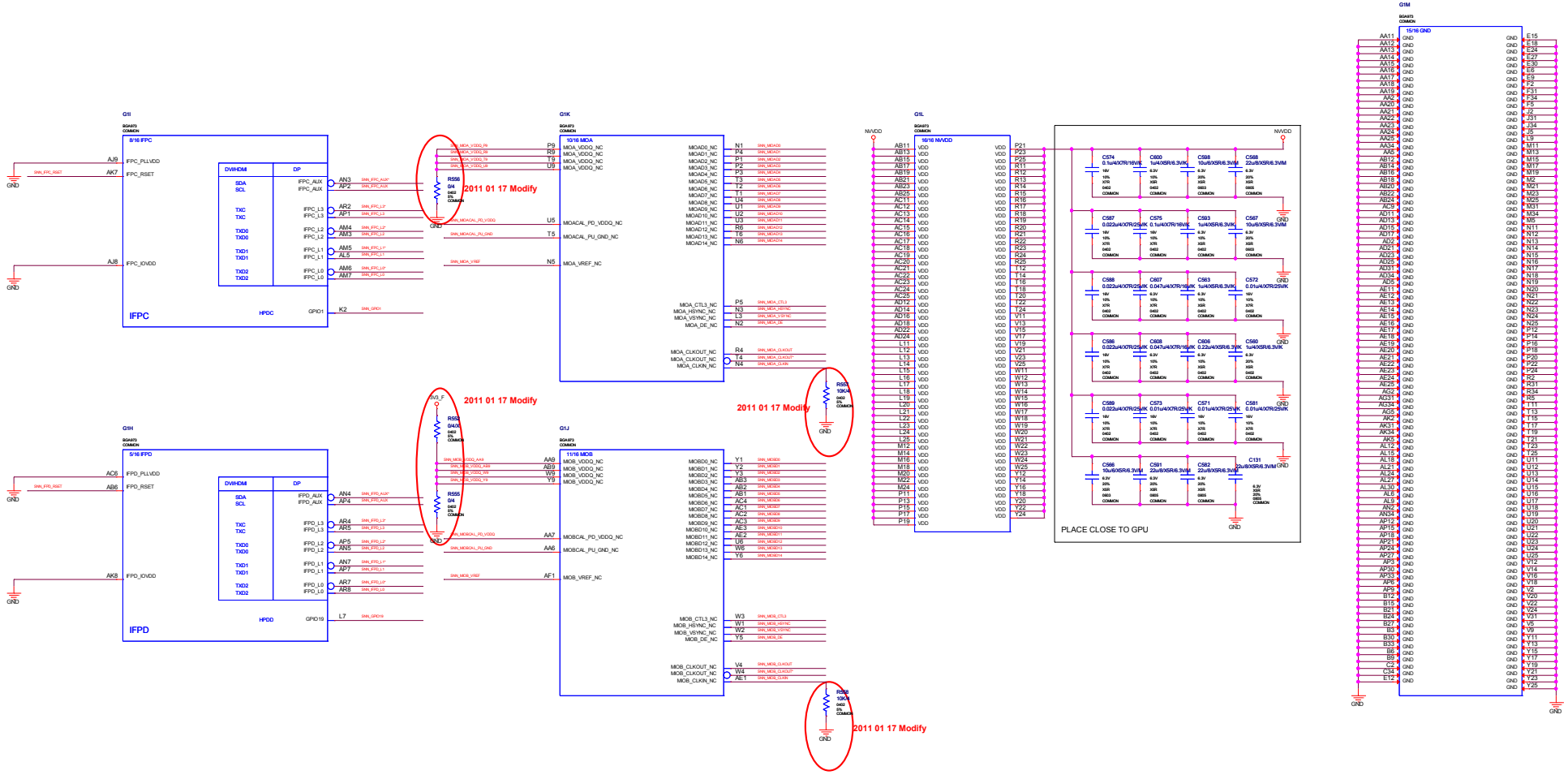


ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, 'MATERIALS') ARE BEING PROVIDED 'AS IS'. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	HDMI Interface

Title	GIGABYTE		
	HDMI Interface		
Size: Custom	Document Number: GV-N730-2GI		Rev: 1.0
Date: Tuesday, May 20, 2014	Sheet 11 of 20		

Unused Interfaces, NVVDD Decoupling

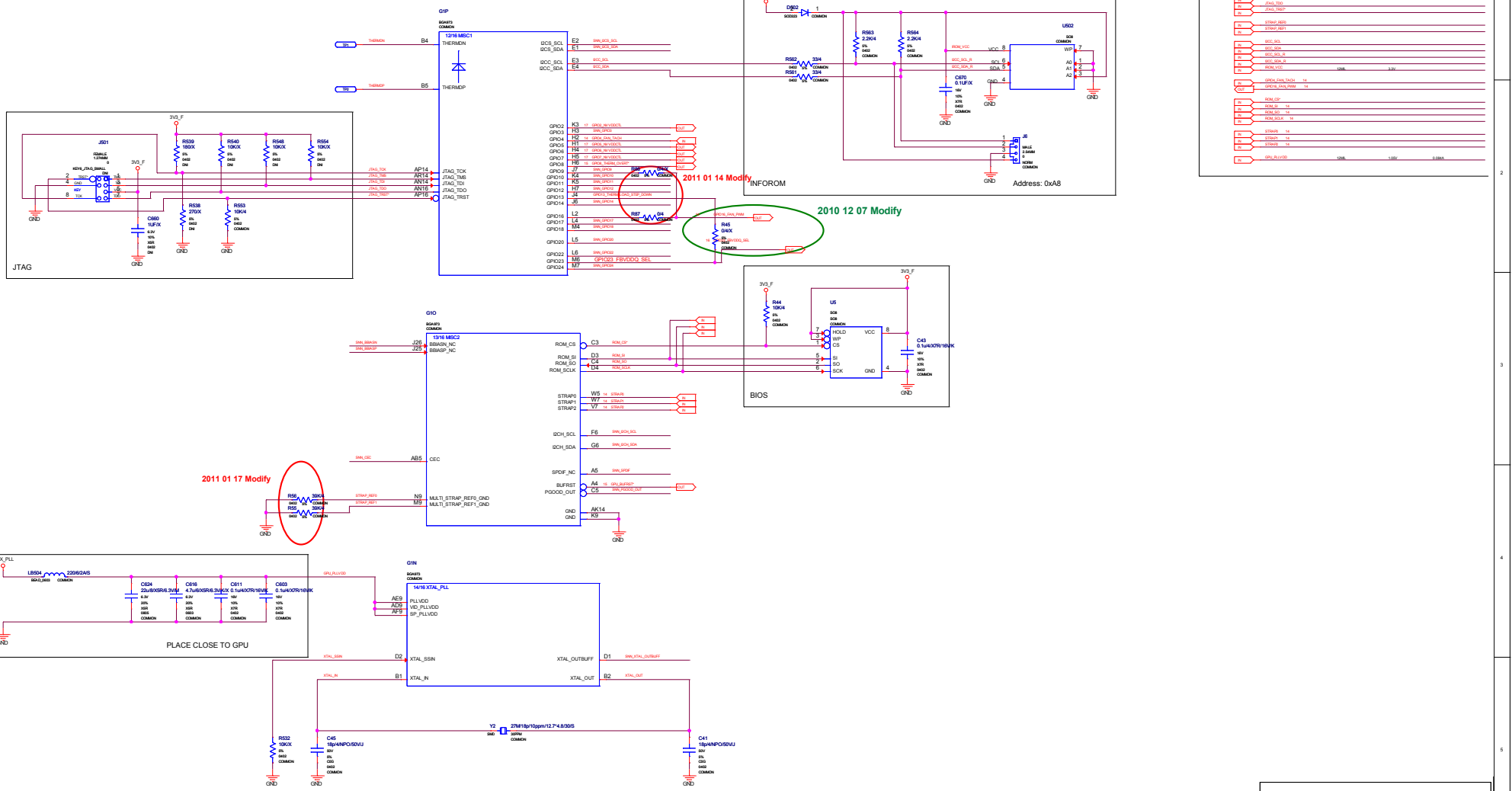


ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Unused Interfaces, NVVDD Decoupling

GIGABYTE

Title	Unused Interfaces, NVVDD Decoupling	
Size: Custom	Document Number: GV-N730-2GI	Rev: 1.0
Date: Tuesday, May 20, 2014	Sheet 12 of 20	

XTAL, ROM, JTAG, Misc



ASSEMBLY
PAGE DETAIL

BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
XTAL, ROM, JTAG, Misc

GIGABYTE

Title	XTAL, ROM, JTAG, Misc	
Size: Custom	Document Number: GV-N730-2GI	Rev: 1.0
Date: Tuesday, May 20, 2014	Sheet 13 of 20	

Straps, Thermal, Mechanical

2010 09 11 Modify

Add Copper under pad 4 (at least 1cm^2)

2010 09 20 Modify

2012.5.9 Colay 消耗22uH

Colay 22uH Chock

GF108 Straps

Bit Signal | Values

Multilevel Straps

Mechanicals

4-Wire Fan

2010 09 11 Modify

2012.5.10 Modify By bingqi.cui

Near NVDDC each high side MOS

Net Name | MIN_WIDTH | VOLTAGE | MAX_CURRENT

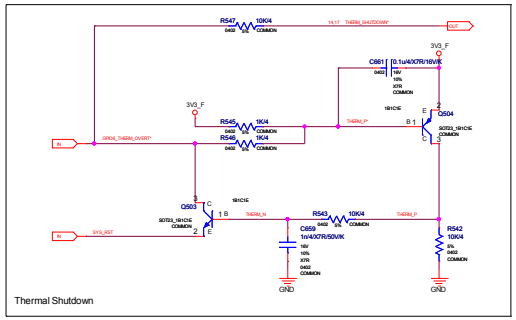
GIGABYTE

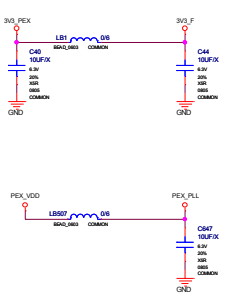
Title: Straps, Thermal, Mechanical

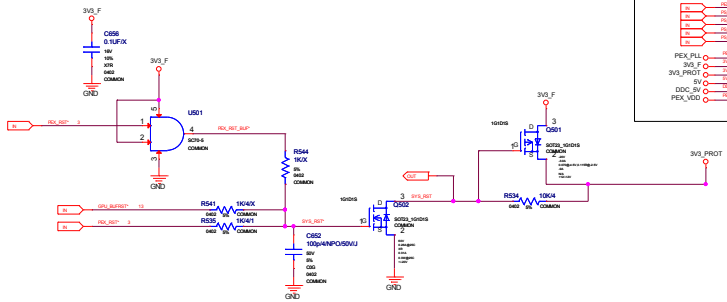
Size: Custom | Document Number: GV-N730-2GI | Rev: 1.0

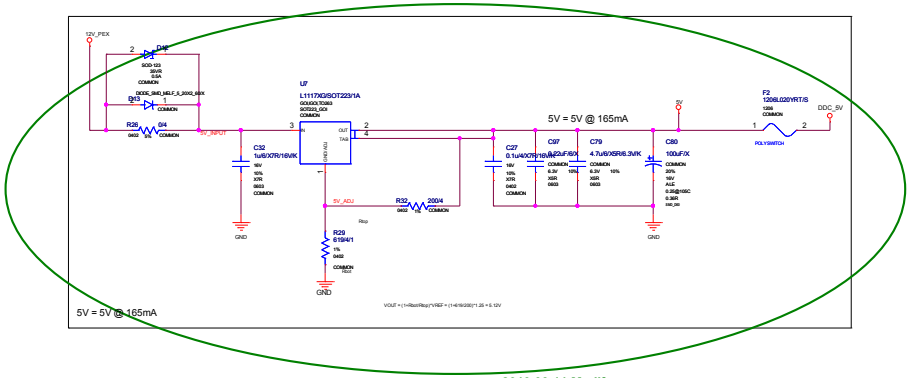
Date: Tuesday, May 20, 2014 | Sheet 14 of 20

Power Supply I: Misc Power, Thermal Shutdown

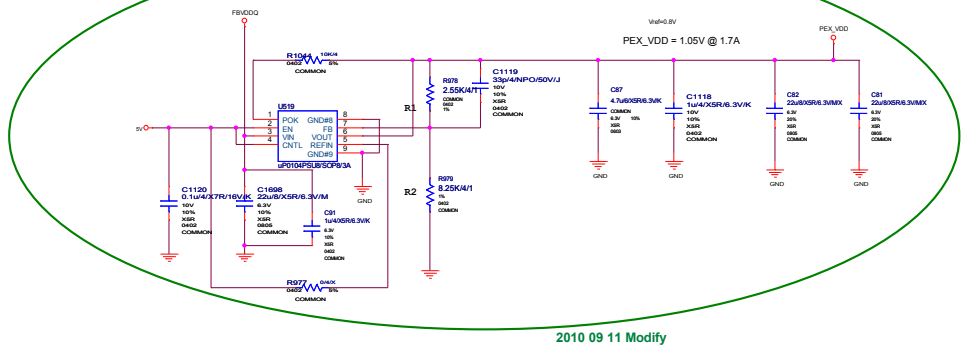




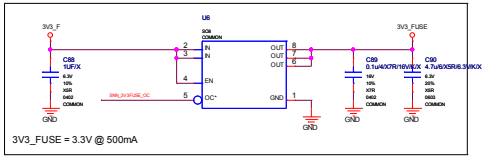




2010 09 11 Modify



2010 09 11 Modify

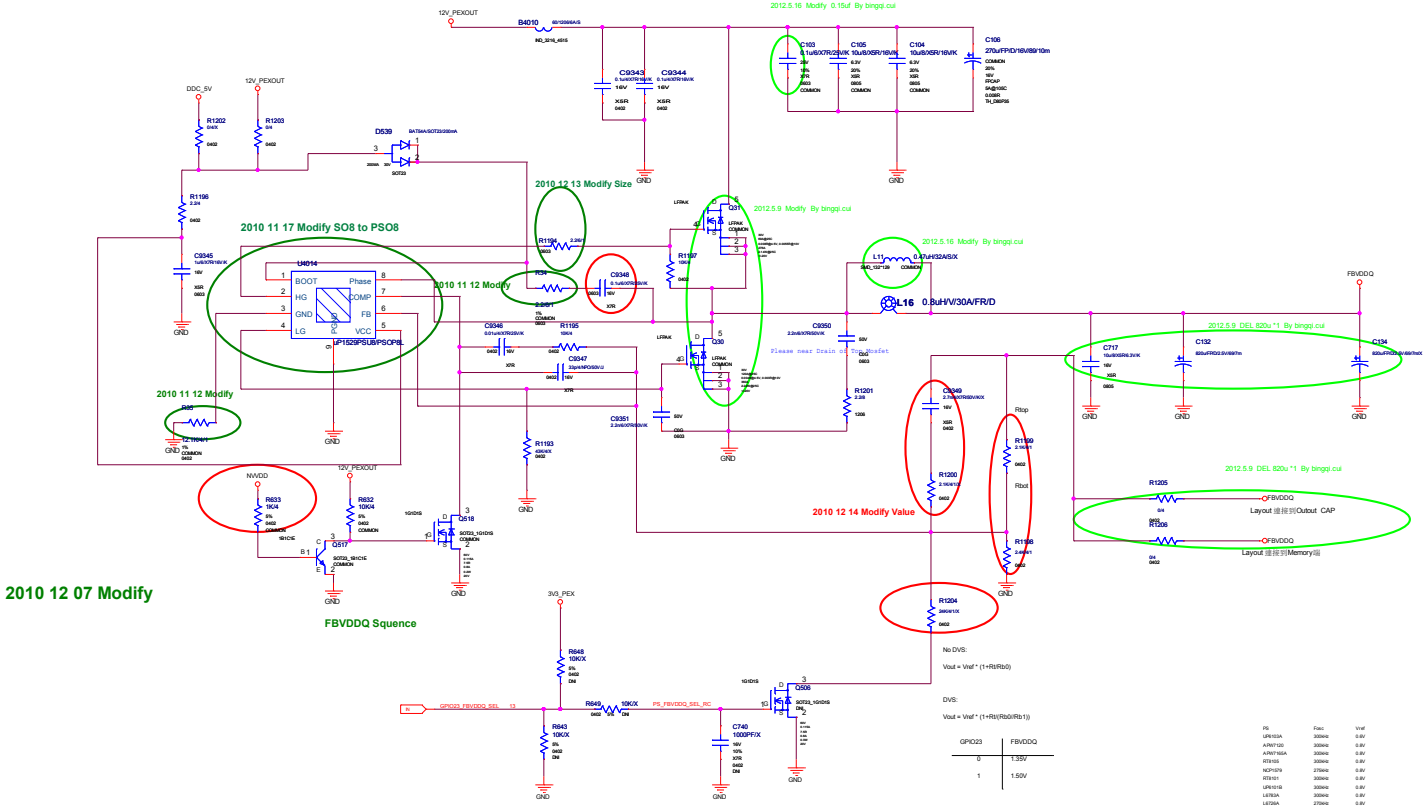


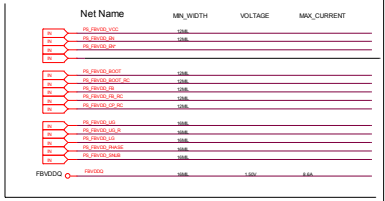
Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
THERM_SHUTDOWN*	14,17		
GPIO6_THERM_OVERT*	13		
THERM_P			
THERM_N			
SYS_RST			
PEX_RST*	3		
GPU_BUFRST*	13		
PEX_RST_BUF*			
PS_5V_BACKDRIVE	12MIL		
PS_5V_PROT	12MIL		
PS_PEXVDD_CNTL	12MIL		
PS_PEXVDD_FB	12MIL		
PEX_PLL	12MIL	1.05V	0.333A
3V3_F	12MIL	3.3V	2A
3V3_PROT	12MIL	3.3V	0.330A
5V	12MIL	5V	0.165A
DDC_5V	12MIL	5V	0.165A
PEX_VDD	16MIL	1.05V	1.7A

Title	Power Supply I: Misc Power
Size: Custom	Document Number: GV-N730-2GI
Rev	1.0
Date: Tuesday, May 20, 2014	Sheet 15 of 20

GIGABYTE

Power Supply II: FBVDDQ





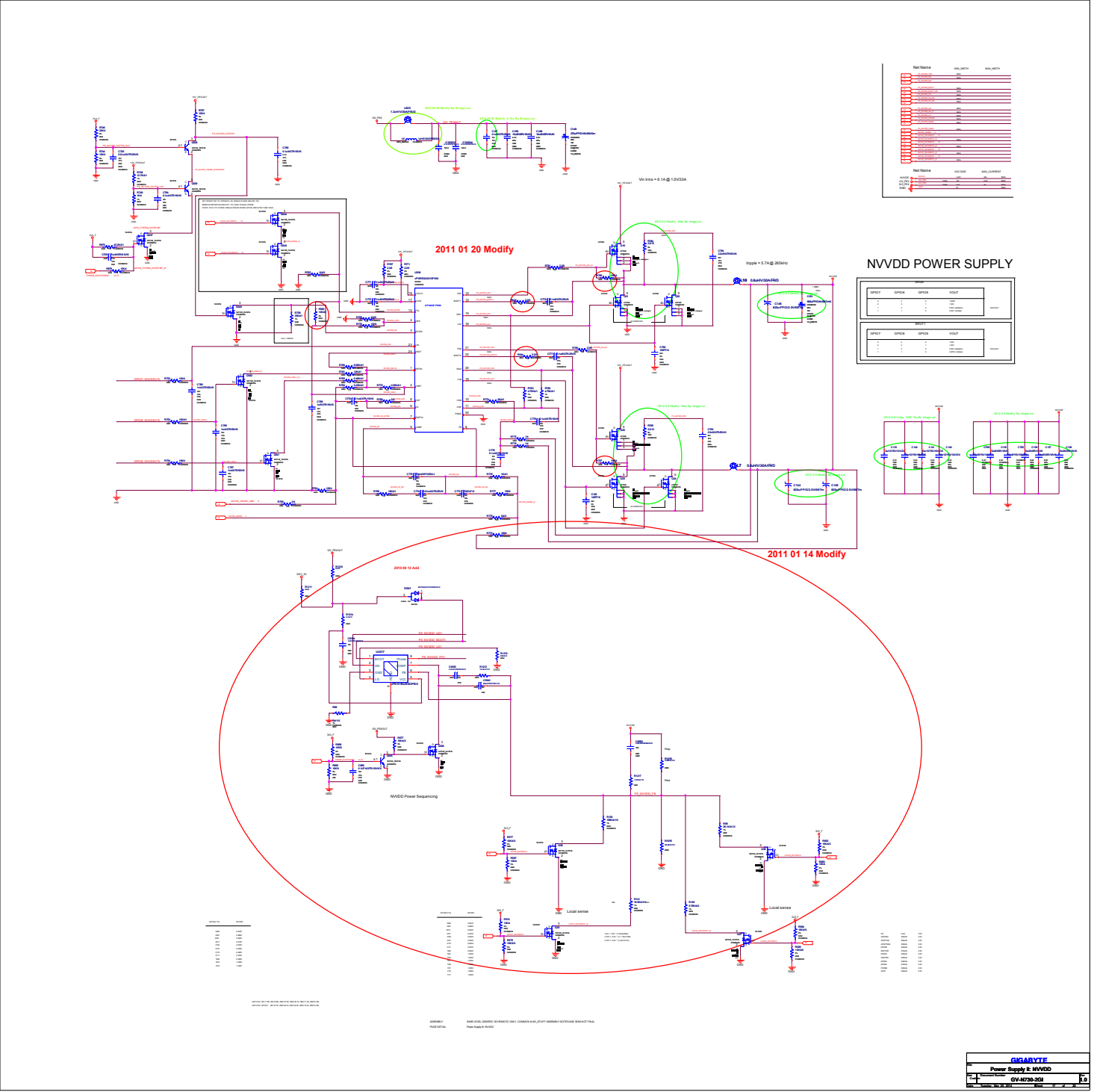
PS	Fosc	Vref
UP6103A	300kHz	0.6V
APW7160	300kHz	0.6V
APW7166A	300kHz	0.6V
RT8105	200kHz	0.6V
NCP1079	270kHz	0.6V
RT8101	300kHz	0.6V
UP6101B	300kHz	0.6V
L6783A	300kHz	0.6V
L6739A	270kHz	0.6V
ITR529	300kHz	0.6V
L6727	300kHz	0.6V

GIGABYTE

Title: Power Supply II: FBVDDQ

Size: Custom | Document Number: GV-N730-2GI | Rev: 1.0

Date: Tuesday, May 20, 2014 | Sheet 16 of 20


NVVDD POWER SUPPLY
2011 01 20 Modify
2011 01 14 Modify
2010 09 13 Add
NVVDD Power Sequencing
Local sense
Local sense
Vin Irms = 6.1A @ 1.0V/3A
Ripple = 5.7A@ 265kHz
GIGABYTE
Power Supply 8: NVVDD
GV-N730-2GI
1.0

GIGABYTE	
Title	Cref Part 1
Size: Custom	Document Number: GV-N730-2GI
Rev	1.0
Date: Tuesday, May 20, 2014	Sheet 18 of 20

GIGABYTE	
Title	Cref Part 2
Size: Custom	Document Number: GV-N730-2GI
Rev: 1.0	
Date: Tuesday, May 20, 2014	Sheet 19 of 20

Title: Cref Part
Report
Design: p1071_a01
Date: Mar 31 14:19:50 2010

BKT1 [14.4F]
C1 [10.2F]
C2 [10.4F]
C3 [11.4F]
C4 [11.4F]
C5 [10.4F]
C6 [10.4E]
C7 [11.3Q]
C8 [8.3C]
C9 [11.3E]
C10 [11.3E]
C11 [11.3E]
C12 [11.3E]
C13 [11.4E]
C14 [11.3E]
C15 [11.3E]
C16 [11.3E]
C17 [11.4E]
C18 [9.3F]
C19 [15.4C]
C20 [15.4C]
C21 [17.3F]
C22 [17.1F]
C23 [15.4B]
C24 [16.2E]
C25 [17.2E]
C26 [17.1F]
C27 [17.1E]
C28 [3.1B]
C29 [16.3G]
C30 [16.3H]
C31 [17.3H]
C32 [17.3H]
C33 [17.3G]
C34 [17.3G]
C35 [16.3F]
C36 [15.4D]
C37 [15.4E]
C38 [3.1A]
C39 [3.1A]
C40 [15.1B]
C41 [13.5D]
C42 [15.4E]
C43 [13.3E]
C44 [15.1C]
C45 [13.5B]
C46 [15.4G]
C47 [15.4H]
C48 [6.4C]
C49 [15.4G]
C50 [3.1G]
C51 [15.4G]
C52 [7.2D]
C53 [6.4G]
C54 [7.3F]
C55 [7.3E]
C56 [7.3D]
C57 [7.2E]
C58 [7.3D]
C59 [7.3D]
C60 [7.3D]
C61 [7.3E]
C62 [6.4H]
C63 [7.3F]
C64 [7.3E]
C65 [6.5C]
C66 [7.2B]
C67 [7.2C]
C68 [7.3C]
C69 [7.3C]
C70 [5.4H]
C71 [7.3B]
C72 [7.3B]
C73 [7.3A]
C74 [7.3B]
C75 [7.2C]
C76 [7.3C]
C77 [7.3C]
C78 [7.3C]
C79 [17.2F]
C80 [17.2F]
C81 [17.2F]
C82 [17.2G]
C83 [14.2D]
C84 [11.2E]
C85 [11.3E]
C86 [11.2F]
C87 [11.4H]
C88 [15.5E]
C89 [15.5F]
C90 [15.5G]
C501 [7.2C]
C502 [7.3B]
C503 [7.3B]
C504 [7.2B]
C505 [7.3B]
C506 [7.3C]
C507 [5.4D]
C508 [5.5C]
C509 [5.4C]
C510 [7.3C]
C511 [7.3B]
C512 [7.2C]
C513 [7.3C]
C514 [7.3A]
C515 [7.2B]
C516 [7.3A]
C517 [7.3C]
C518 [7.2A]
C519 [7.3B]
C520 [7.3C]
C521 [7.3C]
C522 [7.3C]
C523 [7.3B]
C524 [3.5D]
C525 [3.5D]
C526 [3.5D]
C527 [3.5D]
C528 [3.5D]
C529 [3.5D]
C530 [7.3E]
C531 [7.3E]
C532 [7.3E]
C533 [3.4D]
C534 [3.4D]
C535 [7.3F]
C536 [7.3E]
C537 [3.4D]
C538 [3.4D]
C539 [7.3D]
C540 [7.3E]
C541 [3.4D]
C542 [3.4D]
C543 [3.4D]
C544 [3.4D]
C545 [4.1C]
C546 [3.4D]
C547 [4.1D]
C548 [4.1C]
C549 [4.1D]
C550 [7.3D]
C551 [4.1D]
C552 [4.5D]
C553 [4.5C]
C554 [3.4D]
C555 [7.3E]
C556 [4.1C]
C557 [4.5D]
C558 [3.1F]
C559 [4.1D]
C560 [12.3G]
C561 [3.3D]
C562 [3.3D]
C563 [12.3G]
C564 [3.1F]
C565 [3.1F]
C566 [12.4F]
C567 [12.2G]
C568 [12.2G]
C569 [3.1F]
C570 [4.1C]
C571 [12.3G]
C572 [12.3G]
C573 [12.3F]
C574 [12.2F]
C575 [12.2F]
C576 [3.2F]
C577 [3.2F]
C578 [3.3D]
C579 [3.3D]
C580 [3.1F]
C581 [12.3G]
C582 [12.4G]
C583 [17.3G]
C584 [3.2F]
C585 [3.1G]
C586 [12.3F]
C587 [12.2F]
C588 [12.3F]
C589 [12.3F]
C590 [3.1F]
C591 [12.4F]
C592 [4.1D]
C593 [12.2G]
C594 [3.3D]
C595 [3.3D]
C596 [3.4F]
C597 [8.3A]
C598 [12.2G]
C599 [8.3B]
C600 [12.2F]
C601 [3.4F]
C602 [4.1D]
C603 [13.4B]
C604 [3.2F]
C605 [3.2G]
C606 [12.3G]
C607 [12.3F]
C608 [12.3F]
C609 [10.3B]
C610 [3.3F]
C611 [13.4B]
C612 [7.3E]
C613 [7.3E]
C614 [10.3B]
C615 [10.3B]
C616 [13.4B]
C617 [10.3C]
C618 [3.3D]
C619 [3.3D]
C620 [3.3F]
C621 [9.3B]
C622 [10.3B]
C623 [3.2F]
C624 [13.4A]
C625 [3.4G]
C626 [11.3C]
C627 [8.3A]
C628 [9.3A]
C629 [11.3B]
C630 [9.3A]
C631 [11.3B]
C632 [3.4G]
C633 [11.3B]
C634 [11.3B]
C635 [3.3D]
C636 [3.3D]
C637 [11.3B]
C638 [7.2E]
C639 [10.3B]
C640 [7.2E]
C641 [7.2F]
C642 [10.3B]
C643 [7.3D]
C644 [7.3E]
C645 [3.2D]
C646 [3.2D]
C647 [15.1F]
C648 [7.3E]
C649 [3.2D]
C650 [3.2D]
C651 [7.2D]
C652 [15.3F]
C653 [7.3D]
C654 [7.3D]
C655 [7.2D]
C656 [15.2E]
C657 [3.2D]
C658 [3.2D]
C659 [15.3C]
C660 [13.2A]
C661 [15.2C]
C662 [14.2B]
C663 [16.3G]
C664 [17.3G]
C665 [17.3G]
C666 [17.1F]
C667 [17.1F]
C668 [3.1B]
C669 [14.2C]
C670 [13.2F]
C671 [17.4A]
C672 [14.2B]
C673 [9.5E]
C674 [9.5E]
C675 [16.3B]
C676 [14.2E]
C677 [9.4E]
C678 [9.4E]
C679 [16.2D]
C680 [9.4E]
C681 [9.4E]
C682 [16.3D]
C683 [16.3F]
C684 [16.4D]
C685 [16.4C]
C686 [17.3B]
C687 [17.2D]
C688 [17.3D]
C689 [17.3F]
C690 [17.4C]
C691 [8.5E]
C692 [8.4E]
C693 [8.4E]
C694 [17.4G]
C695 [17.4G]
C696 [8.5E]
C697 [8.4E]
C698 [8.4E]
CN1 [3.3C]
D1 [10.4E]
D2 [11.4E]
D3 [17.3F]
D4 [17.3F]
D5 [15.4A]
D6 [16.3E]
D7 [16.3F]
D8 [14.2D]
D501 [14.2G]
D502 [13.1E]
D503 [16.2C]
D504 [17.2C]
F1 [15.4D]
G1 [3.3E]
G1 [4.3F 4.3B]
G1 [8.3C]
G1 [9.3C]
G1 [10.3D]
G1 [11.3D]
G1 [12.3B 12.3E 12.2D 12.4D 12.3H 12.4B]
G1 [13.3C 13.2C 13.4C]
J1 [11.3H]
J2 [10.3Q]
J3 [9.3Q]
J4 [14.2H]
J5 [14.2E]
J6 [13.2F]
J7 [11.4H]
J501 [13.2A]
L1 [17.3F]
L2 [16.3F]
L3 [17.2F]
L4 [16.3F]
L501 [3.4G]
L502 [9.5E]
L503 [9.4E]
L504 [9.3E]
L505 [8.5E]
L506 [8.3E]
L507 [8.4E]
LB1 [15.1C]
LB2 [14.2D]
LB501 [4.5D]
LBS502 [11.3B]
LBS503 [10.3B]
LBS504 [13.4A]
LBS505 [11.2B]
LBS506 [10.2B]
LBS507 [15.1E]
M1 [6.4F 6.5D 6.2D]
M2 [6.4G 6.2B 6.5F]
M3 [6.2F 6.4G 6.5H]
M4 [6.4H 6.2G 6.5G]
M5 [5.2D 5.4F 5.5D]
M6 [5.4G 5.2B 5.5F]
M7 [5.4G 5.5H 5.2G]
M8 [5.4H 5.5G 5.2F]
MEC1 [14.3F]
MEC2 [14.3F]
MEC3 [9.4G]
MEC4 [14.3F]
MEC5 [14.4F]
MEC6 [14.4F]
MEC7 [14.4F]
Q1 [17.4E]
Q2 [17.5E]
Q3 [17.5F]
Q4 [17.4F]
Q5 [11.2F]
Q6 [11.3F]
Q7 [17.3E]
Q8 [17.3E]
Q9 [17.2E]
Q10 [16.3E 16.2E]
Q11 [14.2D]
Q12 [11.2G]
Q13 [11.2H]
Q14 [11.2E]
Q15 [11.3E]
Q501 [15.3G]
Q502 [15.3F]
Q503 [15.3C]
Q504 [15.2C]
Q505 [14.2B]
Q506 [17.4B]
Q507 [16.4B]
Q508 [16.4B]
Q509 [11.5C]
Q510 [17.4B]
R1 [15.4D]
R2 [10.4F]
R3 [11.4F]
R4 [8.1D]
R5 [11.4E]
R6 [17.4E]
R7 [17.4E]
R8 [17.4F]
R9 [17.4F]
R10 [10.4E]
R11 [8.2D]
R12 [10.4E]
R13 [11.2F]
R14 [11.3F]
R15 [11.4E]
R16 [8.2D]
R17 [9.2D]
R18 [8.2D]
R19 [9.2D]
R20 [9.2D]
R21 [8.3D]
R22 [9.3D]
R23 [9.1D]
R24 [8.3D]
R25 [9.3D]
R26 [17.3E]
R27 [11.2E]
R28 [11.3E]
R29 [17.3F]
R30 [15.4A]
R31 [15.4B]
R32 [16.2D]
R33 [16.3E]
R34 [16.3F]
R35 [15.4E]
R36 [14.3B]
R37 [14.3C]
R38 [15.4E]
R39 [14.4B]
R40 [14.3C]
R41 [14.4C]
R42 [14.3B]
R43 [15.4G]
R44 [13.3E]
R45 [15.4G]
R46 [6.4C]
R47 [6.4B]
R48 [6.4B]
R49 [6.4B]
R50 [14.4B]
R51 [14.4C]
R52 [4.4H]
R53 [14.4C]
R54 [14.5C]
R55 [13.4B]
R56 [13.4B]
R57 [10.3C]
R58 [14.4B]
R59 [14.5B]
R60 [6.3D]
R61 [6.4D]
R62 [6.3E]
R63 [6.4H]
R64 [6.3H]
R65 [6.5B]
R66 [6.5B]
R67 [6.5B]
R68 [6.5C]
R69 [5.4H]
R70 [5.3H]
R71 [4.4D]
R72 [5.3C]
R73 [5.3A]
R74 [5.3F]
R75 [5.3E]
R76 [14.2E]
R77 [11.2E]
R78 [11.3E]
R79 [11.2E]
R80 [11.3E]
R81 [11.2F]
R82 [11.3F]
R83 [11.2G]
R84 [11.2G]
R85 [11.2H]
R86 [11.3H]
R501 [4.3D]
R502 [5.5C]
R503 [5.4C]
R504 [4.4D]
R505 [5.5B]
R506 [5.4B]
R507 [5.5B]
R508 [5.4B]
R509 [5.5B]
R510 [5.4B]
R511 [4.3D]
R512 [4.4D]
R513 [4.4H]
R514 [6.3F]
R515 [4.4H]
R516 [3.5F]
R517 [4.5G]
R518 [4.5G]
R519 [4.5G]
R520 [3.5F]
R521 [8.3B]
R522 [6.4D]
R523 [6.4D]
R524 [8.5D]
R525 [9.3B]
R526 [8.3A]
R527 [6.3A]
R528 [9.4D]
R529 [9.4D]
R530 [9.5D]
R531 [11.3C]
R532 [13.5B]
R533 [4.3H]
R534 [15.3G]
R535 [15.3F]
R536 [6.3C]
R537 [4.3H]
R538 [13.2B]
R539 [13.2B]
R540 [13.2B]
R541 [15.3F]
R542 [15.3C]
R543 [15.3C]
R544 [15.3F]
R545 [15.2C]
R546 [15.2C]
R547 [15.2C]
R548 [13.2B]
R549 [17.4D]
R550 [17.5D]
R551 [17.5G]
R552 [17.4G]
R553 [13.2B]
R554 [13.2B]
R555 [17.4D]
R556 [17.5D]
R557 [17.5G]
R558 [17.4G]
R559 [14.2G]
R560 [14.2G]
R561 [13.2E]
R562 [13.1E]
R563 [13.1E]
R564 [13.1E]
R565 [14.2C]
R566 [14.2C]
R567 [14.2D]
R568 [17.4A]
R569 [14.1C]
R570 [17.4B]
R571 [9.5D]
R572 [16.2B]
R573 [9.4D]
R574 [16.2D]
R575 [17.2D]
R576 [16.3G]
R577 [16.4F]
R578 [9.4D]
R579 [16.4D]
R580 [16.4E]
R581 [16.3F]
R582 [16.4B]
R583 [16.4A]
R584 [16.3C]
R585 [17.2D]
R586 [16.4D]
R587 [17.2B]
R588 [17.3C]
R589 [17.3G]
R590 [8.5D]
R591 [8.4D]
R592 [8.4D]
R593 [17.4G]
R594 [11.4C]
R595 [11.4C]
R596 [11.4C]
R597 [11.4C]
R598 [17.4D]
R599 [17.3F]
R600 [17.4E]
R601 [17.4F]
R602 [17.4D]
R603 [11.5C]
R604 [11.5C]
R605 [11.4C]
R606 [11.4C]
TP1 [13.1C]
TP2 [13.2C]
TP3 [4.4G]
TP4 [4.4G]
TP5 [4.4C]
TP6 [4.4C]
U1 [8.2D 8.3D]
U1 [9.3D 9.2D]
U2 [15.4C]
U3 [15.5C]
U4 [15.4F]
U5 [13.3E]
U6 [15.5F]
U501 [15.3E]
U502 [13.1F]
U503 [14.2C]
U504 [16.3C]
U505 [17.3C]
Y1 [13.5C]
Y2 [13.5C]

ASSEMBLY
PAGE DETAIL

BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
<edit here to insert page detail>

GIGABYTE

Title: Cref Part 3

Size: Custom | Document Number: GV-N730-2GI | Rev: 1.0

Date: Tuesday, May 20, 2014 | Sheet 20 of 20