

8884

VER:00A

GF4-Ti 4200, 4MX16 / 8MX16 DDR, DUAL-VGA, S-IN/OUT, AV-IN/OUT, CATV-IN, AGP4X

PCI DEVICE ID 0X0=0X253 FOR NV25.

Title	Section	Page
Cover Sheet	0.a	1
AGP Interface	1.a	2
NV25 Frame Buffer Intefrace	2.a	3
Frame Buffer 0..31	2.b	4
Frame Buffer 32..63	2.c	5
Frame Buffer 64..95	2.d	6
Frame Buffer 96..128	2.e	7
DVO BUS	3.a	8
SAA7108 __ VIVO	3.b	9
NV25 DAC A/B	4.a	10
DAC-B __ PRI-VGA	4.b	11
DAC-A __ SEC-VGA	4.c	12
Power Solution	5.a	13
NV25 Strap / P-S BIOS	6.a	14

Sub Assemblies	Description
COMMON	Components are stuffed for all SKUs
NO_STUFF	Components are not stuffed
PRI_DVI_I	Components stuffed for Primary VGA
PRI_PROT	Components stuffed for Primary Protection
SEC_DVI_I	Components stuffed for Secondary DVI
SEC_PROT	Components stuffed for Secondary Protection
AGP3VFBdq	Components stuffed for AGP 3.3V to FBVDD
FBVDD-FBDQ	Components stuffed for FBVDDQ 2.5V to FBVDD
PLL-SEQ	Components stuffed for PLL Sequence
PWR-SEQ	Components stuffed for Power Sequence
PWR-SEQ-BP	Components stuffed for Power Sequence By-PASS
NVVDD-2602	Components stuffed for NVVDD Power
FBVDD-2602	Components stuffed for FBVDD Power
SC1541	Components stuffed for 3.3VL Fixed
SC1565	Components stuffed for SC1565 - 3.3VL Adj.
FB64LO	Components stuffed for FBA_DATA and FBD_DATA
FB64UP	Components stuffed for FBC_DATA and FBD_DATA
SER-PROM	Components stuffed for Serial PROM
PAR-PROM	Components stuffed for Parallel PROM
FAN_HS-LFT	Components stuffed for Fan / Blower left of the GPU
FAN_HS-RGT	Components stuffed for Fan / Blower east of the GPU
FAN_SNK	Fan Sink Component
HEAT_SNK	Heat Sink Component
VIVO	Components stuffed for Video IN/OUT
VIDO-7104	SAA7104 Video Out
CX-ENC-TVOUT	Components stuffed for CX25871 - TV-OUT
SOCKET	Components stuffed for Socket
64MB-4MX16	Components stuffed for Mem-CFG: 64MB (4Mx16) memory
BRACKET	IO Bracket / VGA-DIN-DVI-I
BRACKET-VGA	IO Bracket / VGA- -DVI-I
FBVDDQ-2602	Components stuffed for FBVDDQ Power
FBVDD_ADJ	Components to adjust FBVVD out voltage.
FBVDQ_ADJ	Components to adjust FBVVDQ out voltage.

NVVDD : 1.641 to 1.690

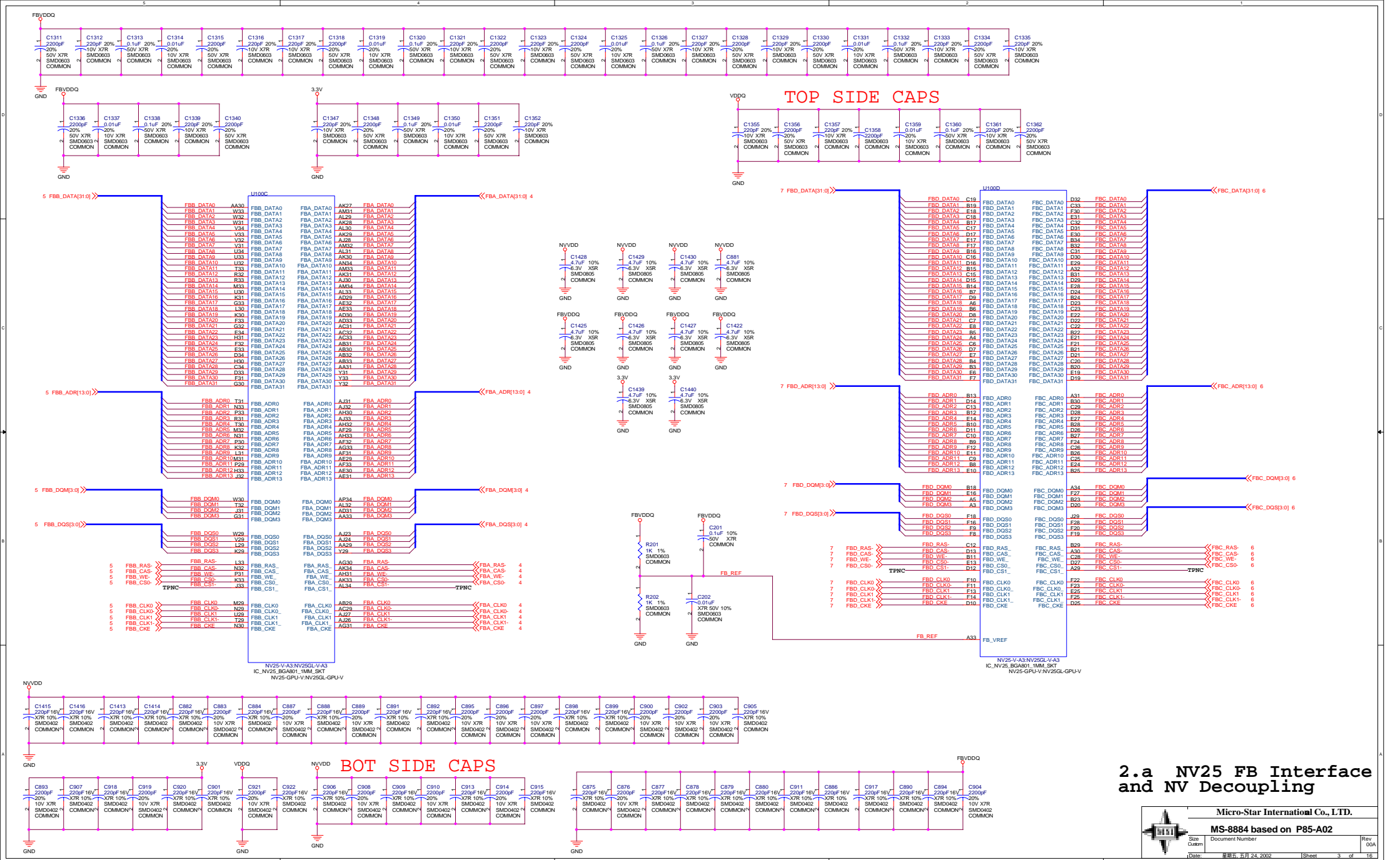
FBVDD : 3.19 to 3.34

FBVDDQ : 2.54 to 2.65

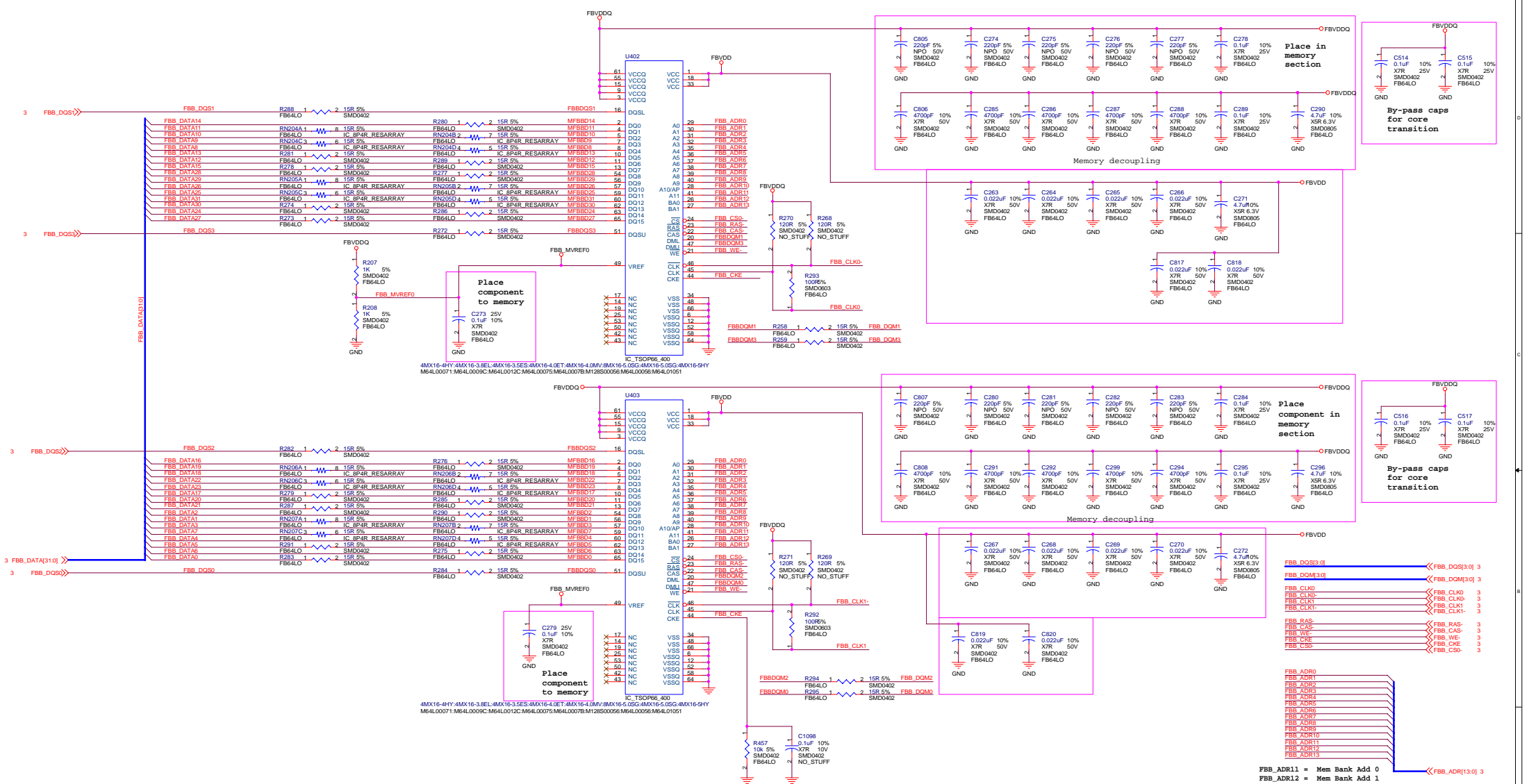
0.a

Cover Page










## 2.c Frame Buffer 32..63

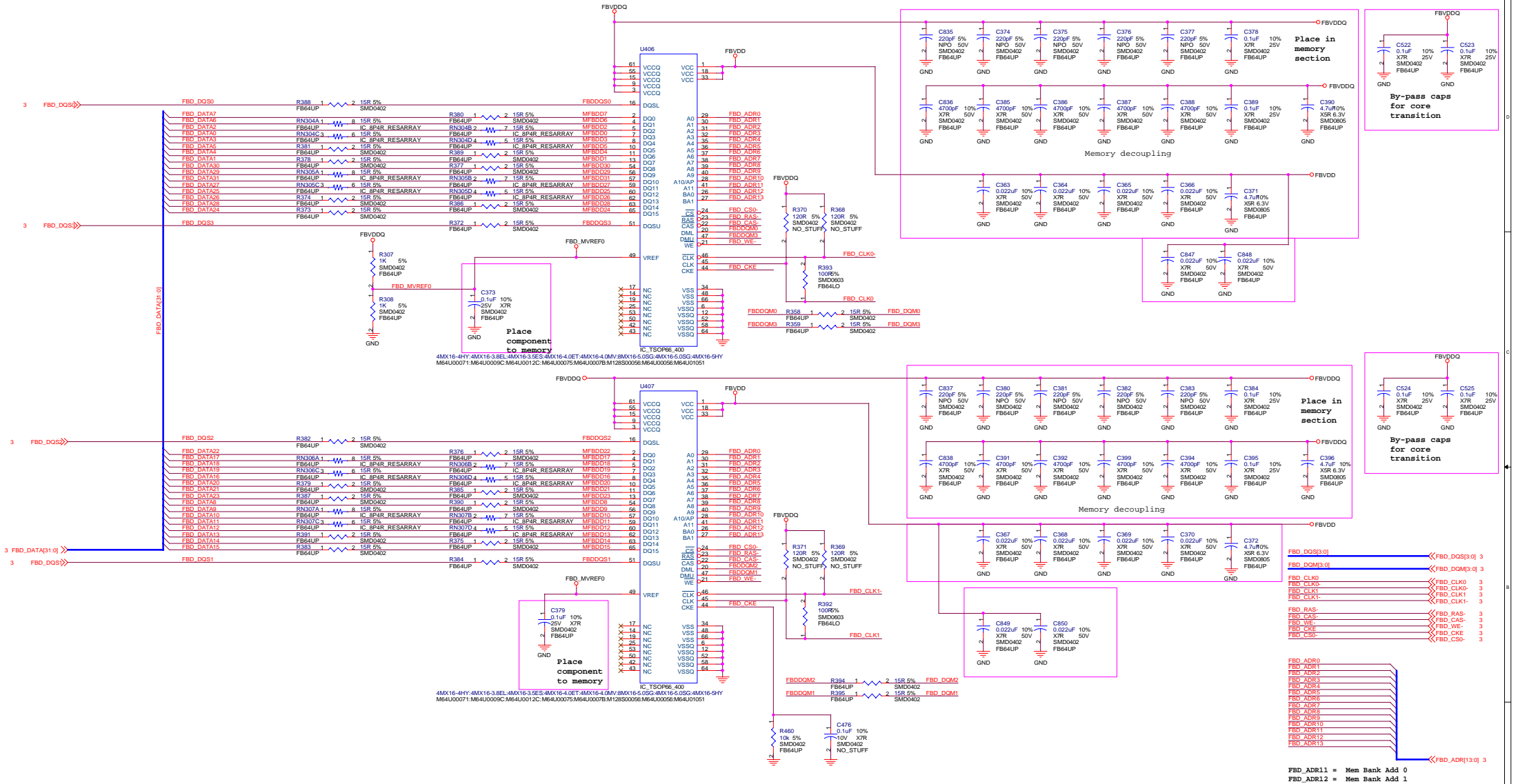


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Size	Document Number	Rev
Custom		00A
Date	2002.05.25	Sheet 5 of 16





2.e Frame Buffer 96..127



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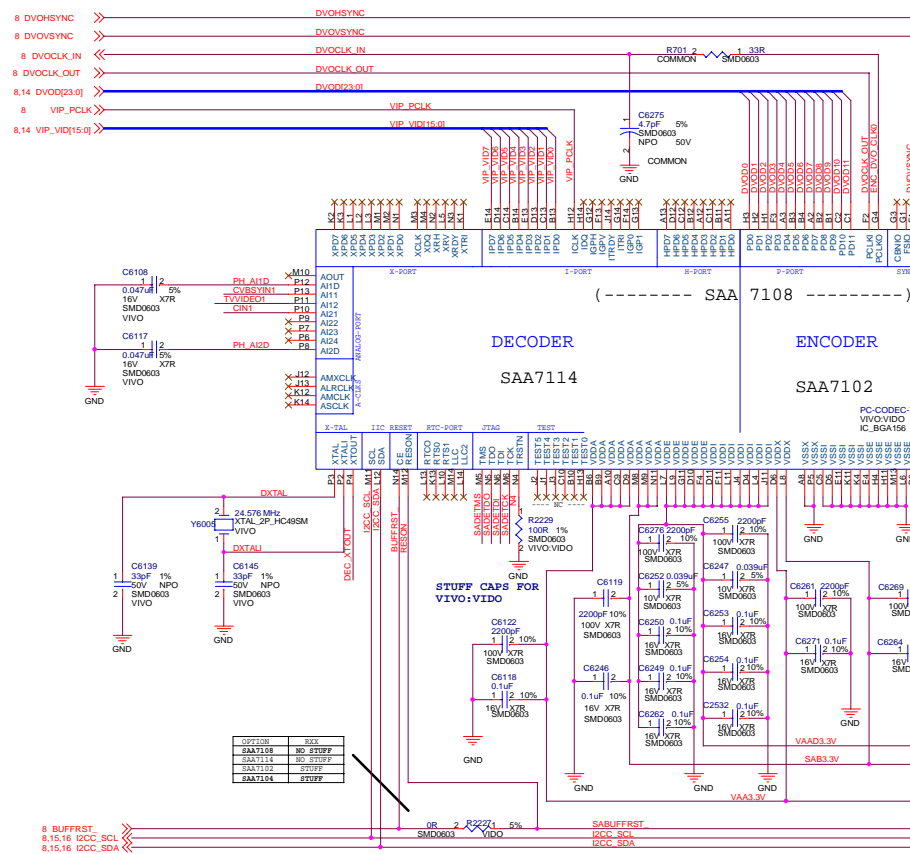
MS-8884 based on P85-A02

Size	Document Number	Rev
Custom		00A
Date:	2002.05.24	Sheet 7 of 16



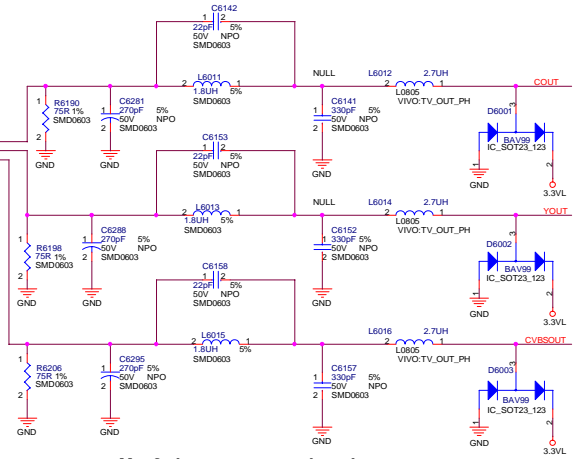






NETS SHOULD BE 10  
mils width.  
C2507 and C2508  
close to Y201  
CX-ENC-TVOUT:VIVO:VIDEO

#### CX-ENC-TVOUT:VIVO:VIDEO



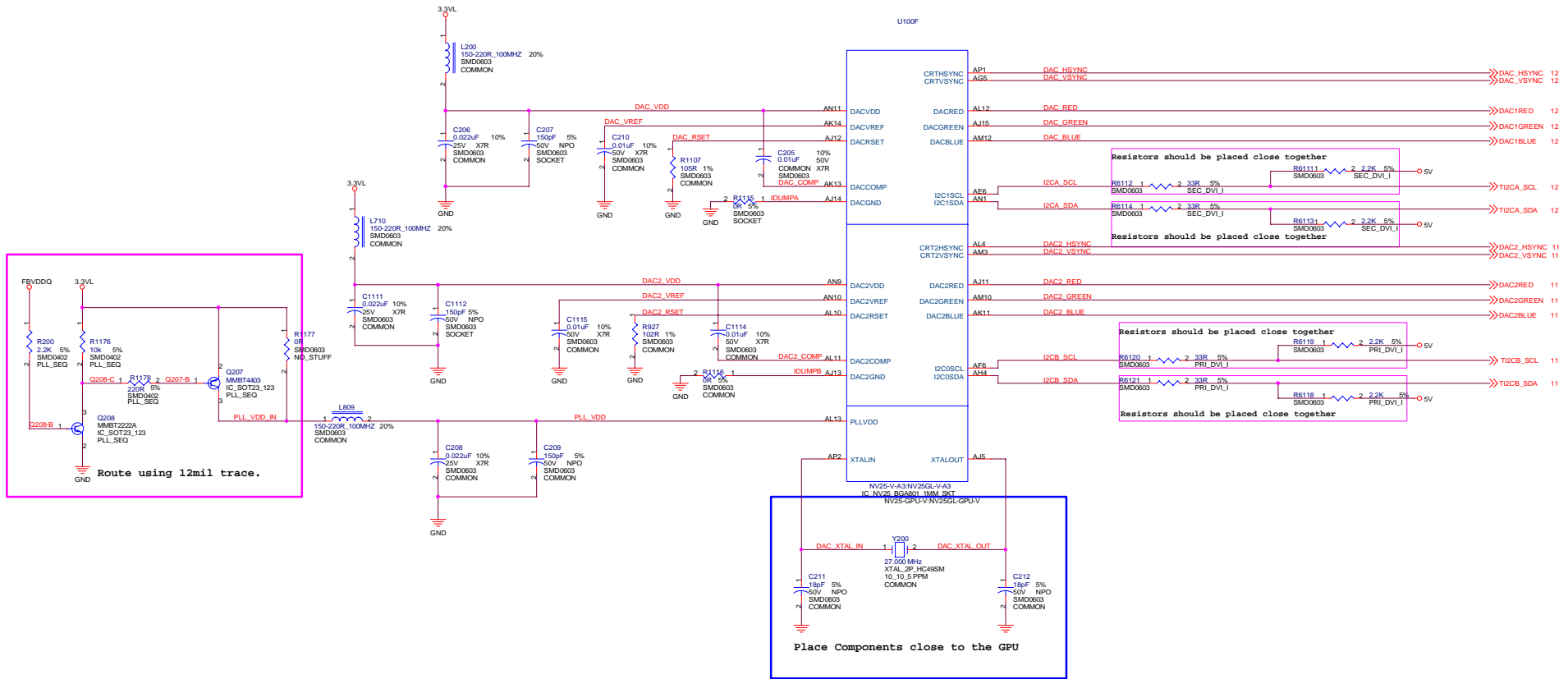
All of these components have been  
designed with the following Assembly:  
CX-ENC-TVOUT:VIVO:VIDEO

#### X-Components

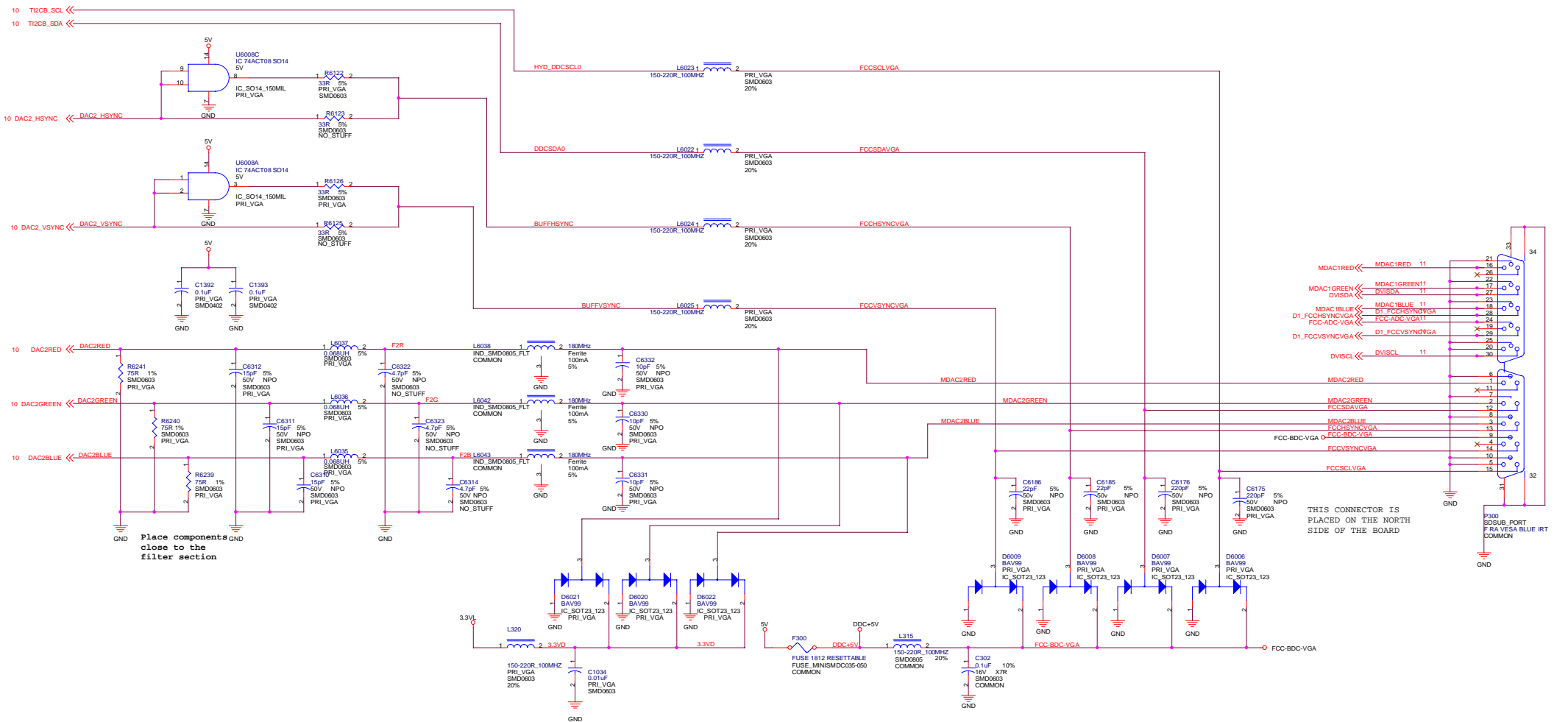
- Place resistor above VGA connector
- Place resistor between the VGA and S-Video connectors
- Place resistor below S-Video and VGA connectors
- Place resistor below the VGA connector

### 3.b SAA7108 - VIVO

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MS-8884 based on P85-A02			
Size	Document Number	Rev	
Custom		00A	
Date	日期	Sheet	9 of 16



#### 4.a NV25 .. PLL / DAC / I2C

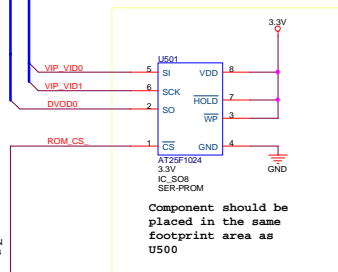
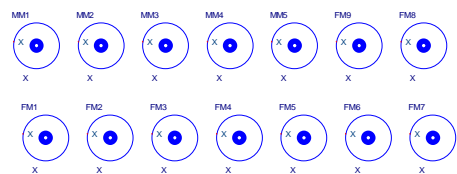
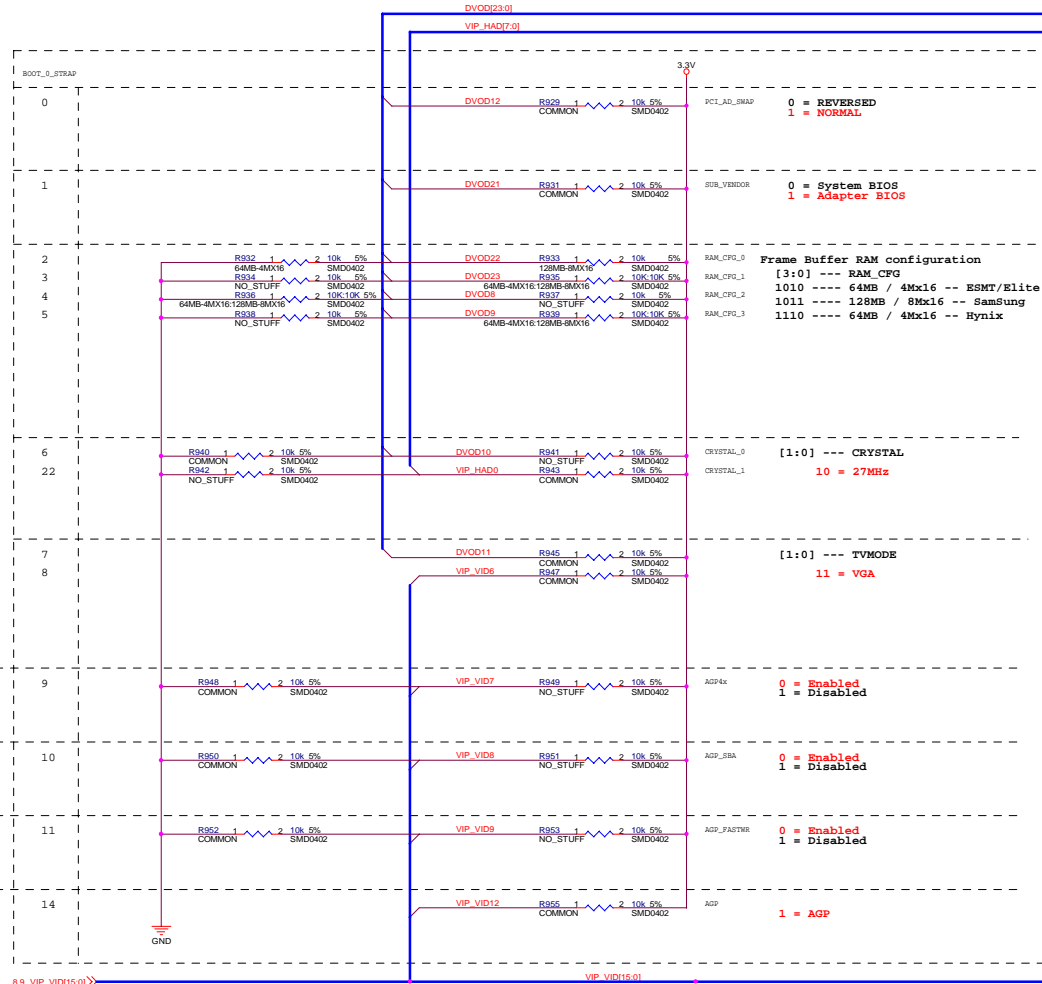


## 4.b DACB I/O, I2C0 -- SEC

Micro-Star International Co., LTD.		Rev
MS-8884 based on P85-A02		00A
Size	Document Number	Rev
Custom		00A
Date: 2002.05.25	Sheet: 11	of 16

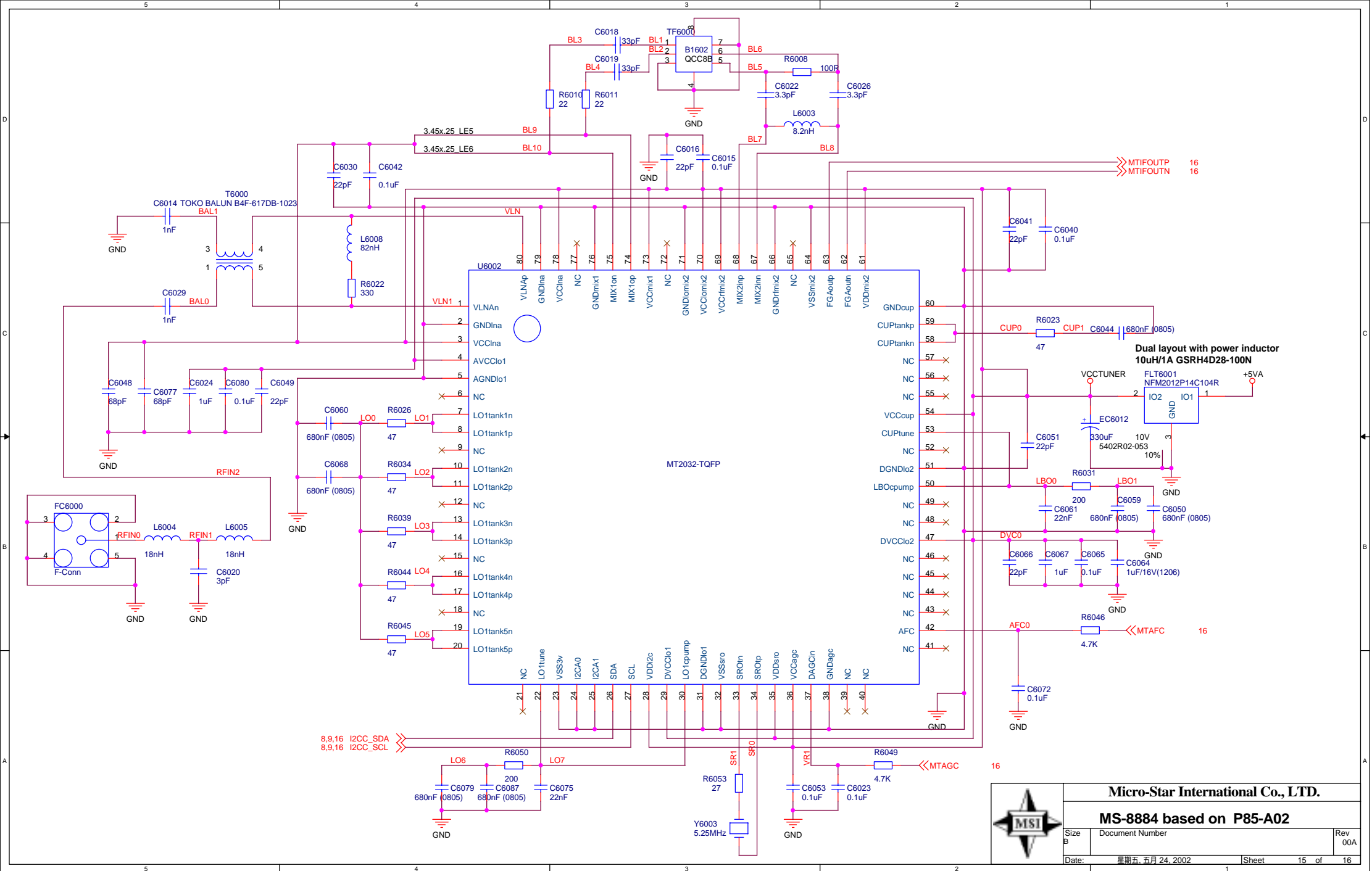






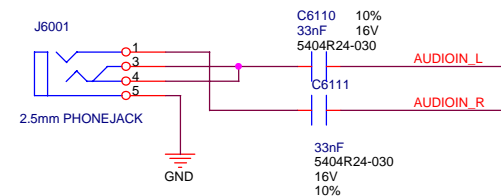
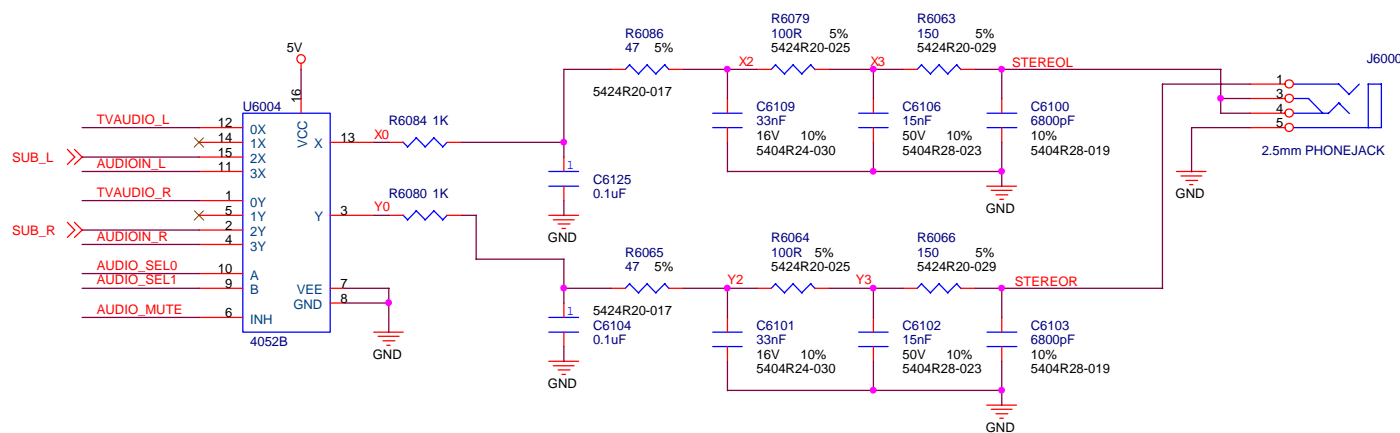
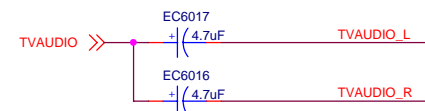
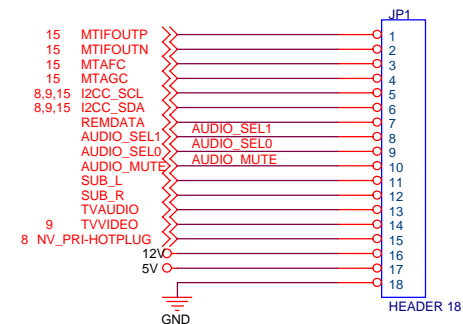
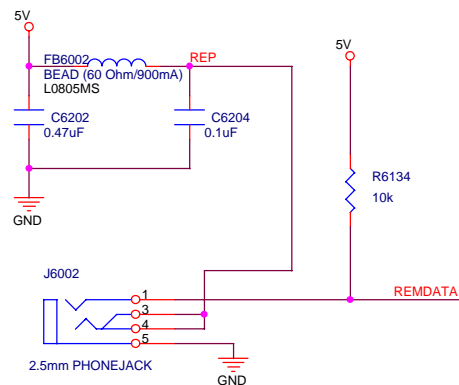
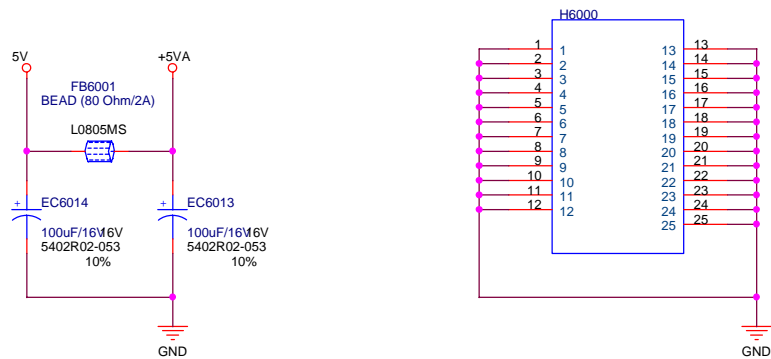
## 6.a STRAP - P-ROM

Micro-Star International Co., LTD.			
MS-8884 based on P85-A02			
Size	Document Number	Rev	00A
Custom			
Date	2002.05.25	Sheet	14 of 16



Micro-Star International Co., LTD.		
MS-8884 based on P85-A02		
Size B	Document Number	Rev 00A
Date: 星期五, 五月 24, 2002	Sheet 15 of 16	





Micro-Star International Co., LTD.			
MS-8884 based on P85-A02			
Size B	Document Number	Rev 00A	
Date:	星期五, 五月 24, 2002	Sheet 16 of 16	