

I2C Configuration Table

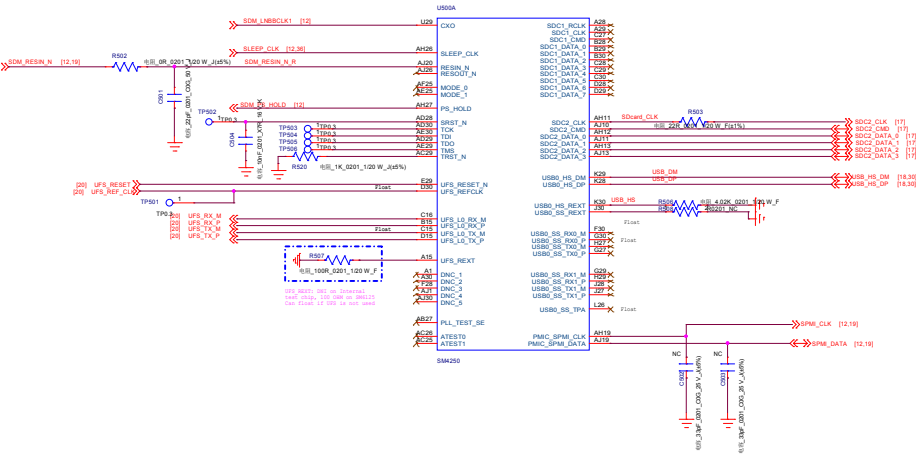
|                          |                        |
|--------------------------|------------------------|
|                          |                        |
| GPIO4<br>GPIO5           | NFC/PM8008/SMB1355 I2C |
| GPIO22<br>GPIO23         | SUB_CAM_I2C (虚化)       |
| GPIO29<br>GPIO30         | MAIN/FRONT/微距_CAM_I2C  |
| GPIO96<br>GPIO97         | SPK_PA I2C             |
| GPIO109<br>GPIO110       | PL Sensor I2C          |
| LPI GPIO21<br>LPI GPIO22 | G/M Sensor I2C         |

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| 10.   | SM4250 Power (Other, LP4X)   | 33:   | RF_WTR3925_RF_PRX     |
| 11.   | SM4250 Ground                | 34:   | RF_WTR3925_RF_DRX     |
| 12.   | PM4250 CLKs/Control_GND      | 35:   | RF_WIFI_BT_GPS        |
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| 22.   | Audio_CODEC WCD9370          |       |                       |
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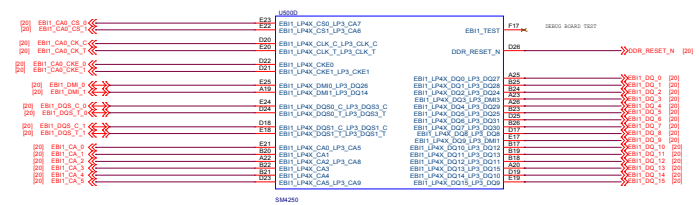
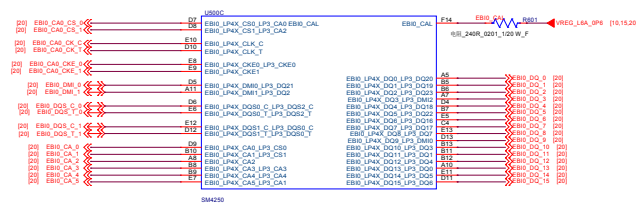
| GPIO MAP      SM4250 GPIO Configuration |                |         |                     |             |                         |
|---|----------------|---------|---------------------|-------------|-------------------------|
| GPIO_0                                  | NFC_SPI_MISO   | GPIO_42 |                     | GPIO_84     |                         |
| GPIO_1                                  | NFC_SPI_MOSI   | GPIO_43 | GRFC_6              | GPIO_85     | LCM_RST                 |
| GPIO_2                                  | NFC_SPI_CLK    | GPIO_44 | GRFC_7              | GPIO_86     | NFC_CLK_REQ             |
| GPIO_3                                  | NFC_SPI_CS1_N  | GPIO_45 | GRFC_8              | GPIO_87     | WCN_SW_CTRL             |
| GPIO_4                                  | APPS_I2C_SDA   | GPIO_46 | GRFC_9              | GPIO_88     | SD_DET                  |
| GPIO_5                                  | APPS_I2C_SCL   | GPIO_47 | GRFC_10             | GPIO_89     | Modem_ID3               |
| GPIO_6                                  | TP_SPI_MISO    | GPIO_48 |                     | GPIO_90     | MSS_LTE_COXM_TXD        |
| GPIO_7                                  | TP_SPI_MOSI    | GPIO_49 | GRFC_12             | GPIO_91     | MSS_LTE_COXM_RXD        |
| GPIO_8                                  | WCN_UART_CTS_N | GPIO_50 |                     | GPIO_92     | WCD9370_RST_N           |
| GPIO_9                                  | WCN_UART_RFR_N | GPIO_51 |                     | GPIO_93     | FP_POWER_EN             |
| GPIO_10                                 | WCN_UART_TX_N  | GPIO_52 |                     | GPIO_94     | FORCED_USB_BOOT_POL_SEL |
| GPIO_11                                 | WCN_UART_RX_N  | GPIO_53 | RFFE1_DATA          | GPIO_95     | FORCED_USB_BOOT         |
| GPIO_12                                 | MSM_UART_TX    | GPIO_54 | RFFE1_CLK           | GPIO_96     | PA_I2C_SDA              |
| GPIO_13                                 | MSM_UART_RX    | GPIO_55 |                     | GPIO_97     | PA_I2C_SCL              |
| GPIO_14                                 | FP_SPI_MISO    | GPIO_56 |                     | GPIO_98     | PA_SPK_RST              |
| GPIO_15                                 | FP_SPI_MOSI    | GPIO_57 | RFFE3_DATA          | GPIO_99     | LCD_ID0                 |
| GPIO_16                                 | FP_SPI_CLK     | GPIO_58 | RFFE3_CLK           | GPIO_100    | LCD_ID1                 |
| GPIO_17                                 | FP_SPI_CS_N    | GPIO_59 |                     | GPIO_101    | LCD_ID2                 |
| GPIO_18                                 | CAM_3_PDN      | GPIO_60 |                     | GPIO_102    | USB_SS_SEL              |
| GPIO_19                                 | CAM_F_RST      | GPIO_61 |                     | GPIO_103    | CTP_RST                 |
| GPIO_20                                 | CAM_R_MCLK     | GPIO_62 | RFFE5_DATA          | GPIO_104    | CTP_EINT                |
| GPIO_21                                 | CAM_F_MCLK     | GPIO_63 | RFFE5_CLK           | GPIO_105    | SMB_STAT                |
| GPIO_22                                 | CAM_I2C_SDA0   | GPIO_64 | EXT_GPS_LNA_EN0     | GPIO_106    | ALS_EINT                |
| GPIO_23                                 | CAM_I2C_SCL0   | GPIO_65 | CH0_GSM_TX_PHASE_D0 | GPIO_107    | PA_RSV_RST              |
| GPIO_24                                 | CAM_2_RST      | GPIO_66 | CAM_R_RST           | GPIO_108    | DSI_TE                  |
| GPIO_25                                 | PM8008_INT_N   | GPIO_67 | NC                  | GPIO_109    | Sensor_I2C_SDA          |
| GPIO_26                                 | PM8008_RST_N   | GPIO_68 | Modem_ID1           | GPIO_110    | Sensor_I2C_SCL          |
| GPIO_27                                 | CAM_MCLK_2     | GPIO_69 | SUB_ID              | GPIO_111    | CONFIG0                 |
| GPIO_28                                 | CAM_MCLK_3     | GPIO_70 | NFC_ENABLE          | GPIO_112    | CONFIG1                 |
| GPIO_29                                 | CAM_I2C_SDA1   | GPIO_71 | NFC_INT_N           | LPI_GPIO_0  | SWR_TX_CLK              |
| GPIO_30                                 | CAM_I2C_SCL1   | GPIO_72 | TP_SPI_SCLK         | LPI_GPIO_1  | SWR_TX_DATA0            |
| GPIO_31                                 | NFC_DWL_REQ    | GPIO_73 | UIM2_DATA           | LPI_GPIO_2  | SWR_TX_DATA1            |
| GPIO_32                                 | FP_ID1         | GPIO_74 | UIM2_CLK            | LPI_GPIO_3  | SWR_RX_CLK              |
| GPIO_33                                 | FP_ID2         | GPIO_75 | UIM2_RESET          | LPI_GPIO_4  | SWR_RX_DATA0            |
| GPIO_34                                 | FP_EINT        | GPIO_76 | UIM2_DET            | LPI_GPIO_5  | SWR_RX_DATA1            |
| GPIO_35                                 | FP_RST         | GPIO_77 | UIM1_DATA           | LPI_GPIO_14 | QCA_SB_CLK              |
| GPIO_36                                 | G_EINT_ACC     | GPIO_78 | UIM1_CLK            | LPI_GPIO_15 | QCA_SB_DATA             |
| GPIO_37                                 | GRFC_0         | GPIO_79 | UIM1_RESET          | LPI_GPIO_21 | Sensor_I3C_SDA          |
| GPIO_38                                 | GRFC_1         | GPIO_80 | UIM1_DET            | LPI_GPIO_22 | Sensor_I3C_SCL          |
| GPIO_39                                 | GRFC_2         | GPIO_81 | TP_SPI_CS_N         | LPI_GPIO_25 |                         |
| GPIO_40                                 | ANT_TEST       | GPIO_82 | Modem_ID2           | LPI_GPIO_26 |                         |
| GPIO_41                                 |                | GPIO_83 | Modem_ID4           |             |                         |

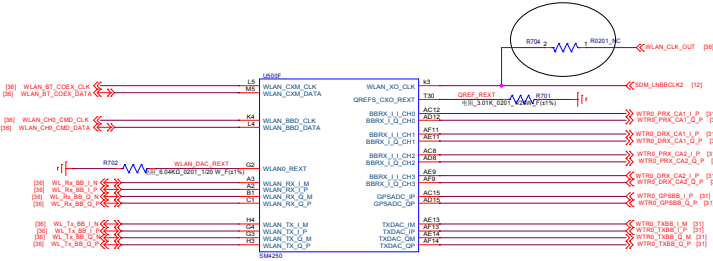
| PM4250 GPIO Configuration |               |        |              |
|---------------------------|---------------|--------|--------------|
| GPIO_1                    | DSP_CLK       | GPIO_8 | USB_MOS_CTRL |
| GPIO_2                    | FLASH_THERMAL | GPIO_9 |              |
| GPIO_3                    | USB1_THERM    |        |              |
| GPIO_4                    | USB2_THERM    |        |              |
| GPIO_5                    | KEY_VOL_UP    |        |              |
| GPIO_6                    | BB_THERM      |        |              |
| GPIO_7                    | Modem_ID5     |        |              |

| PMI632 GPIO Configuration |              |        |            |
|---------------------------|--------------|--------|------------|
| GPIO_1                    | NC           | GPIO_8 | ISNS_SMB_M |
| GPIO_2                    | STAT_CHG     |        |            |
| GPIO_3                    | PMI632_THERM |        |            |
| GPIO_4                    |              |        |            |
| GPIO_5                    |              |        |            |
| GPIO_6                    |              |        |            |
| GPIO_7                    | ISNS_SMB_P   |        |            |

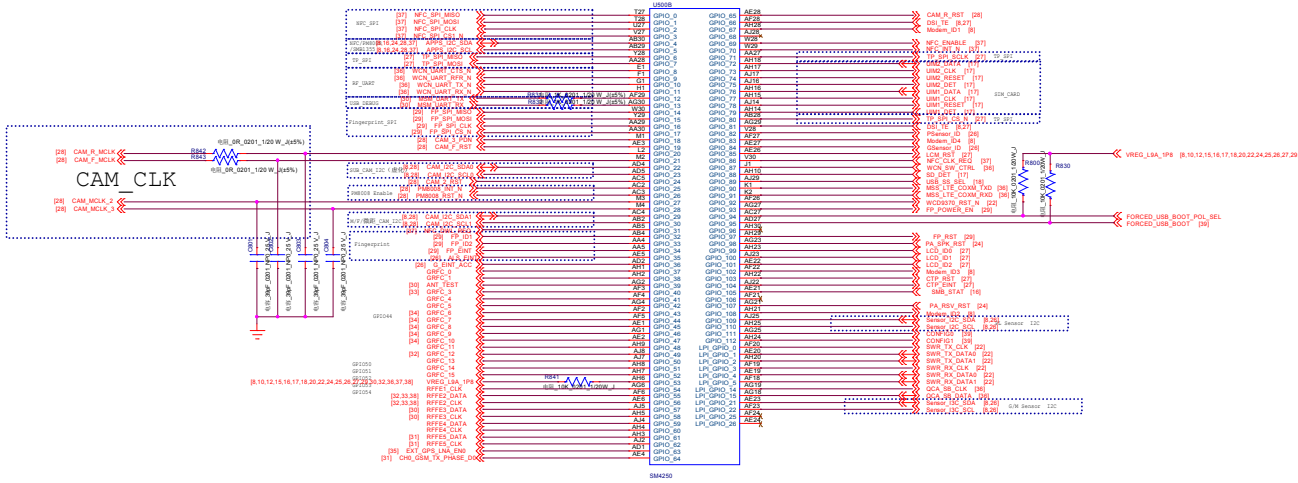


|   |                |  |
|---|----------------|--|
| Company: Hastein                          |                |  |
| Block Title: SDMS30 Control/UF5/SDC1/SDC2 |                |  |
| Project: T10_I-Shape_SMB115_MB_V1         | Rev: 1.0       |  |
| Date: Tuesday, June 30, 2020              | Sheet: 5 of 39 |  |





BOOT Config GPIOs

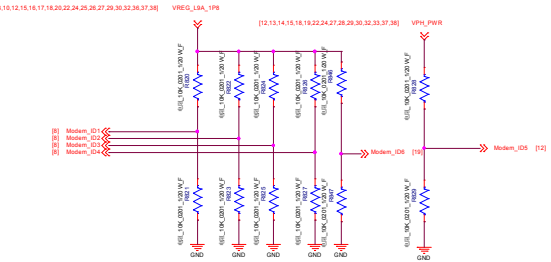


| BOOT Config GPIOs      | Functionality           |
|------------------------|-------------------------|
| GPIO_95                | Forced_USB_BOOT         |
| BOOT Config[0]:GPIO_48 | WDOG_DISABLE            |
| BOOT Config[1]:GPIO_50 | FASTBOOT_SEL[0]         |
| BOOT Config[2]:GPIO_51 | FASTBOOT_SEL[1]         |
| BOOT Config[3]:GPIO_53 | FASTBOOT_SEL[2]         |
| BOOT Config[4]:GPIO_55 | FASTBOOT_SEL[3]         |
| GPIO_94                | Forced_USB_BOOT_POL_SEL |

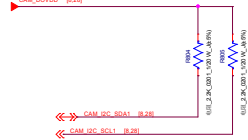
| FASTBOOT_SEL[3:0] | BOOT OPTIONS                                |
|-------------------|---|
| 0b0000            | 0:Default (eMMC>SD>eDL (eDL path-USB only)) |
| 0b0001            | 1:SD>eMMC>eDL (eDL path-USB only))          |
| 0b0010            | 2:SD>eDL (eDL path-USB only)                |
| 0b0011            | 3:USB>eDL (eDL path-SD then USB)            |
| 0b0100            | 5:UFS HS G1>SD>eDL (eDL path-USB only))     |
| 0b0101            | 4:SD>UFS HS G1>eDL (eDL path-USB only))     |
| 0b1100            | 6:UFS HS G1>eDL (eDL path-USB only))        |
| 0b0111            | 7:eMMC>SD>eDL (eDL path-USB only))          |
| 0b1000            | 8:eMMC>eDL (eDL path-USB only))             |

I2C pull up

Hardware ID



Camera0 I2C



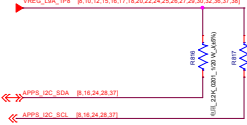
Camera1 I2C



sensor I2C

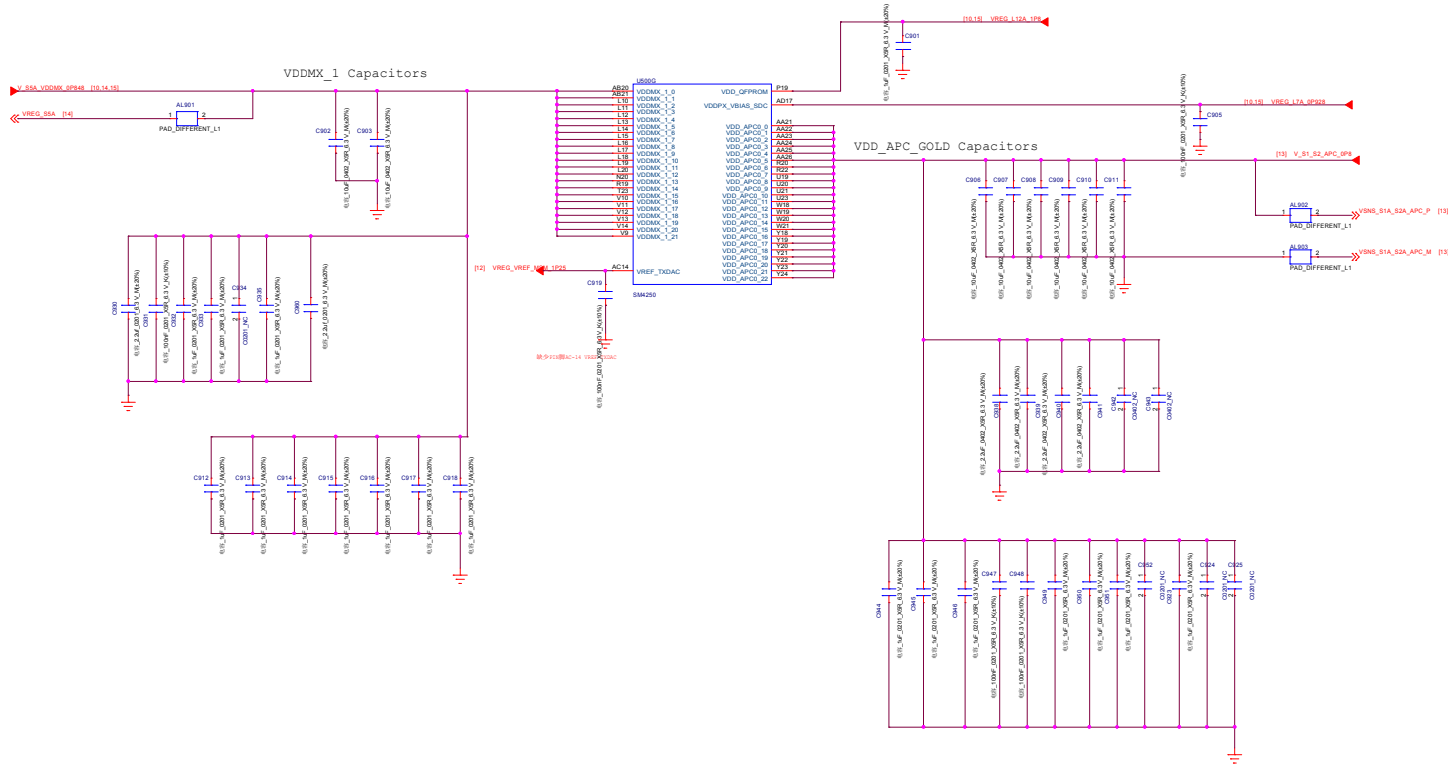


sensor I2C

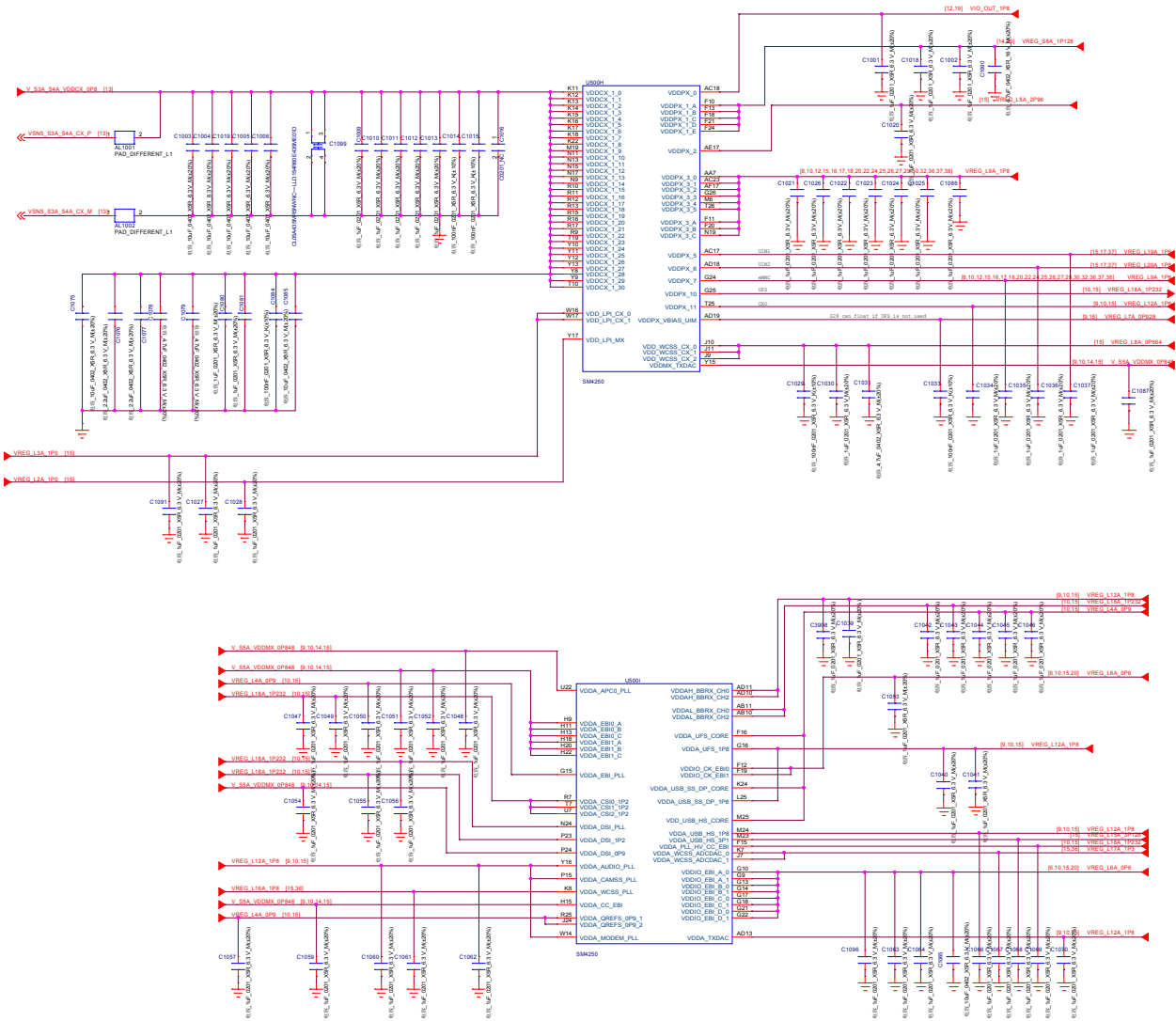


sensor I2C

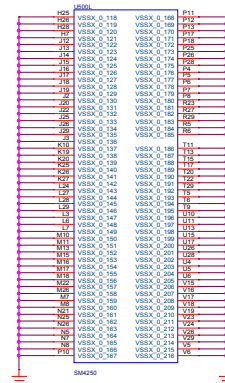
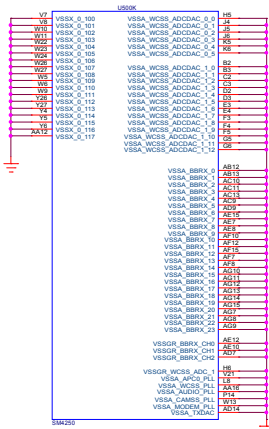
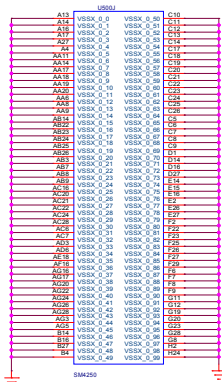


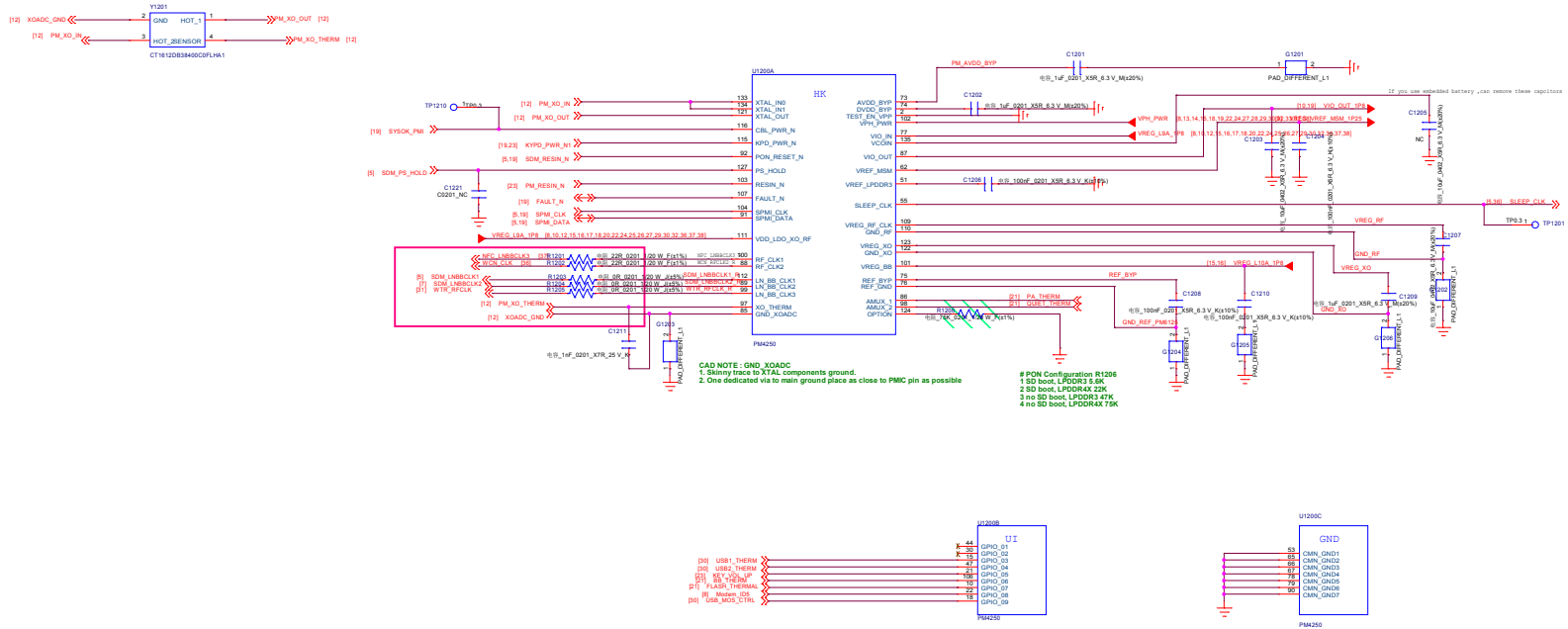


SM6125 Power (Other, LP4X)  
REF 1001-1099



|                 |                                       |
|-----------------|---------------------------------------|
| Company: Hualin |                                       |
| Block Title     | SDM830 Power (Other, LP4X)            |
| Project         | T10_I-Shape_SM6115_MBA/V1 1.0         |
| Date:           | Tuesday, June 30, 2020 Sheet 10 of 39 |



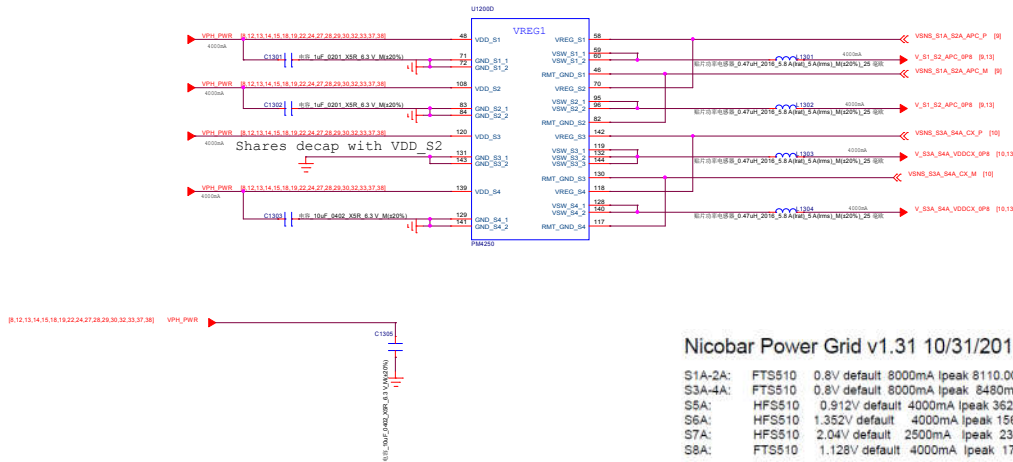


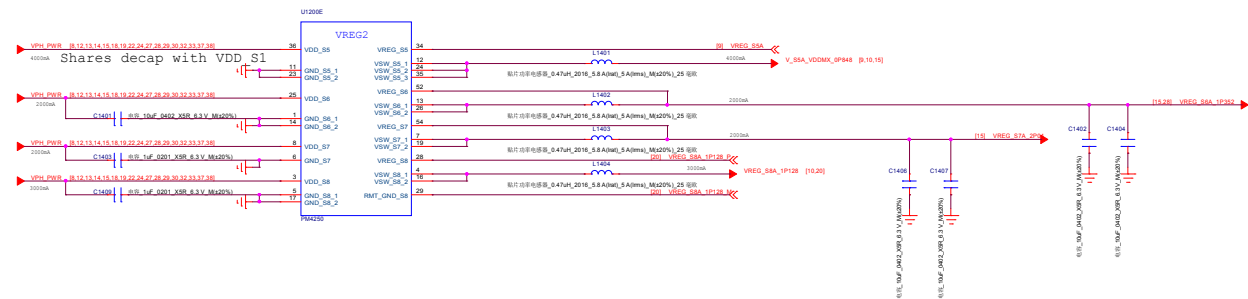
| Option Pin Resistor Value | Configuration                                      |
|---------------------------|--|
| 5.6K                      | No External Boost Bypass, SD Card Boot, LPDDR3     |
| 22K                       | No External Boost Bypass, SD Card Boot, LPDDR4x    |
| 47K                       | No External Boost Bypass, No SD Card Boot, LPDDR3  |
| 75K                       | No External Boost Bypass, No SD Card Boot, LPDDR4x |
| 130K                      | External Boost Bypass, SD Card Boot, LPDDR3        |
| 240K                      | External Boost Bypass, SD Card Boot, LPDDR4x       |
| 330K                      | External Boost Bypass, No SD Card Boot, LPDDR3     |
| 750K                      | External Boost Bypass, No SD Card Boot, LPDDR4x    |

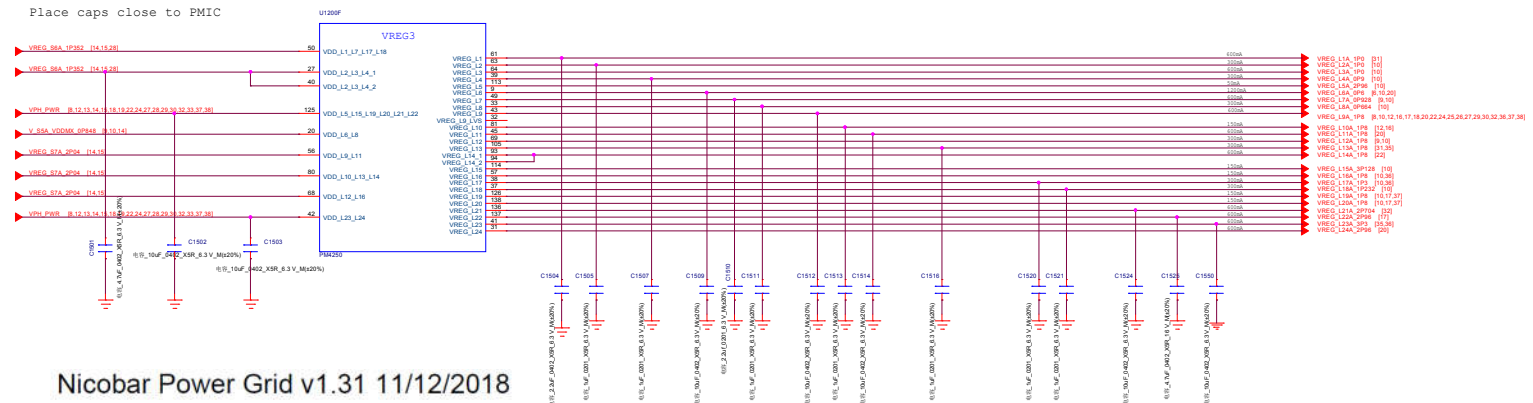
Note: SD Card Boot implies the voltages for SD card will be enabled during Power On sequence  
No SD Card Boot implies the voltages for SD card will be disabled during Power On sequence

| Option Pin Resistor Value | Configuration                                      |
|---------------------------|--|
| 5.6K                      | No External Boost Bypass, SD Card Boot, LPDDR3     |
| 22K                       | No External Boost Bypass, SD Card Boot, LPDDR4x    |
| 47K                       | No External Boost Bypass, No SD Card Boot, LPDDR3  |
| 75K                       | No External Boost Bypass, No SD Card Boot, LPDDR4x |
| 130K                      | External Boost Bypass, SD Card Boot, LPDDR3        |
| 240K                      | External Boost Bypass, SD Card Boot, LPDDR4x       |
| 330K                      | External Boost Bypass, No SD Card Boot, LPDDR3     |
| 750K                      | External Boost Bypass, No SD Card Boot, LPDDR4x    |

Note: SD Card Boot implies the voltages for SD card will be enabled during Power On sequence  
No SD Card Boot implies the voltages for SD card will be disabled during Power On sequence







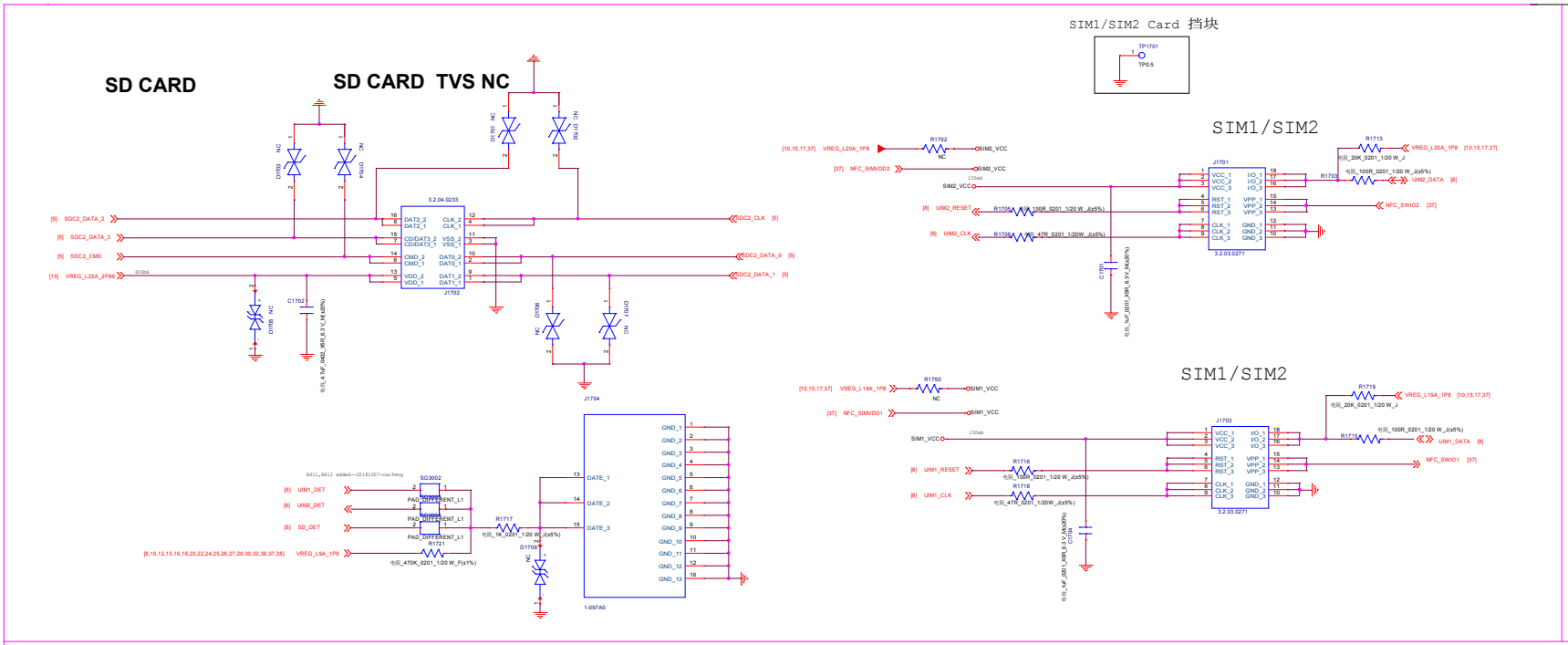
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LDO Type Source V_default (mV) Target PmW
L1 N600    SNA 1.2V (625) GLN SDR 1.2 61
L2 N300    SNA 1V (370) SDR VDD 1.0V 64
L3 N600    SNA 0.925V (126) QUMC EBI, MIFI 39
L5 MYP50    VPH 2.96V (22) PX 113
L6 N1200    SNA 0.624V (104) VDDQ, EBI 9
L7 N600    SNA 0.926V (54) USB 49
L8 N600    SNA 0.641V (81) CX 33
L9 N150    SNA 1.8V (754) Sensors, WCD, TMD, PMI, MSM, QET 43
L10 LVP150  SNA 1.8V (600) EPPROM, USB, UFS, PX 61
L11 LVP600  SNA 1.8V (102) QEMMC/VCCQ 45
L12 LVP300  SNA 1.8V (121) (0V) USB, REDRIVER 69
L13 LVP300  SNA 1.8V (204) VDDQ, VDD, VDD2, VDD3, VDD4, VDD5, VDD6, VDD7, VDD8, VDD9, VDD10, VDD11, VDD12, VDD13, VDD14, VDD15, VDD16, VDD17, VDD18, VDD19, VDD20, VDD21, VDD22, VDD23, VDD24, VDD25, VDD26, VDD27, VDD28, VDD29, VDD30, VDD31, VDD32, VDD33, VDD34, VDD35, VDD36, VDD37, VDD38, VDD39, VDD40, VDD41, VDD42, VDD43, VDD44, VDD45, VDD46, VDD47, VDD48, VDD49, VDD50, VDD51, VDD52, VDD53, VDD54, VDD55, VDD56, VDD57, VDD58, VDD59, VDD60, VDD61, VDD62, VDD63, VDD64, VDD65, VDD66, VDD67, VDD68, VDD69, VDD70, VDD71, VDD72, VDD73, VDD74, VDD75, VDD76, VDD77, VDD78, VDD79, VDD80, VDD81, VDD82, VDD83, VDD84, VDD85, VDD86, VDD87, VDD88, VDD89, VDD90, VDD91, VDD92, VDD93, VDD94, VDD95, VDD96, VDD97, VDD98, VDD99, VDD100, VDD101, VDD102, VDD103, VDD104, VDD105, VDD106, VDD107, VDD108, VDD109, VDD110, VDD111, VDD112, VDD113, VDD114, VDD115, VDD116, VDD117, VDD118, VDD119, VDD120, VDD121, VDD122, VDD123, VDD124, VDD125, VDD126, VDD127, VDD128, VDD129, VDD130, VDD131, VDD132, VDD133, VDD134, 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VDD635, VDD636, VDD637, VDD6
```

PSEUDO CAPLESS LDOs  
P-type are pseudo-capless, cap can be at load  
For CAPLESS LDOs: if decaps on the load side do not  
add up to LDO spec, then install the cap close to the FMIC







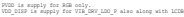
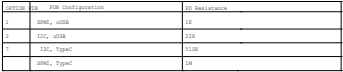
Schematic design notice of "46\_MEMORY\_SD Card" page.

Note 46-1: For better ESD performance, please select suitable components for system protection.

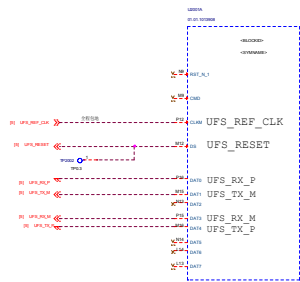
|              |                                |
|--------------|--------------------------------|
| Company Name |                                |
| Block Title  | PM8801, Buck and LDO           |
| Project      | T10_1Shape_SMB1111 1.0         |
| Date         | Yantai, June 30, 2020 17 of 39 |



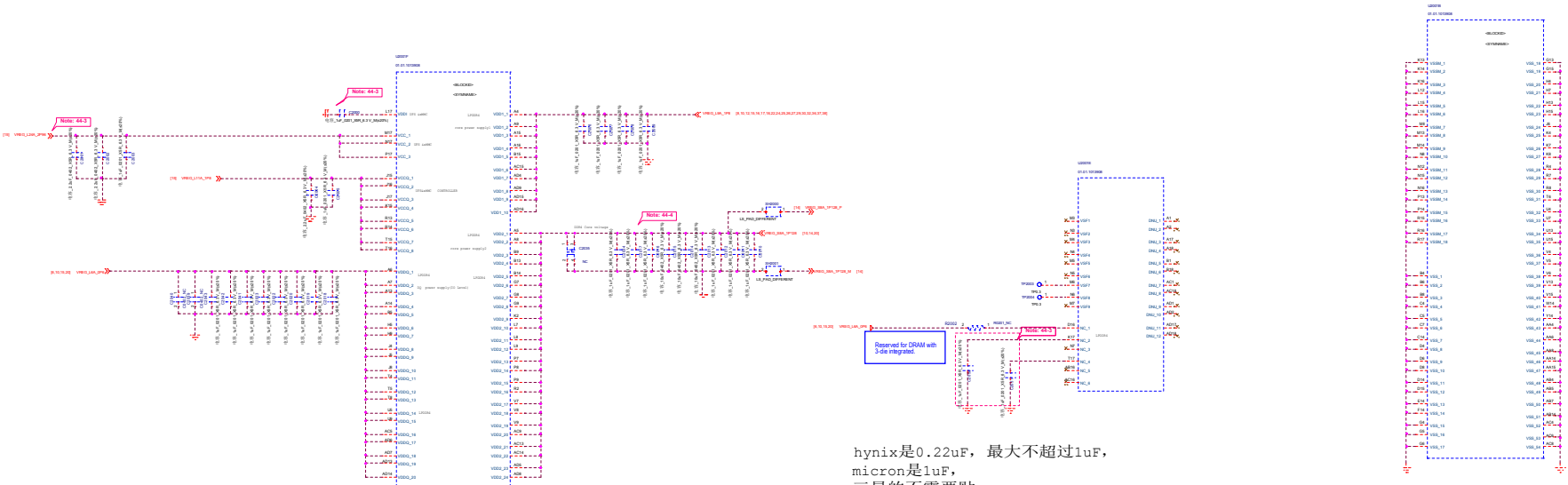
REF 80-PG427-41 Rev. E



UFS MEMORY

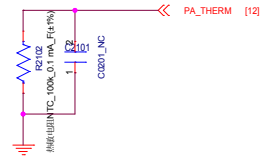


uMCP: LPDDR4x

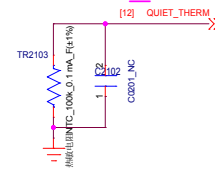


hynix是0.22uF，最大不超过1uF，  
micron是1uF，  
三星的不需要贴。  
但是为了避免项目使用几家的uMCP时bom出错，统一贴1uF

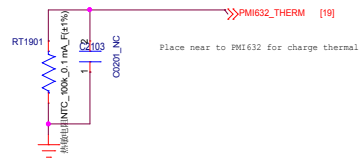
## 4G PA\_THERMAL



## QUIET\_THERM

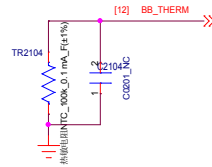


## Charger\_THERMAL

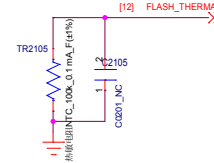


Place near to PMI632 for charge thermal

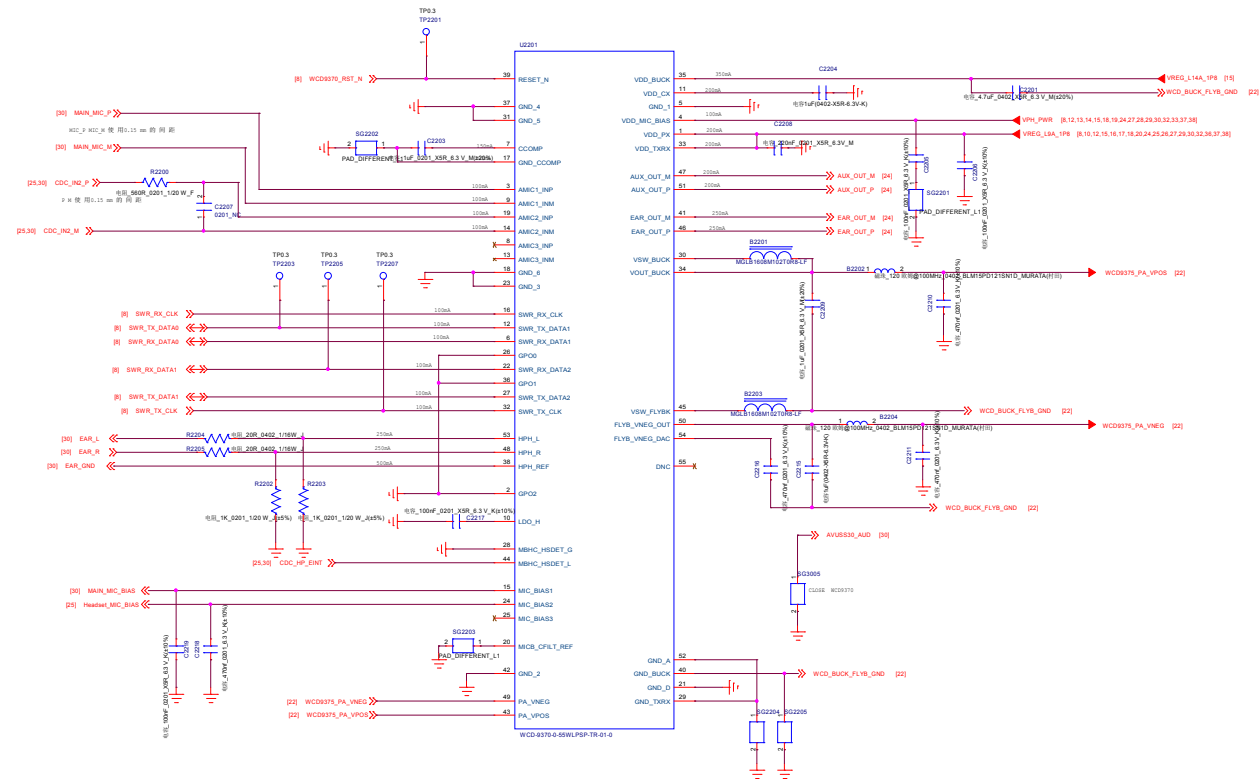
## BB\_THERMAL



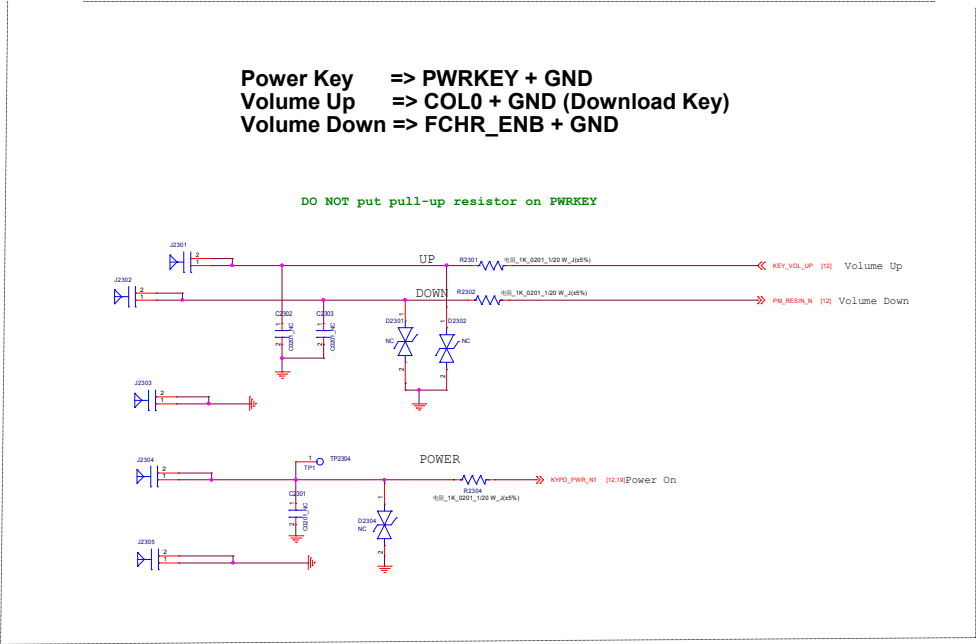
## FLASH\_THERMAL



## WCD9370

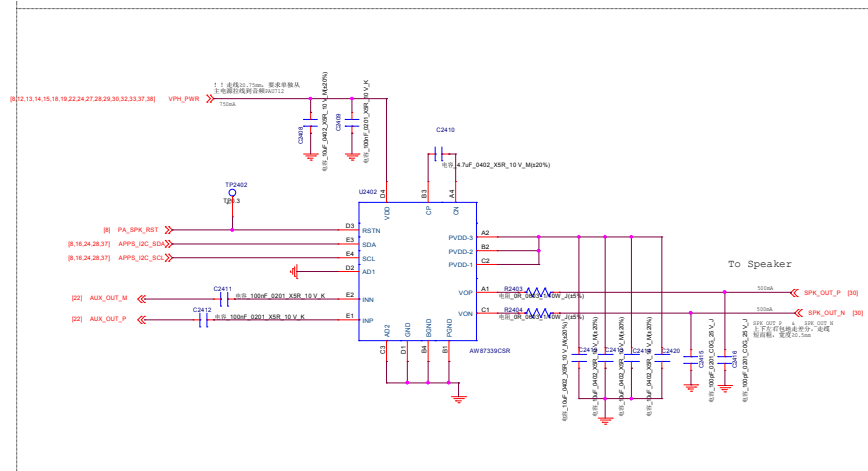
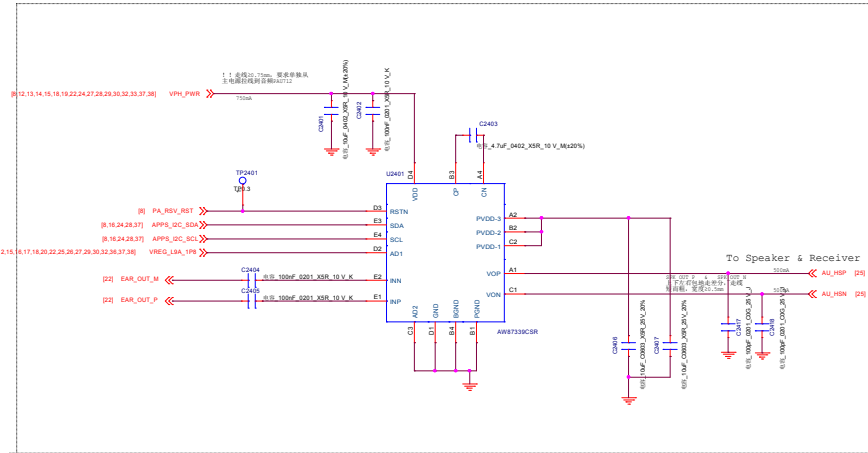


|                |                                       |
|----------------|---------------------------------------|
| Company: Hsueh |                                       |
| Block Title    | Wipower                               |
| Project        | T10_I-Shape_SM8115 MB/W/1 1.0         |
| Date:          | Tuesday, June 30, 2020 Sheet 22 of 39 |



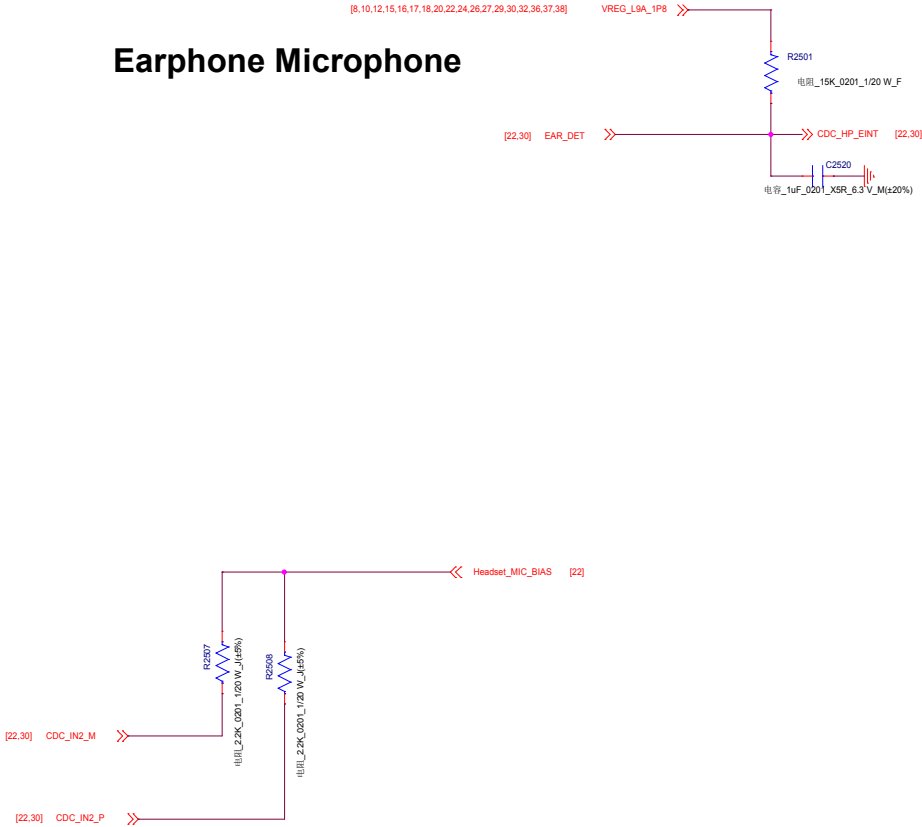
TP/GND/Shielding

|                |                    |
|----------------|--------------------|
| Company Huzhou |                    |
| Block Title    | Test Points        |
| Project        | T10_2-Shape_040110 |
| Date           | 2020-06-20         |

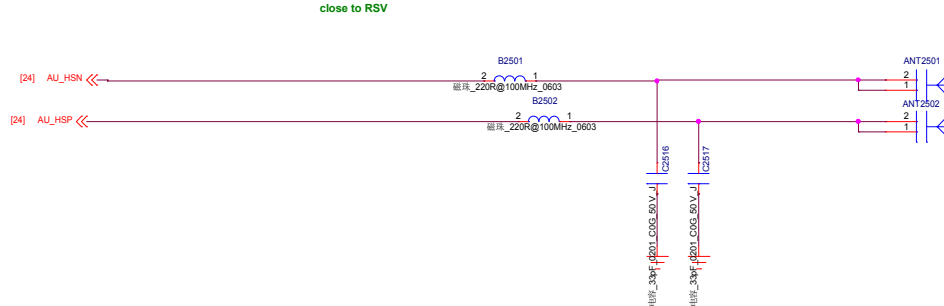




Earphone Microphone

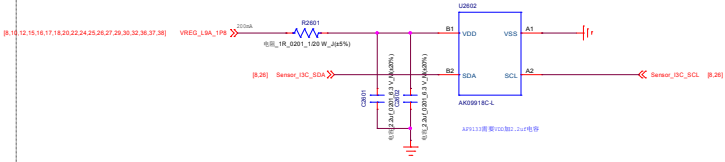


Receiver



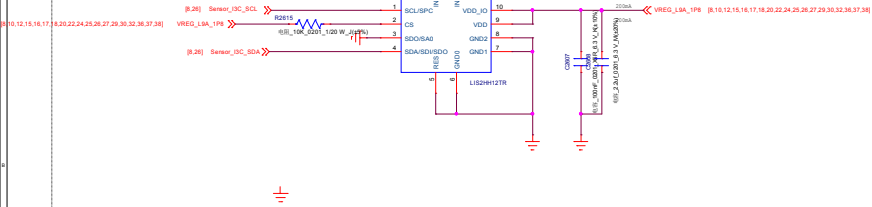
M-Sensor

AK09918 / M-Sensor I2C Address : 0x0C (Write:0x18, Read:0x19)

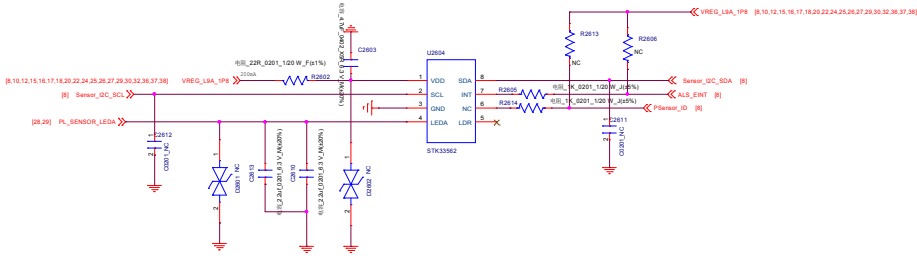


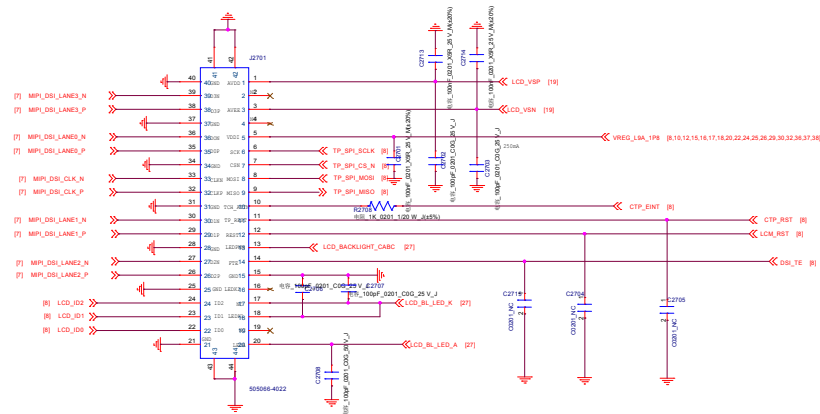
G Sensor

0: no sensor connected (pin17, not using I2C)  
1: pin17 is not connected  
2: pin17 is connected, but not using I2C  
3: I2C sensor on board (pin17 is connected, but not using I2C)

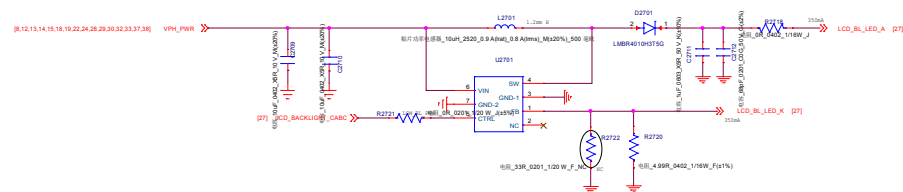


ALS SENSOR

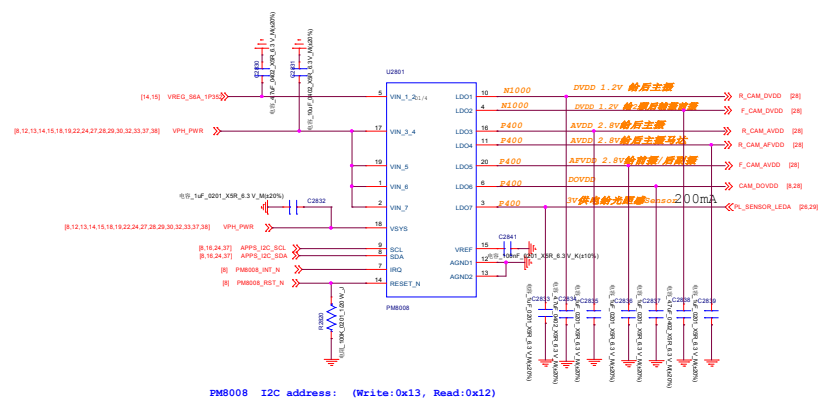
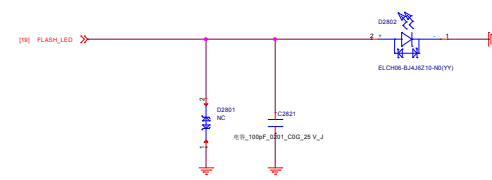
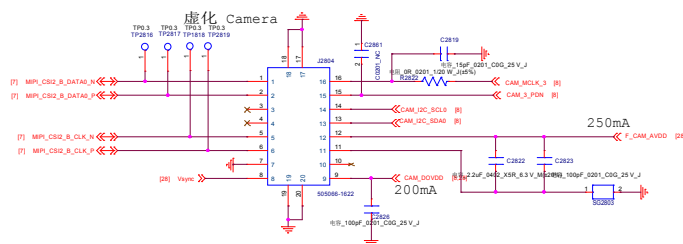
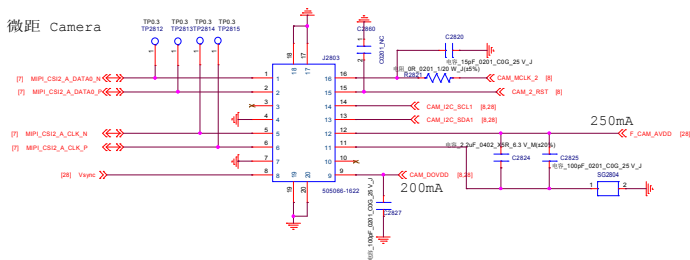
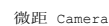
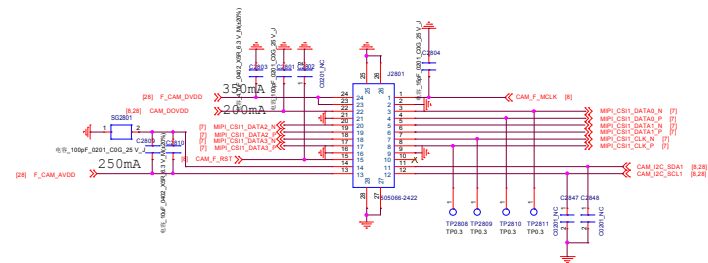
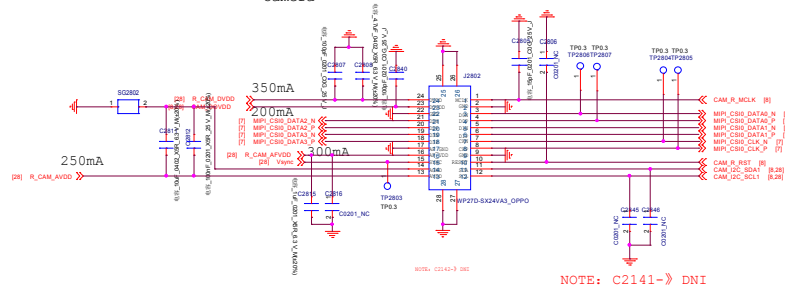
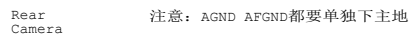


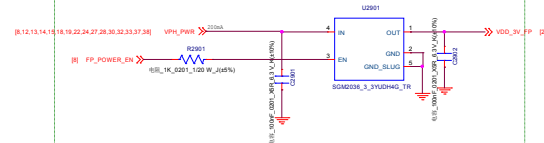


## LCD-BACKLIGHT

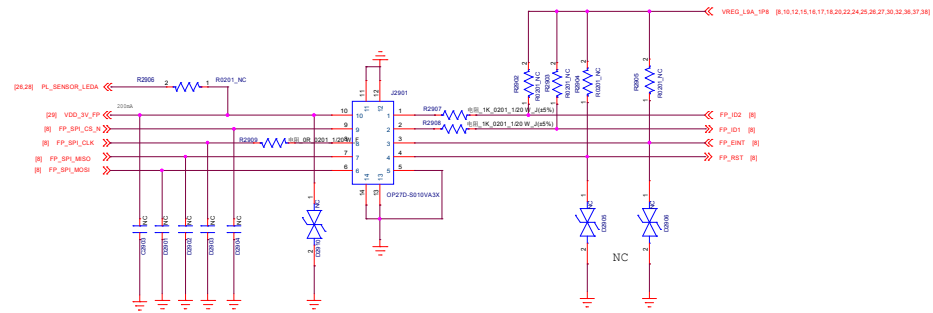


|               |                        |
|---------------|------------------------|
| Company Hsien |                        |
| Stock Title   | LCD_TP                 |
| Project       | T19_2-Shape_SMB115_1.0 |
| Date          | 2020-06-26 10:20 2/2   |



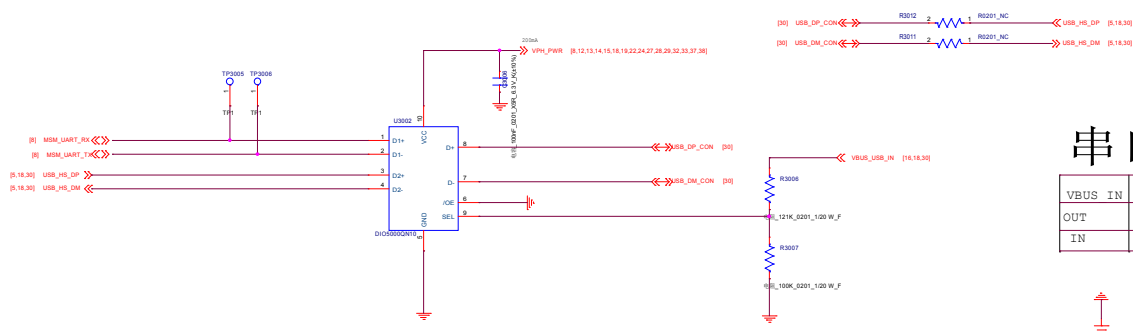
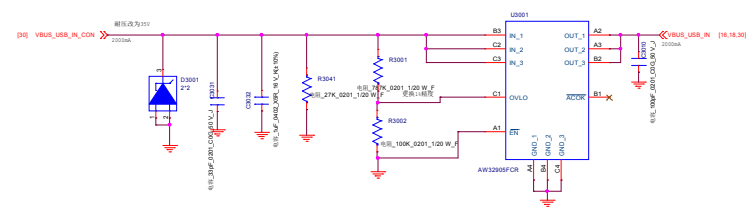


## Fingerprint

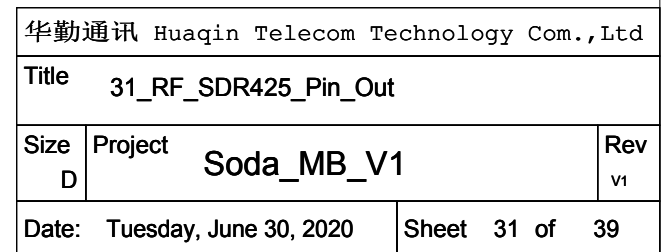


Grond protected separately each lane  
90Ohm Differential impedance

Grond protected separately each lane  
90Ohm Differential Impedance



|         |     |                    |
|---------|-----|--------------------|
| VBUS IN | SEL | SELECT(default PD) |
| OUT     | 0   | D=D1               |
| IN      | 1   | D=D2               |



## REF 3401-3499

PA IN MB

PA IN HB

B40\_41 LNA

换为BGU8H1料号

L3219&R3280 Co-pad

R3219&amp;R3281 Co-pad

[CLOSE wtr](#)

B20 /12TX

**B28B TX**

B28A TX

**B8 TX**

B5/B6/B19/BC0 TX

**B1 TX**

**B3 TX**

B34/39 TX

**B2 TX**

B38 41 TRX

**B40 TRX**

**B7 TX**

## RF Power Supply

|       |                    |
|-------|--------------------|
| Title | 32_RF_SDR425_RF_TX |
|-------|--------------------|

|      |         |            |
|------|---------|------------|
| Size | Project | Soda_MB_V1 |
| F    |         |            |

|     |  |
|-----|--|
| Rev |  |
| v1  |  |

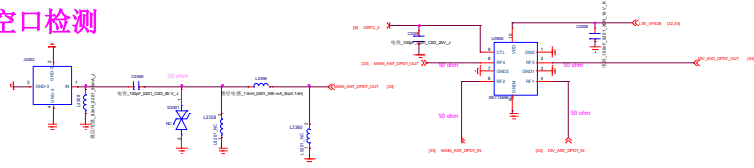
Date: Tuesday, June 30, 2020 Sheet 32 of 39



# DPDT SWITCH

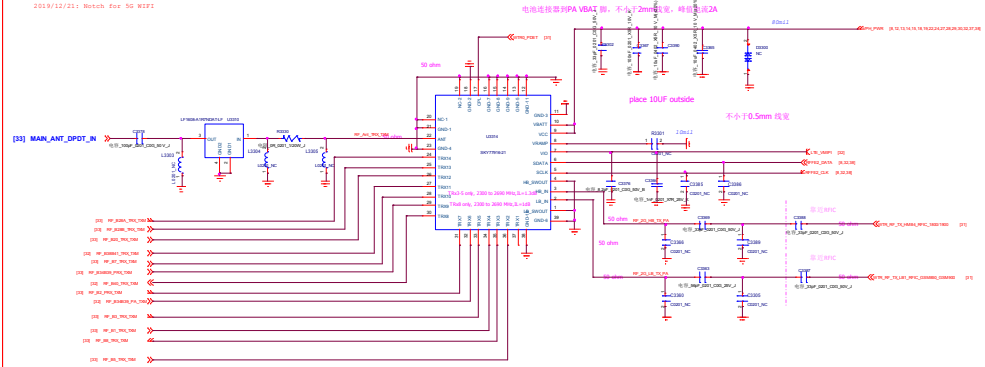
优先保证射频走线，兼容通路，不要有新头线

## 空口检测



# ASM SWITCH

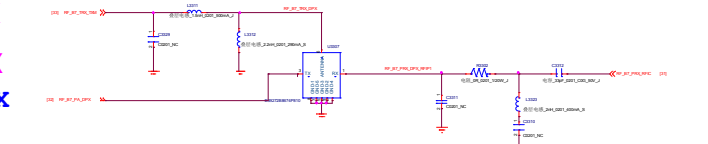
2019/12/21: Notion Rev 50 WIP2



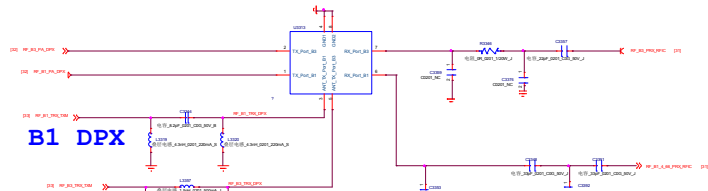
## HMB TRX

## HMB TRX

### B7 DPX



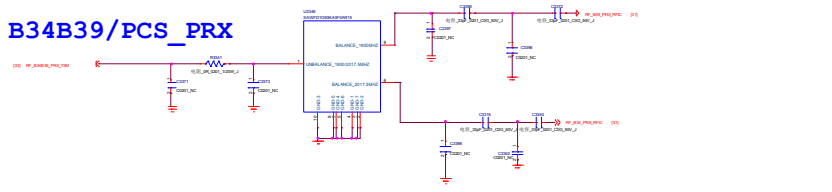
### B1 DPX



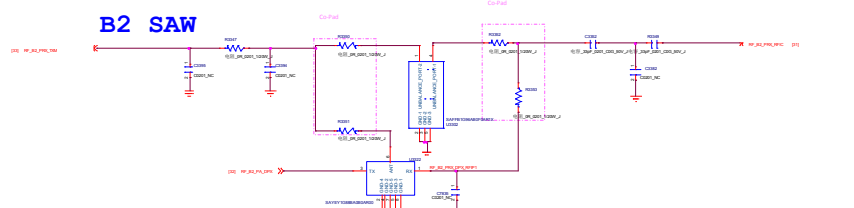
### B3/4 DPX



## B34B39/PCS\_PRX



## B2 SAW

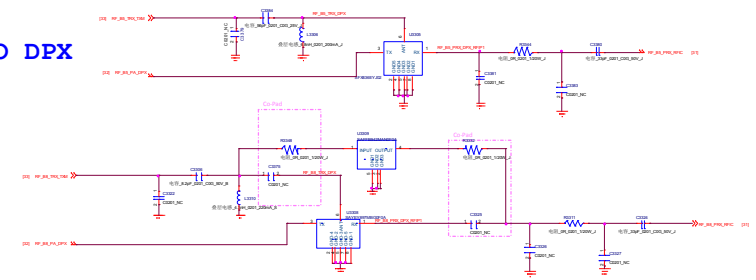


### B2 DPX

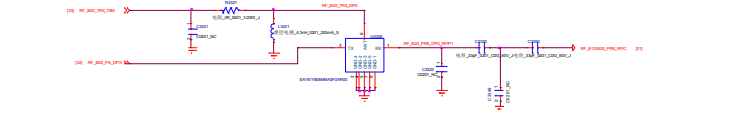
## LB TRX

## B5/B6/B19 BCO DPX

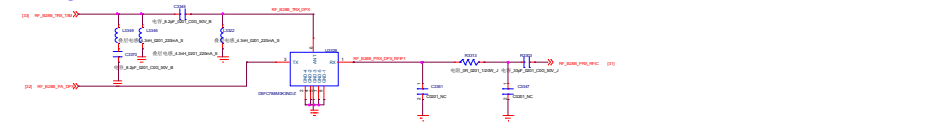
### B8 DPX



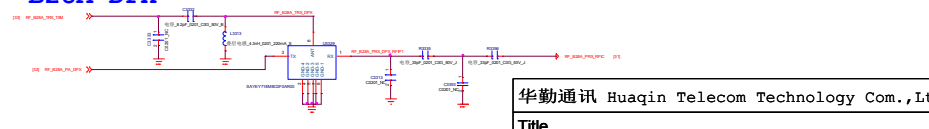
### B12/20 DPX



## B28B DPX

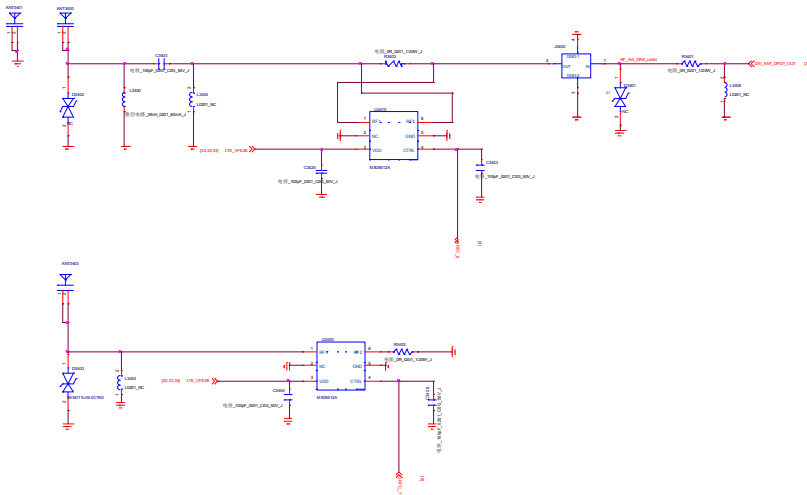


## B28A DPX

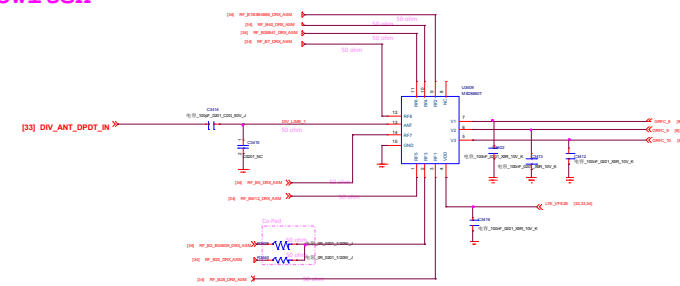


|   |                    |        |  |
|---|--------------------|--------|--|
| 华勤通讯 Huaqin Telecom Technology Com.,Ltd |                    |        |  |
| Title 33_RF_SDR425_RF_PRX               |                    |        |  |
| Size E                                  | Project Soda_MB_V1 | Rev v1 |  |
| Date: Tuesday, June 30, 2020            | Sheet 33 of 39     |        |  |

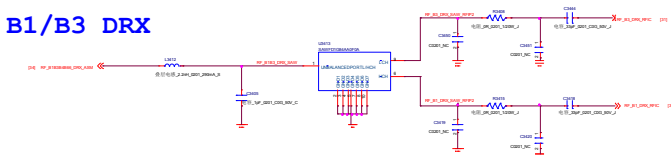
## DIV\_ANT



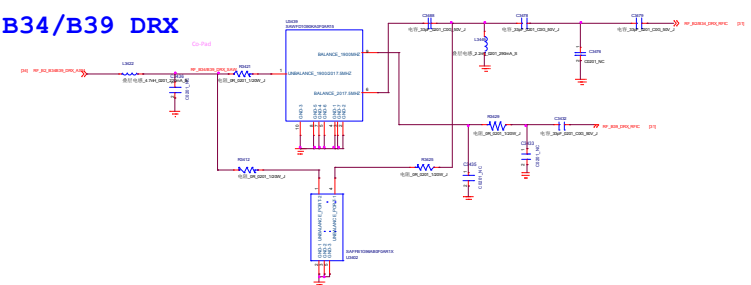
## DRX Switch



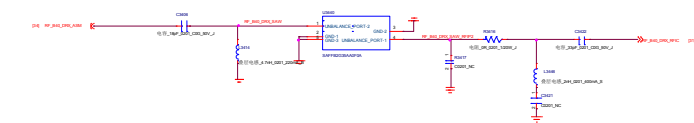
## B1/B3 DRX



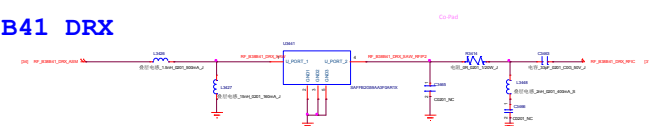
## B34/B39 DRX



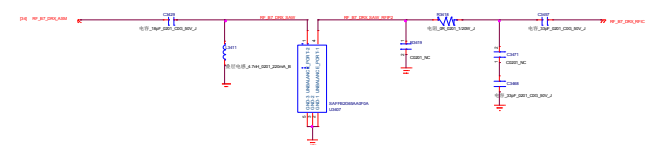
## B40 DRX



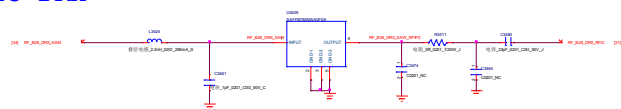
## B38/B41 DRX



## B7 DRX



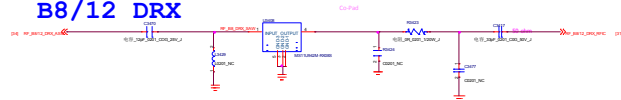
## B28 DRX



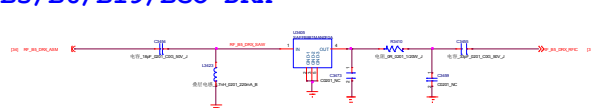
## B20 DRX



## B8/12 DRX



## B5/B6/B19/BCO DRX



华勤通讯 Huaqin Telecom Technology Com.,Ltd

Title 34\_RF\_SDR425\_RF\_DRX

Size E Project Soda\_MB\_V1

Rev v1

Date: Tuesday, June 30, 2020

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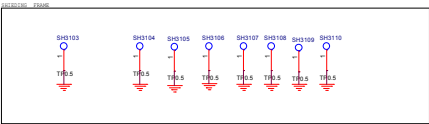
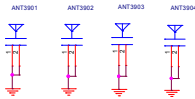








GND



SMT Download Mode



光学定位孔



Screw gasket



TP/GND/Shielding

|                |                        |
|----------------|------------------------|
| Company Huzhou |                        |
| Block Title    | Test Points            |
| Project        | T10_1-Shape_SMT110_1.0 |
| Date           | 2020-06-26 10:00:00    |