

P1041-B01 GF104

GF104-300, 768MB/1536MB, GDDR5 192b 32M/64Mx32  
DVI-I-DL, DVI-I-DL/DP, mHDMI

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變更記錄

- P3 , C55 change to 1206 10u
- P4~P14 , add 跳頁&lt;>’符號
- P14,刪除所有網絡線
- P17 , add 6262 IC
- P25,刪除原公版U2 & J501. 把I2CC接到6262IC
- P27,接PS1\_1V05\_FB到6262,U4 foot\_print 改爲十字的
- P28,改U8的FB接法，換L6爲L04-22A8011-L65
- P29 , core部份PWM換UP6213+6284 , OFS電壓開關抬50mV 。 頻率def400KHz，開關抬到600KHz.
- P30-31，加driver IC 6284；去掉3顆input 電容；同時增加6個 LED燈，增加CP1~CP12；爲精簡線路，刪除L mos端與 DRVL 0OHm電阻，刪除CSP，CSN 0Ω電阻；更換MOS & 電感；power前4相由PEX6\_F1提供，后2相由PEX6\_F2 提供；電容部份保留公版。
- P32,去除input 端12V 的LB電感
- P33,加防彎條mec100，加6個十字光學點，加NVVDD，FBVDD &pex\_vdd的測試點, 增加阻抗條

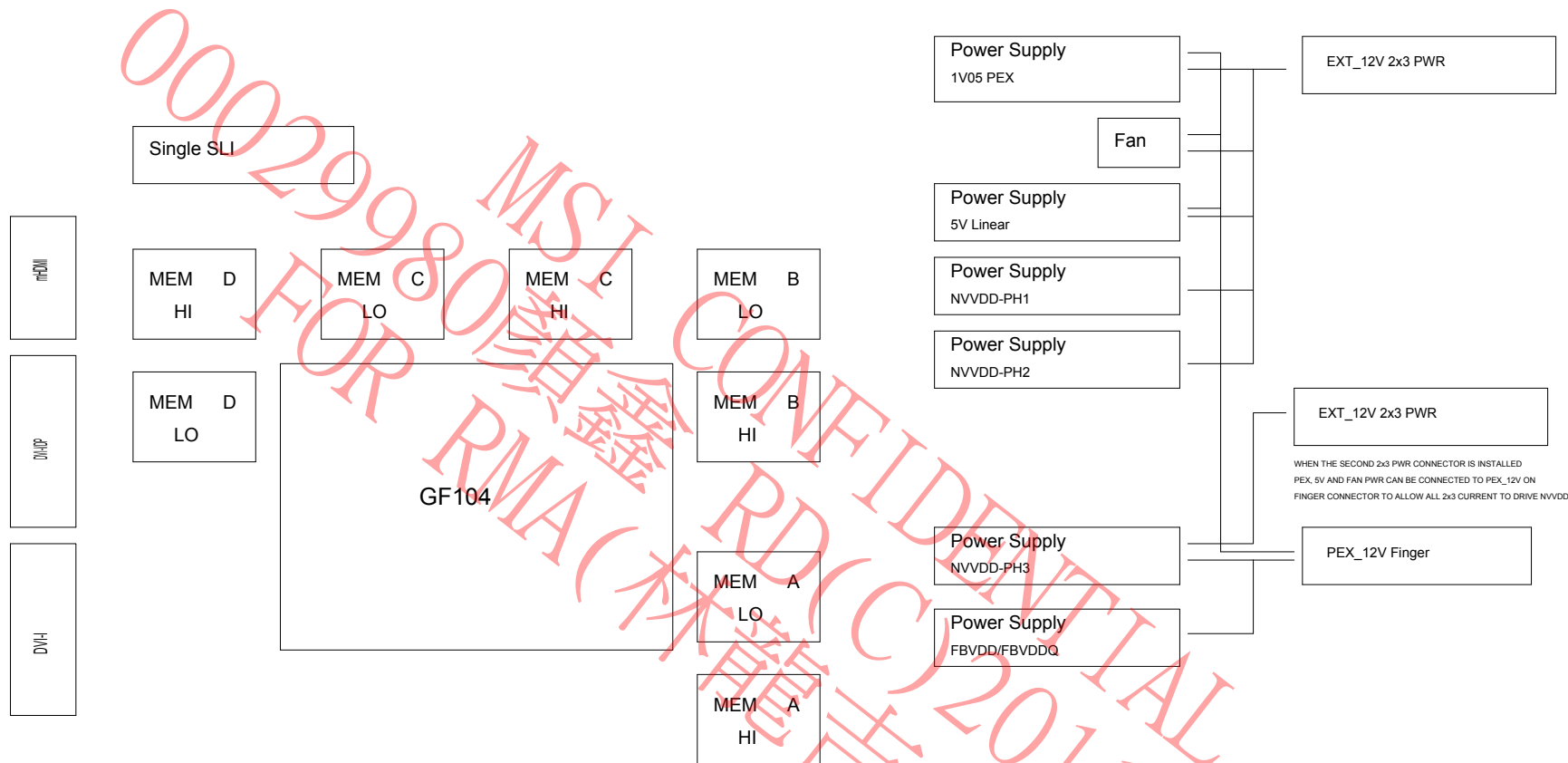
REV	VARIANT	RVPN	ASSEMBLY
0	BASE	600-11041-BASE-000	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU0000	600-11041-0000-300	P1041 OF 104-300 768MB GDDR5 32Mx32 DVI-I+HDMI Frame Buffer
2	SKU0010	600-11041-0010-300	P1041 OF 104-300 1536MB GDDR5 32Mx32 DVI-I+HDMI Frame Buffer
3	Q01	600-11041-0000-Q01	P1041 OF 104-300 768MB GDDR5 32Mx32 Frame Buffer Q01+HDMI
4	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
5	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
6	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
7	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
8	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
9	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
10	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
11	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
12	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
13	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
14	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
15	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>

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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Title

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2701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95050, USA			
REV_PN	600-11041-BASE-000	PAGE	
PCB REV	P1041-B01	DATE	12-MAY-2010
BOM REV	A		



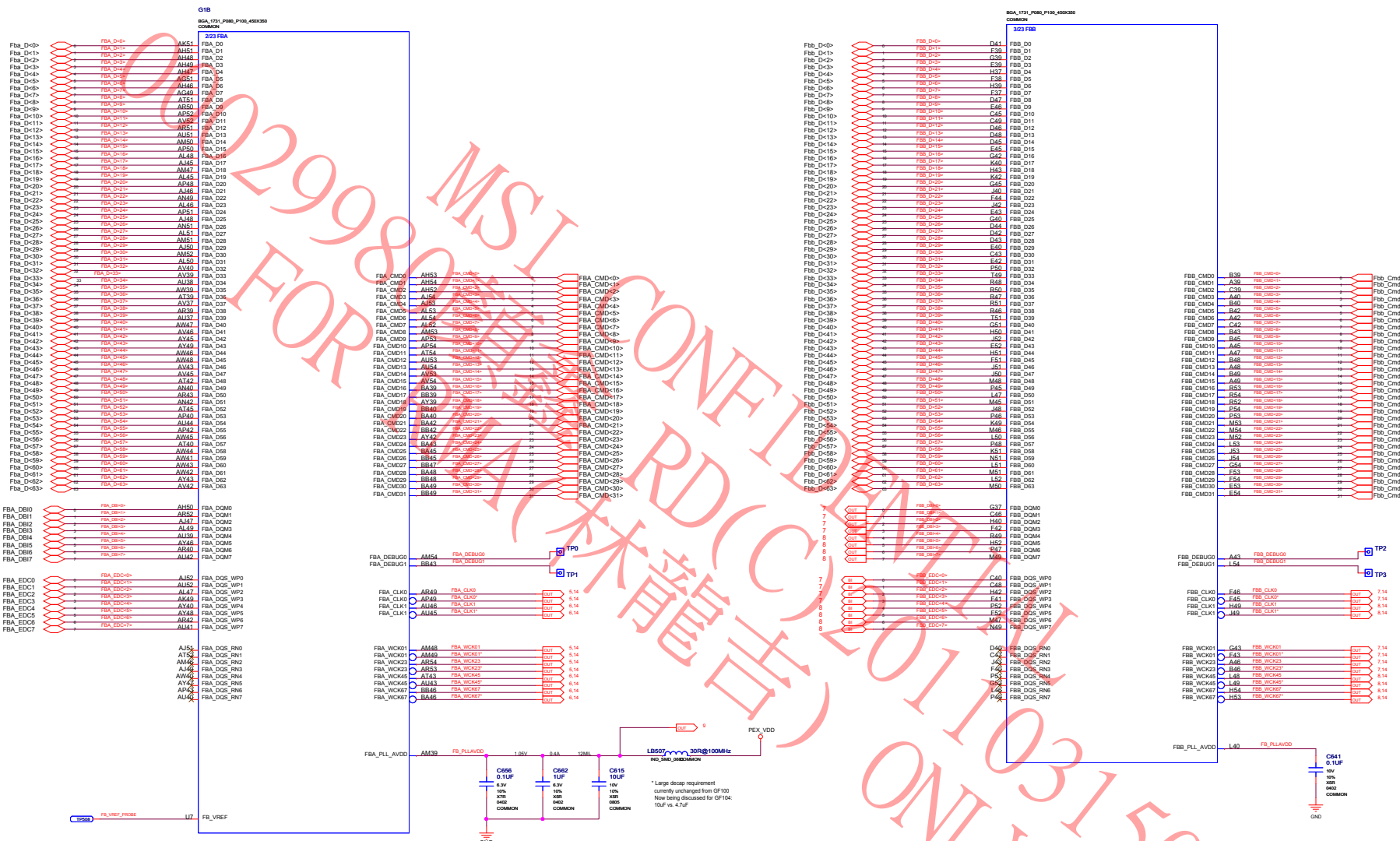


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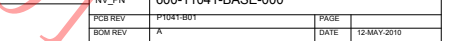
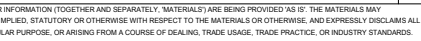
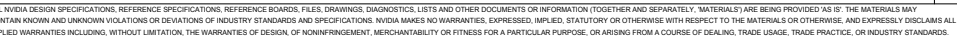
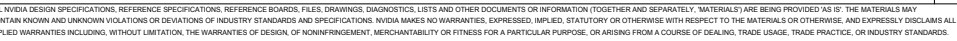
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO-STUFF ASSEMBLY NOTES AND BOARD NOT FINAL
PAGE: DETAIL	Block Diagram

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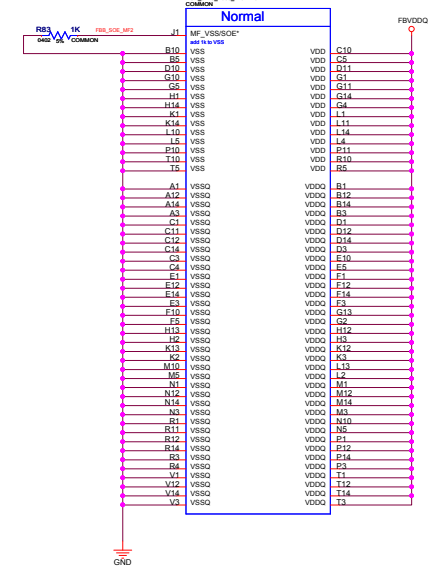
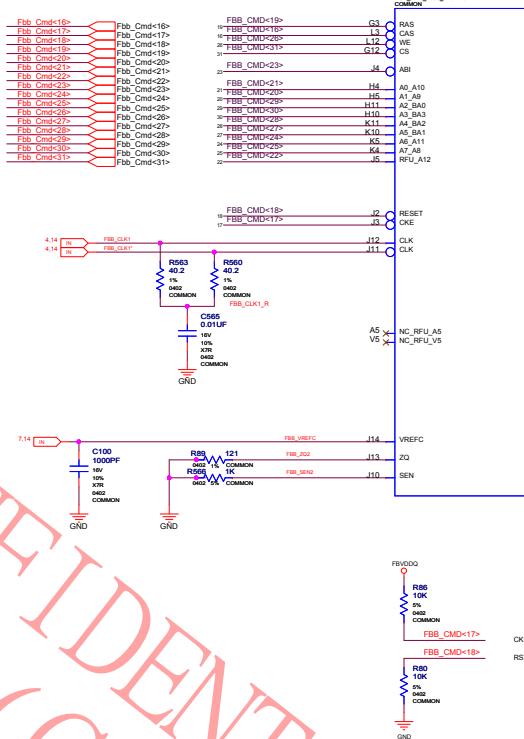
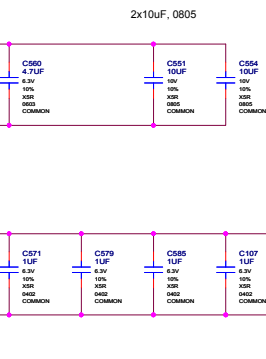
GDDR5 CMD Mapping		
Cmd	0..31	32..63
Cmd0	CAS*	
Cmd1	OE*	
Cmd2	RS*	
Cmd3	WAF	
Cmd4	AL_A0	
Cmd5	AL_A15	
Cmd6	AL2_RFU	
Cmd7	AB*	
Cmd8	AB_A0	
Cmd9	AB_A1	
Cmd10	AB_A2	
Cmd11	AB_A3	
Cmd12	AB_A4	
Cmd13	AB_A5	
Cmd14	AB_A6	
Cmd15	AB_A7	
Cmd16	AB_A8	
Cmd17	AB_A9	
Cmd18	AB_A10	
Cmd19	AB_A11	
Cmd20	AB_A12	
Cmd21	AB_A13	
Cmd22	AB_A14	
Cmd23	AB_A15	
Cmd24	AB_A16	
Cmd25	AB_A17	
Cmd26	AB_A18	
Cmd27	AB_A19	
Cmd28	AB_A20	
Cmd29	AB_A21	
Cmd30	AB_A22	
Cmd31	AB_A23	
Cmd32	AB_A24	
Cmd33	AB_A25	
Cmd34	AB_A26	
Cmd35	AB_A27	
Cmd36	AB_A28	
Cmd37	AB_A29	
Cmd38	AB_A30	
Cmd39	AB_A31	
Cmd40	AB_A32	
Cmd41	AB_A33	
Cmd42	AB_A34	
Cmd43	AB_A35	
Cmd44	AB_A36	
Cmd45	AB_A37	
Cmd46	AB_A38	
Cmd47	AB_A39	
Cmd48	AB_A40	
Cmd49	AB_A41	
Cmd50	AB_A42	
Cmd51	AB_A43	
Cmd52	AB_A44	
Cmd53	AB_A45	
Cmd54	AB_A46	
Cmd55	AB_A47	
Cmd56	AB_A48	
Cmd57	AB_A49	
Cmd58	AB_A50	
Cmd59	AB_A51	
Cmd60	AB_A52	
Cmd61	AB_A53	
Cmd62	AB_A54	
Cmd63	AB_A55	
Cmd64	AB_A56	
Cmd65	AB_A57	
Cmd66	AB_A58	
Cmd67	AB_A59	
Cmd68	AB_A60	
Cmd69	AB_A61	
Cmd70	AB_A62	
Cmd71	AB_A63	
Cmd72	AB_A64	
Cmd73	AB_A65	
Cmd74	AB_A66	
Cmd75	AB_A67	
Cmd76	AB_A68	
Cmd77	AB_A69	
Cmd78	AB_A70	
Cmd79	AB_A71	
Cmd80	AB_A72	
Cmd81	AB_A73	
Cmd82	AB_A74	
Cmd83	AB_A75	
Cmd84	AB_A76	
Cmd85	AB_A77	
Cmd86	AB_A78	
Cmd87	AB_A79	
Cmd88	AB_A80	
Cmd89	AB_A81	
Cmd90	AB_A82	
Cmd91	AB_A83	
Cmd92	AB_A84	
Cmd93	AB_A85	
Cmd94	AB_A86	
Cmd95	AB_A87	
Cmd96	AB_A88	
Cmd97	AB_A89	
Cmd98	AB_A90	
Cmd99	AB_A91	
Cmd100	AB_A92	
Cmd101	AB_A93	
Cmd102	AB_A94	
Cmd103	AB_A95	
Cmd104	AB_A96	
Cmd105	AB_A97	
Cmd106	AB_A98	
Cmd107	AB_A99	
Cmd108	AB_A100	
Cmd109	AB_A101	
Cmd110	AB_A102	
Cmd111	AB_A103	
Cmd112	AB_A104	
Cmd113	AB_A105	
Cmd114	AB_A106	
Cmd115	AB_A107	
Cmd116	AB_A108	
Cmd117	AB_A109	
Cmd118	AB_A110	
Cmd119	AB_A111	
Cmd120	AB_A112	
Cmd121	AB_A113	
Cmd122	AB_A114	
Cmd123	AB_A115	
Cmd124	AB_A116	
Cmd125	AB_A117	
Cmd126	AB_A118	
Cmd127	AB_A119	
Cmd128	AB_A120	
Cmd129	AB_A121	
Cmd130	AB_A122	
Cmd131	AB_A123	
Cmd132	AB_A124	
Cmd133	AB_A125	
Cmd134	AB_A126	
Cmd135	AB_A127	
Cmd136	AB_A128	
Cmd137	AB_A129	
Cmd138	AB_A130	
Cmd139	AB_A131	
Cmd140	AB_A132	
Cmd141	AB_A133	
Cmd142	AB_A134	
Cmd143	AB_A135	
Cmd144	AB_A136	
Cmd145	AB_A137	
Cmd146	AB_A138	
Cmd147	AB_A139	
Cmd148	AB_A140	
Cmd149	AB_A141	
Cmd150	AB_A142	
Cmd151	AB_A143	
Cmd152	AB_A144	
Cmd153	AB_A145	
Cmd154	AB_A146	
Cmd155	AB_A147	
Cmd156	AB_A148	
Cmd157	AB_A149	
Cmd158	AB_A150	
Cmd159	AB_A151	
Cmd160	AB_A152	
Cmd161	AB_A153	
Cmd162	AB_A154	
Cmd163	AB_A155	
Cmd164	AB_A156	
Cmd165	AB_A157	
Cmd166	AB_A158	
Cmd167	AB_A159	
Cmd168	AB_A160	
Cmd169	AB_A161	
Cmd170	AB_A162	
Cmd171	AB_A163	
Cmd172	AB_A164	
Cmd173	AB_A165	
Cmd174	AB_A166	
Cmd175	AB_A167	
Cmd176	AB_A168	
Cmd177	AB_A169	
Cmd178	AB_A170	
Cmd179	AB_A171	
Cmd180	AB_A172	
Cmd181	AB_A173	
Cmd182	AB_A174	
Cmd183	AB_A175	
Cmd184	AB_A176	
Cmd185	AB_A177	
Cmd186	AB_A178	
Cmd187	AB_A179	
Cmd188	AB_A180	
Cmd189	AB_A181	
Cmd190	AB_A182	
Cmd191	AB_A183	
Cmd192	AB_A184	
Cmd193	AB_A185	
Cmd194	AB_A186	
Cmd195	AB_A187	
Cmd196	AB_A188	
Cmd197	AB_A189	
Cmd198	AB_A190	
Cmd199	AB_A191	
Cmd200	AB_A192	
Cmd201	AB_A193	
Cmd202	AB_A194	
Cmd203	AB_A195	
Cmd204	AB_A196	
Cmd205	AB_A197	
Cmd206	AB_A198	
Cmd207	AB_A199	
Cmd208	AB_A200	
Cmd209	AB_A201	
Cmd210	AB_A202	
Cmd211	AB_A203	
Cmd212	AB_A204	
Cmd213	AB_A205	
Cmd214	AB_A206	
Cmd215	AB_A207	
Cmd216	AB_A208	
Cmd217	AB_A209	
Cmd218	AB_A210	
Cmd219	AB_A211	
Cmd220	AB_A212	
Cmd221	AB_A213	
Cmd222	AB_A214	
Cmd223	AB_A215	
Cmd224	AB_A216	
Cmd225	AB_A217	
Cmd226	AB_A218	
Cmd227	AB_A219	
Cmd228	AB_A220	
Cmd229	AB_A221	
Cmd230	AB_A222	
Cmd231	AB_A223	
Cmd232	AB_A224	
Cmd233	AB_A225	
Cmd234	AB_A226	
Cmd235	AB_A227	
Cmd236	AB_A228	
Cmd237	AB_A229	
Cmd238	AB_A230	
Cmd239	AB_A231	
Cmd240	AB_A232	
Cmd241	AB_A233	
Cmd242	AB_A234	
Cmd243	AB_A235	
Cmd244	AB_A236	
Cmd245	AB_A237	
Cmd246	AB_A238	
Cmd247	AB_A239	
Cmd248	AB_A240	
Cmd249	AB_A241	
Cmd250	AB_A242	
Cmd251	AB_A243	
Cmd252	AB_A244	
Cmd253	AB_A245	
Cmd254	AB_A246	
Cmd255	AB_A247	
Cmd256	AB_A248	
Cmd257	AB_A249	
Cmd258	AB_A250	
Cmd259	AB_A251	
Cmd260	AB_A252	
Cmd261	AB_A253	
Cmd262	AB_A254	
Cmd263	AB_A255	
Cmd264	AB_A256	
Cmd265	AB_A257	
Cmd266	AB_A258	
Cmd267	AB_A259	
Cmd268	AB_A260	
Cmd269	AB_A261	
Cmd270	AB_A262	
Cmd271	AB_A263	
Cmd272	AB_A264	
Cmd273	AB_A265	
Cmd274	AB_A266	
Cmd275	AB_A267	
Cmd276	AB_A268	
Cmd277	AB_A269	
Cmd278	AB_A270	
Cmd279	AB_A271	
Cmd280	AB_A272	
Cmd281	AB_A273	
Cmd282	AB_A274	
Cmd283	AB_A275	
Cmd284	AB_A276	
Cmd285	AB_A277	
Cmd286	AB_A278	
Cmd287	AB_A279	
Cmd288	AB_A280	
Cmd289	AB_A281	
Cmd290	AB_A282	
Cmd291	AB_A283	
Cmd292	AB_A284	
Cmd293	AB_A285	
Cmd294	AB_A286	
Cmd295	AB_A287	
Cmd296	AB_A288	
Cmd297	AB_A289	
Cmd298	AB_A290	
Cmd299	AB_A291	
Cmd300	AB_A292	
Cmd301	AB_A293	
Cmd302	AB_A294	
Cmd303	AB_A295	
Cmd304	AB_A296	
Cmd305	AB_A297	
Cmd306	AB_A298	
Cmd307	AB_A299	
Cmd308	AB_A300	
Cmd309	AB_A301	
Cmd310	AB_A302	
Cmd311	AB_A303	
Cmd312	AB_A304	
Cmd313	AB_A305	
Cmd314	AB_A306	
Cmd315	AB_A307	
Cmd316	AB_A308	
Cmd317	AB_A309	
Cmd318	AB_A310	
Cmd319	AB_A311	
Cmd320	AB_A312	
Cmd321	AB_A313	
Cmd322	AB_A314	
Cmd323	AB_A315	
Cmd324	AB_A316	
Cmd325	AB_A317	
Cmd326	AB_A318	
Cmd327	AB_A319	
Cmd328	AB_A320	
Cmd329	AB_A321	
Cmd330	AB_A322	
Cmd331	AB_A323	
Cmd332	AB_A324	
Cmd333	AB_A325	
Cmd334	AB_A326	
Cmd335	AB_A327	
Cmd336	AB_A328	
Cmd337	AB_A329	
Cmd338	AB_A330	
Cmd339	AB_A331	
Cmd340	AB_A332	
Cmd341	AB_A333	
Cmd342	AB_A334	
Cmd343	AB_A335	
Cmd344	AB_A336	
Cmd345	AB_A337	
Cmd346	AB_A338	
Cmd347	AB_A339	
Cmd348	AB_A340	
Cmd349	AB_A341	
Cmd350	AB_A342	
Cmd351	AB_A343	
Cmd352	AB_A344	
Cmd353	AB_A345	
Cmd354	AB_A346	
Cmd355	AB_A347	
Cmd356	AB_A348	
Cmd357	AB_A349	
Cmd358	AB_A350	
Cmd359	AB_A351	
Cmd360	AB_A352	
Cmd361	AB_A353	
Cmd362	AB_A354	
Cmd363	AB_A355	
Cmd364	AB_A356	
Cmd365	AB_A357	
Cmd366	AB_A358	
Cmd367	AB_A359	
Cmd368	AB_A360	
Cmd369	AB_A361	
Cmd370	AB_A362	
Cmd371	AB_A363	
Cmd372	AB_A364	
Cmd373	AB_A365	
Cmd374	AB_A366	
Cmd375	AB_A367	
Cmd376	AB_A368	
Cmd377	AB_A369	
Cmd378	AB_A370	
Cmd379	AB_A371	
Cmd380	AB_A372	
Cmd381	AB_A373	
Cmd382	AB_A374	
Cmd383	AB_A375	
Cmd384	AB_A376	
Cmd385	AB_A377	
Cmd386	AB_A378	
Cmd387	AB_A379	
Cmd388	AB_A380	
Cmd389	AB_A381	
Cmd390	AB_A382	
Cmd391	AB_A383	
Cmd392	AB_A384	
Cmd393	AB_A385	
Cmd394	AB_A386	
Cmd395	AB_A387	
Cmd396	AB_A388	
Cmd397	AB_A389	
Cmd398	AB_A390	
Cmd399	AB_A391	
Cmd400	AB_A392	
Cmd401	AB_A393	
Cmd402	AB_A394	
Cmd403	AB_A395	
Cmd404	AB_A396	
Cmd405	AB_A397	
Cmd406	AB_A398	
Cmd407	AB_A399	
Cmd408	AB_A400	
Cmd409	AB_A401	
Cmd410	AB_A402	
Cmd411	AB_A403	
Cmd412	AB_A404	
Cmd413	AB_A405	
Cmd414	AB_A406	
Cmd415	AB_A407	
Cmd416	AB_A408	
Cmd417	AB_A409	
Cmd418	AB_A410	
Cmd419	AB_A411	
Cmd420	AB_A412	
Cmd421	AB_A413	
Cmd422	AB_A414	
Cmd423	AB_A415	
Cmd424	AB_A416	
Cmd425	AB_A417	
Cmd426	AB_A418	
Cmd427	AB_A419	
Cmd428	AB_A420	
Cmd429	AB_A421	
Cmd430	AB_A422	
Cmd431	AB_A423	
Cmd432	AB_A424	
Cmd433	AB_A425	
Cmd434	AB_A426	
Cmd435	AB_A427	
Cmd436	AB_A428	
Cmd437	AB_A429	
Cmd438	AB_A430	
Cmd439	AB_A431	
Cmd440	AB_A432	
Cmd441	AB_A433	
Cmd442	AB_A434	
Cmd443	AB_A435	
Cmd444	AB_A436	
Cmd445	AB_A437	
Cmd446	AB_A438	
Cmd447	AB_A439	
Cmd448	AB_A440	
Cmd449	AB_A441	
Cmd450	AB_A442	
Cmd451	AB_A443	
Cmd452	AB_A444	
Cmd453	AB_A445	
Cmd454	AB_A446	
Cmd455	AB_A447	
Cmd456	AB_A448	
Cmd457	AB_A449	
Cmd458	AB_A450	
Cmd459	AB_A451	
Cmd460	AB_A452	
Cmd461	AB_A453	
Cmd462	AB_A	



GDDR5 CMD Mapping		
CMD	0..31	32..63
CMD0	CAS	
CMD1	RST	
CMD2	RST	
CMD3	RST	
CMD4	AL_A0	
CMD5	AL_A1	
CMD6	AL_A2	
CMD7	AL_A3	
CMD8	AL_A4	
CMD9	AL_A5	
CMD10	AL_A6	
CMD11	AL_A7	
CMD12	AL_A8	
CMD13	AL_A9	
CMD14	AL_A10	
CMD15	AL_A11	
CMD16	AL_A12	
CMD17	AL_A13	
CMD18	AL_A14	
CMD19	AL_A15	
CMD20	AL_A16	
CMD21	AL_A17	
CMD22	AL_A18	
CMD23	AL_A19	
CMD24	AL_A20	
CMD25	AL_A21	
CMD26	AL_A22	
CMD27	AL_A23	
CMD28	AL_A24	
CMD29	AL_A25	
CMD30	AL_A26	
CMD31	AL_A27	
CMD32	AL_A28	
CMD33	AL_A29	
CMD34	AL_A30	
CMD35	AL_A31	
CMD36	AL_A32	
CMD37	AL_A33	
CMD38	AL_A34	
CMD39	AL_A35	
CMD40	AL_A36	
CMD41	AL_A37	
CMD42	AL_A38	
CMD43	AL_A39	
CMD44	AL_A40	
CMD45	AL_A41	
CMD46	AL_A42	
CMD47	AL_A43	
CMD48	AL_A44	
CMD49	AL_A45	
CMD50	AL_A46	
CMD51	AL_A47	
CMD52	AL_A48	
CMD53	AL_A49	
CMD54	AL_A50	
CMD55	AL_A51	
CMD56	AL_A52	
CMD57	AL_A53	
CMD58	AL_A54	
CMD59	AL_A55	
CMD60	AL_A56	
CMD61	AL_A57	
CMD62	AL_A58	
CMD63	AL_A59	
CMD64	AL_A60	
CMD65	AL_A61	
CMD66	AL_A62	
CMD67	AL_A63	
CMD68	AL_A64	
CMD69	AL_A65	
CMD70	AL_A66	
CMD71	AL_A67	
CMD72	AL_A68	
CMD73	AL_A69	
CMD74	AL_A70	
CMD75	AL_A71	
CMD76	AL_A72	
CMD77	AL_A73	
CMD78	AL_A74	
CMD79	AL_A75	
CMD80	AL_A76	
CMD81	AL_A77	
CMD82	AL_A78	
CMD83	AL_A79	
CMD84	AL_A80	
CMD85	AL_A81	
CMD86	AL_A82	
CMD87	AL_A83	
CMD88	AL_A84	
CMD89	AL_A85	
CMD90	AL_A86	
CMD91	AL_A87	
CMD92	AL_A88	
CMD93	AL_A89	
CMD94	AL_A90	
CMD95	AL_A91	
CMD96	AL_A92	
CMD97	AL_A93	
CMD98	AL_A94	
CMD99	AL_A95	
CMD100	AL_A96	
CMD101	AL_A97	
CMD102	AL_A98	
CMD103	AL_A99	
CMD104	AL_A100	
CMD105	AL_A101	
CMD106	AL_A102	
CMD107	AL_A103	
CMD108	AL_A104	
CMD109	AL_A105	
CMD110	AL_A106	
CMD111	AL_A107	
CMD112	AL_A108	
CMD113	AL_A109	
CMD114	AL_A110	
CMD115	AL_A111	
CMD116	AL_A112	
CMD117	AL_A113	
CMD118	AL_A114	
CMD119	AL_A115	
CMD120	AL_A116	
CMD121	AL_A117	
CMD122	AL_A118	
CMD123	AL_A119	
CMD124	AL_A120	
CMD125	AL_A121	
CMD126	AL_A122	
CMD127	AL_A123	
CMD128	AL_A124	
CMD129	AL_A125	
CMD130	AL_A126	
CMD131	AL_A127	
CMD132	AL_A128	
CMD133	AL_A129	
CMD134	AL_A130	
CMD135	AL_A131	
CMD136	AL_A132	
CMD137	AL_A133	
CMD138	AL_A134	
CMD139	AL_A135	
CMD140	AL_A136	
CMD141	AL_A137	
CMD142	AL_A138	
CMD143	AL_A139	
CMD144	AL_A140	
CMD145	AL_A141	
CMD146	AL_A142	
CMD147	AL_A143	
CMD148	AL_A144	
CMD149	AL_A145	
CMD150	AL_A146	
CMD151	AL_A147	
CMD152	AL_A148	
CMD153	AL_A149	
CMD154	AL_A150	
CMD155	AL_A151	
CMD156	AL_A152	
CMD157	AL_A153	
CMD158	AL_A154	
CMD159	AL_A155	
CMD160	AL_A156	
CMD161	AL_A157	
CMD162	AL_A158	
CMD163	AL_A159	
CMD164	AL_A160	
CMD165	AL_A161	
CMD166	AL_A162	
CMD167	AL_A163	
CMD168	AL_A164	
CMD169	AL_A165	
CMD170	AL_A166	
CMD171	AL_A167	
CMD172	AL_A168	
CMD173	AL_A169	
CMD174	AL_A170	
CMD175	AL_A171	
CMD176	AL_A172	
CMD177	AL_A173	
CMD178	AL_A174	
CMD179	AL_A175	
CMD180	AL_A176	
CMD181	AL_A177	
CMD182	AL_A178	
CMD183	AL_A179	
CMD184	AL_A180	
CMD185	AL_A181	
CMD186	AL_A182	
CMD187	AL_A183	
CMD188	AL_A184	
CMD189	AL_A185	
CMD190	AL_A186	
CMD191	AL_A187	
CMD192	AL_A188	
CMD193	AL_A189	
CMD194	AL_A190	
CMD195	AL_A191	
CMD196	AL_A192	
CMD197	AL_A193	
CMD198	AL_A194	
CMD199	AL_A195	
CMD200	AL_A196	
CMD201	AL_A197	
CMD202	AL_A198	
CMD203	AL_A199	
CMD204	AL_A200	
CMD205	AL_A201	
CMD206	AL_A202	
CMD207	AL_A203	
CMD208	AL_A204	
CMD209	AL_A205	
CMD210	AL_A206	
CMD211	AL_A207	
CMD212	AL_A208	
CMD213	AL_A209	
CMD214	AL_A210	
CMD215	AL_A211	
CMD216	AL_A212	
CMD217	AL_A213	
CMD218	AL_A214	
CMD219	AL_A215	
CMD220	AL_A216	
CMD221	AL_A217	
CMD222	AL_A218	
CMD223	AL_A219	
CMD224	AL_A220	
CMD225	AL_A221	
CMD226	AL_A222	
CMD227	AL_A223	
CMD228	AL_A224	
CMD229	AL_A225	
CMD230	AL_A226	
CMD231	AL_A227	
CMD232	AL_A228	
CMD233	AL_A229	
CMD234	AL_A230	
CMD235	AL_A231	
CMD236	AL_A232	
CMD237	AL_A233	
CMD238	AL_A234	
CMD239	AL_A235	
CMD240	AL_A236	
CMD241	AL_A237	
CMD242	AL_A238	
CMD243	AL_A239	
CMD244	AL_A240	
CMD245	AL_A241	
CMD246	AL_A242	
CMD247	AL_A243	
CMD248	AL_A244	
CMD249	AL_A245	
CMD250	AL_A246	
CMD251	AL_A247	
CMD252	AL_A248	
CMD253	AL_A249	
CMD254	AL_A250	
CMD255	AL_A251	
CMD256	AL_A252	
CMD257	AL_A253	
CMD258	AL_A254	
CMD259	AL_A255	
CMD260	AL_A256	
CMD261	AL_A257	
CMD262	AL_A258	
CMD263	AL_A259	
CMD264	AL_A260	
CMD265	AL_A261	
CMD266	AL_A262	
CMD267	AL_A263	
CMD268	AL_A264	
CMD269	AL_A265	
CMD270	AL_A266	
CMD271	AL_A267	
CMD272	AL_A268	
CMD273	AL_A269	
CMD274	AL_A270	
CMD275	AL_A271	
CMD276	AL_A272	
CMD277	AL_A273	
CMD278	AL_A274	
CMD279	AL_A275	
CMD280	AL_A276	
CMD281	AL_A277	
CMD282	AL_A278	
CMD283	AL_A279	
CMD284	AL_A280	
CMD285	AL_A281	
CMD286	AL_A282	
CMD287	AL_A283	
CMD288	AL_A284	
CMD289	AL_A285	
CMD290	AL_A286	
CMD291	AL_A287	
CMD292	AL_A288	
CMD293	AL_A289	
CMD294	AL_A290	
CMD295	AL_A291	
CMD296	AL_A292	
CMD297	AL_A293	
CMD298	AL_A294	
CMD299	AL_A295	
CMD300	AL_A296	
CMD301	AL_A297	
CMD302	AL_A298	
CMD303	AL_A299	
CMD304	AL_A300	
CMD305	AL_A301	
CMD306	AL_A302	
CMD307	AL_A303	
CMD308	AL_A304	
CMD309	AL_A305	
CMD310	AL_A306	
CMD311	AL_A307	
CMD312	AL_A308	
CMD313	AL_A309	
CMD314	AL_A310	
CMD315	AL_A311	
CMD316	AL_A312	
CMD317	AL_A313	
CMD318	AL_A314	
CMD319	AL_A315	
CMD320	AL_A316	
CMD321	AL_A317	
CMD322	AL_A318	
CMD323	AL_A319	
CMD324	AL_A320	
CMD325	AL_A321	
CMD326	AL_A322	
CMD327	AL_A323	
CMD328	AL_A324	
CMD329	AL_A325	
CMD330	AL_A326	
CMD331	AL_A327	
CMD332	AL_A328	
CMD333	AL_A329	
CMD334	AL_A330	
CMD335	AL_A331	
CMD336	AL_A332	
CMD337	AL_A333	
CMD338	AL_A334	
CMD339	AL_A335	
CMD340	AL_A336	
CMD341	AL_A337	
CMD342	AL_A338	
CMD343	AL_A339	
CMD344	AL_A340	
CMD345	AL_A341	
CMD346	AL_A342	
CMD347	AL_A343	
CMD348	AL_A344	
CMD349	AL_A345	
CMD350	AL_A346	
CMD351	AL_A347	
CMD352	AL_A348	
CMD353	AL_A349	
CMD354	AL_A350	
CMD355	AL_A351	
CMD356	AL_A352	
CMD357	AL_A353	
CMD358	AL_A354	
CMD359	AL_A355	
CMD360	AL_A356	
CMD361	AL_A357	
CMD362	AL_A358	
CMD363	AL_A359	
CMD364	AL_A360	
CMD365	AL_A361	
CMD366	AL_A362	
CMD367	AL_A363	
CMD368	AL_A364	
CMD369	AL_A365	
CMD370	AL_A366	
CMD371	AL_A367	
CMD372	AL_A368	
CMD373	AL_A369	
CMD374	AL_A370	
CMD375	AL_A371	
CMD376	AL_A372	
CMD377	AL_A373	
CMD378	AL_A374	
CMD379	AL_A375	
CMD380	AL_A376	
CMD381	AL_A377	
CMD382	AL_A378	
CMD383	AL_A379	
CMD384	AL_A380	
CMD385	AL_A381	
CMD386	AL_A382	
CMD387	AL_A383	
CMD388	AL_A384	
CMD389	AL_A385	
CMD390	AL_A386	
CMD391	AL_A387	
CMD392	AL_A388	
CMD393	AL_A389	
CMD394	AL_A390	
CMD395	AL_A391	
CMD396	AL_A392	
CMD397	AL_A393	
CMD398	AL_A394	
CMD399	AL_A395	
CMD400	AL_A396	
CMD401	AL_A397	
CMD402	AL_A398	
CMD403	AL_A399	
CMD404	AL_A400	
CMD405	AL_A401	
CMD406	AL_A402	
CMD407	AL_A403	
CMD408	AL_A404	
CMD409	AL_A405	
CMD410	AL_A406	
CMD411	AL_A407	
CMD412	AL_A408	
CMD413	AL_A409	
CMD414	AL_A410	
CMD415	AL_A411	
CMD416	AL_A412	
CMD417	AL_A413	
CMD418	AL_A414	
CMD419	AL_A415	
CMD420	AL_A416	
CMD421	AL_A417	
CMD422	AL_A418	
CMD423	AL_A419	
CMD424	AL_A420	
CMD425	AL_A421	
CMD426	AL_A422	
CMD427	AL_A423	
CMD428	AL_A424	
CMD429	AL_A425	

- \*\* Vref switching options:
  - Internal Vref should be POR for VrefID
  - potential for update

- \*\* Vref switching options:
  - Internal Vref should be POR for VrefID
  - potential for update



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
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4x 4.7uF, D603

2x10uF, 0805

FBVDDQ

GND

4.7uF 4.7uF 4.7uF 4.7uF 10uF 10uF

C754 C746 C753 C741 C777 C740

3.3V 3.3V 3.3V 3.3V 10V 10V

10% 10% 10% 10% 10% 10%

XSR XSR XSR XSR XSR XSR

0805 0805 0805 0805 0805 0805

COMMON COMMON COMMON COMMON COMMON COMMON

FBVDDQ

GND

10 x 1uF, 0402

1uF 1uF 1uF 1uF 1uF 1uF 1uF

C738 C755 C776 C775 C721 C720 C75

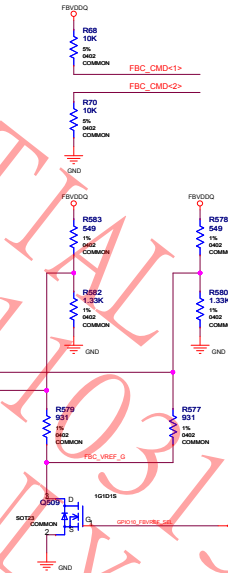
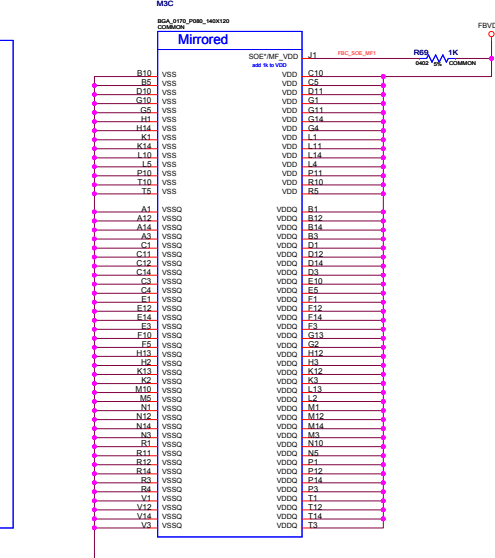
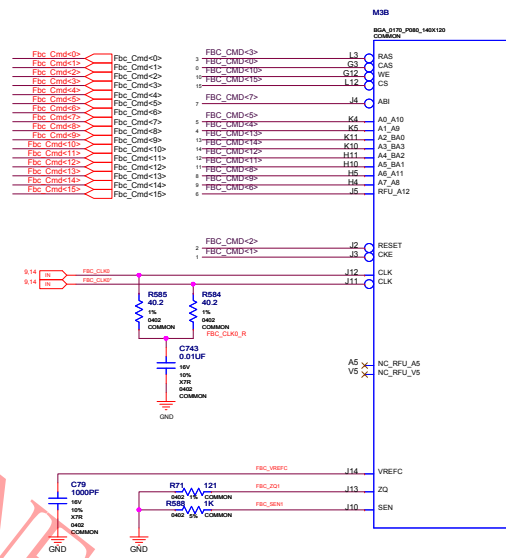
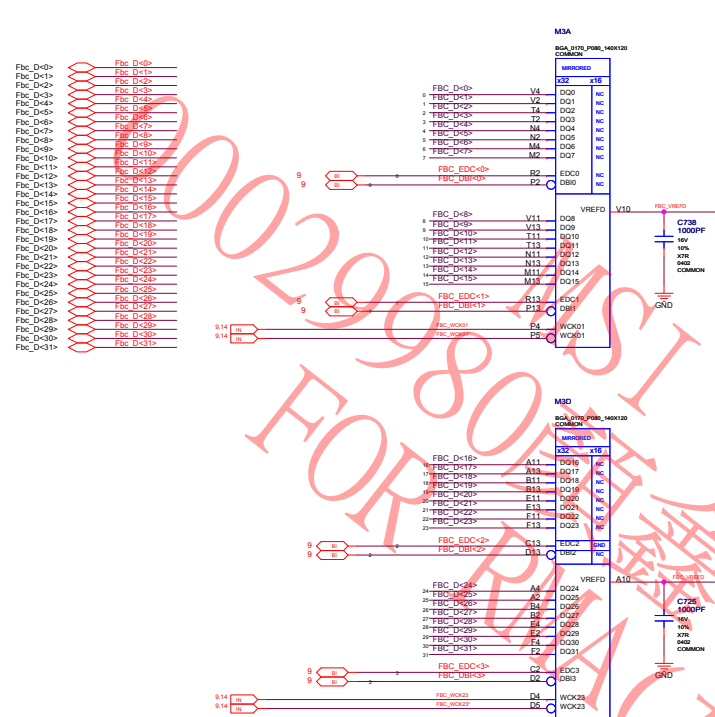
3.3V 3.3V 3.3V 3.3V 3.3V 3.3V 3.3V


10% 10% 10% 10% 10% 10% 10%

XSR XSR XSR XSR XSR XSR XSR

0402 0402 0402 0402 0402 0402 0402

COMMON COMMON COMMON COMMON COMMON COMMON COMMON

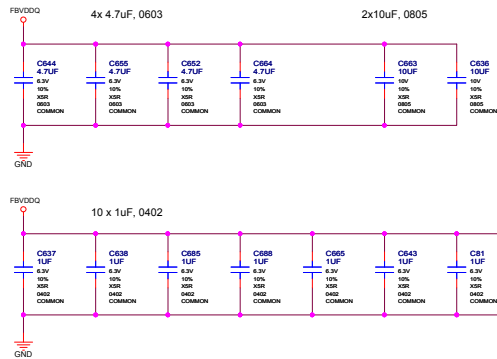
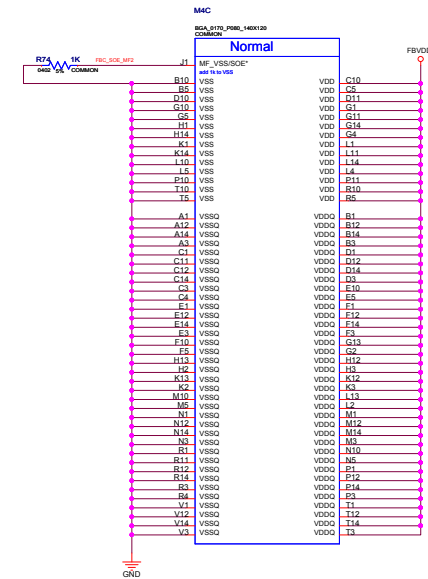
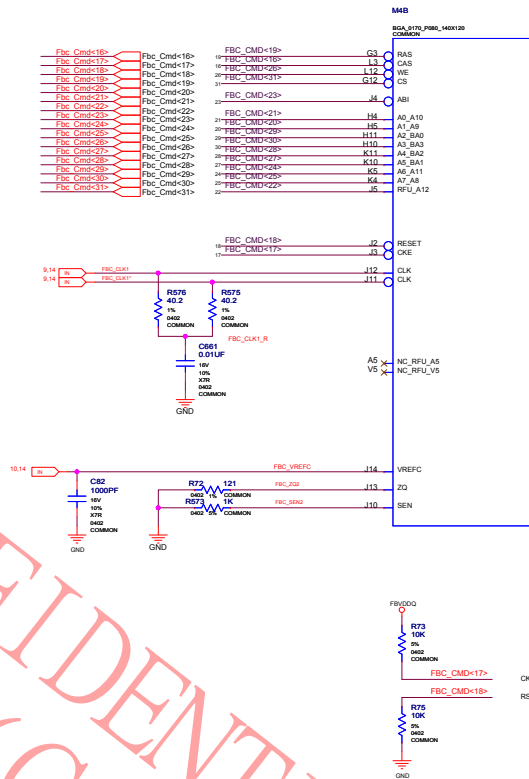

$$V_{REF} = 1.5 * [(542/542+549)] = 0.75V$$


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Fdc\_Dc31a  
 Fdc\_Dc32a  
 Fdc\_Dc33a  
 Fdc\_Dc34a  
 Fdc\_Dc35a  
 Fdc\_Dc36a  
 Fdc\_Dc37a  
 Fdc\_Dc38a  
 Fdc\_Dc39a  
 Fdc\_Dc40a  
 Fdc\_Dc41a  
 Fdc\_Dc42a  
 Fdc\_Dc43a  
 Fdc\_Dc44a  
 Fdc\_Dc45a  
 Fdc\_Dc46a  
 Fdc\_Dc47a  
 Fdc\_Dc48a  
 Fdc\_Dc49a  
 Fdc\_Dc50a  
 Fdc\_Dc51a  
 Fdc\_Dc52a  
 Fdc\_Dc53a  
 Fdc\_Dc54a  
 Fdc\_Dc55a  
 Fdc\_Dc56a  
 Fdc\_Dc57a  
 Fdc\_Dc58a  
 Fdc\_Dc59a  
 Fdc\_Dc60a  
 Fdc\_Dc61a  
 Fdc\_Dc62a

Fdc\_Dc31b  
 Fdc\_Dc32b  
 Fdc\_Dc33b  
 Fdc\_Dc34b  
 Fdc\_Dc35b  
 Fdc\_Dc36b  
 Fdc\_Dc37b  
 Fdc\_Dc38b  
 Fdc\_Dc39b  
 Fdc\_Dc40b  
 Fdc\_Dc41b  
 Fdc\_Dc42b  
 Fdc\_Dc43b  
 Fdc\_Dc44b  
 Fdc\_Dc45b  
 Fdc\_Dc46b  
 Fdc\_Dc47b  
 Fdc\_Dc48b  
 Fdc\_Dc49b  
 Fdc\_Dc50b  
 Fdc\_Dc51b  
 Fdc\_Dc52b  
 Fdc\_Dc53b  
 Fdc\_Dc54b  
 Fdc\_Dc55b  
 Fdc\_Dc56b  
 Fdc\_Dc57b  
 Fdc\_Dc58b  
 Fdc\_Dc59b  
 Fdc\_Dc60b  
 Fdc\_Dc61b  
 Fdc\_Dc62b  
 Fdc\_Dc63b

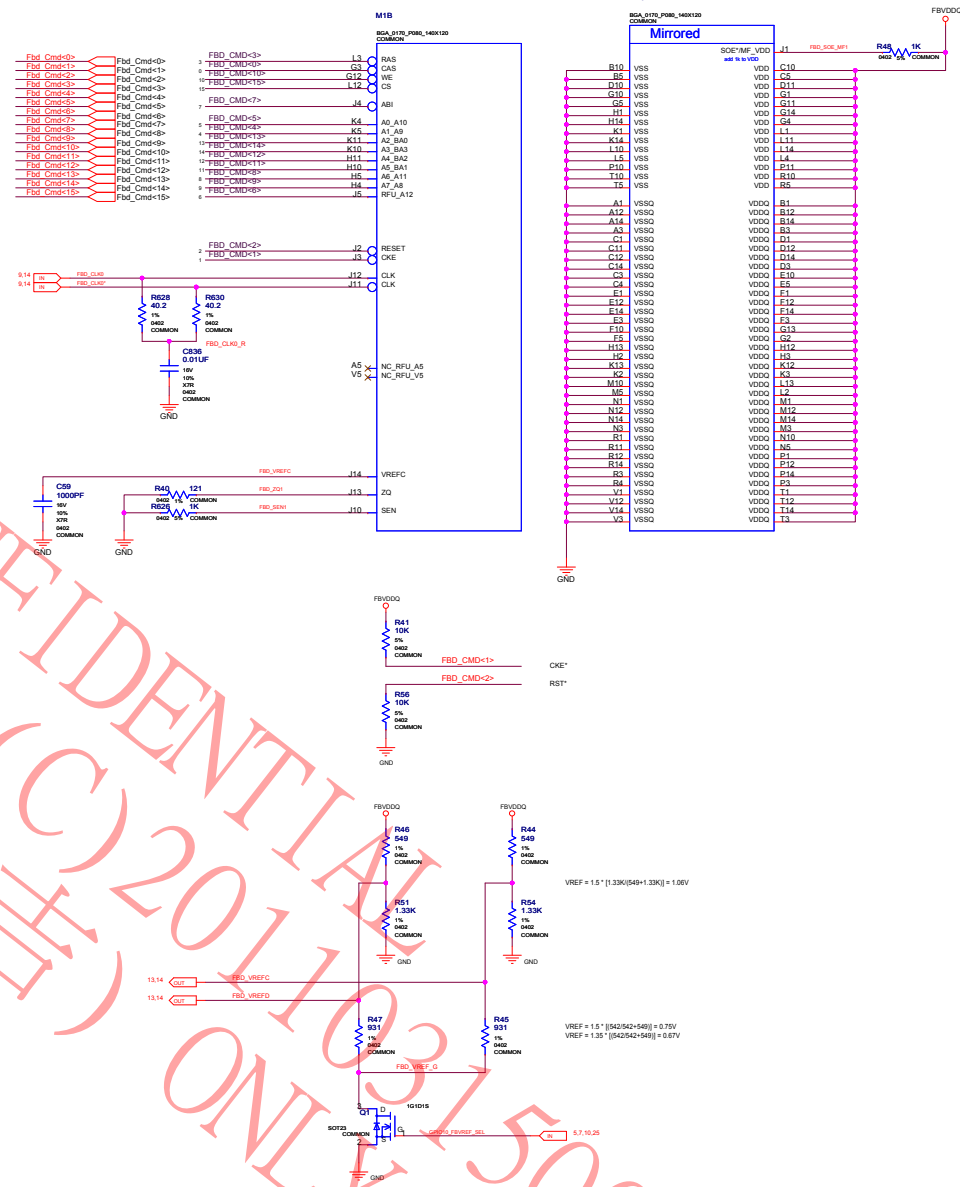
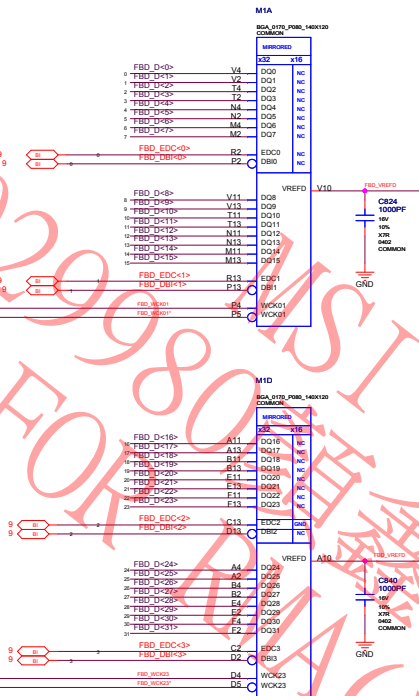
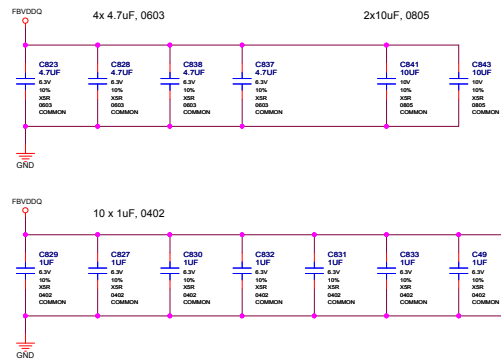
0.14  
 0.14  
 0.14



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GDORs CMD Mapping		
CMD	0..31	32..63
CM00	CM07	
CM01	CM08	
CM02	CM09	
CM03	CM10	
CM04	CM11	
CM05	CM12	
CM06	CM13	
CM07	CM14	
CM08	CM15	
CM09	CM16	
CM10	CM17	
CM11	CM18	
CM12	CM19	
CM13	CM20	
CM14	CM21	
CM15	CM22	
CM16	CM23	
CM17	CM24	
CM18	CM25	
CM19	CM26	
CM20	CM27	
CM21	CM28	
CM22	CM29	
CM23	CM30	
CM24	CM31	

\*\* Vref switching options:  
- Internal Vref should be POR for Vref0  
- potential for update



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PAGE: DETAIL	FBD Partition 31..0

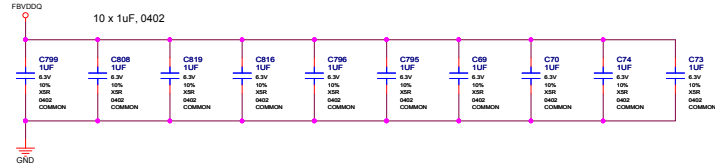
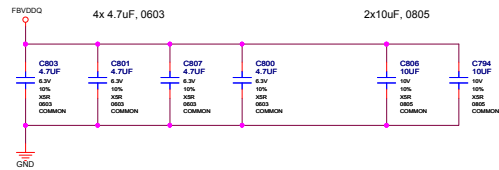
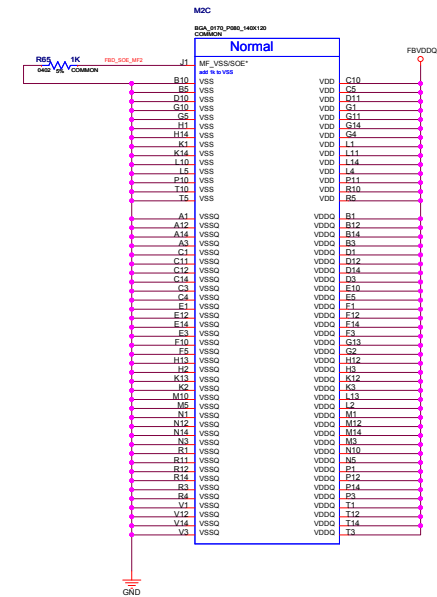
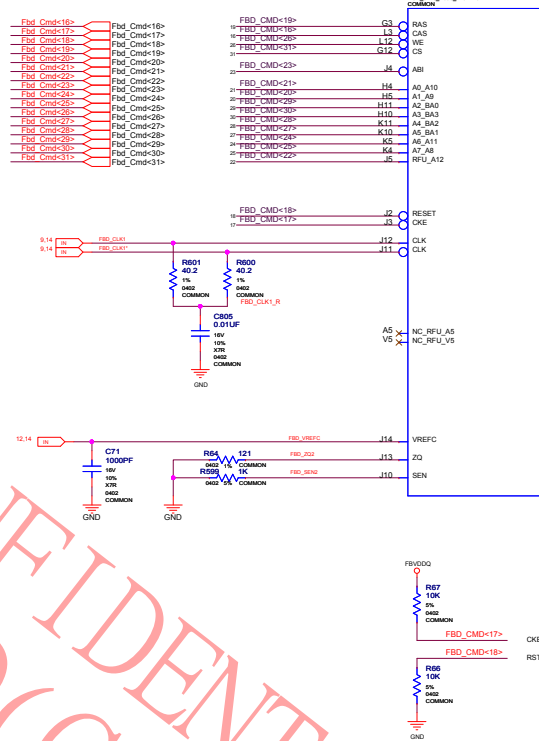
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PCB REV	P104T-B01	PAGE	
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\*\* Vref switching options:  
- internal Vref should be POR for VrefD  
- external Vref for update



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PCB REV	P1041-B01
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BOM REV	A	DATE	12-MAY-2010
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A horizontal number line with a vertical line at the center. The left side is labeled 'H' and the right side is labeled 'H'.

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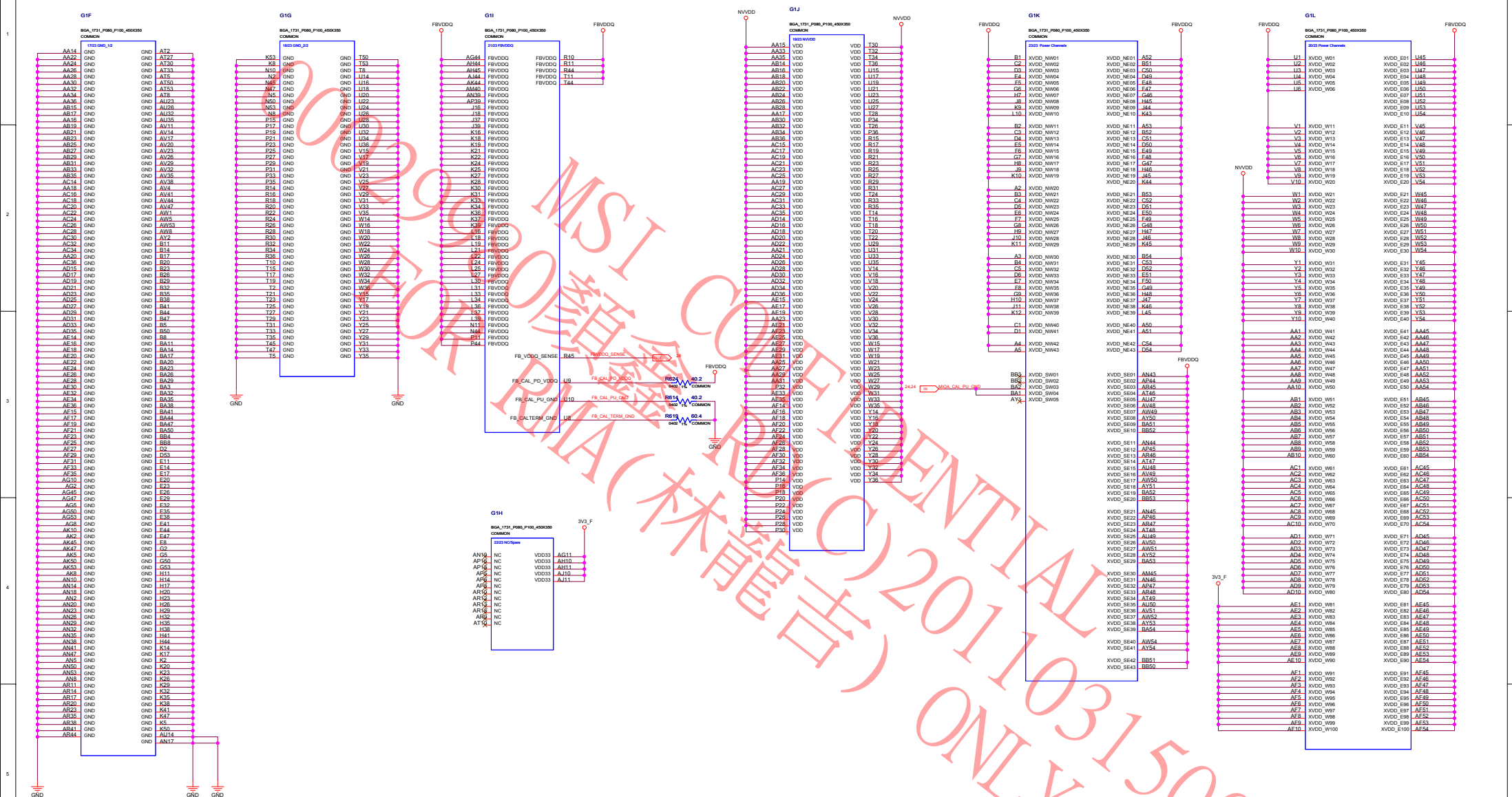
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PAGE DETAIL	FB Net Properties

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\*\* Power channels are configurable.  
XVDD\_\* pins are not connected on the substrate.  
Therefore, XVDD\_\* pins can be assigned as needed.



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ASSEMBLY BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO\_31UFF ASSEMBLY NOTES AND BOARD NOT FINAL  
PAGE: DETAIL GPU PWR and GND

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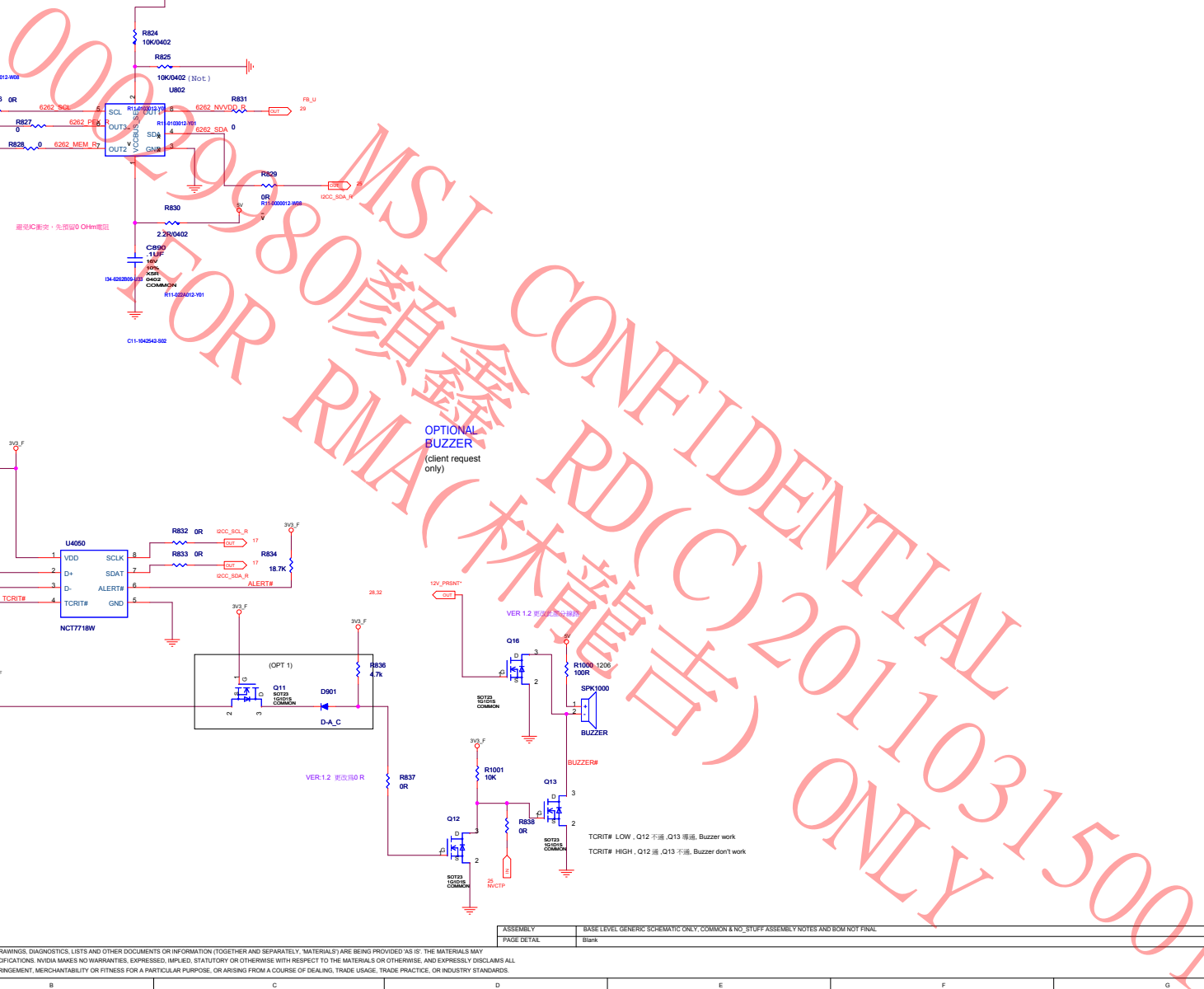
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO_31UFF ASSEMBLY NOTES AND BOP NOT FINAL
PAGE: DETAIL	GPU Decoupling


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SANTA CLARA, CA 95050, USA

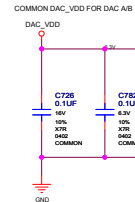


INV_PN	600-11041-BASE-000
PCB REV	P104T-B01
BOX REV	A
PAGE	
DATE	12-MAY-2010



<b>NVIDIA CORPORATION</b> 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA			
<b>NV_PN</b>	<b>600-11041-BASE-000</b>		
<b>PCB REV</b>	P1041-B01	<b>PAGE</b>	
<b>BOM REV</b>	A	<b>DATE</b>	12-MAY-2010





NET	NV_CRITICAL	NV_IMPEDANCE
1	1	1
2	1	1
3	1	1
4	1	1
5	1	1
6	1	1
7	1	1
8	1	1
9	1	1
10	1	1
11	1	1
12	1	1
13	1	1
14	1	1
15	1	1
16	1	1
17	1	1
18	1	1
19	1	1
20	1	1
21	1	1
22	1	1
23	1	1
24	1	1
25	1	1
26	1	1
27	1	1
28	1	1
29	1	1
30	1	1
31	1	1
32	1	1
33	1	1
34	1	1
35	1	1
36	1	1
37	1	1
38	1	1
39	1	1
40	1	1
41	1	1
42	1	1
43	1	1
44	1	1
45	1	1
46	1	1
47	1	1
48	1	1
49	1	1
50	1	1
51	1	1
52	1	1
53	1	1
54	1	1
55	1	1
56	1	1
57	1	1
58	1	1
59	1	1
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61	1	1
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69	1	1
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72	1	1
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79	1	1
80	1	1
81	1	1
82	1	1
83	1	1
84	1	1
85	1	1
86	1	1
87	1	1
88	1	1
89	1	1
90	1	1
91	1	1
92	1	1
93	1	1
94	1	1
95	1	1
96	1	1
97	1	1
98	1	1
99	1	1
100	1	1

18,20	IN	DACA_RED_DVI	1	750HM
18,20	IN	DACA_GREEN_DVI	1	750HM
18,20	IN	DACA_BLUE_DVI	1	750HM

18,20	IN	DACA_HS_DVI	2	SOC1M
18,20	IN	DACA_VS_DVI	2	SOC1M

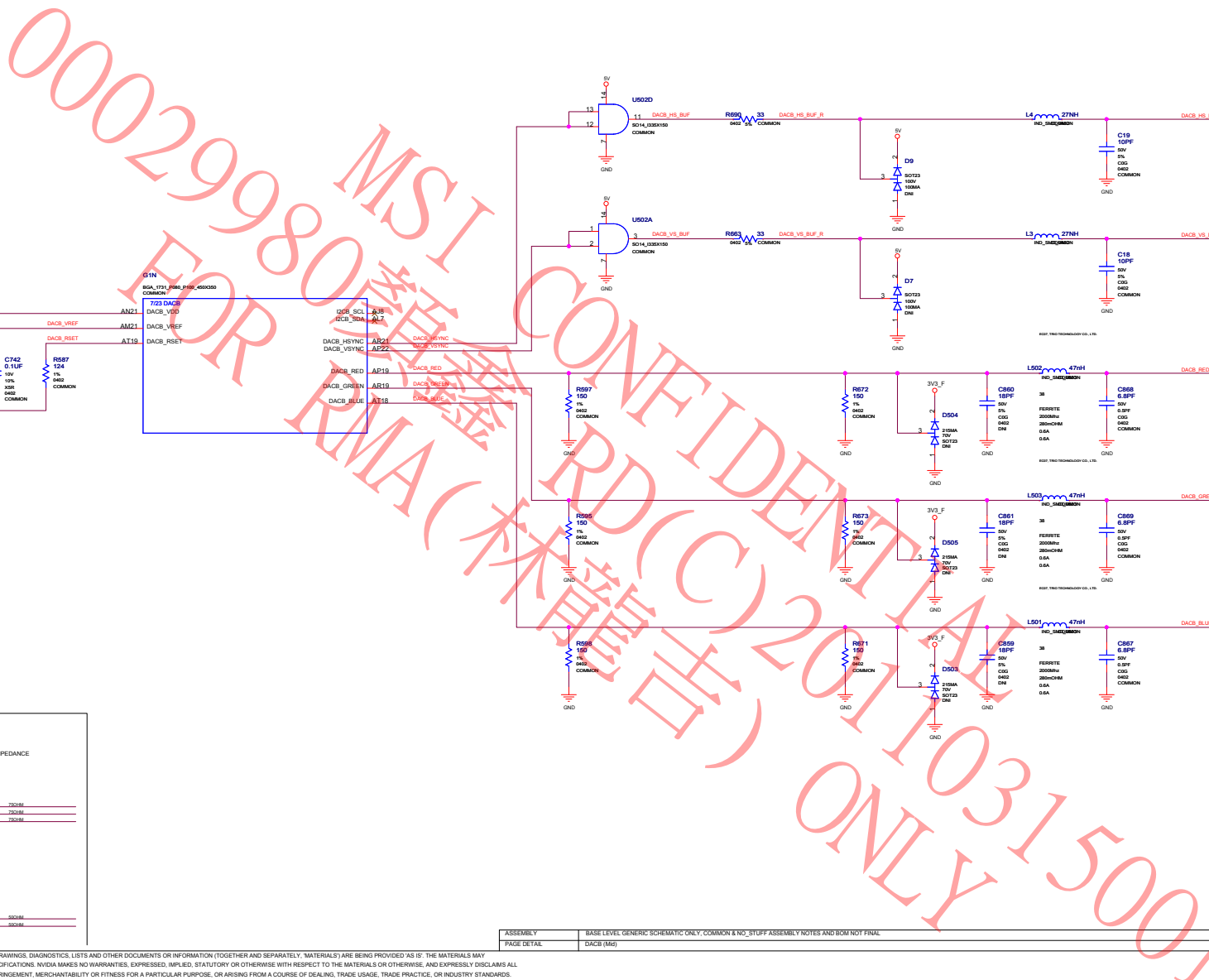
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACA (South)



	NET	NV_CRITICAL	NV_IMPORTANCE
15:21	IN	DACS_RED_OV1	1
15:21	IN	DACS_GREEN_OV1	1
15:21	IN	DACS_BLUE_OV1	1
15:21	IN	DACS_YELLOW_OV1	2
15:21	IN	DACS_YELLOW_OV2	2

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACB (Mid)

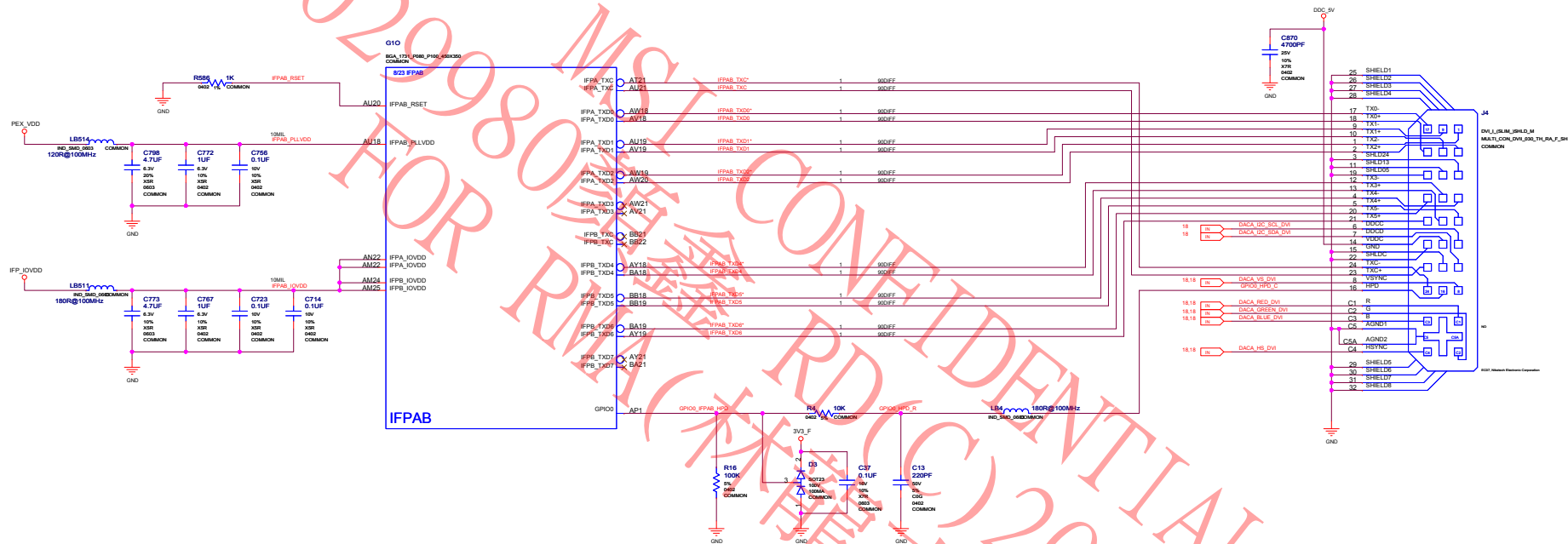
BOM REV	A
---------	---

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PAGE	
DATE	12-MAY-2010

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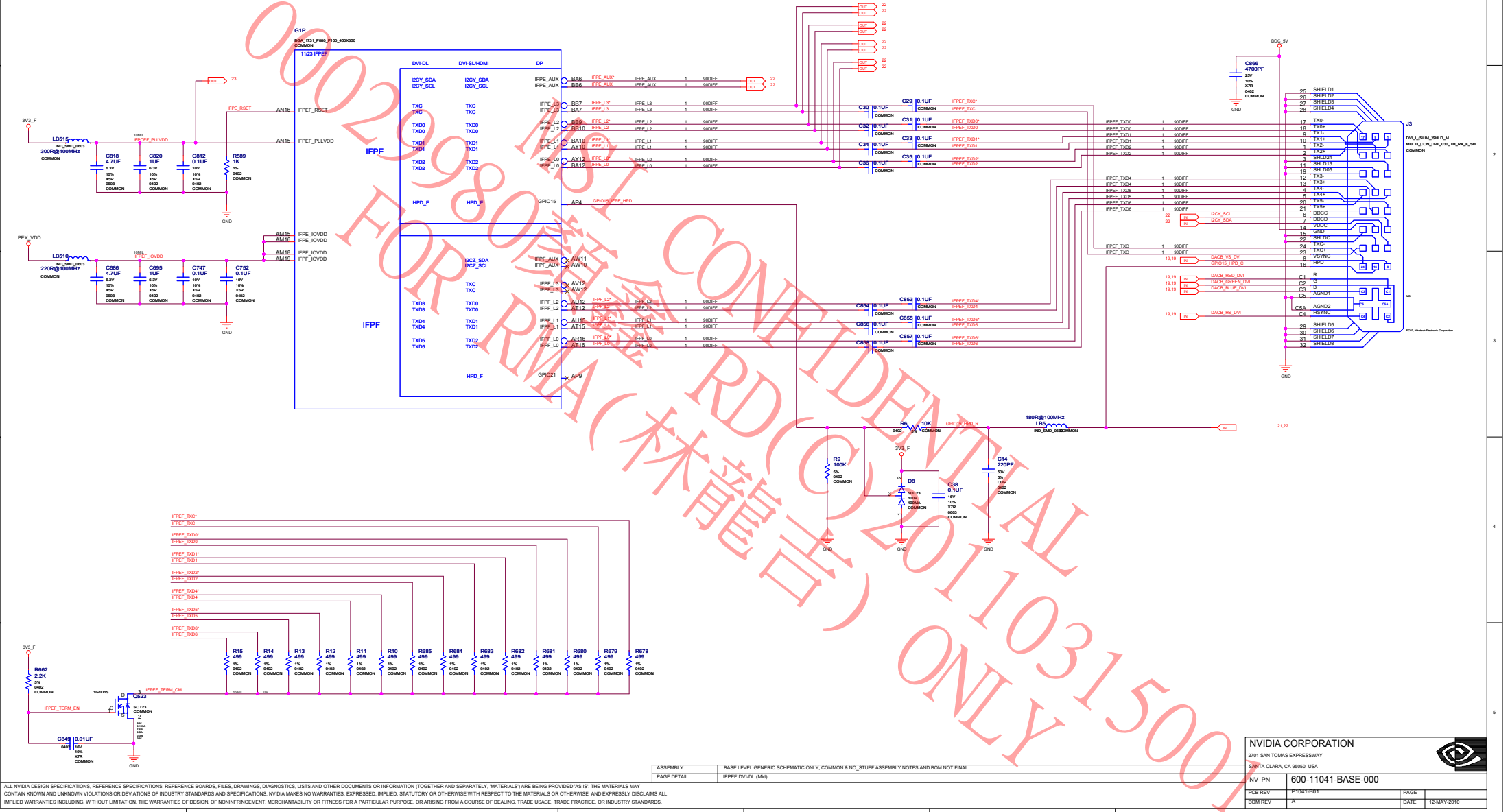
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REV_PN	600-11041-BASE-000
PCB REV	P104T-B01
BCM REV	A

PAGE	
DATE	12-MAY-2010

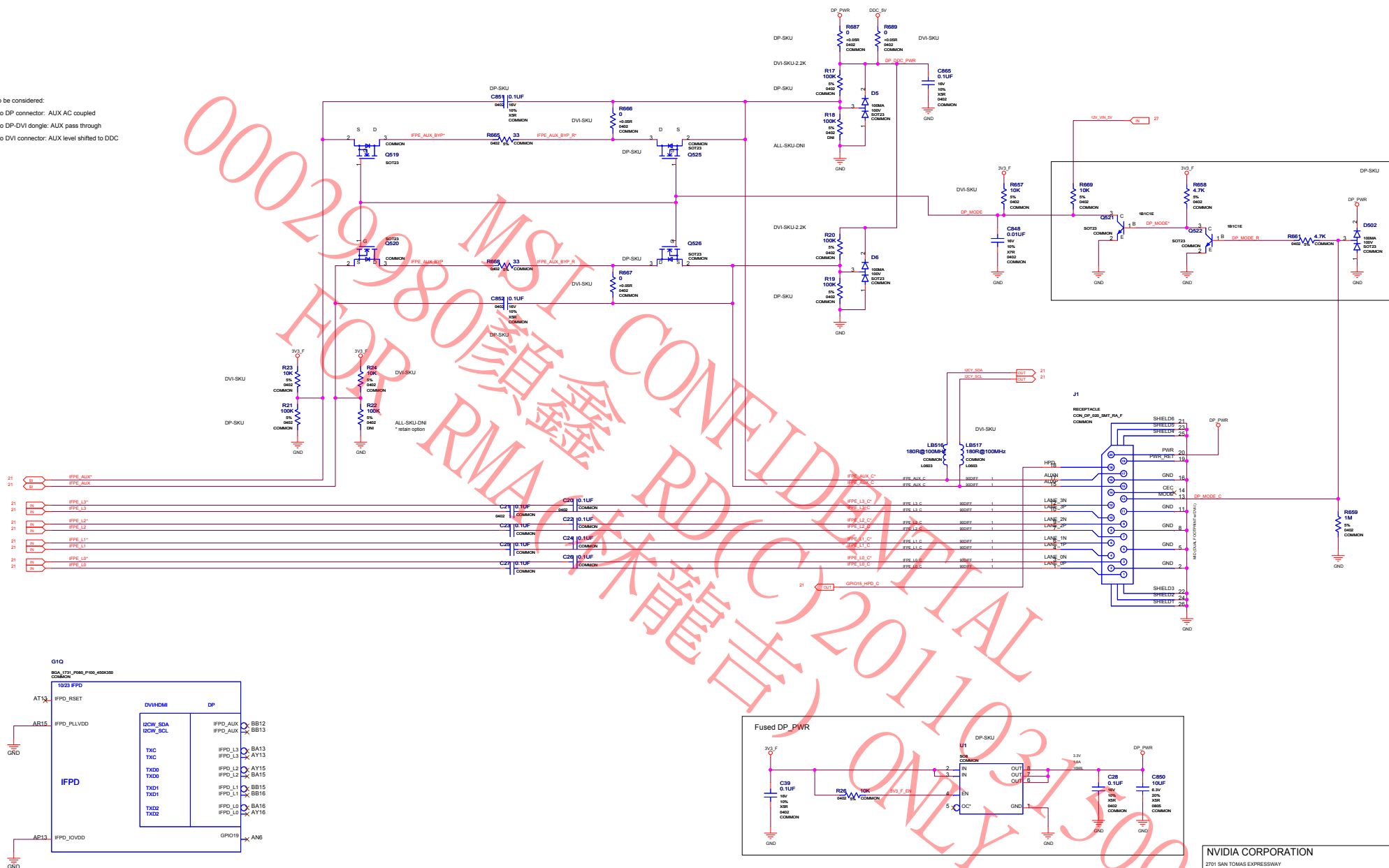
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Three cases to be considered:

1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through
3. DP AUX to DVI connector: AUX level shifted to DDC



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	IFPEF DP (Mid)

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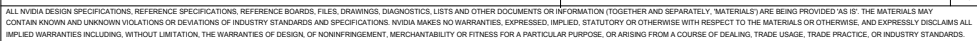
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-------	--------------------


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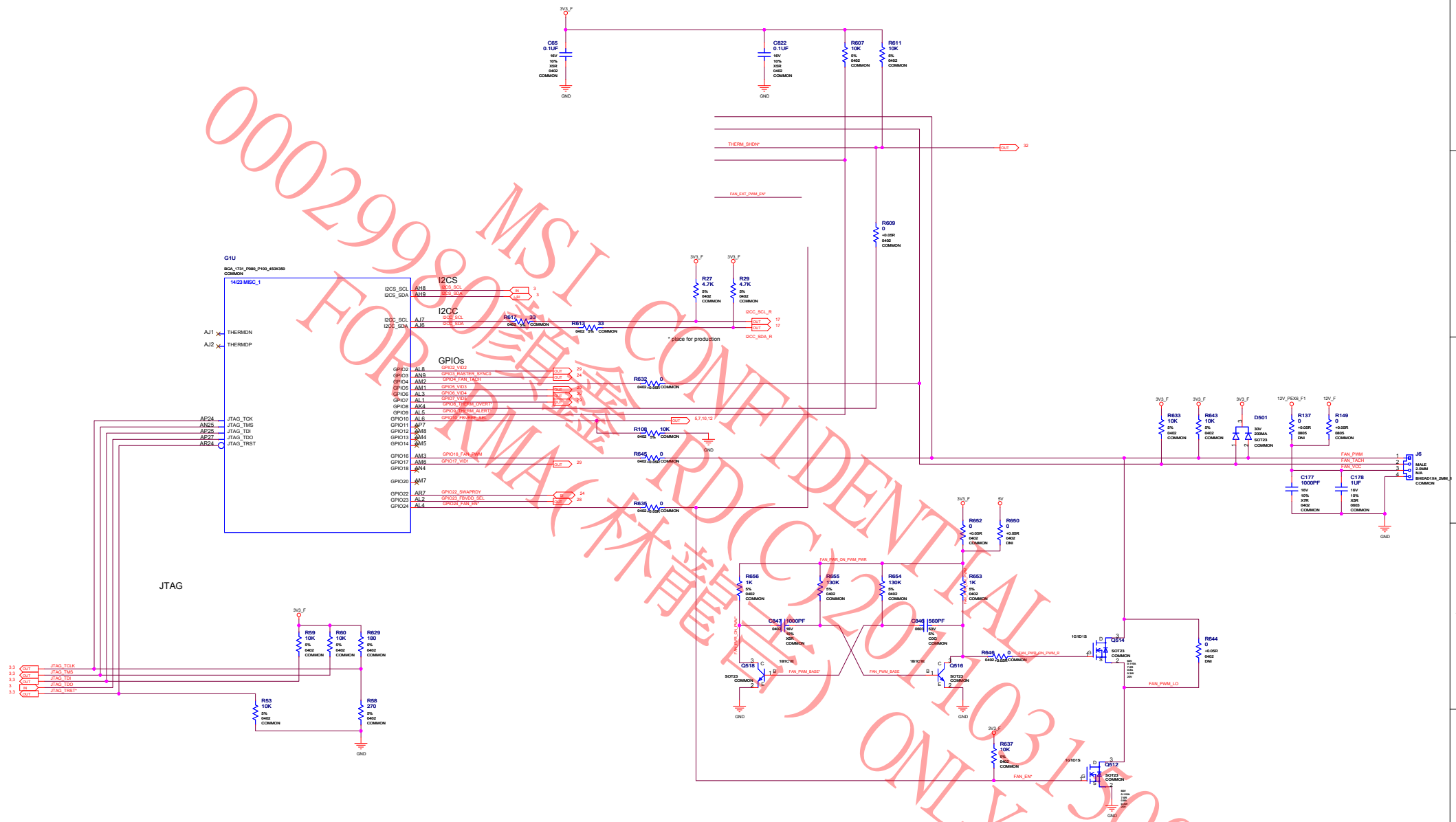
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[illegible]

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	IFPC mHOMI (North)

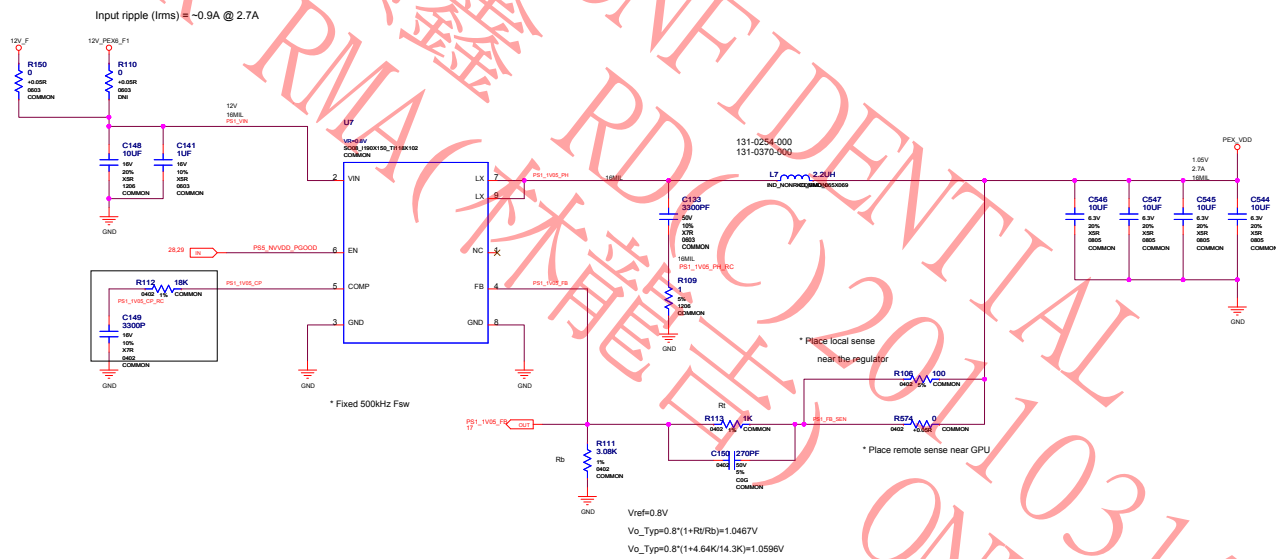


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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PS: 5V, PEX_VDD

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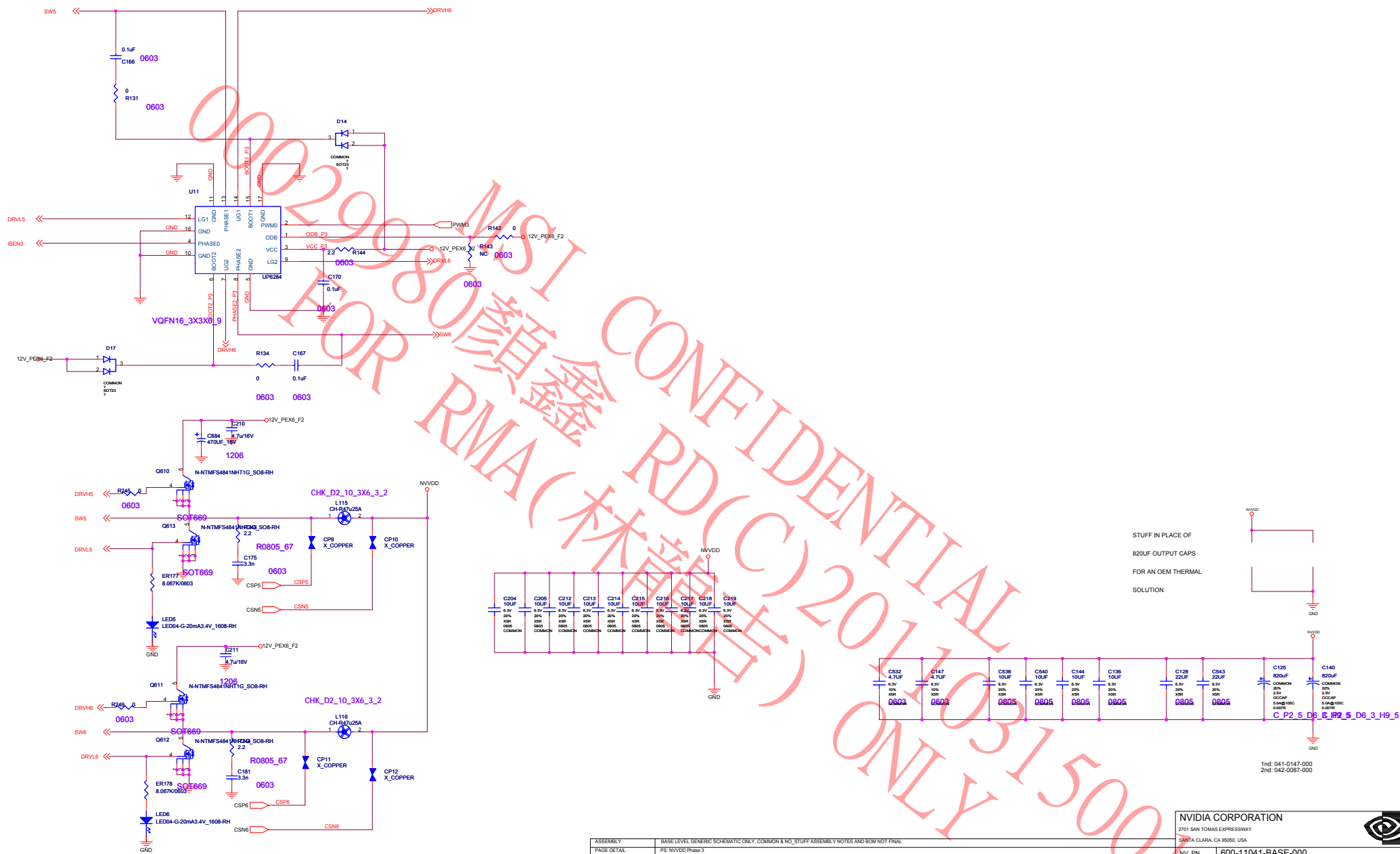
A horizontal number line with several tick marks. A vertical line segment is drawn from the second tick mark to the right, labeled 'H'.



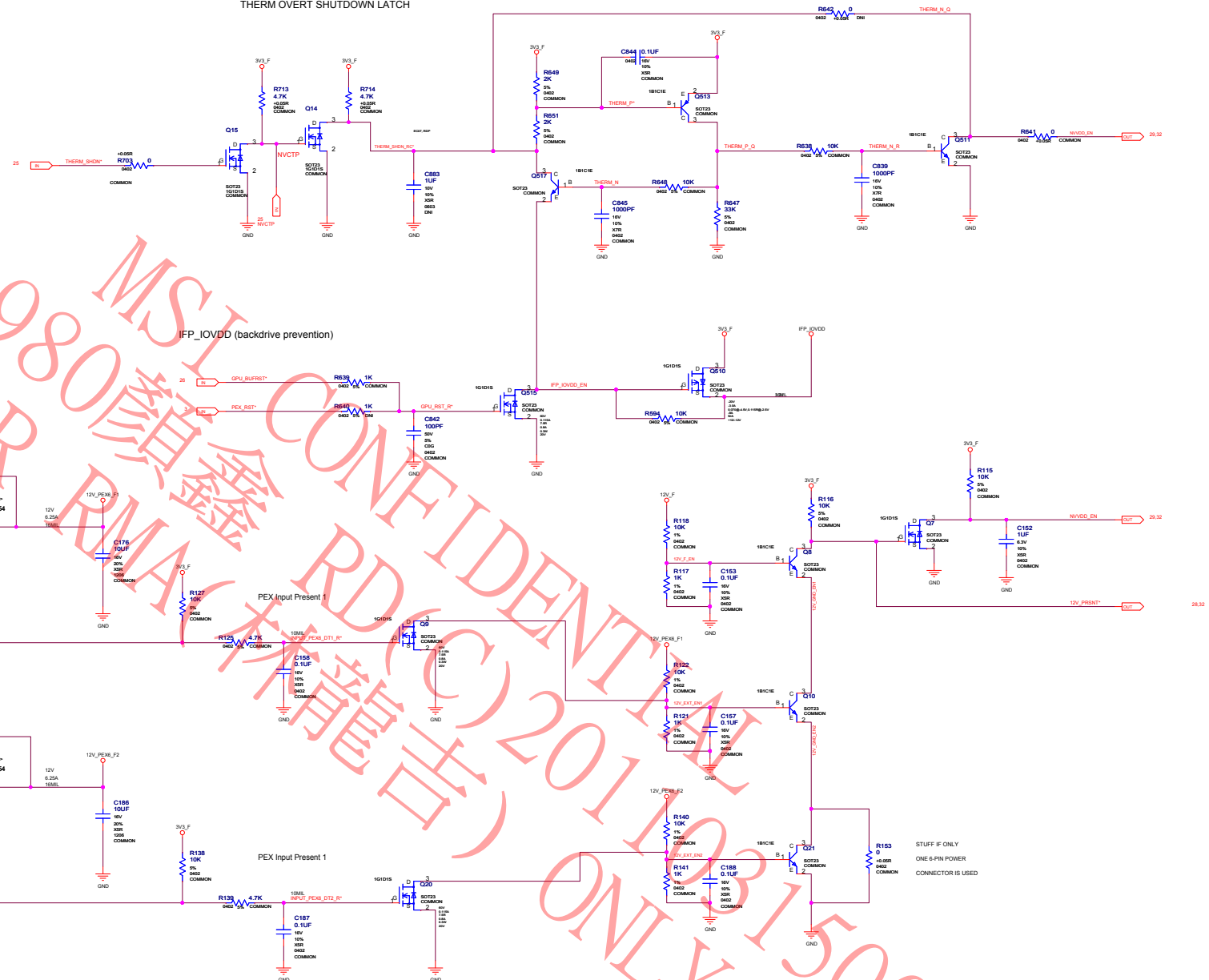
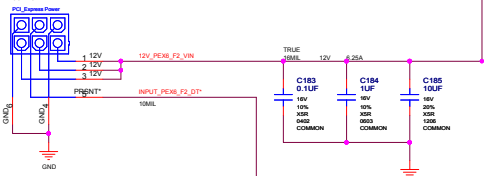
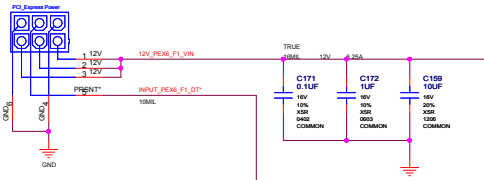
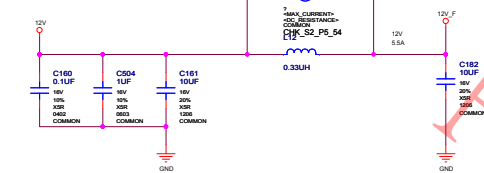
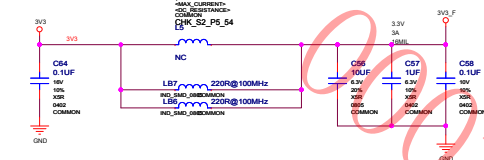
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Brackets:

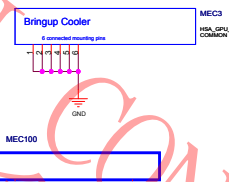
Bracket with DVI\_DP\_rHEMI : 151-10001-0355-071



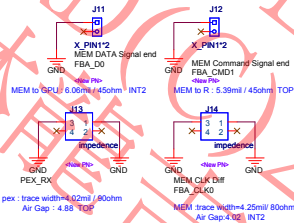
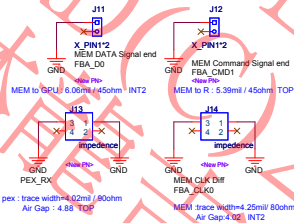
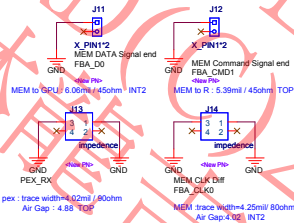
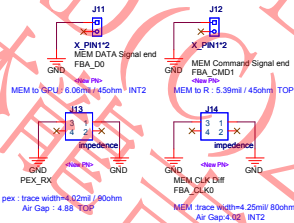
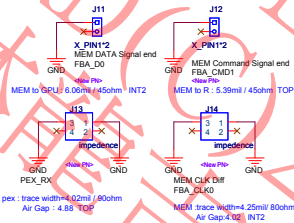
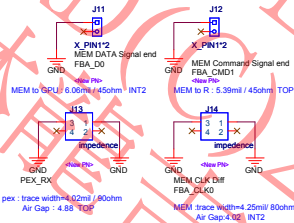
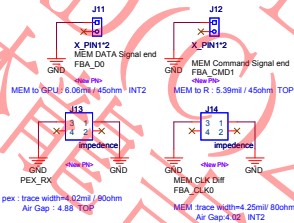
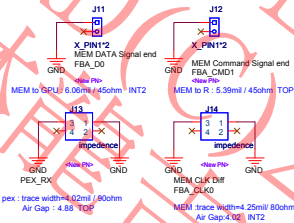
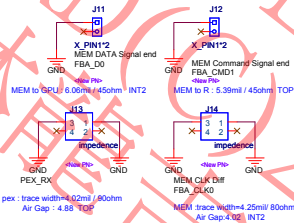
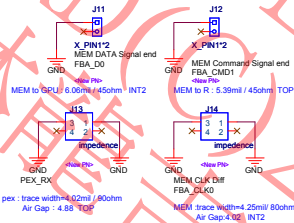
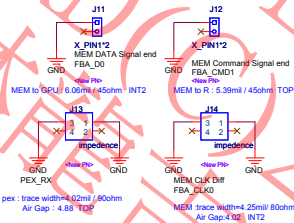
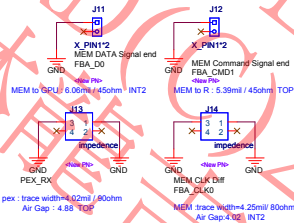
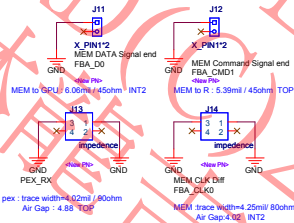
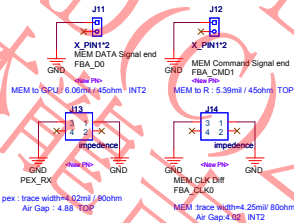
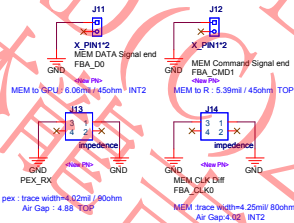
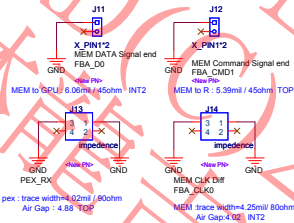
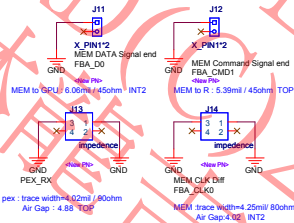
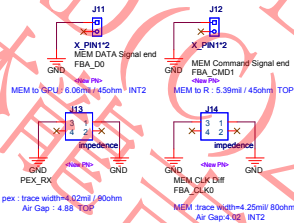
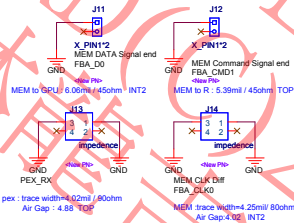
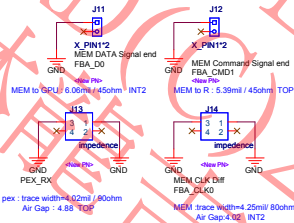
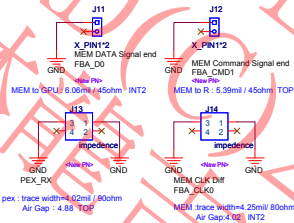
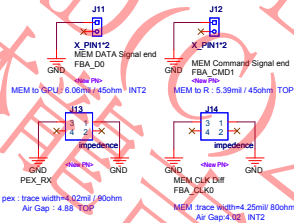
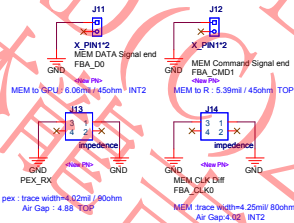
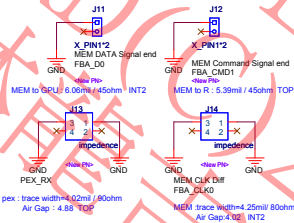
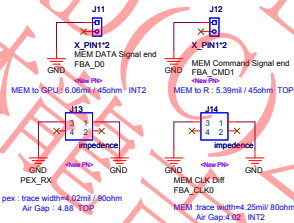
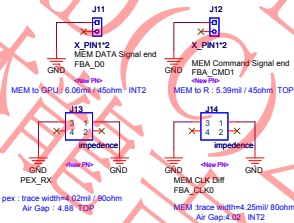
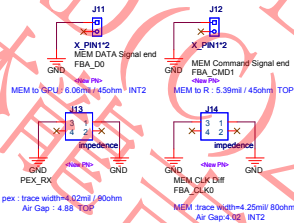
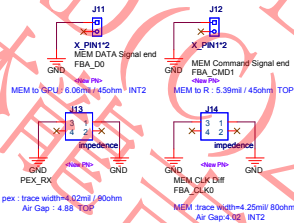
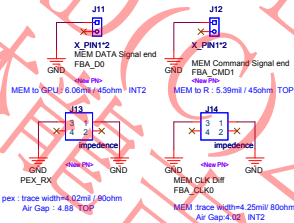
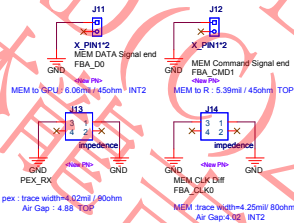
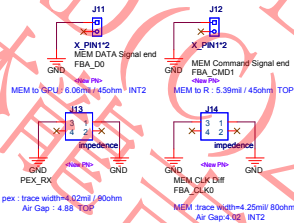
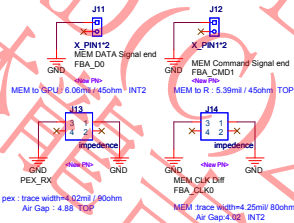
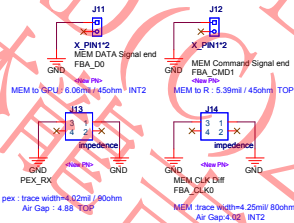
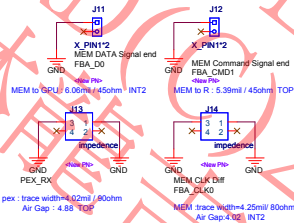
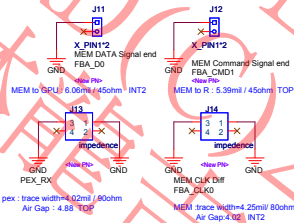
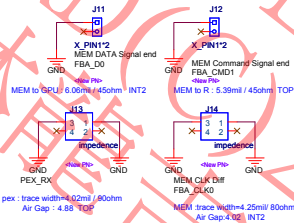
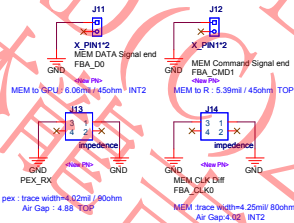
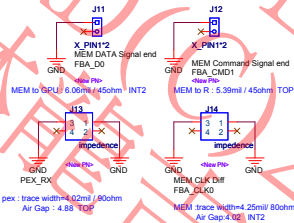
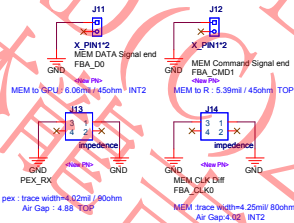
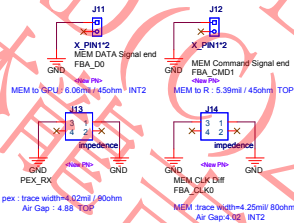
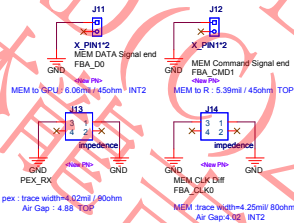
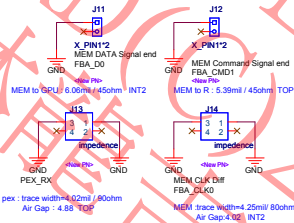
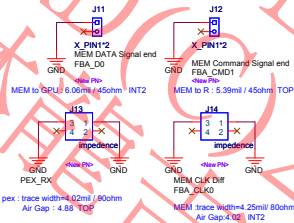
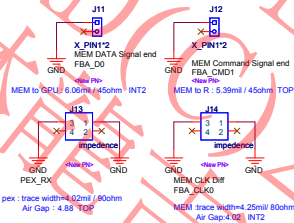
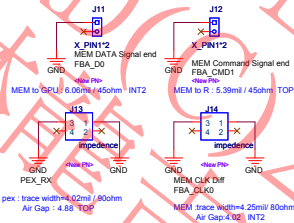
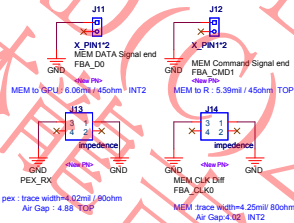
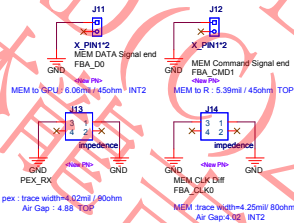
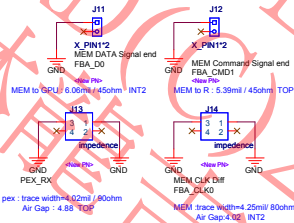
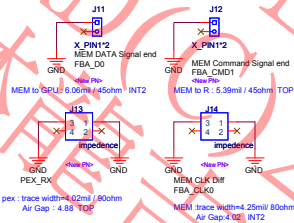
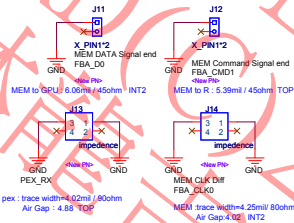
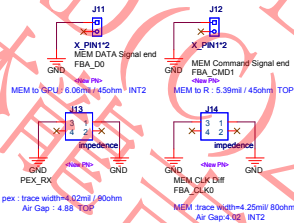
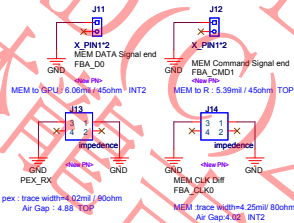
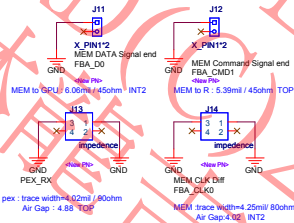
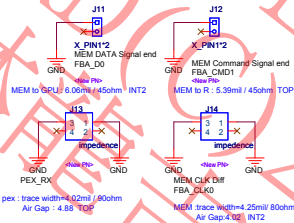
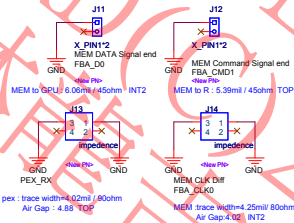
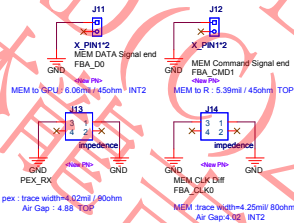
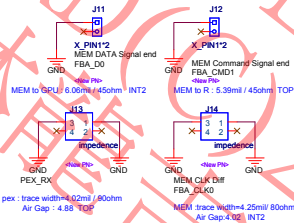
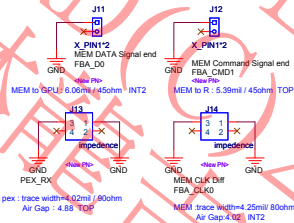
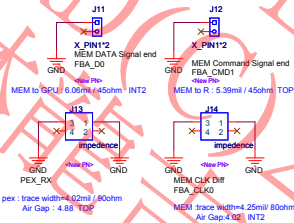
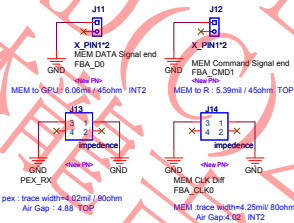
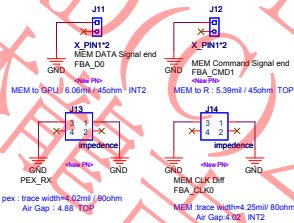
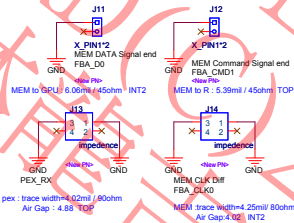
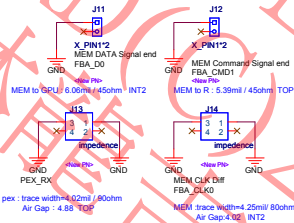
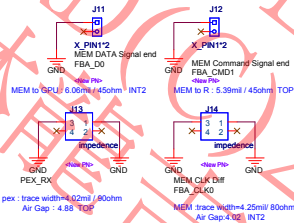
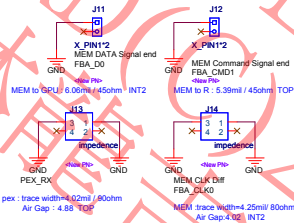
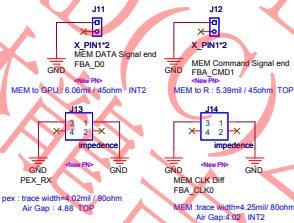
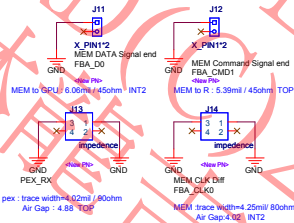
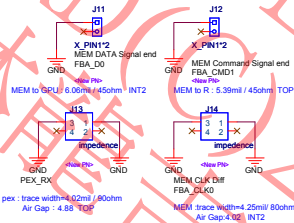
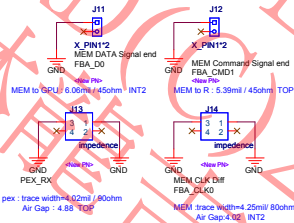
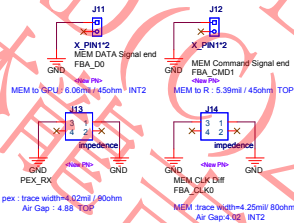
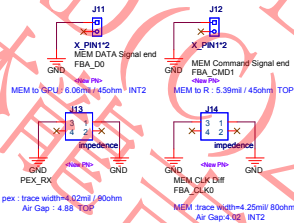
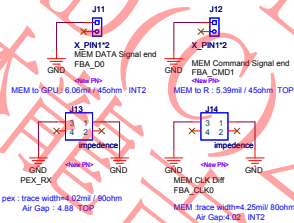
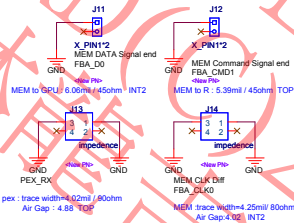
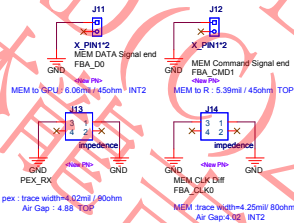
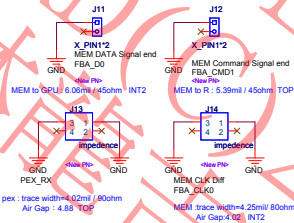
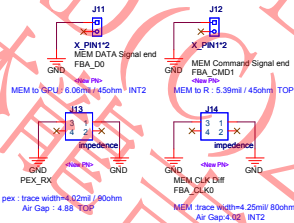
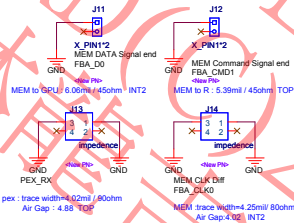
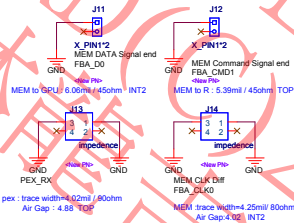
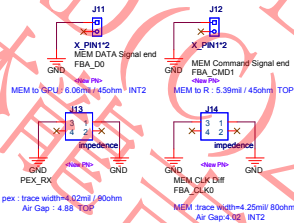
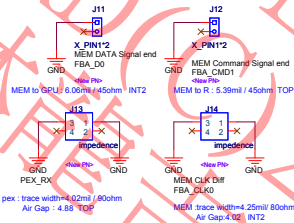
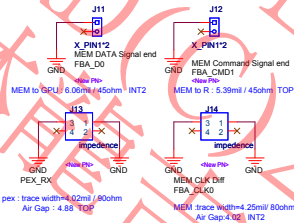
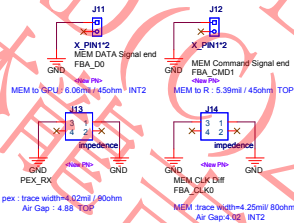
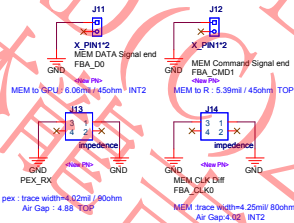
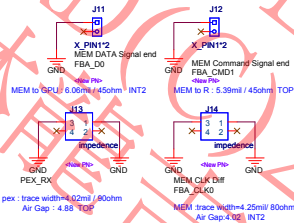
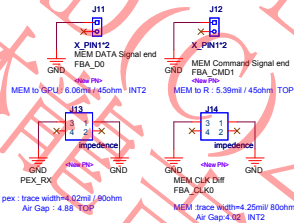
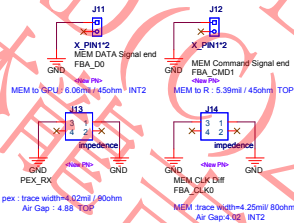
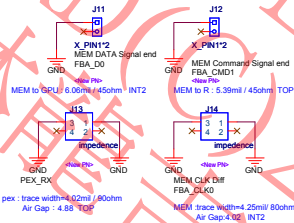
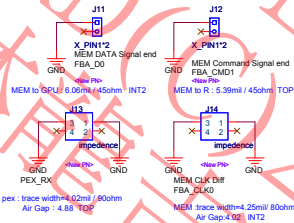
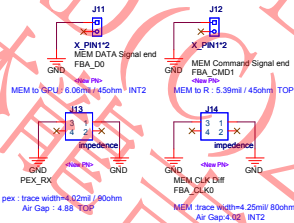
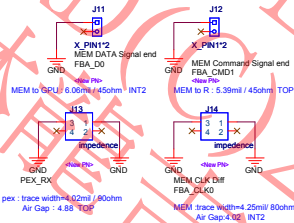
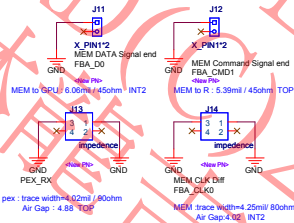
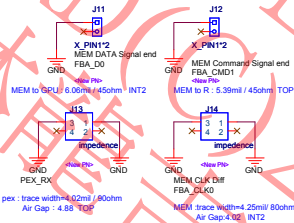
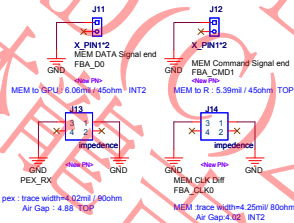
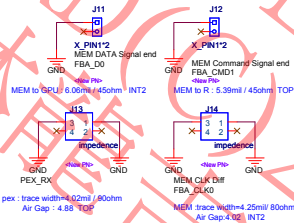
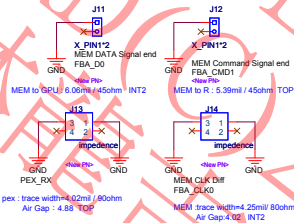
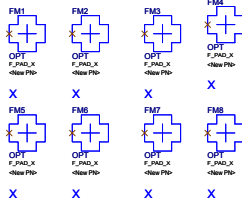
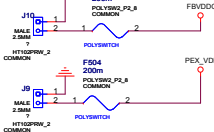
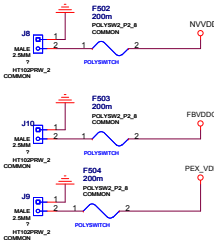
Bracket Screw



Cooler/GPU Stiffener



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