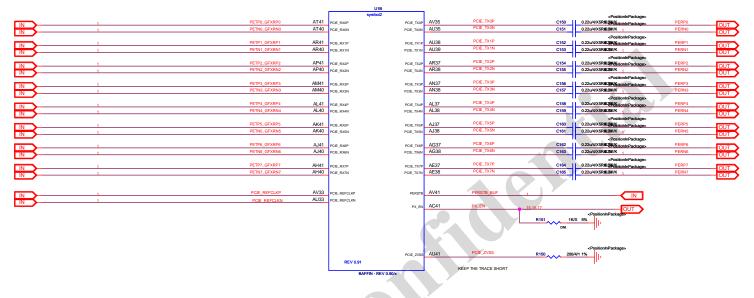
# (1) PCIE EDGE CONNECTOR 2016.5.15 ADD By Bingqi.cui Modify TO 0402 By Xiang.I@20170516 IN 2016.5.17 ADD COLAY By bingqi.cui 2016.7.12 Modify X8 By Bingqi.cui PLACE THESE CAPS AS CLOSE TO PCIE CONNECTOR AS POSSIBLE SYMBOL LEGEND $\stackrel{}{\uparrow}$ ANALOG GROUND

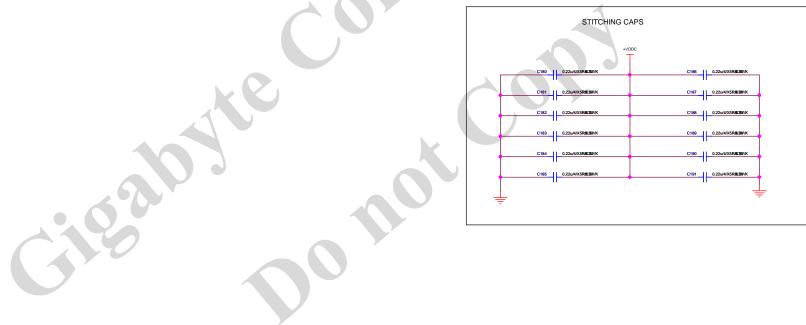
Title GV-RX560OC-4GD

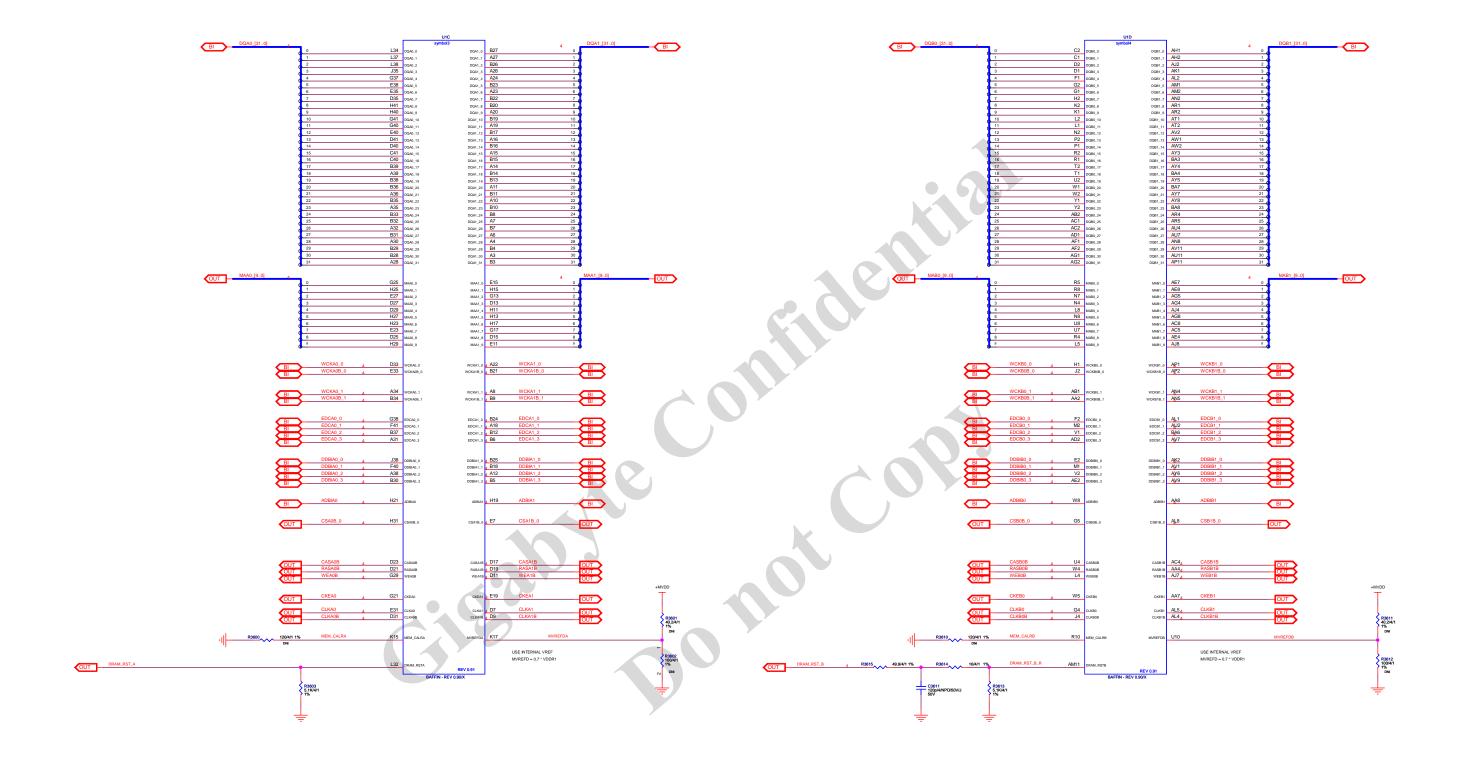
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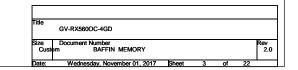
Date: Wednesday, November 01, 2017 Sheet 1 of 22

(2) BAFFIN PCIE



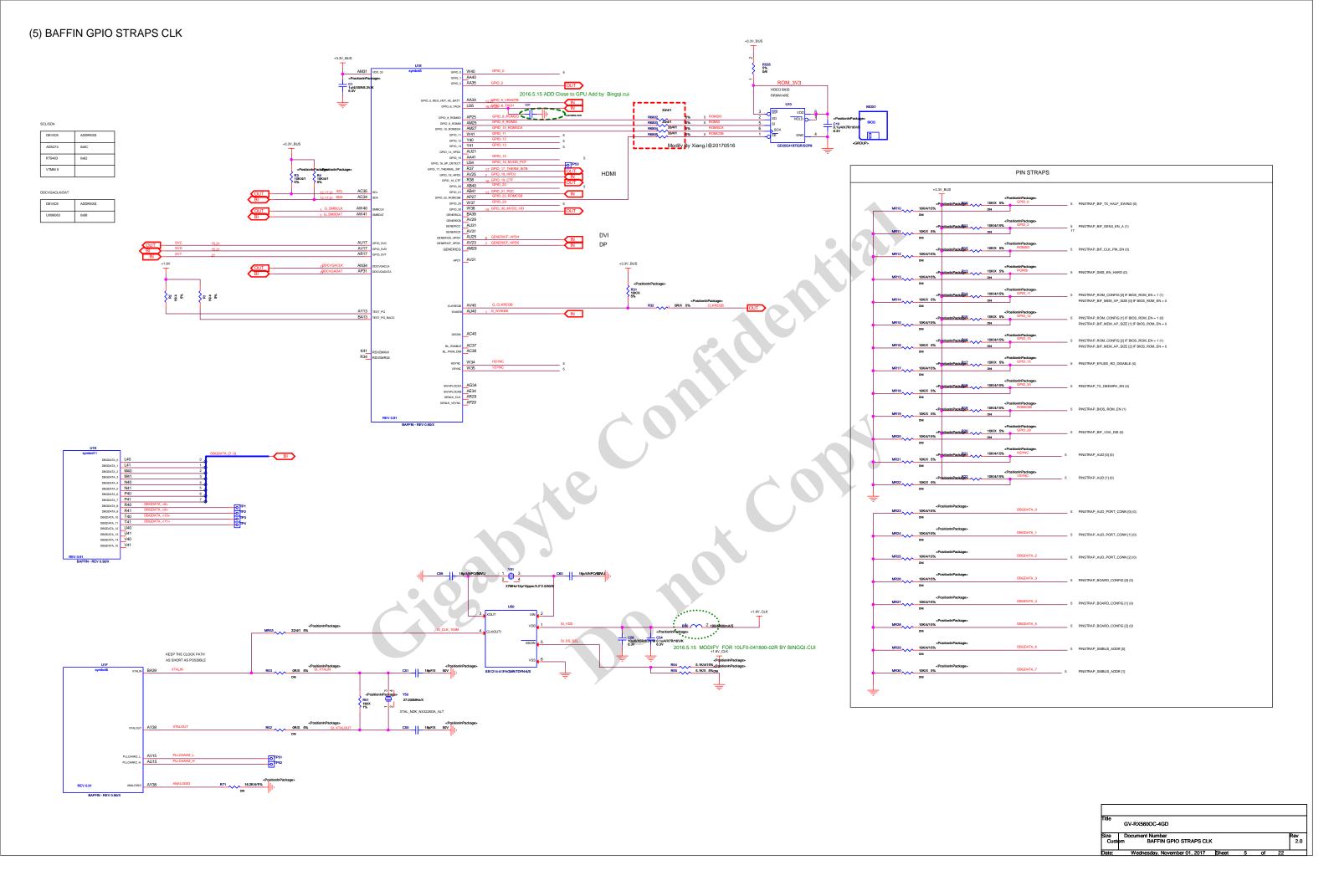


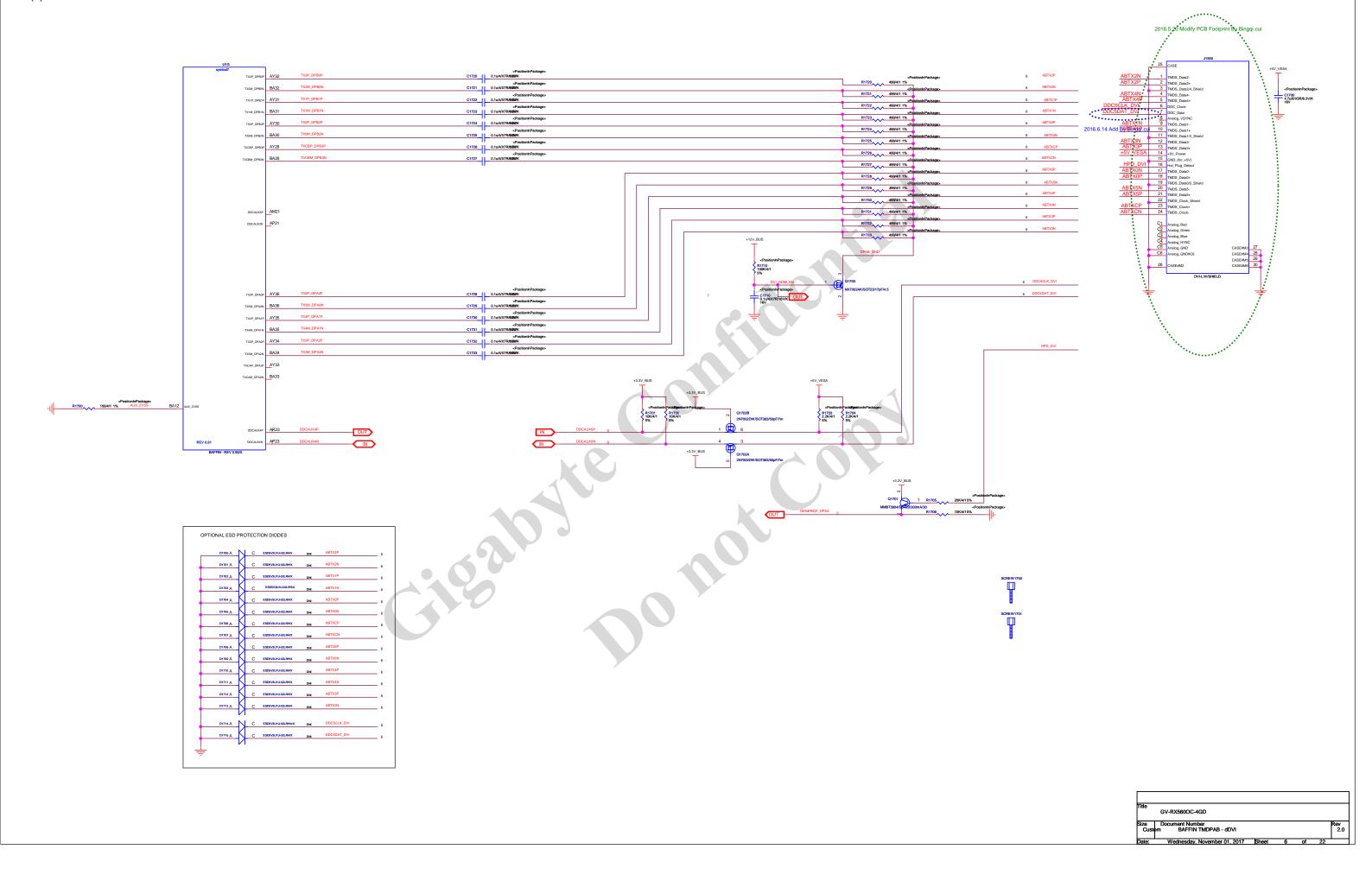


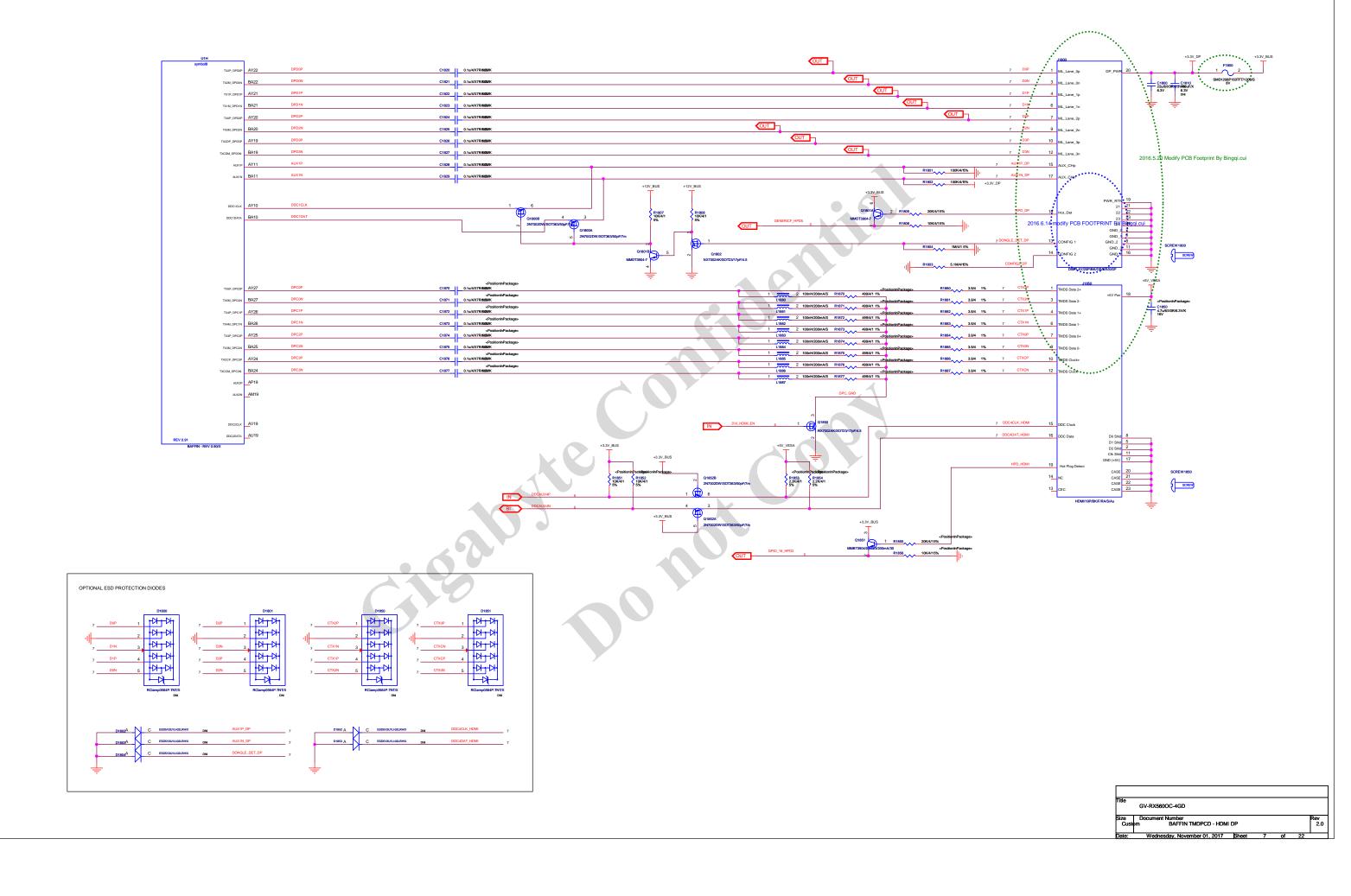


#### (4) GDDR5 CHAB x32

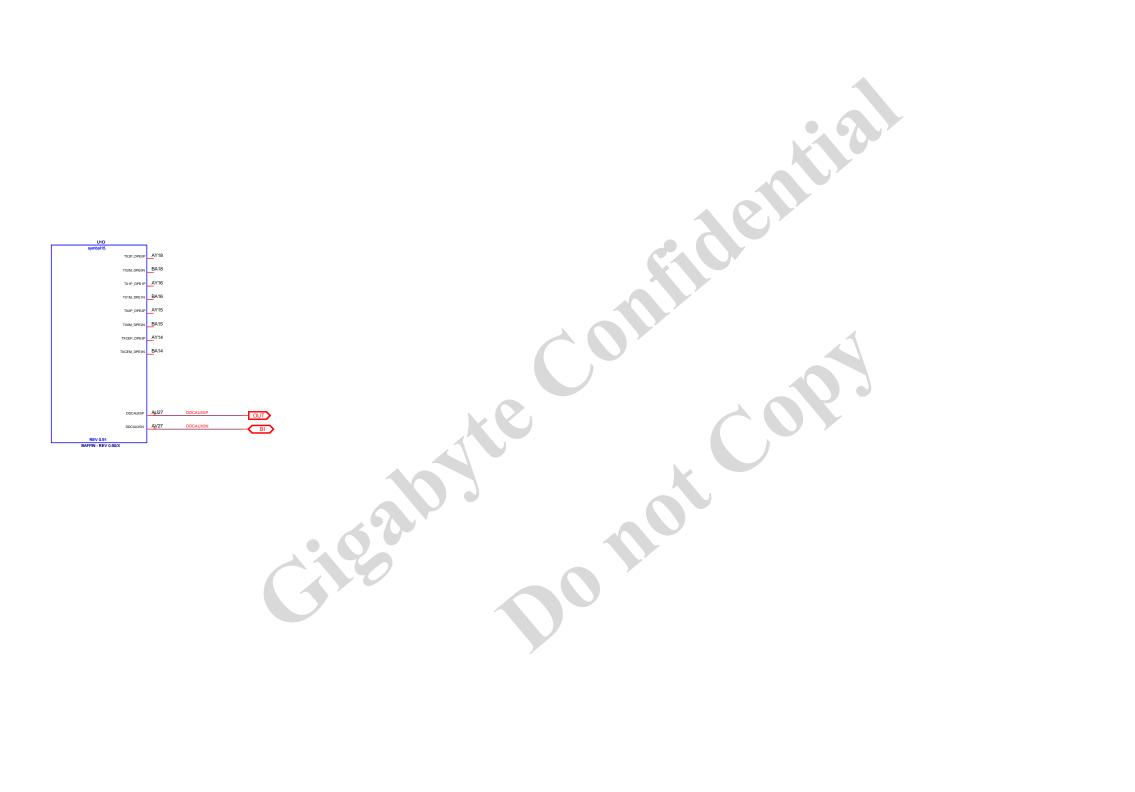




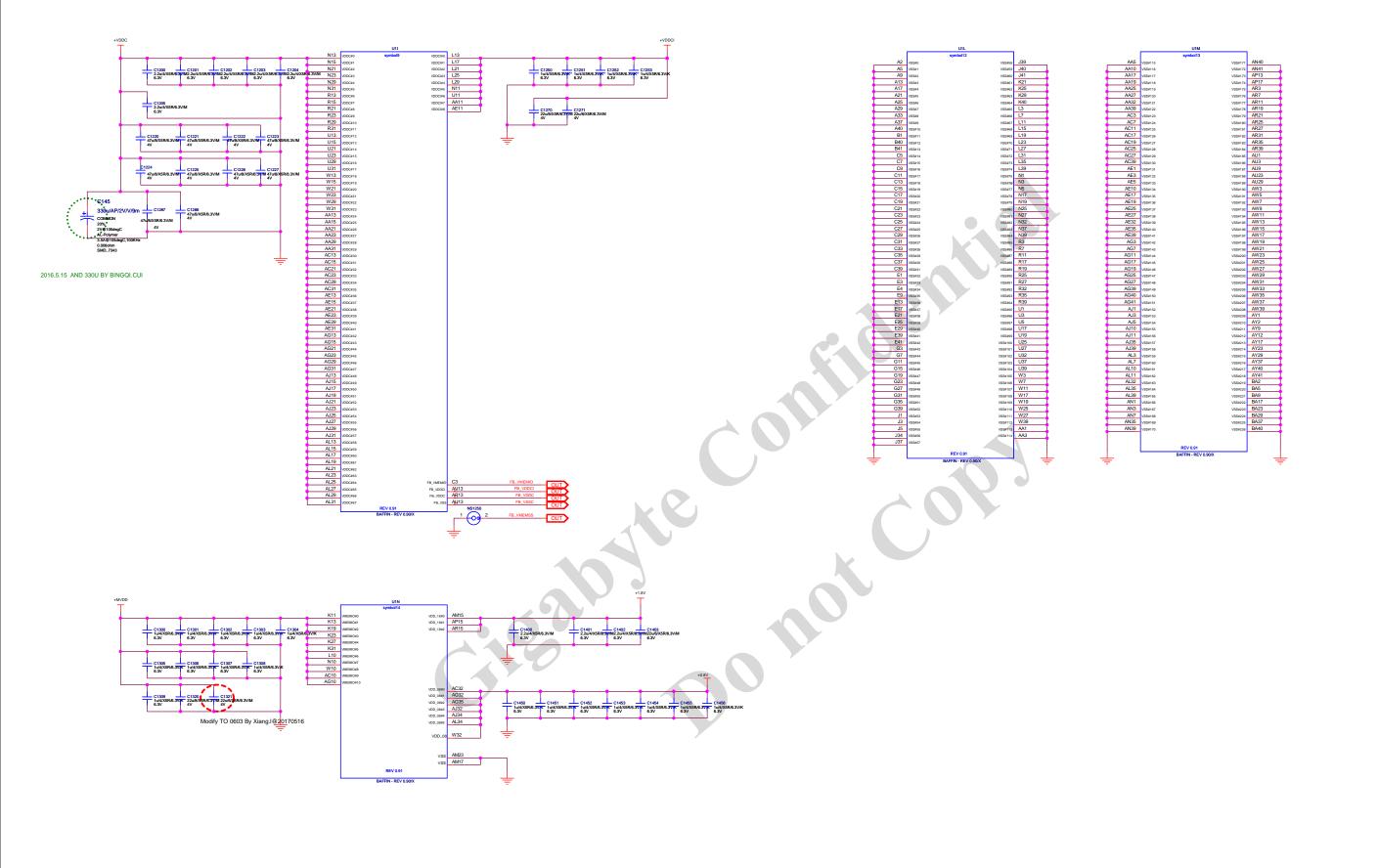


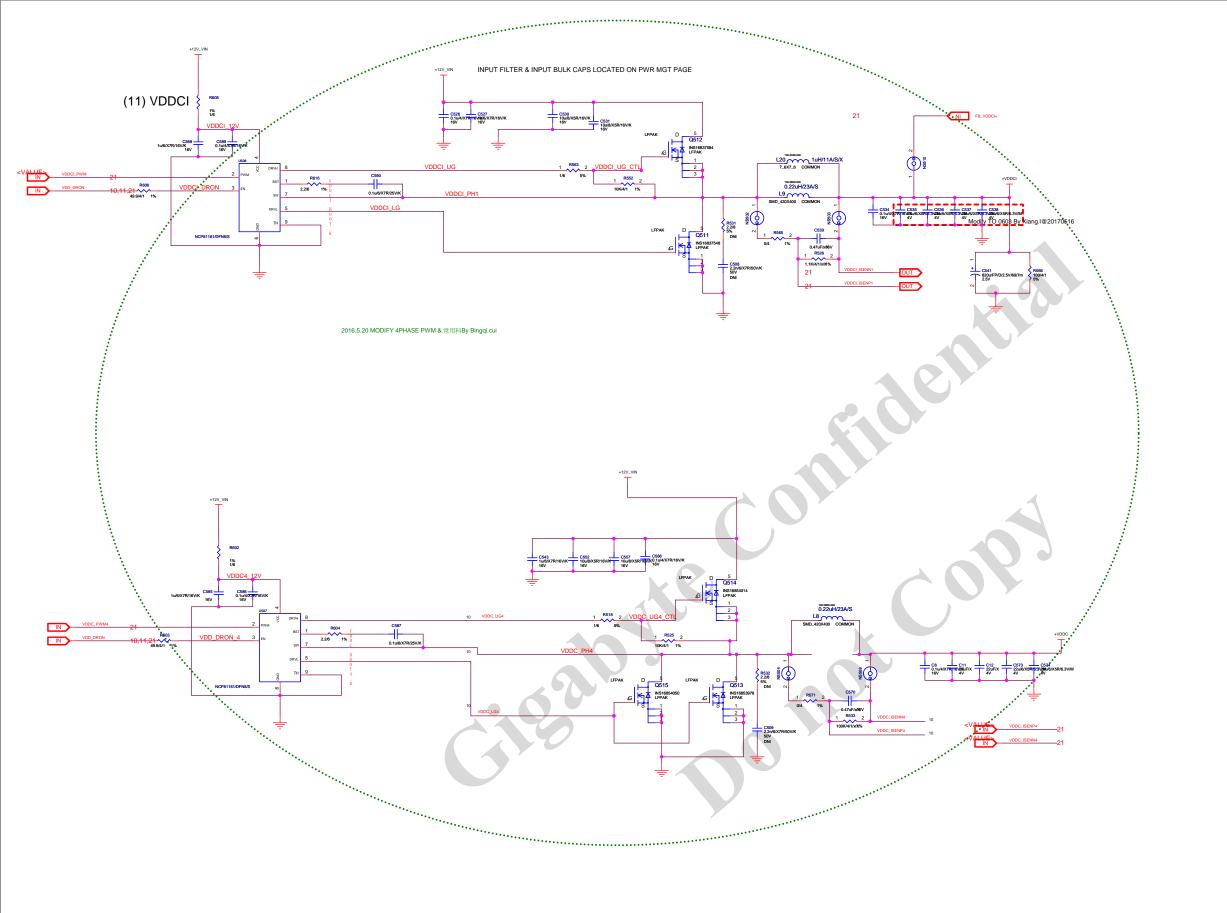


(8) BAFFIN TMDPE

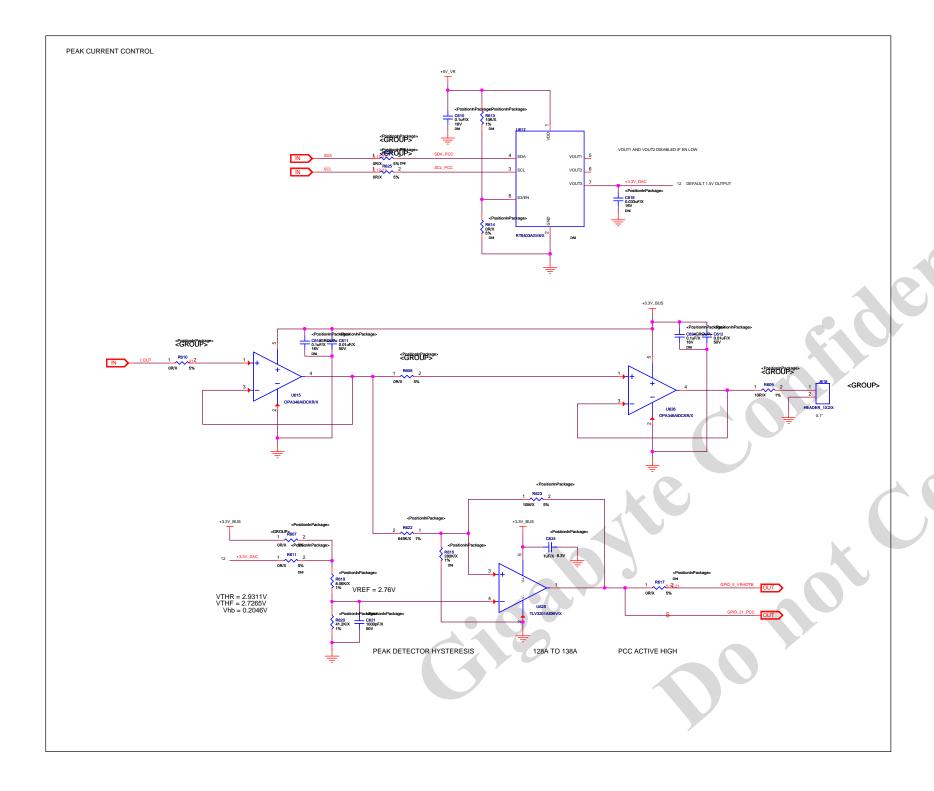


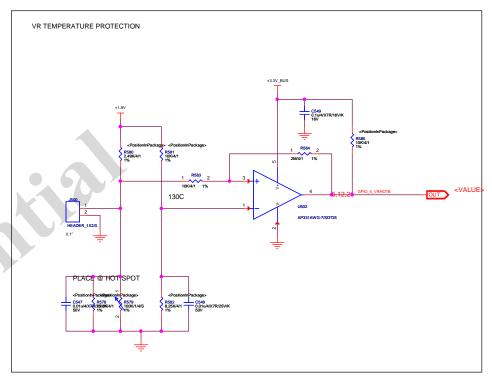
#### (9) BAFFIN POWER GND

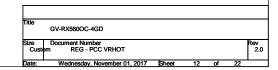


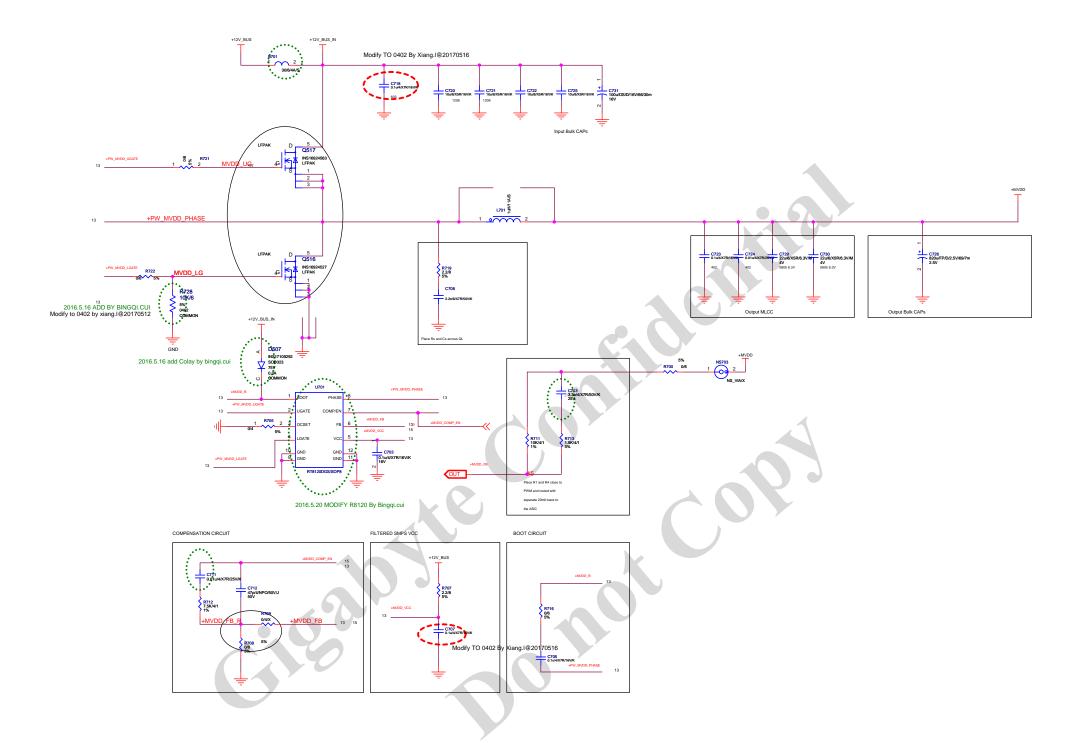


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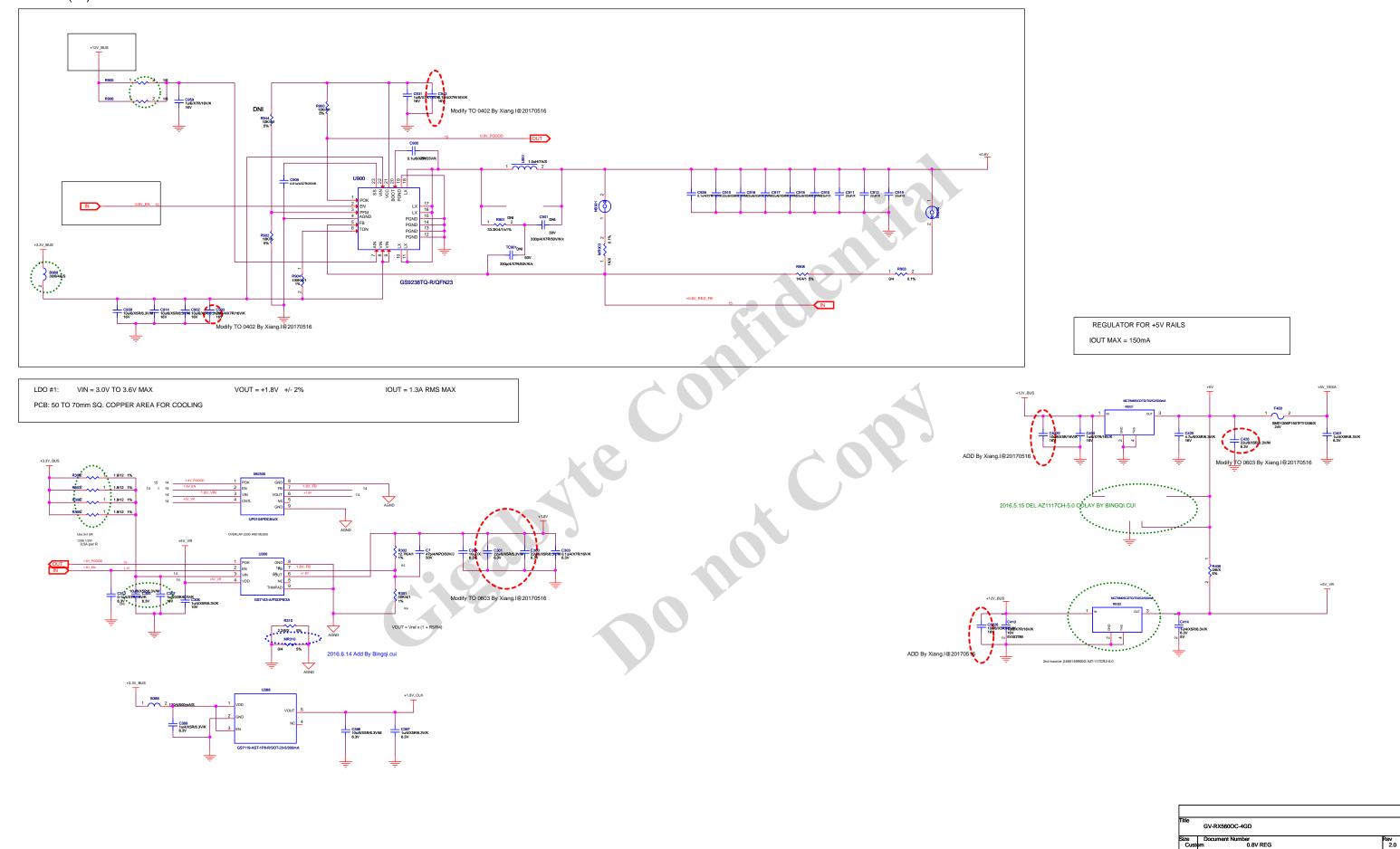


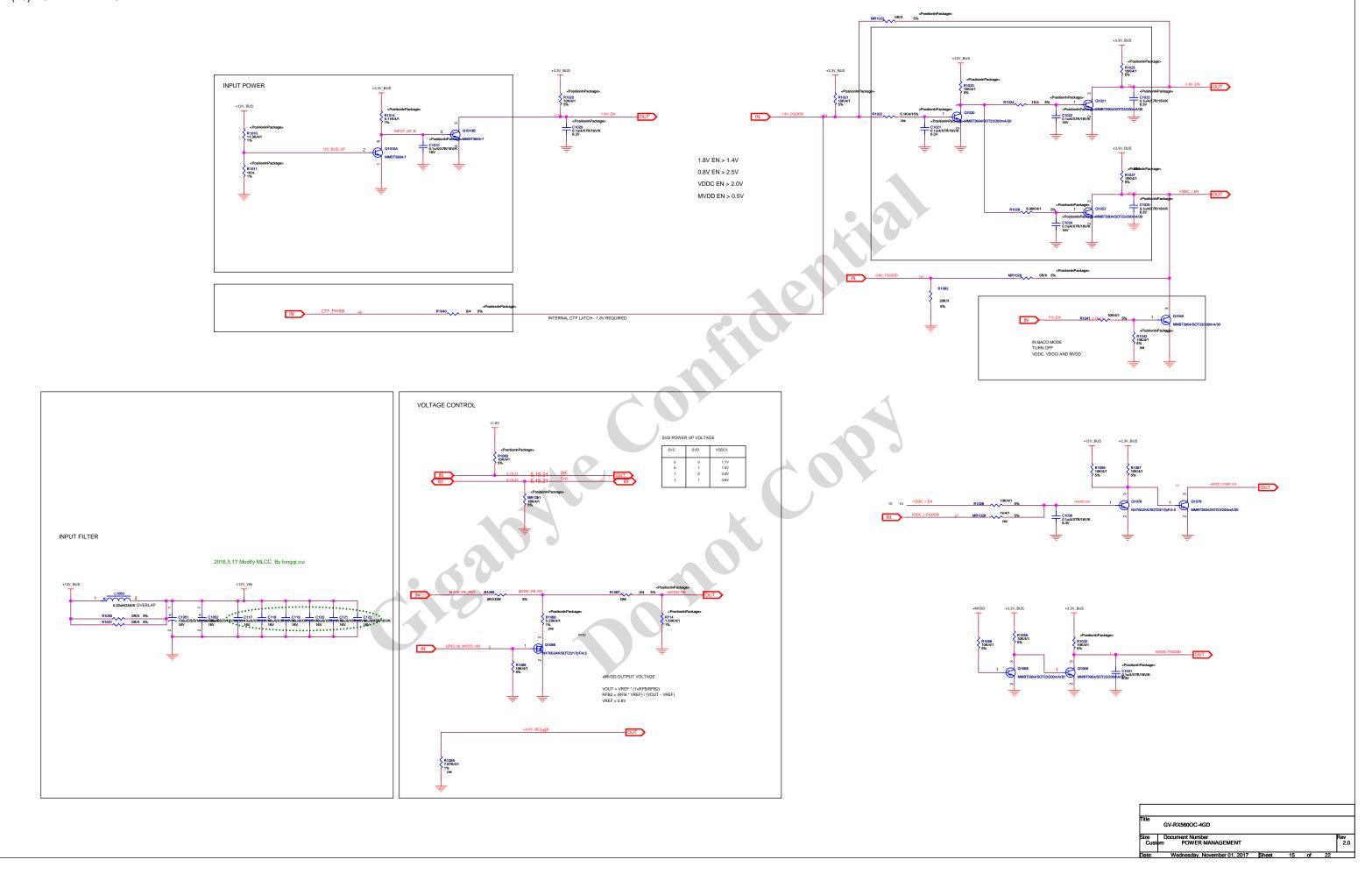






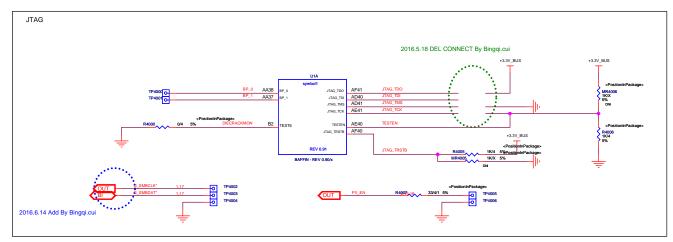
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| Cust  | ndom MVDD       |  |    |

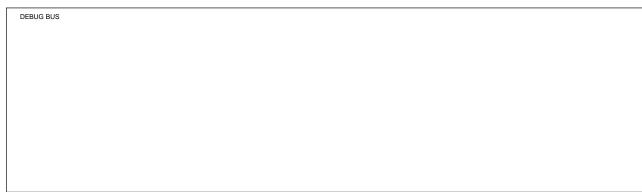


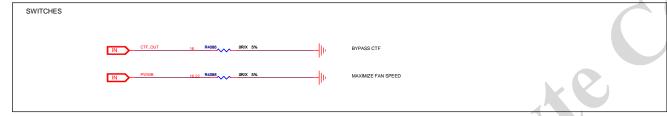


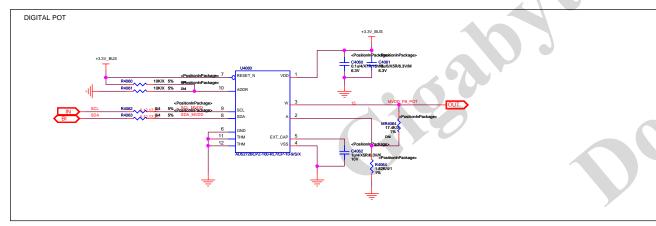
## (17) THERMAL MANAGEMENT CRITICAL TEMPERATURE FAULT FOR 4-WIRE FAN HEATSINKS ASIC SOCKET BOARD STIFFENER BOARD STIFFENER BOARD STIFFENER BRACKET BRACKET MT HOLE 10 9 8 7 6 5 4 3 2 BOARD STIFFENER BOARD STIFFENER 8020058600G SS DP W TAB, HDMI, DVI GV-RX560OC-4GD

### (18) DEBUG CIRCUITS

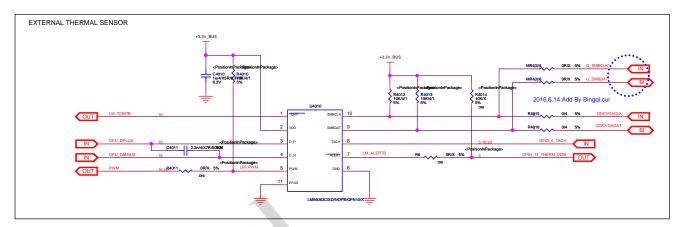


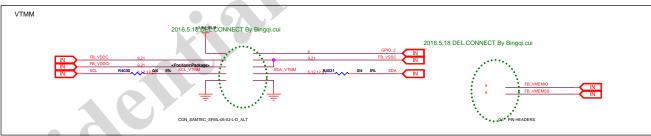




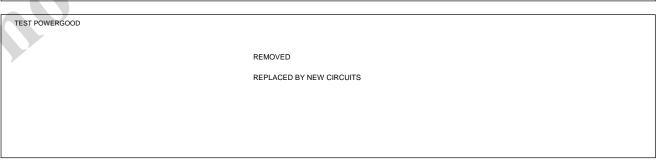




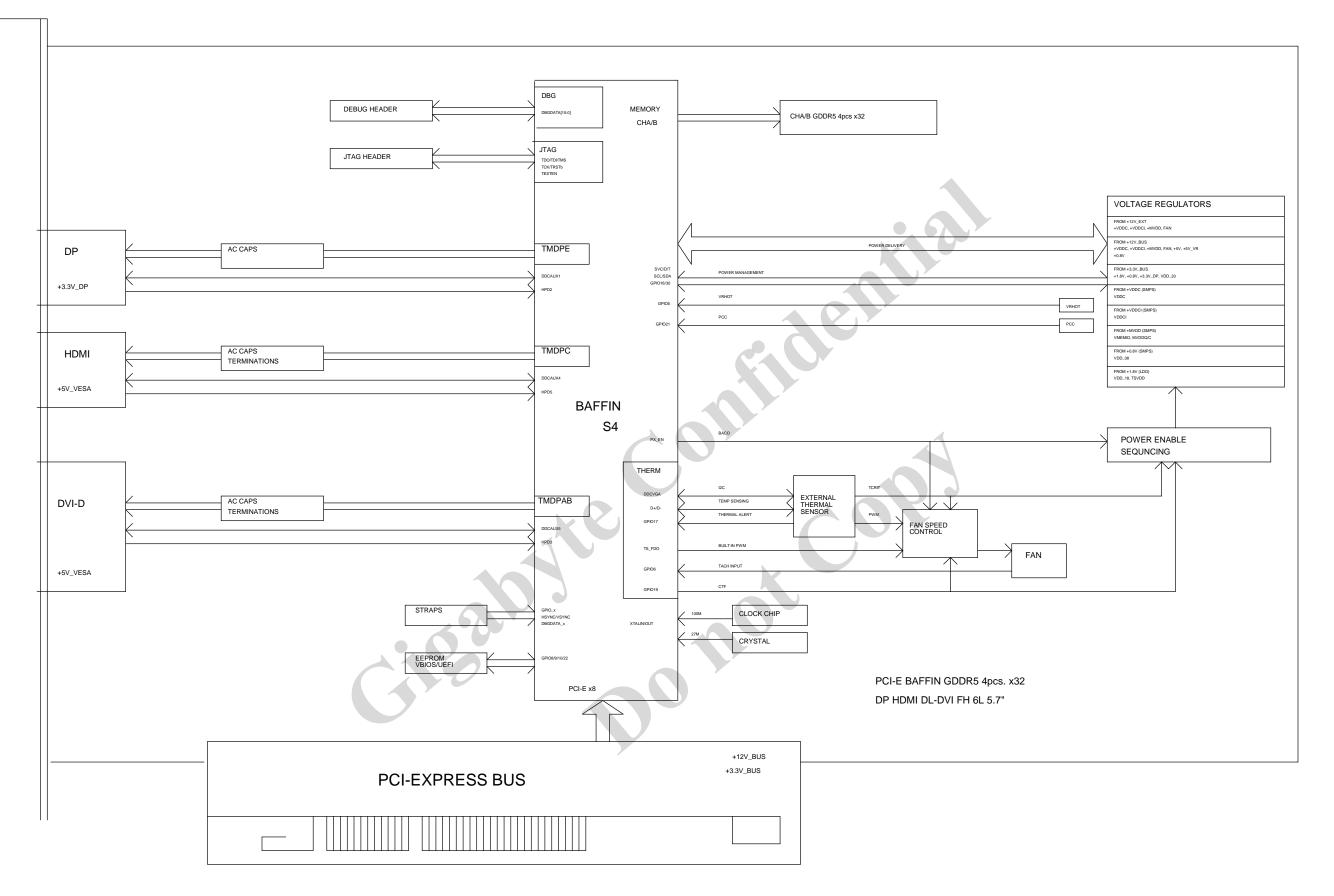








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| Date:        | Wednesday, November 01, 2017        | Sheet | 18 | of | 22 | <u> </u>   |

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