P681-A01 GT215/216 DESKTOP GB1-128 DDR3 PCI-EXPRESSx16 DL-DVI VGA HDMI

Ver. 0A

Page 1: P681-A01 OVERVIEW Page 2: PCI-EXPRESS INTERFACE Page 3: PARTITION A FRAME BUFFER INTERFACE Page 4: PARTITION A MEMORIES Page 5: FBA DECOUPLING CAPS & NVVDD DECOUPLING CAPS Page 6: PARTITION C FRAME BUFFER INTERFACE Page 7: PARTITION C MEMORIES Page 8: FBC DECOUPLING CAPS Page 9: DACA (SOUTH DVI-I) Page 10: DACB (MID VGA) Page 11: IFP AB (SOUTH DVI-I) Page 12: IFP C (NORTH HDMI) Page 13: IFP D (UNUSED) Page 14: IFP EF (UNUSED)

Page 15: MIOA & MIOB Page 16: XTAL, MECHANICALS, THERMALS Page 17: EXTERNAL THERMAL SENSOR, FAN CONTROL, GPIO, JTAG $^{05/11}$ Page 18: BIOS ROM, HDCP ROM, STRAPPING OPTIONS Page 19: LINEAR POWER SUPPLIES

Page 20: FBVDDQ/PEXVDD POWER SUPPLY Page 21: NVVDD POWER SUPPLY

Page 01 1.Remove C690,C70 47uF Page 10 1.Remove J1 D-Sub Page 11 1.Remove R16.Q2 Page 12 1.Remove R1,Q1 Page 16 1.Change Crystal Footprint to 2-PIN SMD 2.Remove C71 47uF 05/08 Page 17 1.Remove Thermal Sensor Circuit Page 18 1.Remove U2,C54,R31,R30,D16 INFOROM circuit Page 19 1.Remove 3V3 to 1.8V circuit 2.Change 5V REGULATOR Circuit Page 20 1.Change L8 Footprint to 1.6uH 2. Change Footprint to multi cap for EL 1500uF Page 21 1.Change NVVDD to UP6161 2.Change L12 footprint to 1.2uH Page 05 1. Remove Decoupling for EMI cap

Page 20 1. C84 change Footprint to multi cap Page 21 1.Add C39 270uF 2.Add R922,R924 for APW7068 OCSET Page 16 1.Add XTALIN R566 Risister Add XTALOUT R573 Risister

Page 02 1.Change C51 to 0805 1UF and ADD C54 0805 1UF Page 03 1.C642,C666,C633 change to .01UF Page 05 1.Remove C516.C525.C549.C556.C541.C512 Remove C513,C546,C540,C528,C547,C507 Remove C544,C545,C520,C523,C532,C508 Remove C535, C526, C543, C553, C537, C88 Page 08 1.Remove C566, C567, C576, C578, C82, C564

Remove C619.C592.C586.C585.C622.C574 Remove C705, C706, C680, C729, C691, C683 Remove C742, C738, C731, C725, C743, C728

Page 20 1.Remove C86,C562,C563,C87

Page 17 1.Remove 4-PIN FAN Circuit

Page 12 1.Change Q509 to Q513,Q514 SOT23 footprint

Page 16 1.Change FAN Screw hole

Page 20 1.Add C102 820uF

05/14

Page 21 1.Remove D94 scottky diode

05/19

Page 11 1.Add EMI bridge Page 12 2.Add EMI bridge

Page 03 1.Add RP24 termination risister

Page 06 2.Add RP23 termination risister

05/26

SWAP CMD

Page 03/06 RP5.1 , RP5.2 FBC_CMD10 , RP5.3 , RP5.4 FBC CMD22 RP4.1, RP4.2 FBC CMD18, RP4.3, RP4.4 FBC CMD7 RP24.1, RP24.2 FBA CMD30, RP24.3, RP24.4 FBA CMD7 RP20.1, RP20.2 FBA CMD14, RP20.3, RP20.4 FBA CMD18 RP14.1, RP14.2 FBA_CMD1, RP14.3, RP14.4 FBA_CMD20

05/27

Page 05 1. Add Decoupling for EMI cap C80,C524,C517,C102

RP12.3, RP12.4 FBA CMD29

Ver. 10

07/31

Page 02 1.Remove JTAG Circuit

Page 09 1.Remove EMI filter, protection diode, C780, C775, C779

Page 10 1.Remove EMI filter,protection diode,C771,C772,C773

Page 12 1.IFP_PLLVDD change netname to 3V3 Page 15 1.Remove R545,C664,R554 of MIOA

Page 17 1.Remove JTAG Circuit

2 Remove FAN PWM Circuit

Page 18 1.Remove HDCP EEROM

Page 19 1.Remove U4 UP7703 Circuit

2.Change 5V Circuit SOT223-->SOT252

Page 20 1.Remove C86,C105,C90

2.Add R917 LMOS Gate risister

3.Add L10

4.Add L9.LB23.LB24

5.Add EL 680UF FootPrint

Page 21 1.Add L13

2.Remove APW7068 OCSET

3.Add GPIO 5,6 Circuit

4.Remove C927,C931

08/04

Page 21 1.Remove C933.C30.C31.C38 2.Change C39 FootPrint for 470uF

3.NVVDD Power Sequencing and PWM power change to 12V from 12V_F

NVIDIA CORPORATION

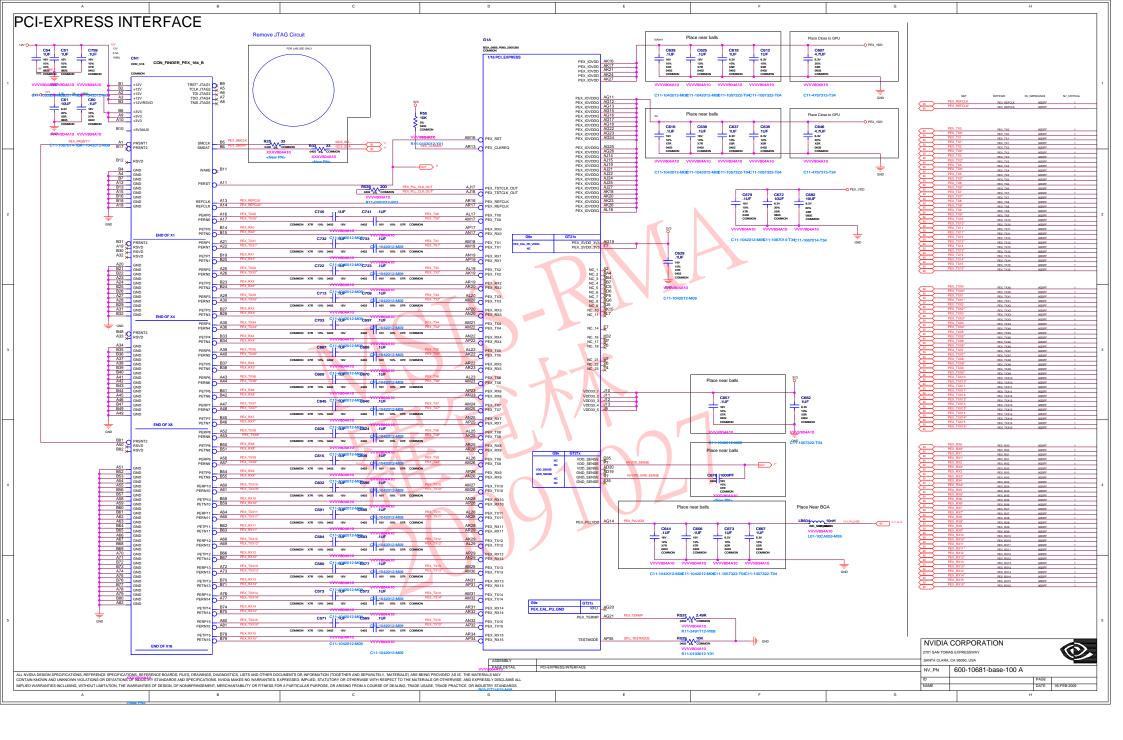
Page 20 1.Add C86,C87,R26

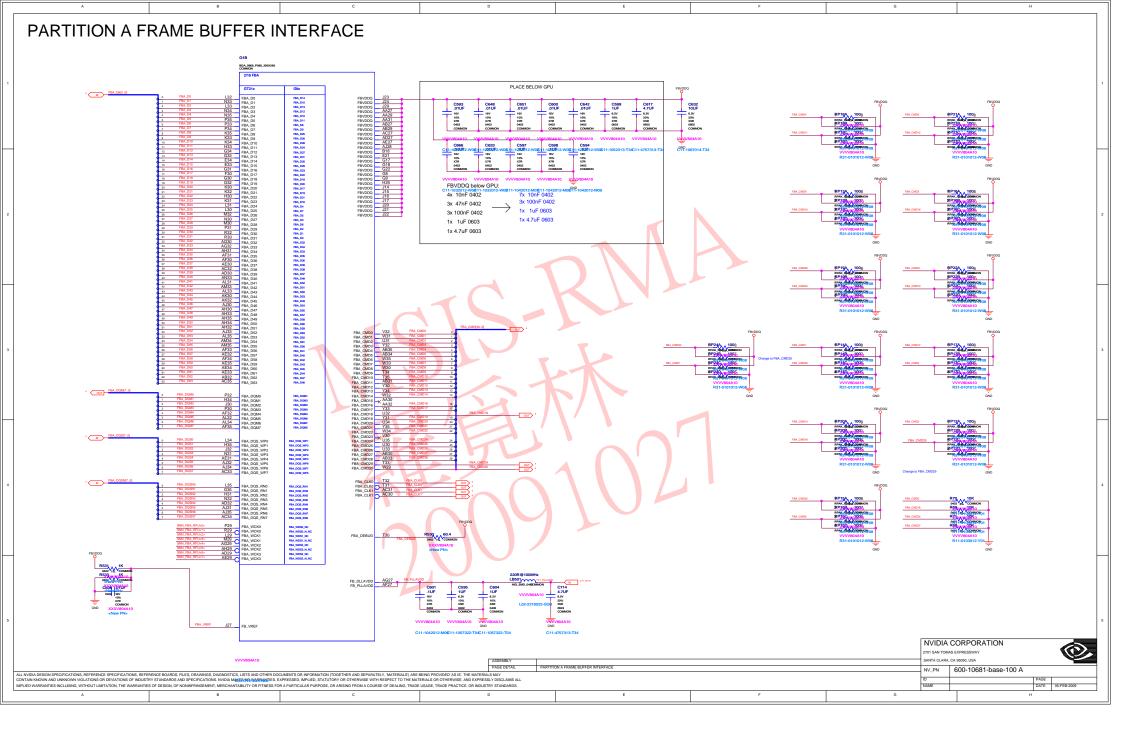
2.Remove C557

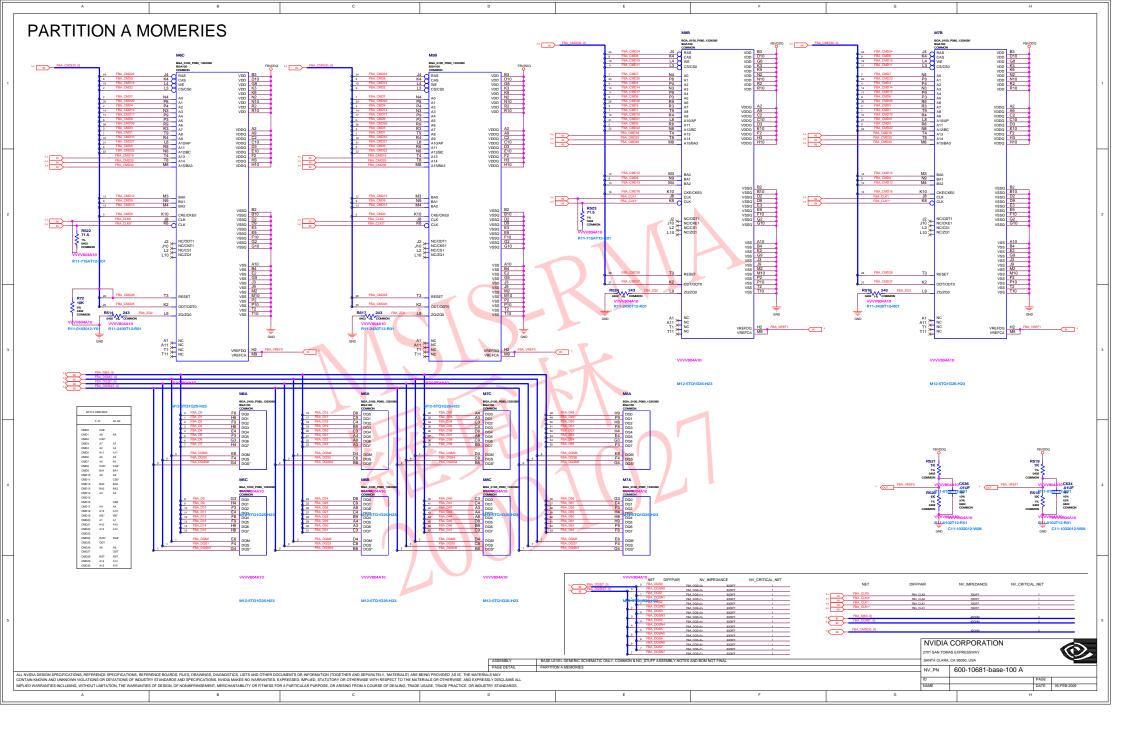
3. Power Sequencing and PWM power change to 12V from 12V_F

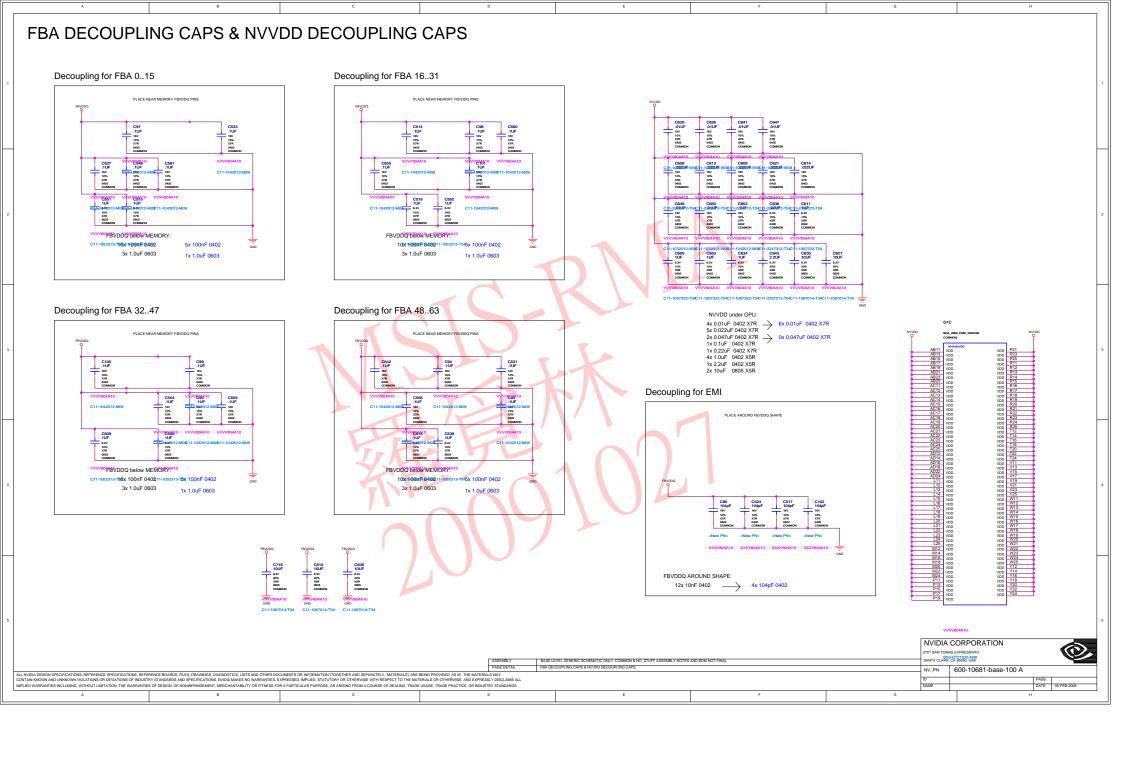
SHL	VARIANT	NVPN	ASSEMBLY
E	BASE	600-10681-base-100	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU0001	600-10681-0001-100	GT216-300 600/1500MHz 1024MB 64Mx16 BGA100 800MHz DDR3 DVI-IV/GA/HDMI
2	SKU0002	600-10681-0002-100.	GT216-300 600/1500MHz 1024MB 64Mx16 BGA100 1000MHz DDR3 DVI-I/VGA/HDMI
3	SKU0011	600-10681-0011-100	GT215-300 600/1500MHz 1024MB 64Mx16 BGA100 900MHz DDR3 DVI-IV/GA/HDMI
4	<undefined></undefined>	<undefined></undefined>	«UNDEFINED»
	<undefined></undefined>	<undefined></undefined>	«UNDEFINED»
6	<undefined></undefined>	<undefined></undefined>	«UNDEFINED»
7	<undefined></undefined>	<undefined></undefined>	«UNDEFINED»
8	<undefined></undefined>	<undefined></undefined>	«UNDEFINED»
9	<undefined></undefined>	<undefined></undefined>	«UNDEFINED»
10	<undefined></undefined>	<undefined></undefined>	«UNDEFINED»
11	<undefined></undefined>	<undefined></undefined>	«UNDEFINED»
12	<undefined></undefined>	<undefined></undefined>	«UNDEFINED»
13	<undefined></undefined>	<undefined></undefined>	«UNDEFINED»
14	<undefined></undefined>	<undefined></undefined>	«UNDEFINED»
15	<undefined></undefined>	<undefined></undefined>	«UNDEFINED»
	1		

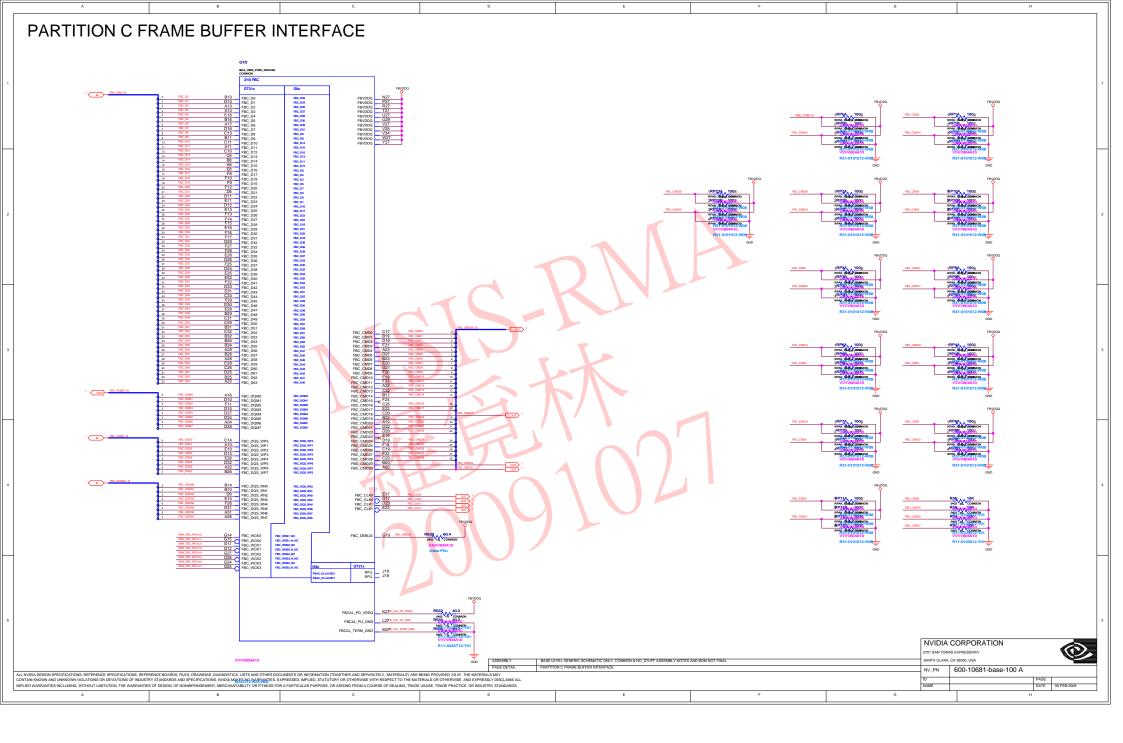
701 SAN TOMAS EXPRESSWA ANTA CLARA, CA 95050, USA DAGE I EVEL GENERIC SCHEMATIC ONLY COMMON & NO STILLE ASSEMBLY NOTES AND BOM NOT EL 600-10681-base-100 A NV_PN ALL INFOLD SEIGN SPECIPICATIONS, REFERENCE SPECIFICATIONS, REFERENCE SOURCE, FLES, DRAWNSS, DUGNOSTICS, LISTS AND OTHER DOCUMENTS OR INCROMATION (TOCETHER AND SEPARATELY, MATERIALS) ARE EIRN PROVIDED AS IS THE MATERIALS MAY CONTAIN INCOMINA AD LINKNOWN VIOLATIONS OR DEVIATIONS OF ROLUSTRY STANDARDS AND SPECIFICATIONS, NIVIDA MAKES NO WARRANTES, EXPRESSED, MPLED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY OR SOCIATIONS AND ADMINISTRY STANDARDS.
MAY LED WARRANTES NOLLDING, WITHOUT LIMITATION, THE WARRANTES OF DESIGN, OF NONINFRINGEMENT, INEGROWATED HITTERS FOR A PRINTICAL PRIPOSE, OR ARBINING FROM A COURSE OF DEALMS, TRADE USINGE, TRADE PRINTING, OR ADMINISTRY STANDARDS.

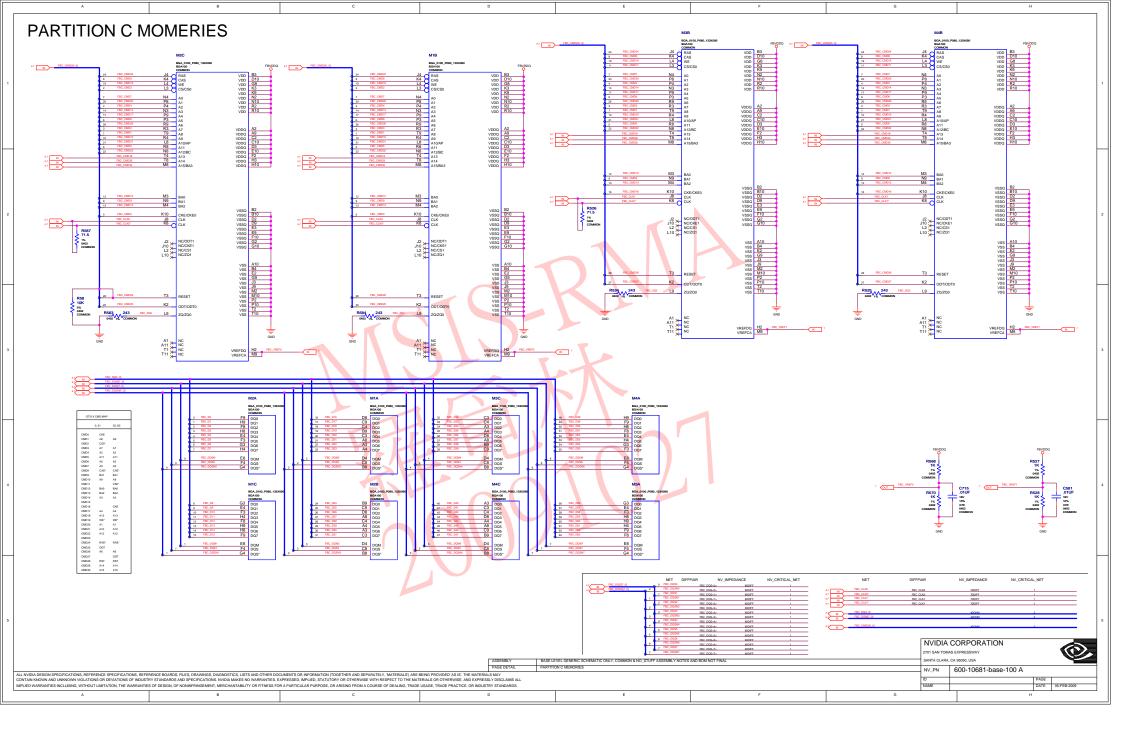


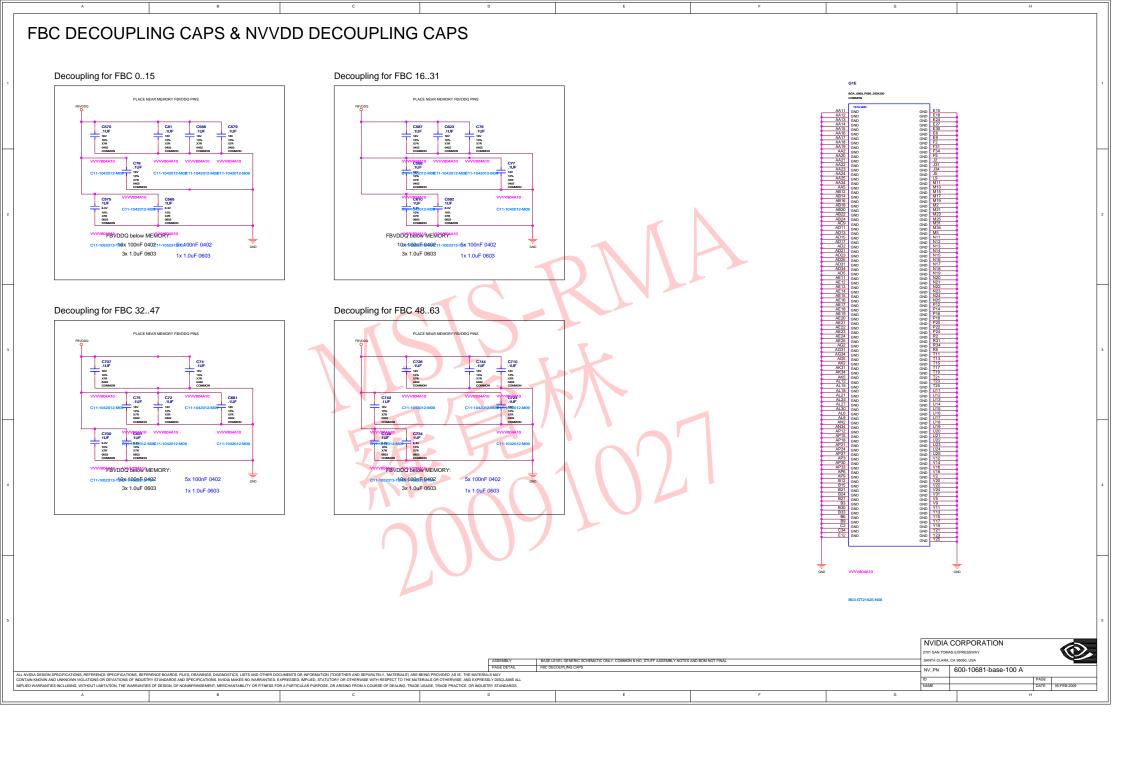


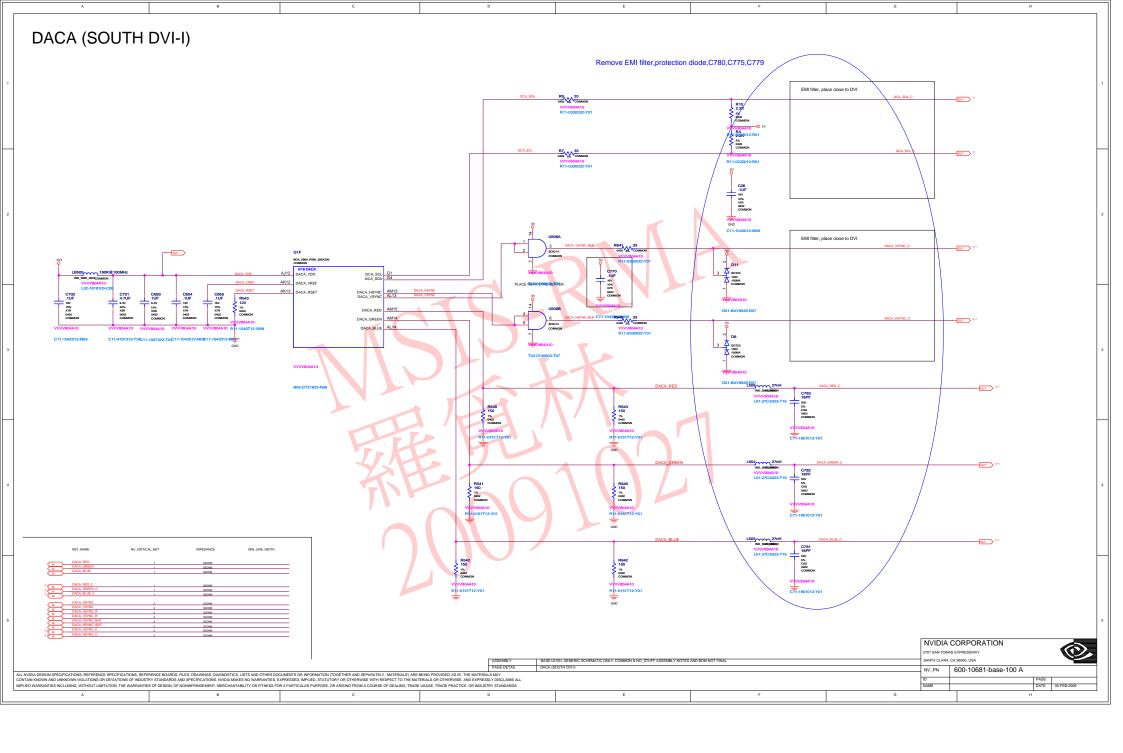


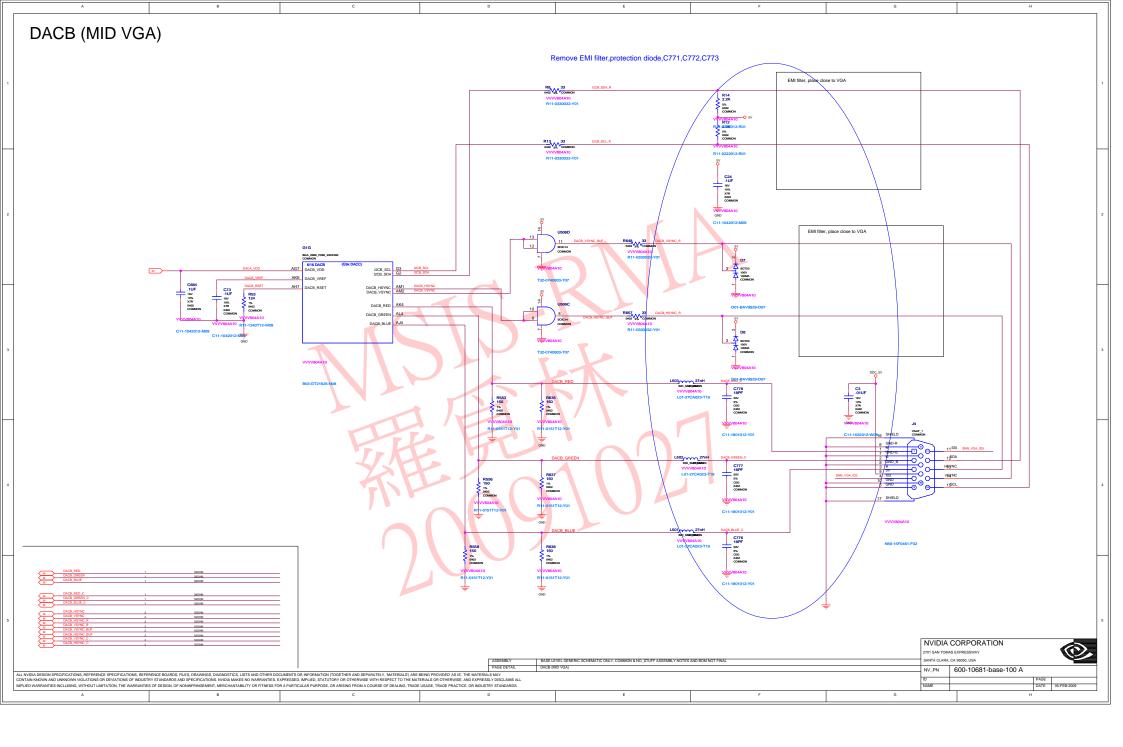


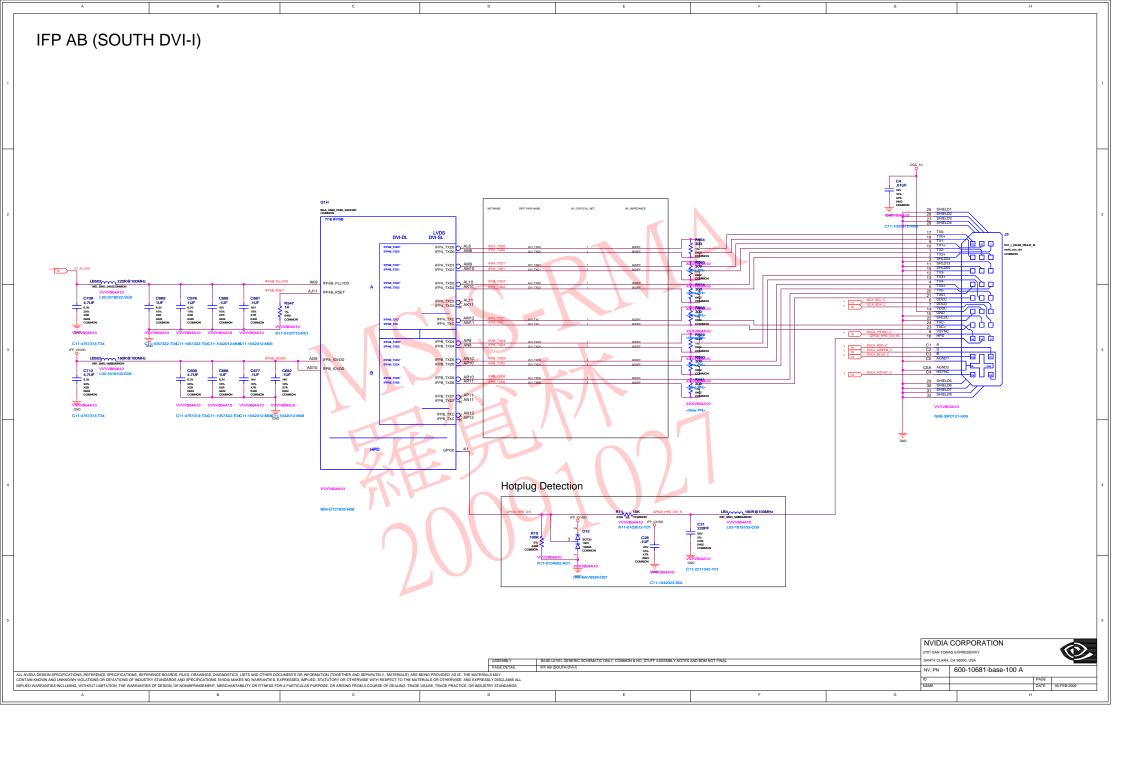


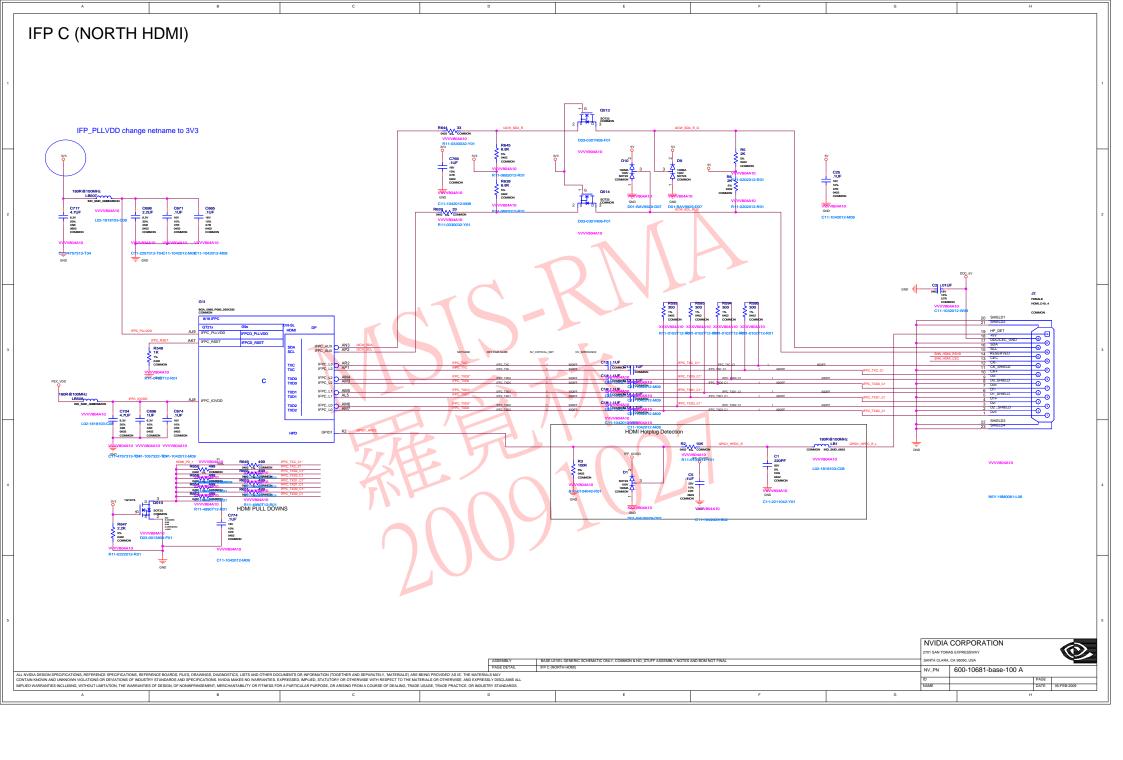


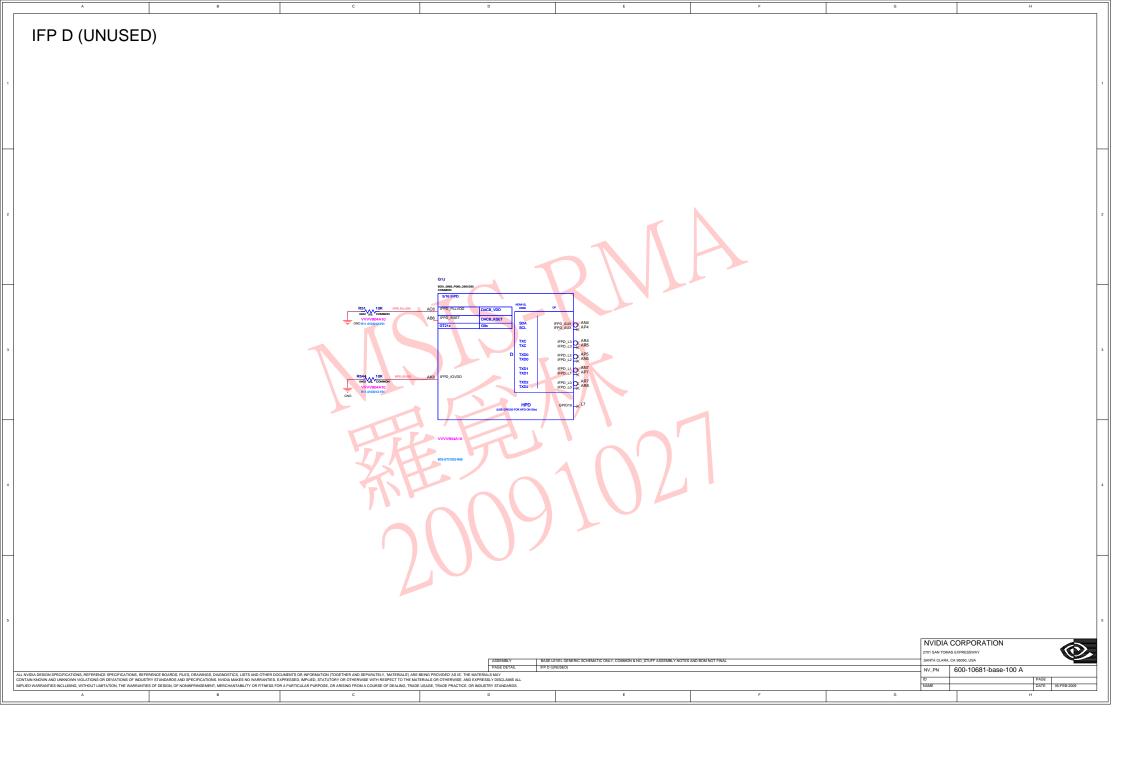


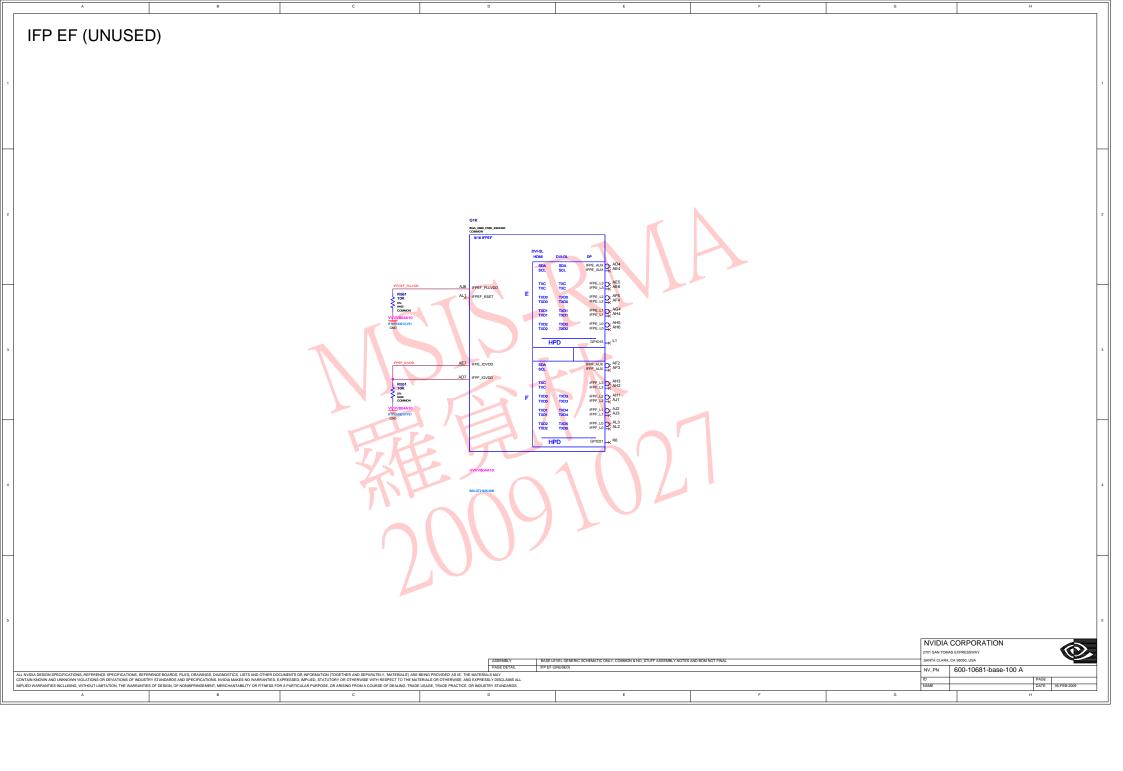


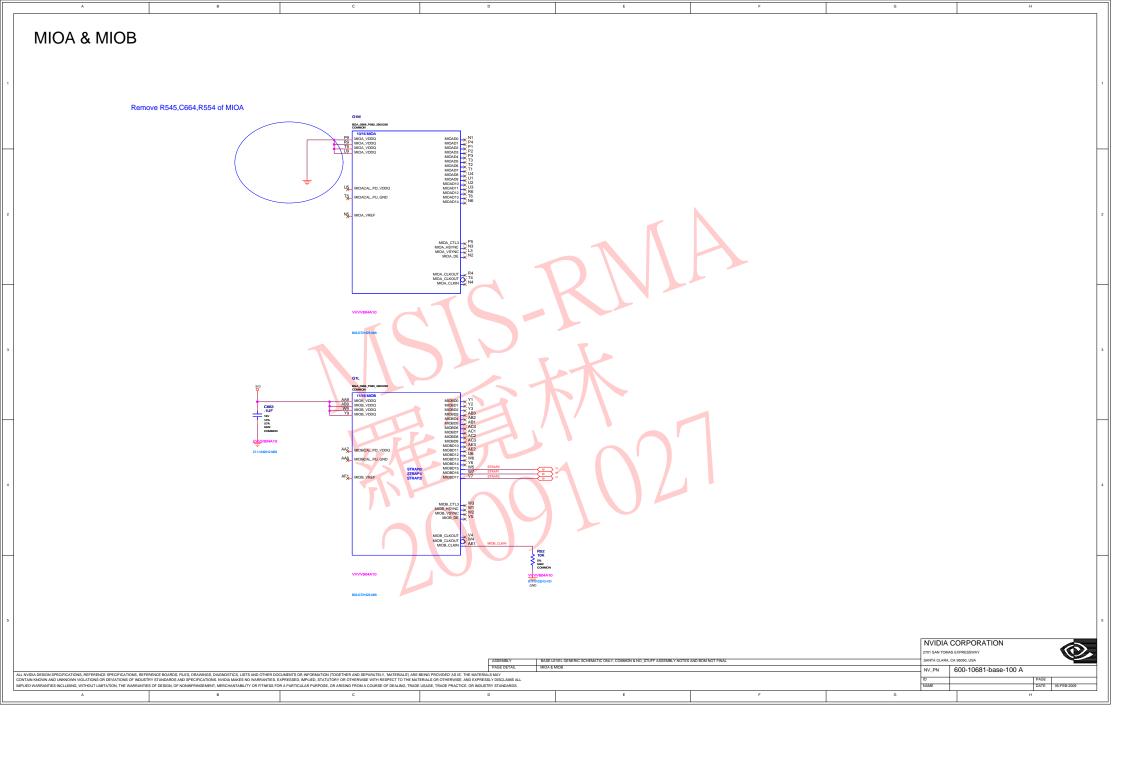


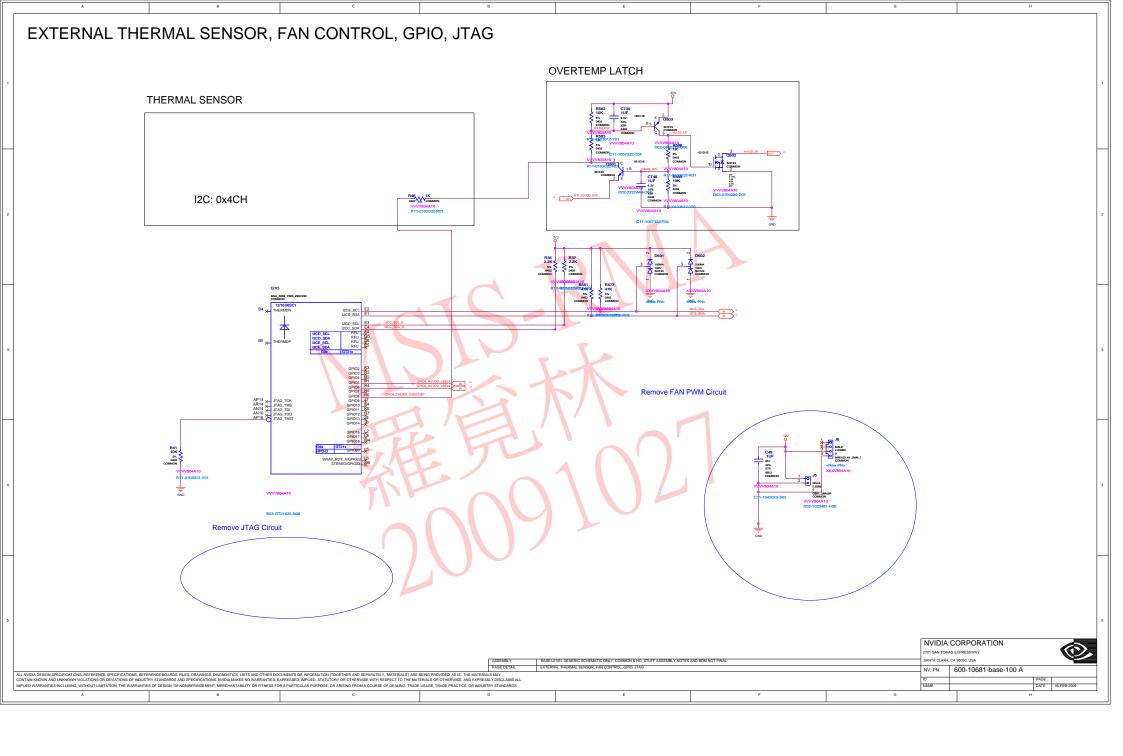


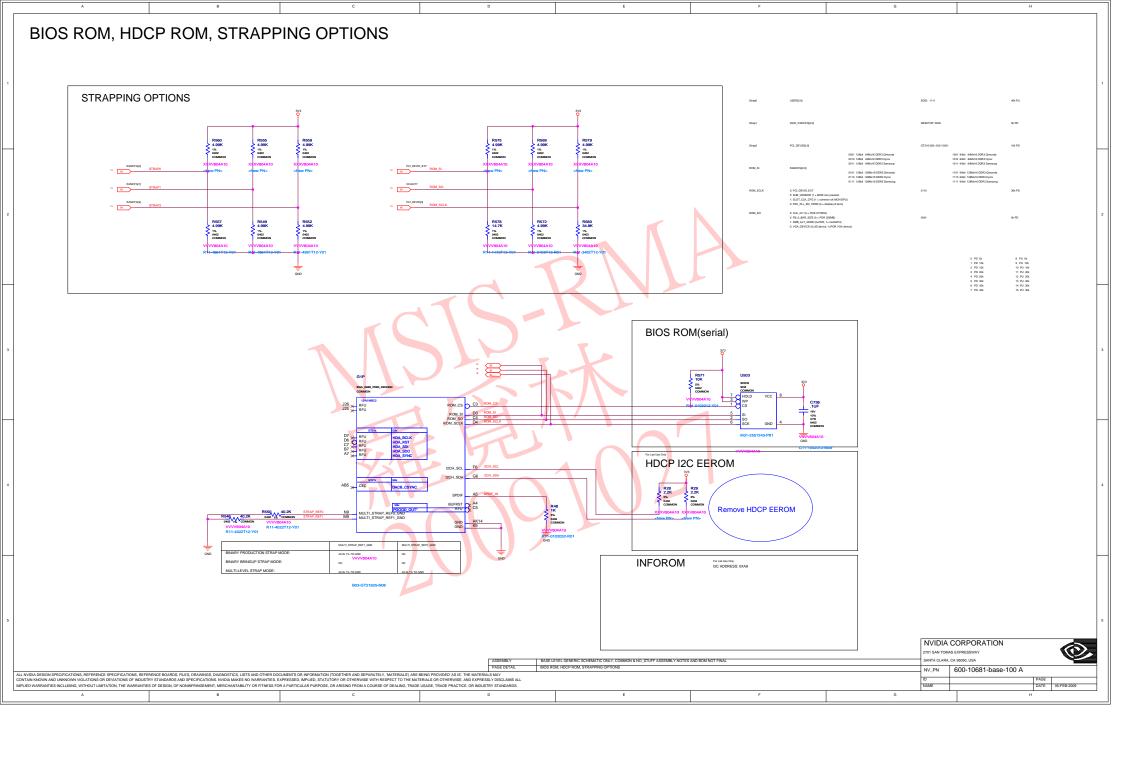


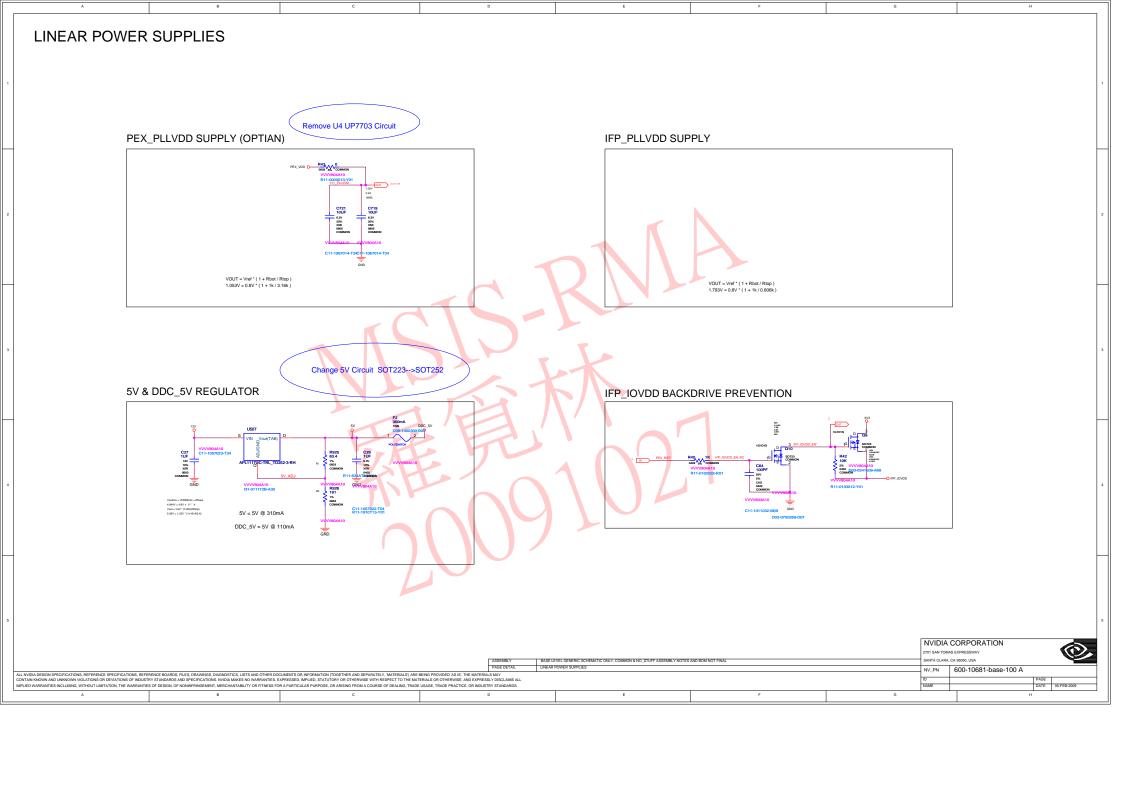


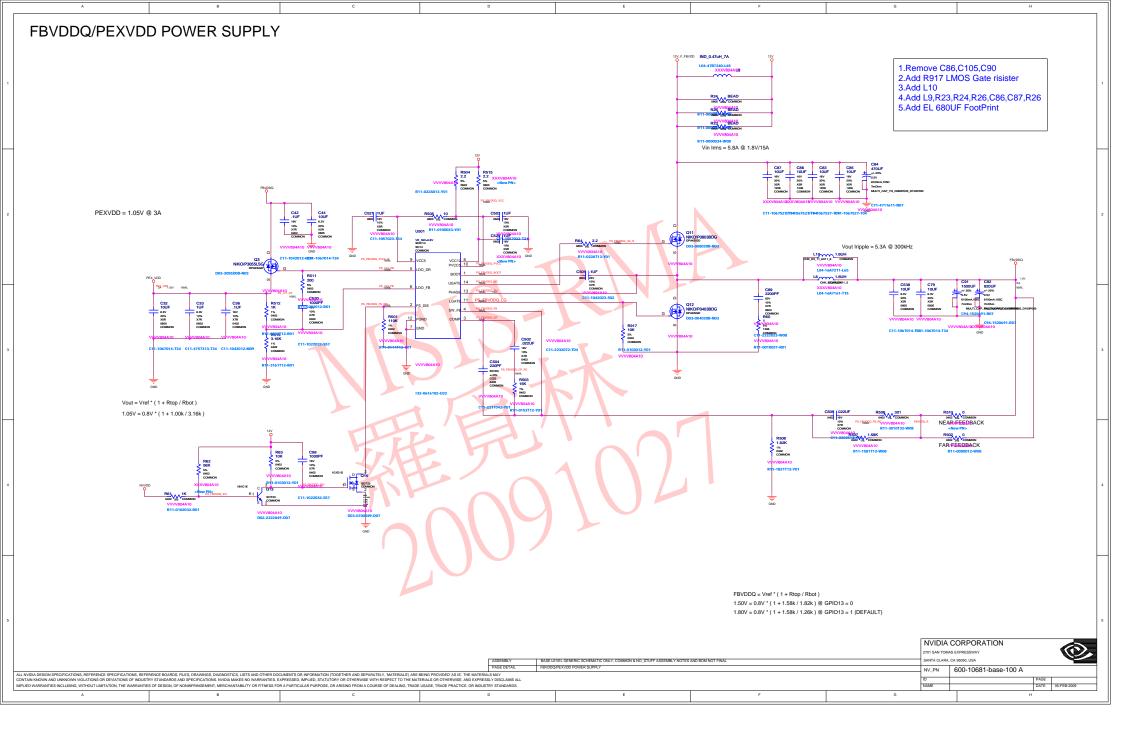


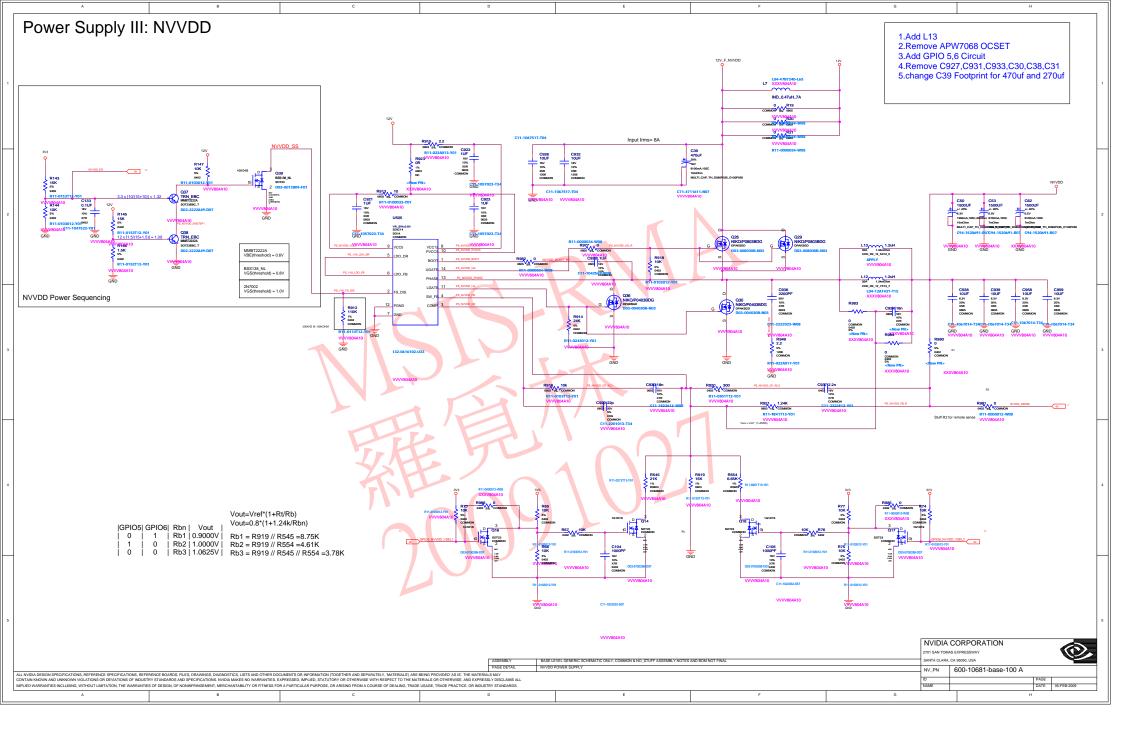










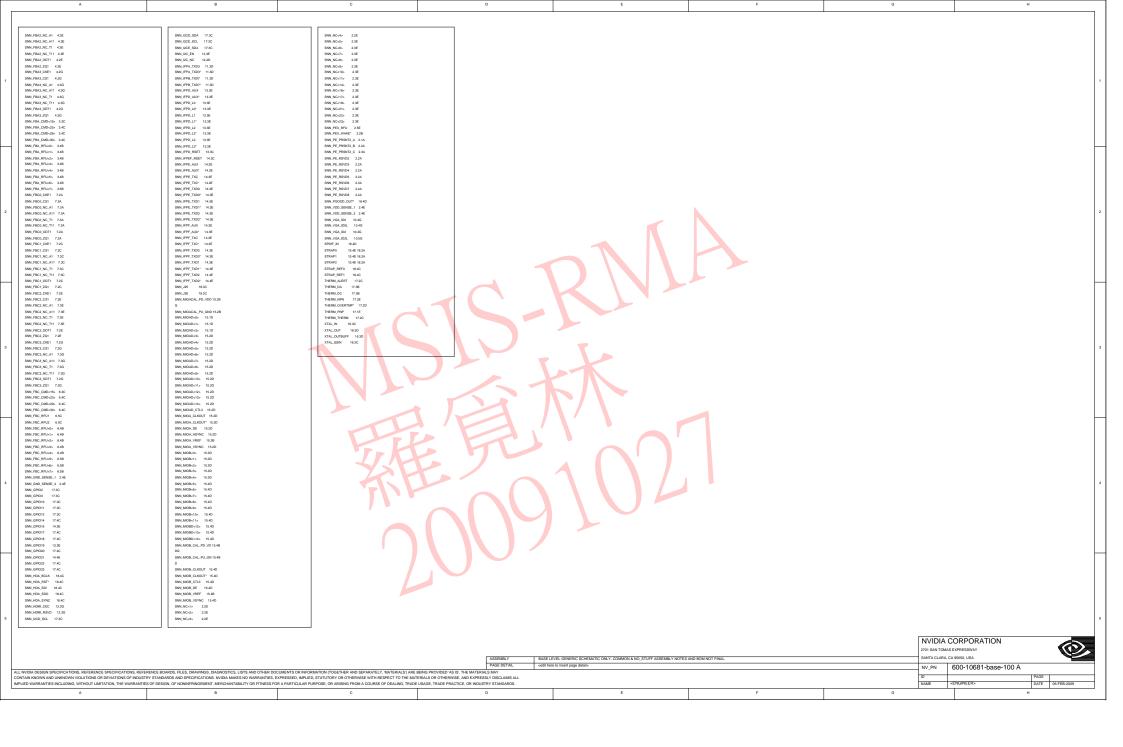


Title: Basenet Report	FBA_CMD<21> 3.20 3.40 4.14 4.10	FBA_DQSN-0> 3.4B 4.4B 4.5E	FBC_D-25> 6.28 7.4C	GPIO0_FAN_PWM_O_L 17.4G	NV/DD_GND_SENSE_R 21.2B 21.4C	PEX_TXXXX 228 23G	
Dasign: p681	4.1E 4.1G	FBA_DQSNx7.0> 3.4A 4.3A 4.5E	FBC_D<26> 6.28 7.4C	GPIO9_FAN_PWM_R 17:3F	NV/DD_JOFS 21.2C	PEX_TXXXY	
Date: Jan 22 13:35:02 2009	FBA_CMD<22> 3.4C 3.4F 4.1E 4.1G 4.2A 4.2C	FBA_DQSN<1> 3.48 4.48 4.5E FBA_DQSN<2> 3.48 4.4C 4.5E	FBC_D<27> 628 7.4C FBC_D<28> 628 7.4C	GPIO13_FBVDDQ_VSEL 17.4D 20.4D GPIO16_FAN_PWM 17.4C	NV/DD_MODE 21.2C NV/DD_MODE_Q 21.18	PEX_TXX1	
Base nets and synonyms for	4.24.4.2C FBA_CMD<24> 3.3F 3.4C 4.1A 4.1C	FBA_DQSN-25 3.48 4.4C 4.5E FBA_DQSN-25 3.4B 4.4C 4.5E	FBC_D<28> 62B7.4C FBC_D<29> 62B7.4C	GPU_PLLVDD 16:3B	N/VDD_MODE_R 212B	PEX_TXX1* 22823G PEX_TXX2 22823G	
681_lib.P681(@p681_lib.p681(sch_1))	4.1E 4.1G	FBA DQSNo4> 3.48 4.4D 4.5E	FBC D<30> 628.7.4C	GPU TESTMODE 2.5E	NV/DD REFIN 21.3C	PEX_TXX2* 22823G	
Sase Signal Location([Zone](dirl)	FBA_CMD<25> 3.4C 3.4G 4.3A 4.3C FBA_CMD<28> 3.2F 3.4C 4.1A 4.1C	FBA_DQSN-d> 3.48 4.40 4.5E FBA_DQSN-d> 3.48 4.45 4.45	FBC_D<31> 62B 7.4C FBC_D<32> 62B 7.3D	HDM_PD_1 12.48 12CA_SCL 9.2C.9.2D	NVVDL_RSET 21.9C NVVDD_SENSE 2.4F.21.4D	PEX_TXXX 2:38:2:30 PEX_TXXX 2:38:2:30	
V1 ADJ 19.28	FBA_CMD-286 3.2F 3.4C 4.1A 4.1C 4.1E 4.1G	FBA_DQSN-65 3.48 4.4E 4.5E FBA_DQSN-7> 3.48 4.4E 4.5E	FBC_D<32> 62B 7:3D FBC D<33> 62B 7:4D	12CA_SCL 9.2C 9.2D 12CA_SCL C 9.2H 11.3G	NV/DD_SENSE 2.4F 21.4D NV/DD SENSE R 21.4E	PEX_TXX4 23B 23G PEX_TXX4 23B 23G	
/1_PLLVDD 2.4G 3.5E 11.2A 16.3A	FBA_CMD-27> 3.4C 3.4G 4.2E 4.2G	FBA_VREF 3.5B	FBC_D<34> 62B 7.4D	I2CA_SCL_T 92F	N/VDD_SS 21.9C	PEX_TXX4* 23823G	
19.2C	FBA_CMD<28> 3.4C 4.2E 4.2G 4.3A	FBA_VREF0	FBC_D<35> 6.28 7.4D	12CA_SDA 9.1D 9.2C	NV/DD_VID 21.3C	PEX_TXX5 23B23G	
8_ADJ 19.2F	4.90	FBA_VREF1	FBC_D<96> 6.2B 7.4D	I2CA_SDA_C 9.1H 11.3G	NVVDD_VREF 21.9C	PEX_TXXS* 2:38 2:3G	
/3 2.1A /3 INFO 18.5F	FBA_D-05 3.18 4.38 FBA_D-65.05 3.1A 4.3A 4.5G	FBA_Z00 4.3A FBA_Z01 4.3C	FBC_D<37> 6.28 7.4D FBC D<38> 6.28 7.4D	12CA_SDA_T 2.1F 12CB_SCL 10.2C	NVVDD_VSEL2 21.38 NVVDD_VSEL2 Q 21.38	PEX_TXX8	
/3_INFO 18.5F / 19.4C	FBA_D<63.0> 3.1A 4.3A 4.5G FBA_D<1> 3.1B 4.3B	FBA_ZQ1 4.3C FBA_ZQ2 4.3E	FBC_D<38> 6.28 7.4D FBC_D<39> 6.28 7.4D	12CB_SCL 10.2C 12CB_SCL_R 10.1E	NVVDD_VSEL2_Q 21.38 NVVDD_VSEL3 21.48	PEX_TXX8* 238.23G PEX_TXX7 238.23G	
_ADJ 19.48	FBA_D<2> 3.18 4.48	FBA_ZQ3 4.3Q	FBC_D-40> 628 7.4D	12CB_SCL_R_L 10.1G	NV/DD_VSEL3_Q 21:3C	PEX_TXX7* 23823G	
2V 2.1A	FBA_D<3> 3.18 4.48	FBC_CLK0 6.4D 7.2A 7.2C 7.5G	FBC_D-41> 6.28 7.4D	12CB_SDA 10.2C	PEX_CLKREQ* 2.1C	PEX_TXX8	
V_D 19.4A V F 21.1F	FBA_Do4o 3.18 4.48	FBC_CLK0* 6.4D 7.2A 7.2C 7.5G	FBC_D-42> 6.38 7.4D	I2CB_SDA_R 10.1E	PEX_PLLVDD 2.4E	PEX_TXX8* 2.3G 2.4B	
V_F 21.1F CA BLUE 9.4E 9.5A	FBA_D-do 3.18 4.48 FBA_D-do 3.18 4.48	FBC_CLK1	FBC_D+43> 638 7.4D FBC_D+44> 638 7.4D	12CB_SDA_R_L 10.1G 12CC_SCL 17.2B 17.3F 18.5E	PEX_PLL_CLK_OUT 22C PEX_PLL_CLK_OUT* 22C	PEX_TXX9	
CA_BLUE_C 9.4H 9.5A 11.3G	FBA_D<7> 3.18 4.48	FBC_CMD-0> 8.90 6.40 7.24 7.20	FBC_Do45	12CC_SCL_G 17.3C	PEX_PRENTI* 2.1A	PEX_TXX10 23G 24B	
CA_GREEN 9.4E 9.5A	FBA_D<8> 3.18 4.48	FBC_CMD<30.0> 6.3D 7.1A 7.1C 7.1D	FBC_D-46> 6.38 7.4D	I2CC_SDA 17.2B 17.3F 18.5E	PEX_REFCLK 2.1G.2.2B	PEX_TXX10* 2.9G 2.4B	
CA_GREEN_C 9.4H 9.5A 11.3G	FBA_D<0> 3.18 4.48	7.1F 7.5G	FBC_D<47> 6:38 7:4D	12CC_SDA_G 17:9C	PEX_REFCLK* 2.1G 2.2B	PEX_TXX11 2.3G 2.4B	
LHSYNC 9.3C 9.5A	FBA_D<10> 3.1B 4.4B	FBC_CMD<1> 6.2F 6.3C 7.1A 7.1C	FBC_D<48> 6.3B 7.3E	I2CH_SCL 18.4D	PEX_RST* 2.2D 19.4E	PEX_TXX11* 2.9G 2.4B	
A_HSYNC_BUF 9.3E 9.5A A_HSYNC_C 9.3H 9.5A 11.9G	FBA_D<11> 3.18.4.48 FBA_D<12> 3.28.4.48	7.1E 7.1G FBC CMD-2> 8.1G 8.3C 7.1A 7.1C	FBC_D-60> 6.38 7.4E FBC_D-60> 6.38 7.4E	12CH_SDA 18.4D 12CS SCL 2.1C 17.3F	PEX_RX0	PEX_TXX12 2.9G 2.4B PEX_TXX12* 2.9G 2.4B	
A_HSYNC_C	FBA_D<12> 32B 4.4B FBA_D<13> 32B 4.4B	FBC_CMD-2> 6.1G 6.3C 7.1A 7.1C FBC_CMD-3> 6.2G 6.3C 7.1A 7.1C	FBC_D-50> 63B 7.4E FBC_D-51> 63B 7.4E	12CS_SCL 2.1C 17.3F 12CS_SDA 2.2C 17.3F	PEX_RX0* 22B 2.4G PEX_RX1 22B 2.4G	PEX_TXX12* 2.3G 2.4B PEX_TXX13 2.3G 2.5B	
A_RED 9.3E.9.5A	FBA_D<14> 3.28 4.48	7.1E 7.1G	FBC_0-62> 6.38 7.4E	12CW_SCL 12.9C	PEX_RX1* 22B24G	PEX_TXX13* 2.30.25B	
_RED_C 9.3H 9.5A 11.3G	FBA_D<15> 3.28 4.48	FBC_CMD-4> 6.2G 6.3C 7.1A 7.1C	FBC_D<53> 6:38 7:4E	12CW_SCL_R 12.2D	PEX_RX2	PEX_TXX14 2.9G 2.5B	
_RSET 23B	FBA_D<16> 3.28 4.3C	7.1E 7.1G	FBC_D<54> 6.38 7.4E	12CW_SCL_R_Q 12.2E	PEX_RX2* 2:38:2:4G	PEX_TXX14* 2.4G.2.5B	
VDD 9.2B VREF 9.3B	FBA_D<17> 328 4.3C FBA_D<18> 328 4.4C	FBC_CMD<6> 6.3C 6.4G 7.1A 7.1C 7.1E 7.1G	FBC_D<65> 6.38 7.4E FBC_D<66> 6.38 7.4E	12CW_SDA 12.9C 12CW_SDA_R 12.1D	PEX_RX3	PEX_TXX15 24G 25B PEX_TXX15* 24G 25B	
VREF 23B VSYNC 23C 25A	FBA_D<18> 3.28 4.4C FBA_D<19> 3.28 4.4C	7.1E 7.1G FBC_CMD+6> 6.3C 6.4F 7.1A 7.1C	FBC_D-d6> 63B 7.4E FBC_D-d7> 63B 7.4E	12CW_SDA_R 12:10 12CW_SDA_R_Q 12:16	PEX_RX4 2:38 2:4G PEX_RX4 2:38 2:4G	PEX_TXX15* 2.4G 2.5B PEX_VDD 20.3B	
SYNC_8UF 9.2E 9.5A	FBA_D<20> 3.28 4.4C	7.1E 7.1G	FBC_D-68> 6.38 7.4E FBC_D-68> 6.38 7.4E	IFPAB_IOVDD 11.38	PEX_RX4* 23824G	PS_1V1_CP 20.38	
VSYNC_C 9:2H 9:5A 11:3G	FBA_D<21> 3.28 4.4C	FBC_CMD+7> 8:9C 6:9G 7:1A 7:1C	FBC_D<69> 638 7.4E	IFPAB_PLLVDD 11.2B	PEX_RX5 2:38:2:4G	PS_1V1_DR 20:2C	
SYNC_R 9.2E 9.5A	FBA_D<22> 3.28 4.40	7.1E 7.1G	FBC_D<60> 638 7.4E	IFPAB_RSET 11.38	PEX_RXS* 2.3B 2.4G	PS_1V1_FB 20.3C	
BLUE 10.4D 10.5A BLUE_C 10.4F 10.5A	FBA_D<23> 328 4.4C FBA_D<24> 328 4.4C	FBC_CMD-8b 6.3C 6.4F 7.1A 7.1C 7.1E 7.1G	FBC_D+81> 6.3B 7.4E FBC_D+82> 6.3B 7.4E	IFPA_TXC 11:3D IFPA_TXC* 11:3D	PEX_RX6 2:38 2:4G PEX_RX6* 2:38 2:4G	PS_FBVDDQ_BOOT 20:20 PS_FBVDDQ_CP 20:30	
UE_C 10.4F10.5A REEN 10.4D10.5A	FBA_D<24> 3.28 4.4C FBA_D<25> 3.28 4.4C	7.1E 7.1G FBC_CMD <b< td=""><td>FBC_D-62> 6.3B 7.4E FBC_D-63> 6.3B 7.4E</td><td>IFPA_TXC* 11.3D IFPA_TXD0 11.2D</td><td>PEX_RX6* 23B 2.4G PEX_RX7 2.3B 2.4G</td><td>PS_FBVDDQ_CP 20:3D PS_FBVDDQ_CP_RC 20:3D</td></b<>	FBC_D-62> 6.3B 7.4E FBC_D-63> 6.3B 7.4E	IFPA_TXC* 11.3D IFPA_TXD0 11.2D	PEX_RX6* 23B 2.4G PEX_RX7 2.3B 2.4G	PS_FBVDDQ_CP 20:3D PS_FBVDDQ_CP_RC 20:3D	
REEN_C 10.4F 10.5A	FBA_D<28> 3.28 4.40	7.2E 7.2G	FBC_DEBUG 6.4C	IFPA_TXD0" 11.20	PEX_RX7* 2.4B.2.4G	PS_FBVDDQ_EN 20.4B	
SYNC 10.9C 10.5A	FBA_D<27> 3.28 4.4C	FBC_CMD<10> 6.9C 6.9G 7.1A 7.1C	FBC_DQM<0s 6:38 7:48	IFPA_TXD1 11.2D	PEX_RX8 2.4B 2.4G	PS_FBVDDQ_ENr 20.4C	
SYNC_BUF 10.3E 10.5A	FBA_D<28> 32B 4.4C	7.1E 7.1G	FBC_DQM<7.05 6.3A 7.3A 7.5G	IFPA_TXD1* 11.20	PEX_RX8* 2.4B.2.4G	PS_FBVDDQ_FB 20.3D	
SYNC_C 10.3G 10.5A SYNC_R 10.3E 10.5A	FBA_D<29> 328 4.4C FBA_D<30> 328 4.4C	FBC_CMD<11> 6.3C 6.4F 7.1E 7.1G FBC_CMD<12> 6.3C 6.4G 7.2A 7.2C	FBC_DQMc1> 6.38 7.48 FBC_DQMc2> 8.38 7.4C	IFPA_TXD2 11.3D IFPA_TXD2* 11.2D	PEX_RX9 24B 24G PEX_RX9* 24B 24G	PS_FBVDDO_FB_RC 20.40 PS_FBVDDO_FS_DIS 20.3C	
EYNC_R 10.3E 10.5A ED 10.3D 10.5A	FBA_D<30> 32B 4.4C FBA_D<31> 32B 4.4C	FBC_CMD<12> 6.3C 6.4G 7.2A 7.2C 7.2E 7.2G	FBC_DQM<3> 6.38 7.4C	IFPA_TXD2* 11.20 IFPB_TXD4 11.3D	PEX_RX9° 2.4B 2.4G PEX_RX10 2.4B 2.4G	PS_FBVDDQ_FS_DIS 20.3C PS_FBVDDQ_LG 20.3D	
D_C 10.3F 10.5A	FBA_D<32> 3.2843D	FBC_CMD<13> 6.3C 6.3F 7.2A 7.2C	FBC_DOM-d> 6.387.40 FBC_DOM-d> 6.387.40	IFPB_TXD4* 11.3D	PEX_RX10* 2.4B 2.4G	PS_FBVDDQ_PH 20.3D	
BET 10.38	FBA_D<33> 3.28 4.3D	7.2E 7.2G	FBC_DQMc5> 6.38 7.4D	IFPB_TXD5 11.3D	PEX_RX11 2.4B 2.4G	PS_FBVDDQ_PVCC 20.2D	
00 10.28	FBA_D<34> 3.28 4.4D	FBC_CMD<14> 6.1G 6.3C 7.1A 7.1C	FBC_DQM-65	IFPB_TXD5* 11.30	PEX_RX11* 2.4B.2.4G	PS_FBVDDQ_PVCC_R 20.2C	
REF 10.2B SYNC 10.3C 10.5A	FBA_D<35> 328 4.4D FBA_D<38> 328 4.4D	7.1E 7.1G FBC_CMD<16> 6.3C 6.4G 7.2E 7.2G	FBC_DQM:7> 6:48 7:4E FBC_DQS:0> 6:48 7:48 7:5E	IFP8_TXD8 11.3D IFP8_TXD8* 11.3D	PEX_RX12	PS_FBVDDO_RC 20.2F PS_FBVDDO_UG 20.2D	
SYNC 10:3C 10:5A SYNC_BUF 10:2E 10:5A	FBA_D<38> 32B 4.4D FBA_D<37> 32B 4.4D	FBC_CMD<17> 6.3C 6.3G 7.1A 7.1C	FBC_DQS<7.0> 6.48.7.38.7.5E FBC_DQS<7.0> 6.44.7.34.7.5E	IFPE_IXDB* 11.3D IFPCEF_PLLVDD 14.3C	PEX_RX12* 2:5B 2:5G PEX_RX13 2:5B 2:5G	PS_FBVDDQ_UG_R 20.2E PS_FBVDDQ_UG_R 20.2E	
SYNC_C 10.2G 10.5A	FBA_D<38> 3.28 4.4D	7.1E 7.1G	FBC_DQS<1> 6.48 7.48 7.5E	IFPC_IOVDD 12:3A	PEX_RX13* 2.5B 2.5G	PS_FBVDDQ_VCC 20:2D	
SYNC_R 10.2E 10.5A	FBA_D<30> 3.28 4.4D	FBC_CMD<18> 6.1F 6.3C 7.1E 7.1G	FBC_DQ8<2> 6.48 7.4C 7.5E	IFPC_PLLVDD 12:3A	PEX_RX14 2:5B 2:5G	PS_NVVDD_BOOT1 21.2D	
19.4D	FBA_D<40> 3.28 4.4D	7:2A 7:2C	FBC_DQ8<3> 6.48.7.4C.7.5E	IFPC_RSET 12:3A	PEX_RX14* 2.5B 2.5G	PS_NV/DD_BOOT2 21:3D	
K0 3.4D 4.2A 4.2C 4.5G K0° 3.4D 4.2A 4.2C 4.5G	FBA_D-41> 3.28 4.4D FBA_D-42> 3.38 4.4D	FBC_CND <tb> 6.9C 6.4F 7.1A 7.1C 7.1E 7.1G</tb>	FBC_DQS-4> 6.48 7.40 7.5E FBC_DQS-5> 6.48 7.40 7.5E	IFPC_TXC 12:3D IFPC_TXC* 12:3D	PEX_RX15	PS_NVVDD_EN 21.20 PS_NVVDD_EN 21.28	
K0* 3.4D 4.2A 4.2C 4.5G K1 3.4D 4.2D 4.2F 4.5G	FBA_D<42> 3.38 4.4D FBA_D<43> 3.38 4.4D	7.1E 7.1G FBC_CMD-20% 6.3F 6.4C 7.1A 7.1C	FBC_DQS:65	IFPC_TXC* 12:3D IFPC_TXC_C1 12:3F 12:4B	PEX_RX15* 2.58.2.5G PEX_SMCLK 2.1B	PS_NVVDD_EN* 21.2B PS_NVVDD_LG1 21.2D	
K1* 3.4D 4.2D 4.2F 4.5G	FBA_D+44> 3.38 4.4D	7.1E 7.1G	FBC_DQS<7> 8.4B 7.4E 7.5E	IFPC_TXC_C1* 12.3F 12.4B	PEX_SMDAT 2.1B	PS_NVVDD_LG2 21:30	
D-0> 33C 34G 42A 42C	FBA_D<46> 3.38 4.4D	FBC_CMD<21> 6.2G 6.4C 7.1A 7.1C	FBC_DQSN<0> 6.4B 7.4B 7.5E	IFPC_TXD0 12:30	PEX_TCLK 2.1B	PS_NVVDD_PH1 21.2D	
D<30.0> 3.3D 4.1A 4.1C 4.1D	FBA_D<46> 3.38 4.4D	7.1E 7.1G	FBC_DQSN<7.0> 6.4A 7.3A 7.5E	IFPC_TXD0* 12:3D	PEX_TDI 2.1B	PS_NVVDD_PH2 21.3D	
4.1F 4.5G D<1> 3.3C 3.4F 4.1A 4.1C	FBA_D<47> 338 4.4D FBA_D<48> 338 4.3E	FBC_CMD-225 6.1F 6.4C 7.1E 7.1G	FBC_DQSN<1> 64B 7.4B 7.5E FBC_DQSN<2> 64B 7.4C 7.5E	IFPC_DXD_C1 12:3F 12:4B IFPC_DXD_C1 12:3F 12:4B	PEX_TDO 2:1B PEX_TERMP 2:5E	PS_NVVDD_RC1 21.2F PS_NVVDD_RC2 21.3F	
4.1E 4.1G	FBA_D<48> 3.38 4.3E FBA_D<49> 3.38 4.3E	FBC_CND<24> 6.2F 6.4C 7.1A 7.1C	FBC_DQSN-cbs	IFPC_TX00_C1* 12:3F 12:4B IFPC_TX01 12:30	PEX_TEMM 2.5E PEX_TMS 2.1B	PS_NVVDD_VG1 21:35 PS_NVVDD_VG1 21:20	
D-2> 33C34G4.1A4.1C	FBA_D<50> 3.38 4.4E	7.1E 7.1G	FBC_DQSN-4> 6.48 7.4D 7.5E	IFPC_TXD1* 12:3D	PEX_TRST* 2.1B	PS_NVVDD_UG1_R 212E	
D-35 3.2G 3.3C 4.1A 4.1C	FBA_D<51> 3.38 4.4E	FBC_CMD<25> 6.4C 6.4G 7.8A 7.3C	FBC_DQSN<5> 6.48 7.40 7.5E	IFPC_TXD1_C1 12:3F 12:4B	PEX_TX0 2:1G:2:2D	PS_NVVDD_UG2 21:3D	
4.1E 4.1G	FBA_D<52> 3.38 4.4E	FBC_CMD<28> 6.2F 6.4C 7.1A 7.1C	FBC_DQSN-6> 6.48 7.4E 7.5E	IFPC_TXD1_C1* 123F124B	PEX_TX0* 2.1G.2.2D	PS_NVVDD_UG2_R 21.3E	
3-6- 3.1F 3.3C 4.1A 4.1C	FBA_D<53> 3.38 4.4E	7.16 7.10	FBC_DOSN-7> 6.48 7.46 7.56	IFPC_TX02 12:30	PEX_TX1 2.1G.2.2D	PS_NVVDD_VCC9 21.2C	
4.1E 4.1G De5> 3.3C 3.3G 4.1A 4.1C	FBA_D<54> 338 4.4E FBA_D<55> 338 4.4E	FBC_CMD<27> 6.4C 6.4G 7.2E 7.2G FBC_CMD<28> 6.4C 7.2E 7.2G 7.3A	FBC_VREF0 7.9C 7.9E 7.4G FBC_VREF1 7.9F 7.9H 7.4H	IFPC_TXD2* 12:3D IFPC_TXD2_C1 12:3F 12:4B	PEX_TX1* 2:1G 2:2D PEX_TX2 2:2D 2:2G	PS_NVVDD_VCC12 21.2C ROM_CS* 18.5D	
0-55 33C 33G 4.1A 4.1C 4.1E 4.1G	FBA_D<56> 3.38 4.4E FBA_D<56> 3.38 4.4E	FBC_CMD-285 6.4C 7.2E 7.2G 7.3A 7.3C	FBC_VREF1 7.3F 7.3H 7.4H FBC_Z00 7.3A	IFPC_TXD2_C1	PEX_TX2 2:20:2:26 PEX_TX2 2:20:2:26	ROM_CS* 18.3D ROM_SCLK 18.2C 18.3D 18.3D	
Neb 3.1G 3.3C 4.1A 4.1C	FBA_D<57> 3.38 4.4E	FBC_D<0> 6.18 7.38	FBC_201 7.9C	IFPD_IOVDD 13.3C	PEX_TX3	ROM_SI 18.2C 18.3D 18.3D	
4.1E 4.1G	FBA_D<58> 3.38 4.4E	FBC_D+63.0> 6.1A.7.3A.7.5G	FBC_Z02 7.3E	IFPD_PLLVDD 13.9C	PEX_TX3* 2.2G.2.3D	ROM_SO 18.2C 18.3D 18.3D	
33C 34F 4.1A 4.1C	FBA_D<59> 3.38 4.4E	FBC_D<1> 6.18 7.48	FBC_203 7.3G	IFPEF_JOVDO 14.3C	PEX_TX4 2.2G 2.3D	SNN_3V3AUX 2.1A	
4.1E 4.1G 0x8b 3.9C 3.4F 4.1A 4.1C	FBA_D<60> 338.4.4E FBA_D<61> 338.4.4E	FBC_D<2> 6.18 7.48 FBC_D<3> 6.18 7.48	FBVDDQ 20.2H FBVDDQ RBOT 20.4F	IFP_JOVDD_EN* 17:2E 19:4G IFP_JOVDD_EN_RC 19:4F	PEX_TX4* 22G 23D PEX_TX5 22G 23D	SNN_BIOB_HSYNC 15.4D SNN_BTXC 11.3D	
4.1E 4.1G	FBA_D<62> 3.38 4.4E	FBC_Do4o 6.18 7.48	FBVDDQ VSEL 20.4E	IFP_PLLVDD 12.2A 19.2G	PEX_TX5* 22G 23D	SNN_BTXC* 11.3D	
Ndb 32F 33C 42A 42C	FBA_D<63> 3.38 4.4E	FBC_D-d> 6.18 7.48	FB_CAL_PO_VDDQ 6.5C	JTAG_TCLK 2.1C 17.4A	PEX_TX6 2.2G 2.3D	SNN_BUFRST* 18.4D	
4.2E 4.2G	FBA_DEBUG 3.4C	FBC_D-65 6.18 7.48	FB_CAL_PU_GND 6.5C	JTAG_TDI 2.1C 17.4A	PEX_TX8* 2.2G.2.3D	SNN_CEC 18.4C	
<10> 3.2F 3.3C 4.1A 4.1C 4.1E 4.1G	FBA_DOM<0> 3.38 4.48 FBA_DOM<7.0> 3.34 4.34 4.5G	FBC_D<7> 6.18 7.48 FBC_D<8> 6.18 7.48	FB_CAL_TERM_GND 6.5C	JTAC_TDO 2:1C:17.5A JTAC_TMS 2:1C:17.4A	PEX_TX7 2:20:2:3D PEX_TX7 2:20:2:3D	SNN_FBA0_CKE1 4.2A	
4.1E 4.1G <11> 3.1F 3.3C 4.1E 4.1G	FBA_DQM<7.0> 3.34 4.34 4.5G FBA_DQM<1> 3.38 4.4B	FBC_Dolb 6.18 7.48 FBC Dolb 6.18 7.48	FB_PLIAVDD 3:50 GPI00_HP0_DVI 11.4D	JTAG_TMS 2:1C:17.4A JTAG_TRST* 2:1C:17.5A	PEX_TXP	SNN_FBA0_CS1 4.2A SNN_FBA0_NC_A1 4.3A	
0<12> 3.1G 3.3C 42A 4.2C	FBA_DQM<2> 3.38 4.4C	FBC_D<10> 6.187.48	GPI00_HPD_DVI_Q 11.4D	MIOA_CLKIN 15.2D	PEX_TX8* 2.2G.2.4D	SNN_FBA0_NC_A11 4.3A	
4.2E 4.2G	FBA_DQM<3> 3.38 4.40	FBC_D<11> 6.187.48	GPIO0_HPD_DVI_R 11.4E	MIOA_VDDQ 15.1C	PEX_TX9 2.2G 2.4D	SNN_FBA0_NC_T1 4.3A	
1<13> 3.3C 3.3G 4.2A 4.2C	FBA_DQMo4> 3.38 4.4D	FBC_D<12> 6.28 7.48	GPI00_HPD_DVI_RL 11.9G	MIOB_CLKIN 15.4D	PEX_TXV 22G 24D	SNN_FBA0_NC_T11 4:3A	
4.2E 4.2G <16> 3.3C 3.3F 4.1A 4.1C	FBA_DOMe65 3.38 4.4D FBA_DOMe65 3.4B 4.4E	FBC_0<13> 8.28.7.48 FBC_0<14> 8.28.7.48	GPIO1_HPOC_12.4C GPIO1_HPOC_Q 12.4D	NVVDD 21:2H NVVDD_CP 21:3C	PEX_TX10 22G 24D PEX_TX10* 22G 24D	SNN_FBA0_ODT1 4.2A SNN_FBA0_ZQ1 4.2A	
4.1E 4.1G	FBA_DQM<8> 3.48 4.4E FBA_DQM<7> 3.48 4.4E	FBC_D<15> 6.28.7.48 FBC_D<15> 6.28.7.48	GPI01_HP0C_R 12.4E	NVVDD_CP_RC 21.4D	PEX_TX10* 22G 24D PEX_TX11 22G 24D	SNN_FBA1_CKE1 4.2C	
1<16> 3.3C 3.4G 4.2E 4.2G	FBA_DQ8<0> 3.4B 4.4B 4.5E	FBC_D<16> 6.287.3C	GPI01_HPDC_R_L 124G	NVVDD_CSN 21:3D	PEX_TX11* 2.2G.24D	SNN_FBA1_CS1 42C	
<17> 3.9C 3.9G 4.1A 4.1C	FBA_DQ8<7.0> 3.4A 4.3A 4.5E	FBC_D<17> 6.28 7.4C	GPIO4_FAN_TACH 17.9C	NVVDD_CSN_R 21.3D	PEX_TX12	SNN_FBA1_NC_A1 4.9C	
4.1E 4.1G	FBA_DQ8<1> 3.48 4.48 4.5E	FBC_D<18> 6.287.4C	GPI05_NVVDD_VSEL1 17:3D 21:1A 21:3A	NW00_CSP 21.30	PEX_TX12* 22G 24D	SNN_FBA1_NC_A11 4.3C	
1<18> 3.3C 3.4G 4.1E 4.1G 4.2A 4.2C	FBA_DQS<2> 3.48 4.4C 4.5E FBA_DQS<3> 3.48 4.4C 4.5E	FBC_D<19> 6.28.7.4C FBC_D<20> 6.28.7.4C	GPIO6_NVVDD_VSEL2 17:3D 21:3A GPIO7_NVVDD_VSEL3 17:3D 21:2A 21:4A	NVVDD_EAP 21.3C NVVDD_EN 17.2F 21.2A	PEX_TX13	SNN_FBA1_NC_T1 4.3C SNN_FBA1_NC_T11 4.3C	
428 42C 1-19- 32G 33C 4.1A 4.1C	FBA_DUS<3> 3.48 4.4C 4.5E FBA_DUS<4> 3.48 4.4D 4.5E	FBC_D-205 6287-4C FBC_D-21> 6287-4C	GPIOF_NVVID_VSELS 17:3D 21:2A 21:AA GPIO8_THERM_OVERTM 17:3C	NVVDD_EN 17.2F 21.2A NVVDD EN: 17.1E	PEX_TX19* 22G 25D PEX_TX14 22G 25D	SNN_FBA1_NC_TT1 4.3C SNN_FBA1_ODT1 4.2C	
4.1E 4.1G	FBA_DQS<5> 3.48 4.4D 4.5E	FBC_D<22> 6:287.4C	P*	NW00_FB 21.40	PEX_TX14* 22G 25D	SNN_FBA1_ZQ1 42C	
0<20> 3.3F 3.4C 4.1A 4.1C	FBA_DQS<6> 3.48 4.4E 4.5E	FBC_D<23> 6.287.4C	GPIO9_FAN_PWM 17.3C	NVVDD_FB_RC 21.4D	PEX_TX15 2.2G 2.5D	SNN_FBA2_CKE1 4.2E	
4.1E 4.1G	FBA_DQS<7> 3.4B 4.4E 4.5E	FBC_D<24> 6.28 7.4C	GPIOR_FAN_PVIM_Q 17.4F	NVVDD_GND_SENSE 24F21.4B	PEX_TX15* 22G 2:5D	SNN_FBA2_CS1 42E	
						NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY	
		ASSEMBLY BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON B NO STUFF ASSEMBLY MOTES AND BOM NOT FINAL					
				COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL			
	ADDE FILES DOMINAGE DISCHOSTOR (1999)	MATION (TOOFTIER AND SERVICE VIA THE SERVICE OF SERVICE	PAGE DETAIL <edit detail="" here="" insert="" page="" to=""></edit>	COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL		NV_PN 600-10681-base-100 A	
SIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BIO IN AND UNKNOWN VICLATIONS OF BEVIATIONS OF BRUSTRY STANDI	ARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFO RRDS AND SPECIPICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLE	MATION (TOGETHER AND SEPARATELY, MATERIALS) ARE BEING PROVIDED 'AS IS: T), STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, A	PAGE DETAIL <edit all<="" details="" disclaims="" expressly="" he="" here="" insert="" materials="" may="" nd="" page="" td="" to=""><td>COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL</td><td></td><td>ID PAGE</td></edit>	COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL		ID PAGE	
SIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BO, MY AND UNKNOWN VOLATIONS OF DEVULONS OF ROUSETY STAND, AMTERS RECLUSIONS, WITHOUT LIMITATION, THE WARRANNES OF DESK	ARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFO RRDS AND SPECIFICATIONS. INVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIE DN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR P	IMATION (TOGETHER AND SEPARATELY, MATERIALS) ARE BEING PROVIDED AS IS. T S, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, A RPOSE, OR ARROSH PROM A COURSE OF DEALING, TRANS USAGE TRADE PRACTICE	PAGE DETAIL <edit all<="" details="" disclaims="" expressly="" he="" here="" insert="" materials="" may="" nd="" page="" td="" to=""><td>COMMON & NO, STUFF ASSEMBLY NOTES AND BOM NOT FRAIL</td><td></td><td>NV_PN 600-10681-base-100 A D</td></edit>	COMMON & NO, STUFF ASSEMBLY NOTES AND BOM NOT FRAIL		NV_PN 600-10681-base-100 A D	

Е

A

С



Title: Cred Plant report report resigns: p881 rese: Jah 22 Jah 22 Jah 22 Jah 22 Jah 22 Jah 22 Jah 24 Jah 25 Jah 24 Jah 25 Jah 25 Jah 26	CBB [90.27] CBB [90.27] CBB [90.374] CBB [91.40] CBB [91.50] CBB [83.50] CBB [83.50]	CM2 [R-2A] CM3 [R-2M] CM4 [R-1M] CM6 [R-1M] CM6 [R-1A] CM7 [R-1A] CM7 [R-1A] CM7 [R-1A]	C668 [11.36] C609 [2.37] C600 [15.36] C601 [12.26] C601 [3.25] C603 [11.36] C604 [3.25]	CPG [6.26] CPG [10.26] CPG [10.46] CPG [10.46] CPG [12.48] CPG [8.47]	7:28 10 [7:27:40 7:46 10 [7:47:40 7:29 10 [4:40:48] 4:20	R62 [7748] R63 [12.50] R64 [12.50] R65 [12.50] R66 [1.64] R67 [2.10] R68 [1.64]	R520 (R.50) R520 (R.50) R530 (R.57) R531 (R.40) R532 (R.40) R532 (R.50) R533 (R.50)	576 [R-416-44-6-44 6-4] 579 [R34-23-4-23-4 6-24] 570 [R34-23-4-23-4 6-24] 571 [R46-64-6-6-6-6-4	
[16.45] [10.47] [10.30] [10.30] [10.30] [10.30] [10.30] [10.30] [10.30] [10.40] [10.40] [10.40] [10.40] [10.40] [10.40] [10.40] [10.40] [10.40] [10.40] [10.40] [10.40] [10.40] [10.40] [10.40]	Colo [8-30] Colo [8-14] Colo [8-14] Colo [8-14] Colo [8-34] Colo [8-47] Colo [8-47] Colo [8-47] Colo [8-47] Colo [8-54] Colo [8-54] Colo [8-55] Colo [Come Justica Come Come Justica	C666 [23-C] C666 [24-C] C667 [12-26] C668 [22-C] C669 [17-46] C677 [13-36] C677 [13-36] C677 [13-36] C677 [23-7] C679 [3-34]	C770 [0.47] C771 [0.37] C772 [9.57] C773 [9.57] C774 [9.57] C774 [9.57] C776 [9.57] C776 [9.57] C776 [9.57] C777 [MB [1231-440 442] MF [1221-44] MB [127-44] MB [127-44] MBCC [1627] MBCCS [1623] MBCS [1623	600 [8-06] 600 [7-34] 601 [8-06] 602 [9-37] 603 [9-46] 604 [9-46] 605 [9-24] 605 [9-24] 606 [9-46] 607 [9-47]	855 [850] 850 [850] 850 [850] 850 [854] 850 [134] 850 [123] 851 [123] 851 [123] 851 [123] 851 [123] 852 [145] 853 [145] 854 [145] 855 [145] 855 [145] 856 [145] 857 [145] 857 [145]	6-60] 8972 [B413-613-61 3-69] 8973 [B203-03-02-03 3-809] 8974 [B303-03-03-03 3-809] 8975 [B403-03-03-03 3-809] 8976 [B303-03-03-03 3-809] 8977 [B303-03-03-03 3-809] 8978 [B303-03-03-03 3-309]	
19.3883 19.3885 19.385 19.3885 19.3885 19.3885 19.3885 19.3885 19.3885	C11 [94-6] C12 [94-6] C13 [94-6] C14 [94-6] C15 [94-6] C15 [94-6] C16 [94-6] C17 [94-6] C17 [94-6] C19 [94-6]	Com 3-C	Cells 17.38 Cells 17.38 Cells 23.5 Cells 13.5 Cells 13.5 Cells 15.5	08 (0.87) 09 (0.87) 09 (0.87) 010 [0.87] 011 [11.45] 012 [12.85] 013 [12.47] 014 [10.46] 015 [12.97] 016 [14.50] 0500 [10.87] 0500 [10.87] 0500 [10.87] 0500 [10.87] 0500 [10.87] 0500 [10.87] 0500 [10.87] 0500 [10.87]	On (11-0) On (21-0) On (21	874 D230] 875 D230] 876 D240 877 D244 877 D244 880 D144 880 D144 881 D148 882 D148 883 D144 883 D144 883 D147 884 D230 884 D247 885 D147 885 D147 886 D147 887 887 887 888 D147	8800 [M-2] 8801 [M-2] 8802 [M-2] 8803 [M-2] 8804 [7-34] 8806 [7-32] 8806 [7-32] 8807 [7-32] 8807 [7-32] 8807 [7-32] 8807 [7-32] 8807 [7-32] 8807 [7-32] 8807 [7-32] 8807 [7-32] 8807 [7-32] 8807 [7-32] 8807 [7-32]	### (\$20.20.20.20.20.20.20.20.20.20.20.20.20.2	
possis (Fix-4) (Fix	CGM R-44 CGM R-44 CGM R-52 CGM R-67 CGM R-29 CGM R-32 CGM R-	COMD [8-44] COMD [8-45] COMD [8-25] COMD [8-25] COMD [8-27] COMD [Com (12.3A) Com (12.3A) Com (10.3A) Com (12.3C) Com (12.4A) Com (12.4A) Com (12.4A) Com (13.3A) Com (1	Design Design	000 [77:45] 0000 [77:47] 0000 [Riso (21-34)	8600 [M-20] 8600 [77-26] 8600 [77-26] 8600 [77-26] 8600 [77-26] 8600 [77-26] 8600 [77-26] 8600 [77-26] 8600 [77-26] 8600 [77-26] 8600 [77-26] 8600 [77-26] 8600 [77-26] 8600 [77-26] 8600 [77-26] 8600 [77-26] 8600 [77-26]	6500 [18.37] 6504 [19.37] 6506 [9.320 3.05] 6500 [9.320 9.320] 71 [9.327]	
pr.34 Pr.54 Pr.50	CS1 B.14 CS2 B.35 CS2 B.34 CS3 B.15 CS3 B.25 CS3 B.25 CS3 B.25 CS3 B.25 CS3 B.35 CS3 B.35 CS3 B.34 CS3 B.35 CS3	CRY [5-15] CRY [5-27]	CP1 15:88 CP1 (122A) CP2 (122A) CP3 (122A) C	G1 [M-42] J1 [M-65] J2 [12-94] J3 [11-65] J4 [10-67] J5 [17-94] J6 [17-94] J7 [77-98] L1 [32-3] L2 [32-3] L3 [10-55] L4 [10-55] L5 [10-55] L5 [10-55] L5 [10-55] L5 [17-96] L7 [17-98]	811 (9.84) 182 (9.84) 183 (9.84) 184 (9.87) 186 (9.22) 186 (9.22) 186 (9.23) 187 (9.22) 187 (9.22) 187 (9.22) 187 (9.22) 187 (9.22) 187 (9.23) 187 (9.23) 187 (9.23) 187 (9.24) 187 (9.24) 187 (9.25) 187 (9.26) 187 (9.26) 187 (9.26) 187 (9.26) 187 (9.26) 187 (9.26) 187 (9.26) 187 (9.26)	RISS [PLAS] RISS [8507 (2.12) 8500 (2.12) 8600 (2.12) 8600 (2.12) 8600 (2.12) 8600 (2.14) 8610 (2.13) 8611 (2.13) 8614 (2.13) 8614 (2.13) 8615 (2.14) 8616 (0.04) 8617 (0.04) 8618 (0.04) 8619 (0.04) 8619 (0.04)		
	CSS [8-42] CSS [8-22] CSF [8-28] CSS [8-32]	GRI [3-27]	C72 1855 C73 1950 C73 1843 C73 1843 C73 1843 C73 1843 C73 1843 C74 1843 C75 1845 C76 1855 C77 1855 C78 1855 C79 1855 C79 1855 C70	LB [71-6] LB [71-6] LB [70-6]	955 (77-97) 764 (918) 767 (918) 767 (918) 768 (77-55) 768 (78-65) 768 (78-65) 768 (92-65) 768 (77-55) 768 (77-55) 768 (77-55) 768 (77-55) 768 (77-55) 768 (77-55) 768 (77-55) 768 (77-55) 768 (77-55) 768 (77-55) 768 (77-55) 768 (77-55) 769 (77-55)	601 D220] 602 D250] 603 D250] 604 D250] 605 D250] 607 C250] 607 C2	MADO DANS MADO DANS MADO DANS MADO DANS MADO DOLD MADO DOLD		
(B-20) (B-24) (B-44) (B-49) (B-20) (B	Con [5:30]	Come [5247] Come [5147]	CM4 [23.97] CM4 [23.97] CM4 [23.97] CM5 [23.97] CM6 [23.97] CM7 [23.97] CM7 [23.97] CM7 [23.97] CM8 [23.97] CM9 [23.97]	LISSON TRANS LISSON TRANS LISSON PAPE LISS	MOD 17-ME MAI 17-ME	1810 [4:28] 1817 [4:38] 1818 [7:28] 1819 [7:28] 1820 [7:48] 1822 [7:48] 1822 [7:48] 1822 [2:49] 1822 [2:49] 1822 [2:49] 1822 [2:49] 1822 [2:49] 1822 [2:49] 1822 [2:49] 1822 [2:49] 1822 [2:49] 1822 [2:49] 1822 [2:49]	POP 39-20-20-20-20 EASO EASO EASO EASO EASO EASO EASO EASO		
OCCOPY SPECIE	CHES PEANWING DIAGNAY	ONLY DOCUMENTO OR INFORMATION	(TOGETHER AND SEPARATELY, MATERIALS) ARE BEING PROVIDE	PAGE DETAIL <edit ho<="" td=""><td>SE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO. STUFF /</td><td>FASSEMBLY NOTES AND BOM NOT FRAL</td><td></td><td>2701 SAN TOMAS EXPRI SANTA CLARA, CA 9505</td><td></td></edit>	SE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO. STUFF /	FASSEMBLY NOTES AND BOM NOT FRAL		2701 SAN TOMAS EXPRI SANTA CLARA, CA 9505	
	ONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA I	A MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTOR	TORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OT					ID NAME <engi< td=""><td>PAGE WGINEER: DATE 05-FEB-2009</td></engi<>	PAGE WGINEER: DATE 05-FEB-2009

XTAL, MECHANICALS, THERMALS

