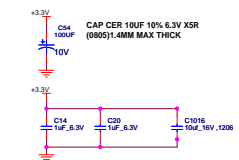
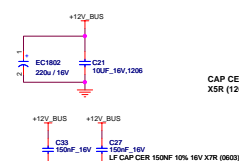
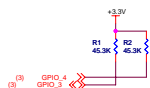
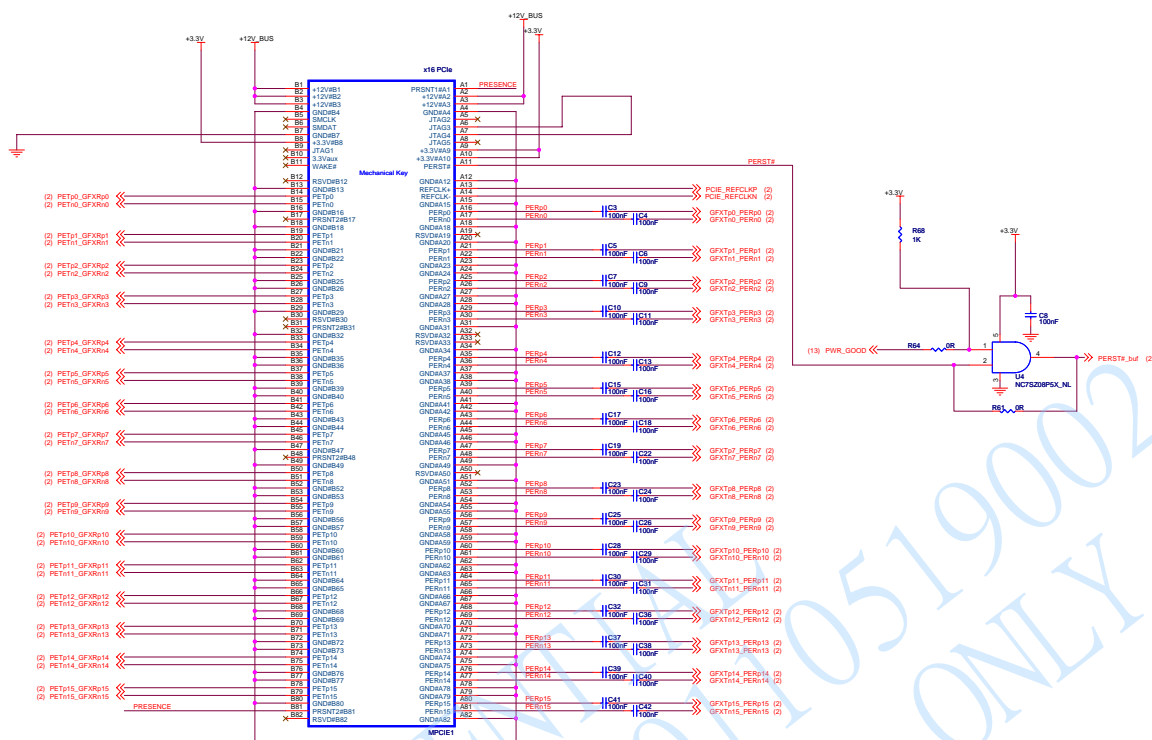


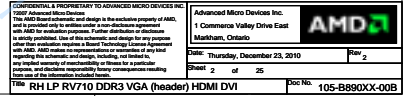
PCI-EXPRESS EDGE CONNECTOR



Place these caps last,
ideally as close to the bus
connector as possible

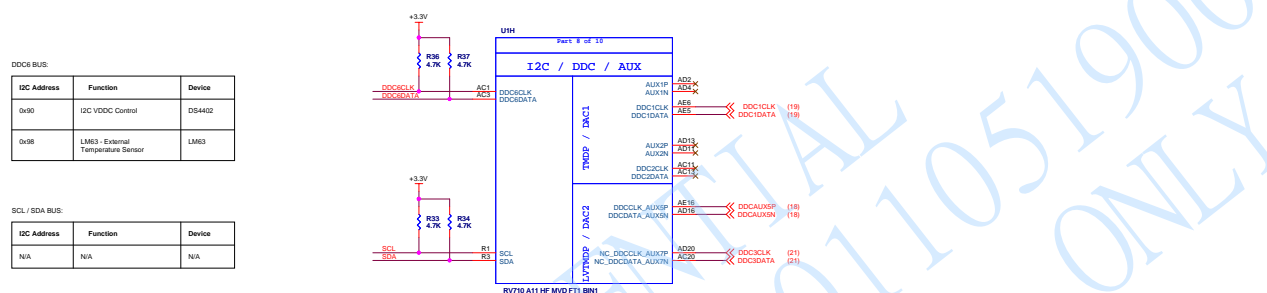


NOTE: some of the PCIE testpoints will be available through via on traces.

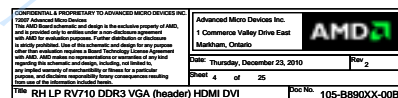


DDC Address	Function	Device
0x30	DDC VDDC Control	DS4402
0x3B	LM33 External Temperature Sensor	LM33

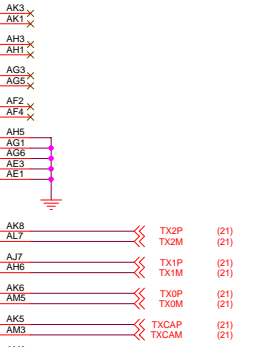
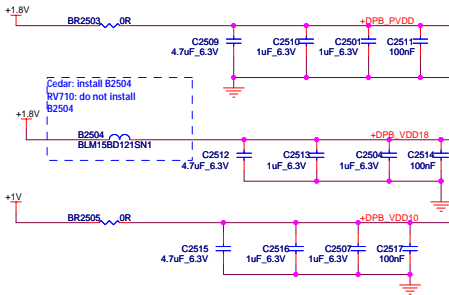
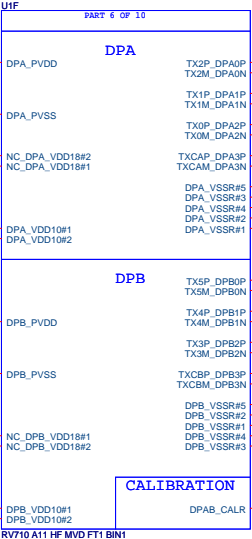
DDC Address	Function	Device
N/A	N/A	N/A



I2C Address	Function	Device
N/A	N/A	N/A



TMDP INTERFACE



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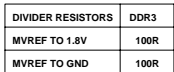
Date: Thursday, December 23, 2010 Rev 2

Sheet 5 of 25 Doc No: 105-B890XX-00B

Title RH LP RV710 DDR3 VGA (header) HDMI DVI



Doc No.	105-B890XX-00B
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Modify5

R300 1K

R40 1K

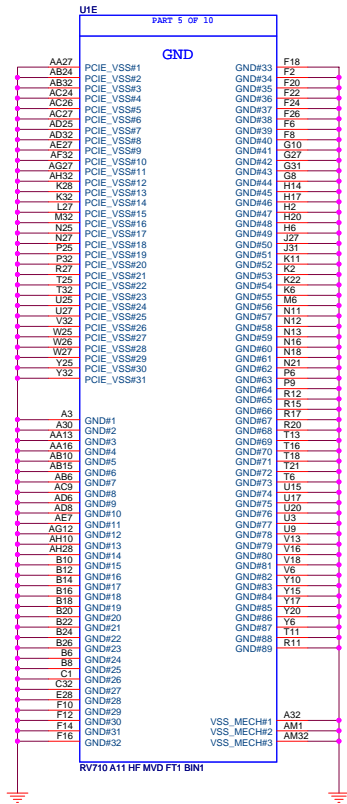
Test_EN

Overlap

RV730
NC_MEM_CALRNT
CEDAR_Test_EN

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<p>Date: Thursday, December 23, 2010</p> <p>Sheet 8 of 25</p>		<p>Rev 2</p>	
<p>TYPE RH LP RV710 DDR3 VGA (header)</p>		<p>Doc No. 105-B890XX-00B</p>	
<p>FORM HDMI DVI</p>			

(09) RV710 GND



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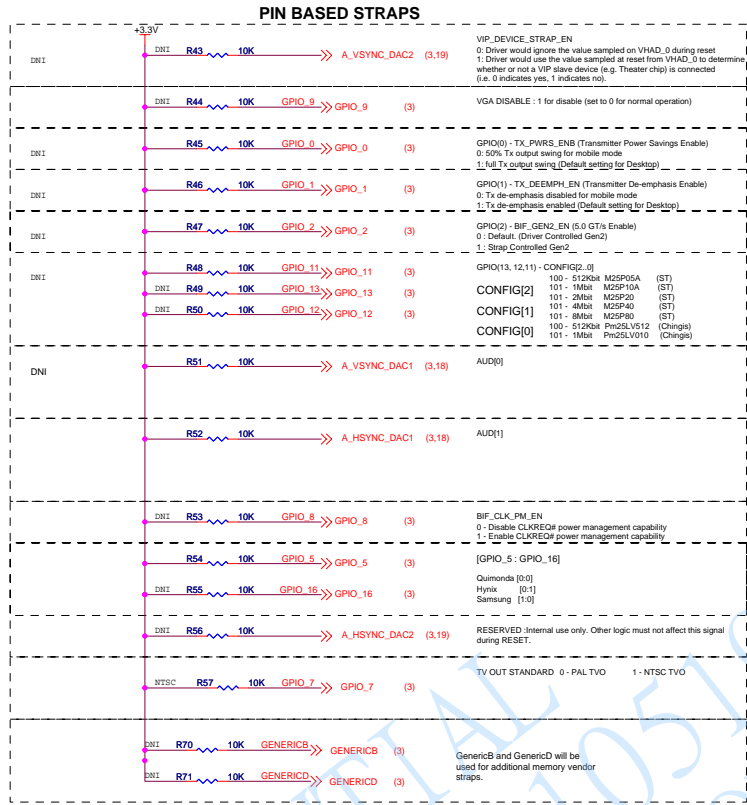
Rev 2

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Doc No. 105-B890XX-00B

Title RH LP RV710 DDR3 VGA (header) HDMI DVI

(10) RV710 STRAPS




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Rev 2

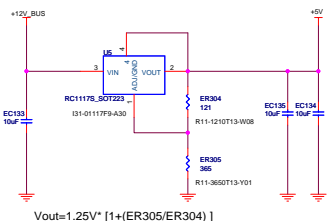
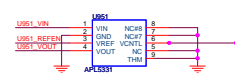
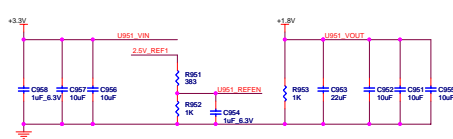
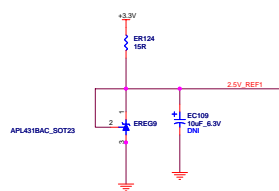
Doc No. 105-B890XX-00B



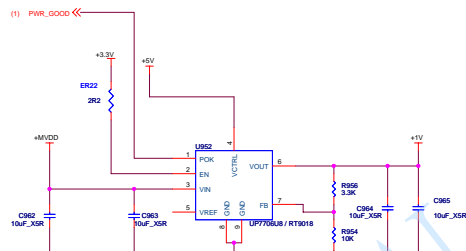
Title RH LP RV710 DDR3 VGA (header) HDMI DVI

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Sheet <u>12</u> of <u>25</u>		Doc No: <u>105-B890XX-00B</u>	
Title: <u>RHLP RV770 DDR3 (header) HDMI DVI</u>			

(13) Linear Regulators

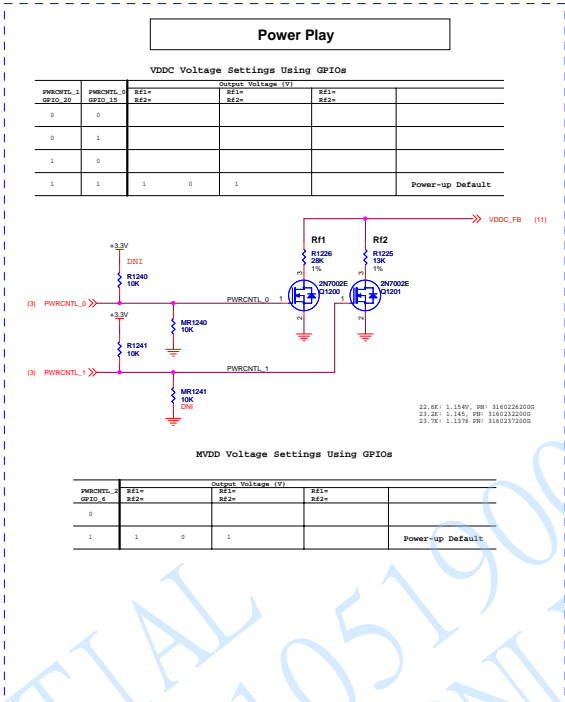
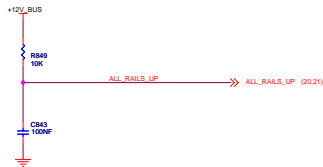


$$V_{out} = 1.25V * [1 + (R_{305} / R_{304})]$$



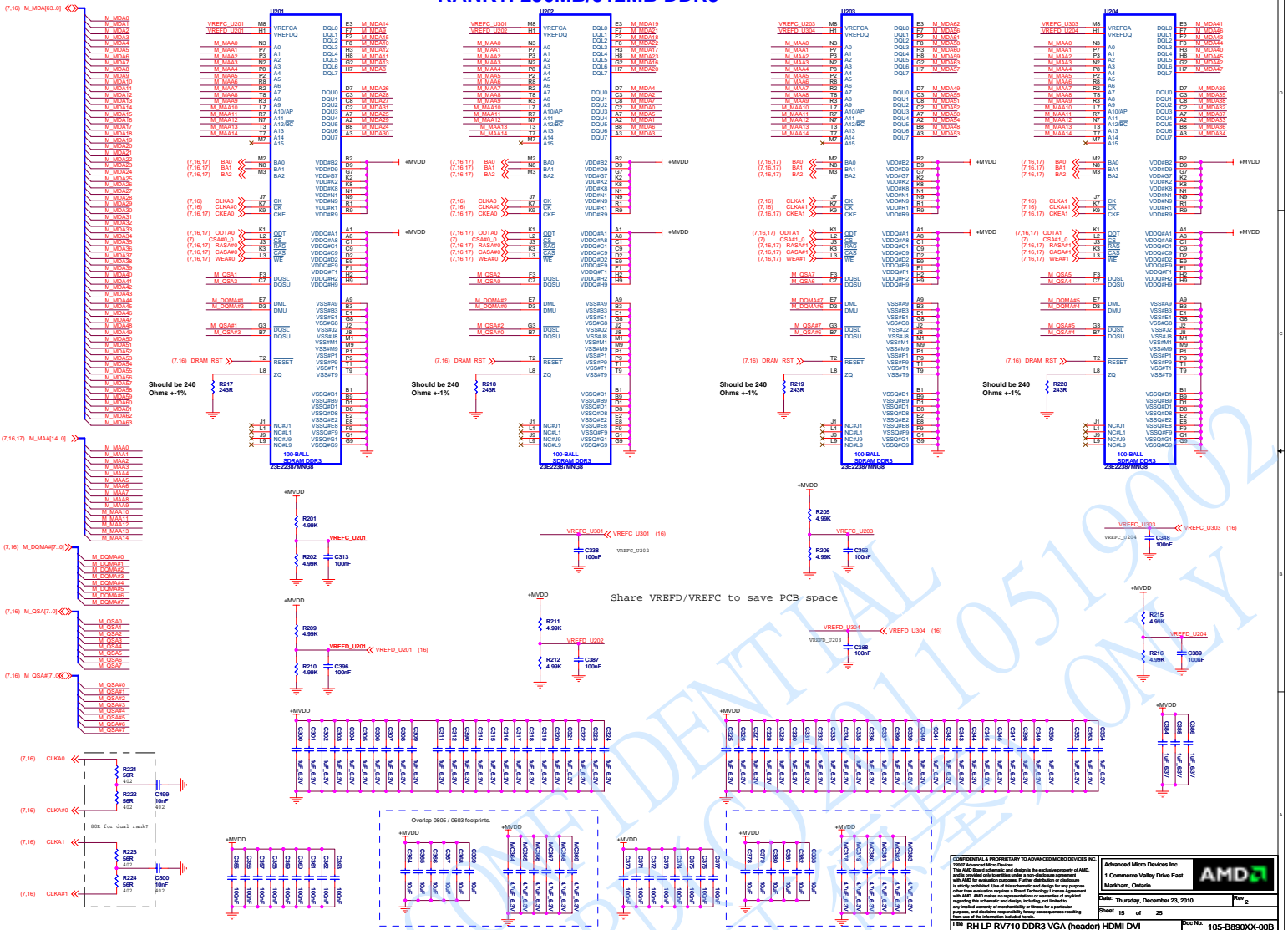
$$V_{out} = 0.8V * (1 + R_{956} / R_{954})$$

(14) Power Management

[illegible]

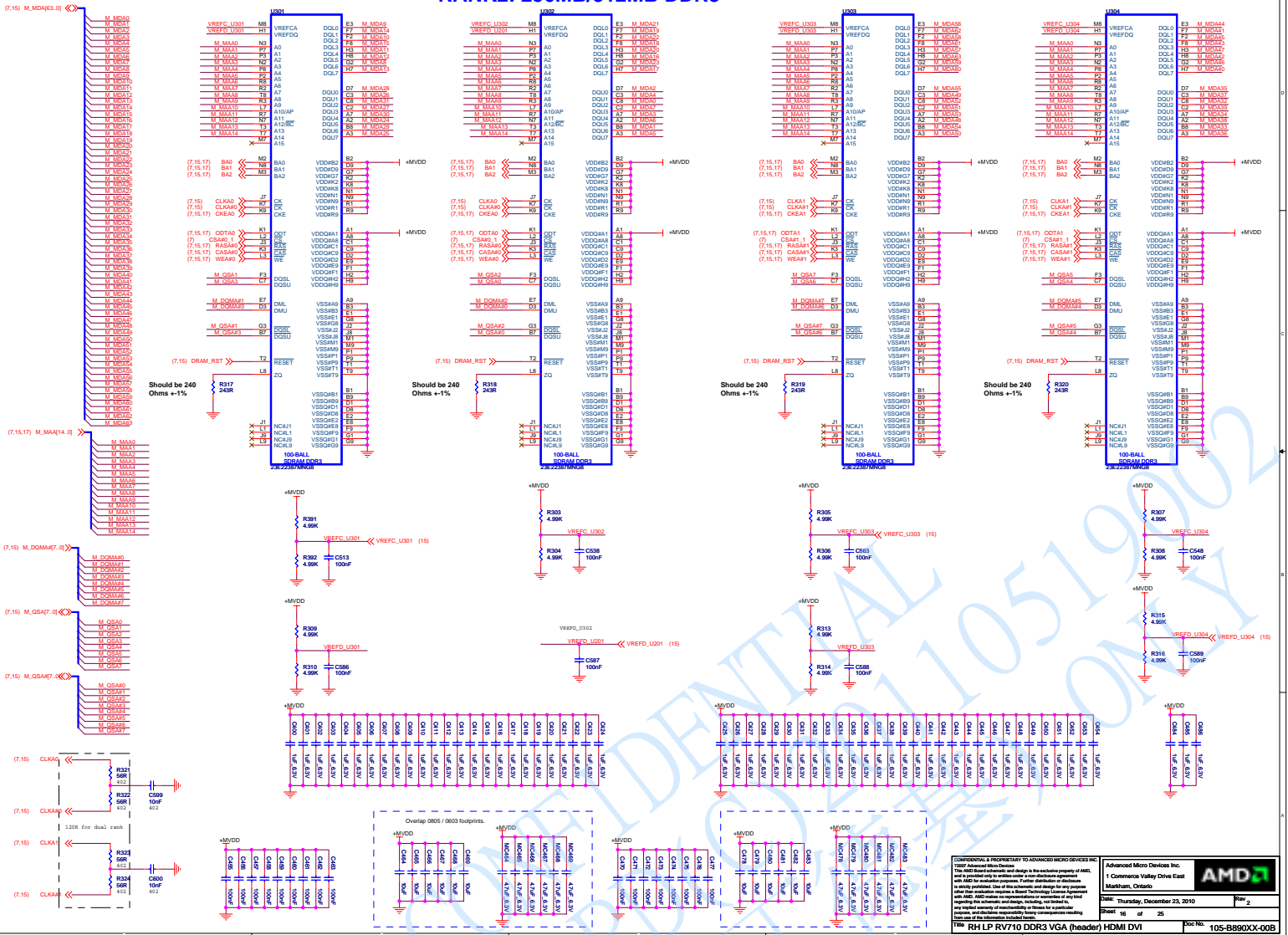
(15) DDR3 RANK1

RANK1: 256MB/512MB DDR3



(16) DDR3 RANK2

RANK2: 256MB/512MB DDR3



00029980 顏鑫修課 (丁德基)

MSI

FOR MB 維修課

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7.15.16) M_MAA14.02

M_MAA0

M_MAA1

M_MAA2

M_MAA3

M_MAA4

M_MAA5

M_MAA6

M_MAA7

M_MAA8

M_MAA9

M_MAA10

M_MAA11

M_MAA12

M_MAA13

M_MAA14

7.15.16) BAO

7.15.16) BAI

7.15.16) BA2

M_MAA12

M_MAA13

M_MAA14

BAD

BA1

BA2

R271_56R

R272_56R

R273_56R

R274_56R

R275_56R

R276_56R

R277_56R

R278_56R

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R281_56R

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R427_56R

R428_56R

R429_56R

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R433_56R

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R442_56R

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R444_56R

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R450_56R

R451_56R

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R454_56R

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R462_56R

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R466_56R

R467_56R

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R469_56R

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R472_56R

R473_56R

R474_56R

R475_56R

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R478_56R

R479_56R

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R523_56R

R524_56R

R525_56R

R526_56R

R527_56R

R528_56R

R529_56R

R530_56R

R531_56R

R532_56R

R533_56R

R534_56R

R535_56R

R536_56R

R537_56R

R538_56R

R539_56R

R540_56R

R541_56R

R542_56R

R543_56R

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R545_56R

R546_56R

R547_56R

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R550_56R

R551_56R

R552_56R

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R555_56R

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R562_56R

R563_56R

R564_56R

R565_56R

R566_56R

R567_56R

R568_56R

R569_56R

R570_56R

R571_56R

R572_56R

R573_56R

R574_56R

R575_56R

R576_56R

R577_56R

R578_56R

R579_56R

R580_56R

R581_56R

R582_56R

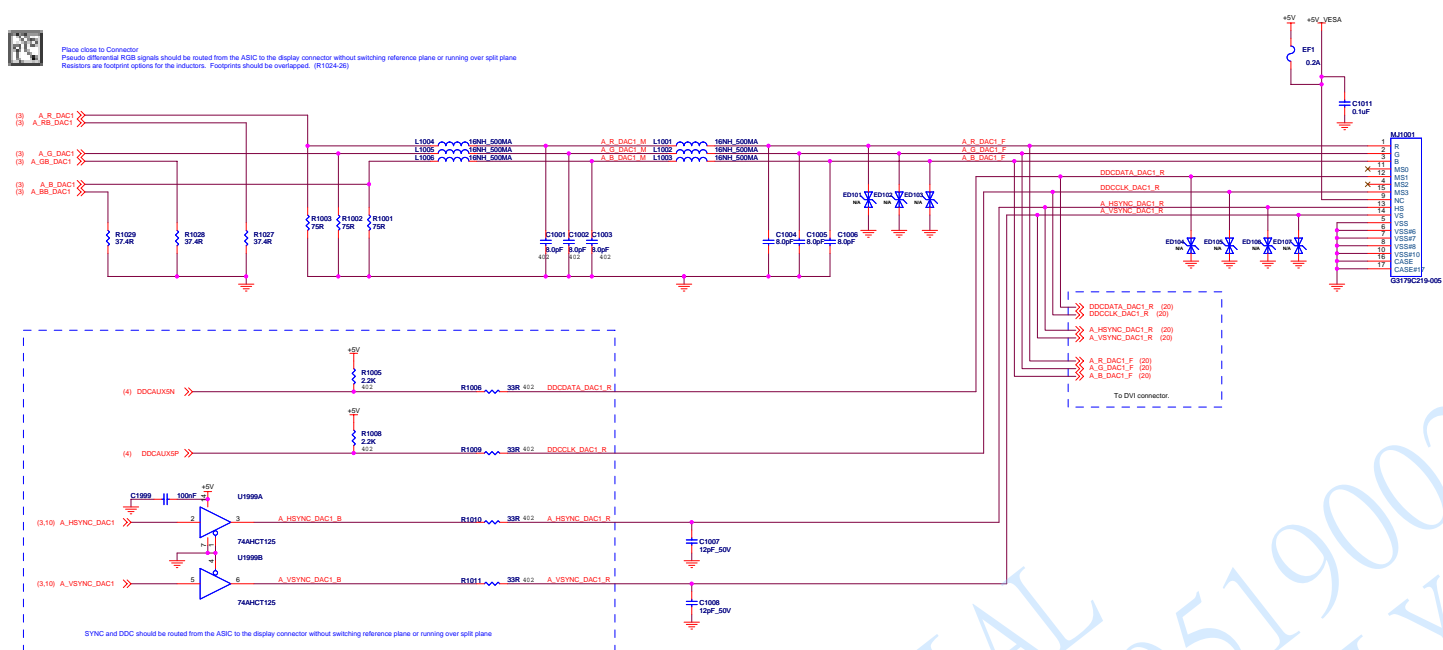
R583_56R

R584_56R

R5



DAC 1 OUTPUT



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
Title RH1P RV710 HD3 VGA (hard

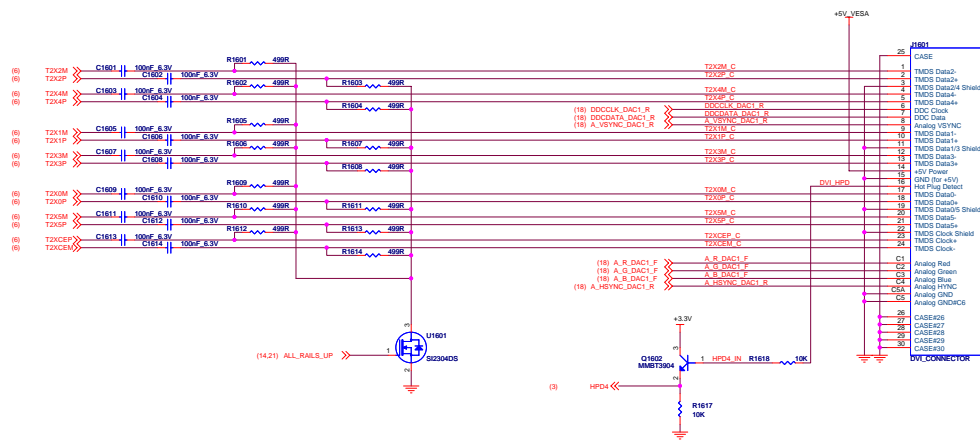
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Sheet	18 of 25		



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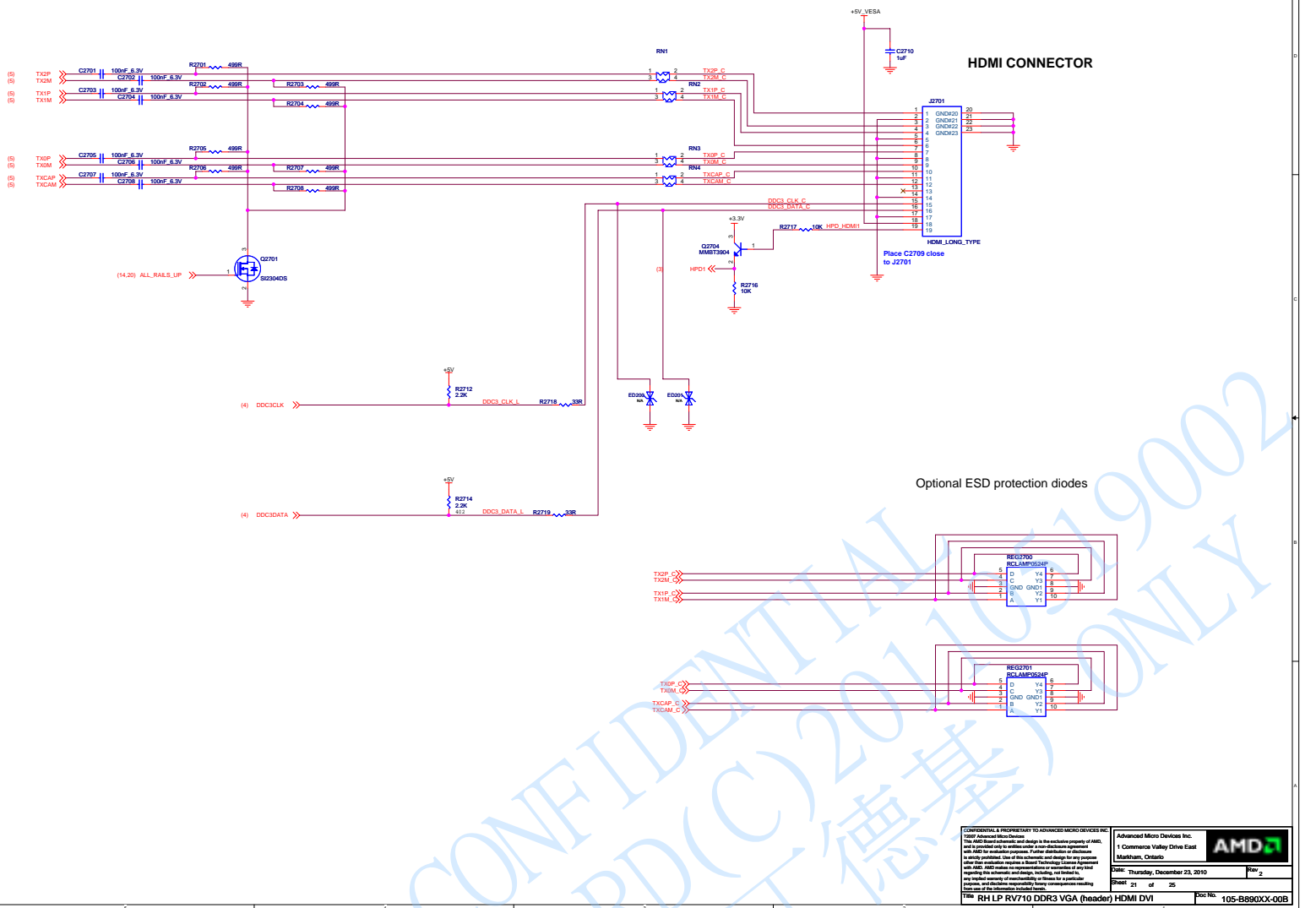
Title RH LP RV710 DDR3 VGA (header)

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Date: Thursday, December 23, 2010		Rev 2	
Sheet 19 of 25			
(a) HDMI DVI		Doc No. 105-B890XX-00B	

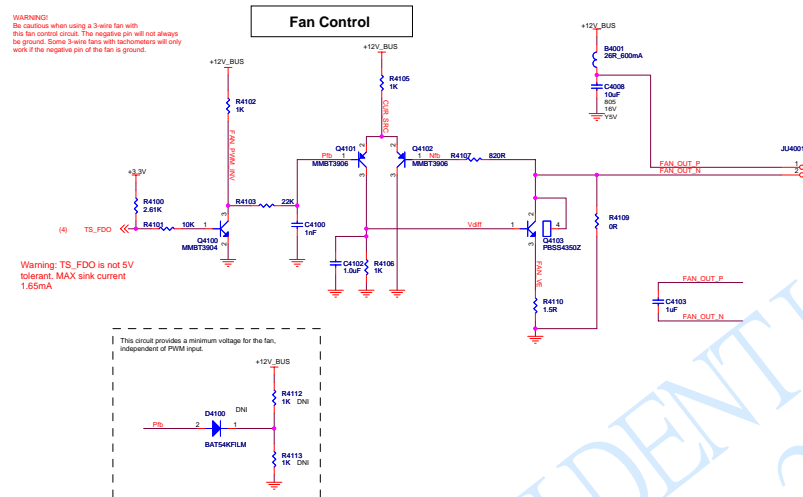


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Sheet: <u>20</u> of <u>25</u>			
Title: <u>RH LP RV710 DDR3 VGA (header)</u>		Doc No: <u>105-B890XX-00B</u>	

TMDP-B OUTPUT




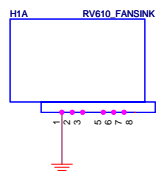
(22) Thermal Management



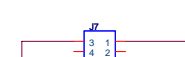
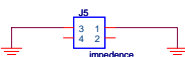
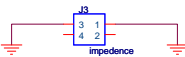
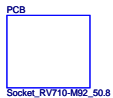
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(r) HDMI DVI		Doc No. 105-B890XX-00P	



71200362000



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Date: Thursday, December 23, 2010 Rev 2

Sheet 23 of 25 Doc No: 105-B890XX-00B

Title RH LP RV710 DDR3 VGA (header) HDMI DVI

<div>AMD</div>			Title RH LP RV710 DDR3 VGA (header) HDMI DVI		Schematic No. 105-B890XX-00B		Date: Thursday, December 23, 2010	
REVISION HISTORY			NOTE: This schematic represents the PCB, it does not represent any specific SKU. For Stuffing options (component values, DNI , ? please consult the product specific BOM. Please contact AMD representative to obtain latest BOM closest to the application desired.					Rev 2
Sch Rev	PCB Rev	Date	RV710 ENGINEERING BOARD REVISION DESCRIPTION					
0	00A	2008.12.30	INITIAL RELEASE OF CUSTOM-WIDE BOARD. BASED ON B625 REV06.					
1	00B	2009.01.22	Sch no change, just modify HDMI connecro location on PCB					
<div>CONFIDENTIAL RD(C)20110519002 ONLY</div>								