

DC-DC CONTVR



Micro-Star International Co., LTD.	
P41 , MS-8832 , 16M	
HISTORY	

NV11, 1/2/4MX32 DDR, RGB, INTERNAL DVI-I, TV, AGP.

PCI DEVICE ID 0X0=0X112 FOR NV11.

STRAPS: AGP4X, SIDEBAND DIS, FAST WRITE ENA, ADP BIOS, NORMAL PCIAD, 14.318MHZ
STRAP_RAM_TYPE [5:2]=1011-1MX32 DDR SDRAM

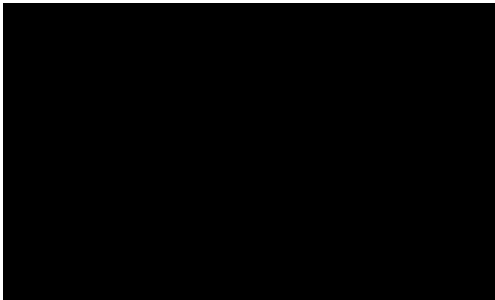
01. List of Schematic sheets
02. HOST INTERFACE_NEW
03. FB(MEMORY)/NVXX DECOUPLING
04. NVXX BOOT STRAPS
05. DAC and MEDIA PORT
06. LOWER SDRAM FBI
07. BT & CH TV ENCODER
08. DFP/DVI/CLK_DVR
09. DC-DC CONVTR

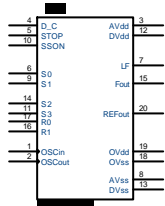
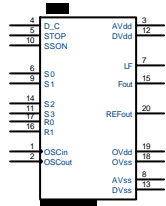
- 5) E00:
 - Implemented ECO change list "P41_D00_ECOs-X02.doc" in current working directory:
\\nvcorp\applied\designs\working\nv11\XXX-P0041-0000-E00
 - Sync'd up netlist with Layout.
 - Connect the S0 input of the IMISM530 to 3.3VRUN instead of CLK_VDD
- 6) E02:
 - Added 1000pf cap (C535) in parallel to R535 for EMI supression.
 - Changed mem termination from +3.3VRUN to +3.3VSUS. Affects R183-R186, C416
- 6a) E02-X01: Change C279 to no stuff per Dell request.
- 7) E03: Added SMD0603 foot print to the backside of board, C536 to DVI, DFP page.

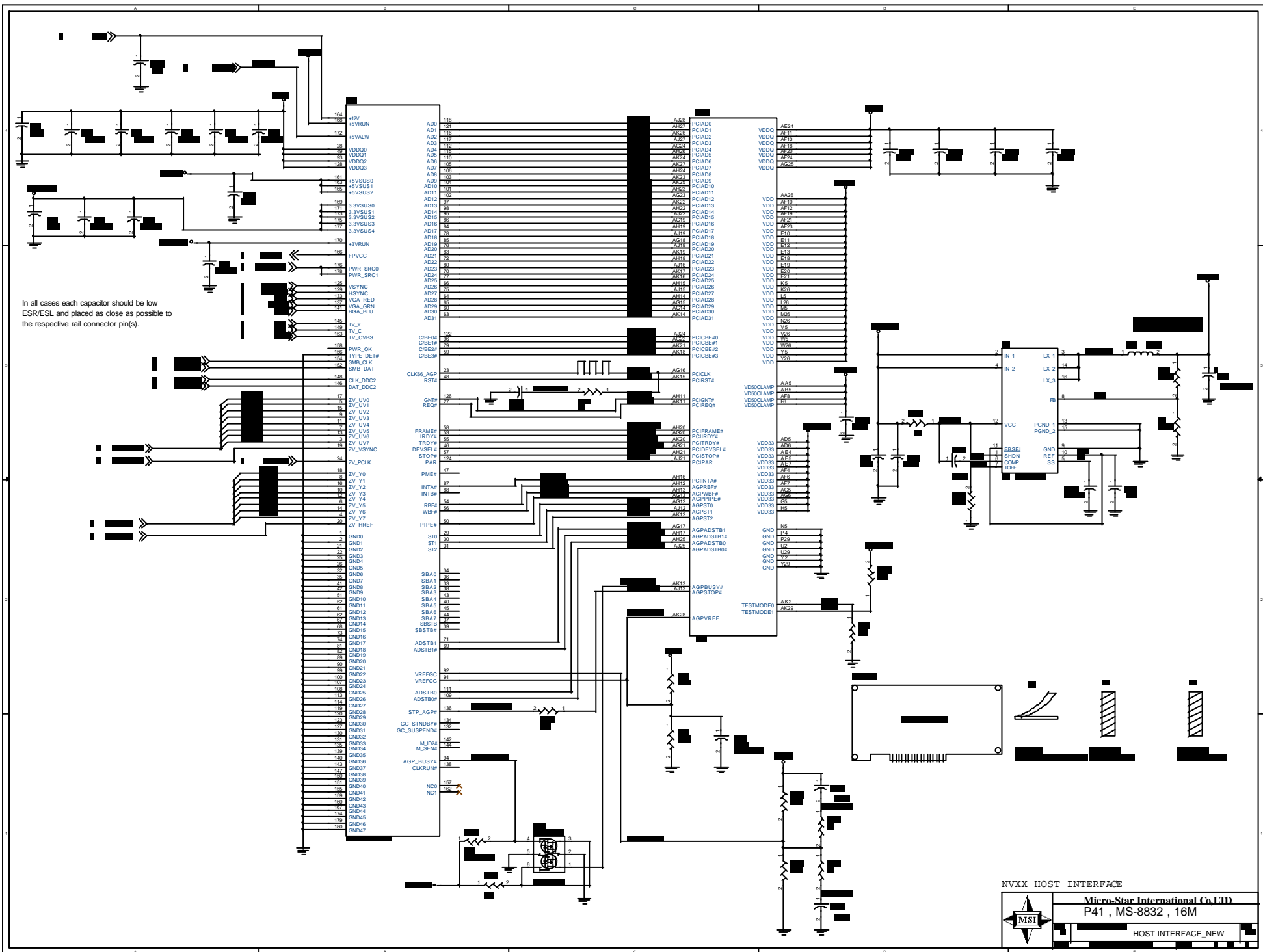
HISTORY REV. :

- 1) Based on P39-A00
- 2) Schematic changes; role rev to B00
 - remove BT device
 - strap resistor changes
 - add/remove resistors and caps
 - remove high dropout reg (u514)
 - R502 changed from 20K to 10K
- 3) C00:
 - Swapped LVDS signals back to "Rev A connections".
 - Hi Freq. cap arrays Decoupled to GND:
 - 1) SMB_CLK
 - 2) SMB_DAT
 - 3) PID2_I2C_CLK (aka I2CSCL)
 - 4) PID3_I2C_DAT (aka I2CSDA)
 - 5) PID3_I2CDAT (aka PID3)
 - 6) PID2_I2CCLK (aka PID2)
 - 7) PID1
 - 8) PID0
 - Added 10K, NO STUFF, from AGP_BUSY# to +3.3VSUS.
 - Connected C279 to +5VSUS.

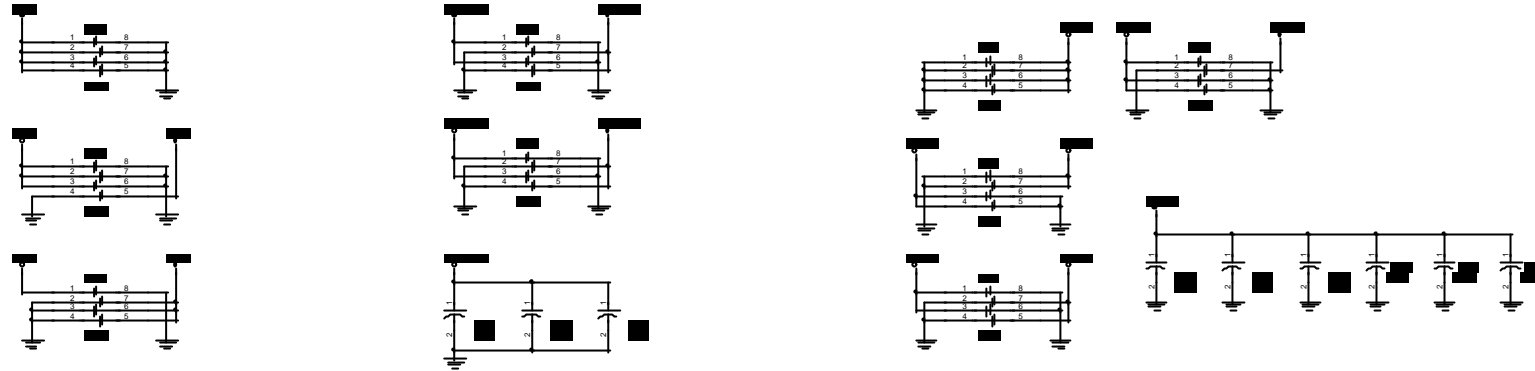
4) D00:



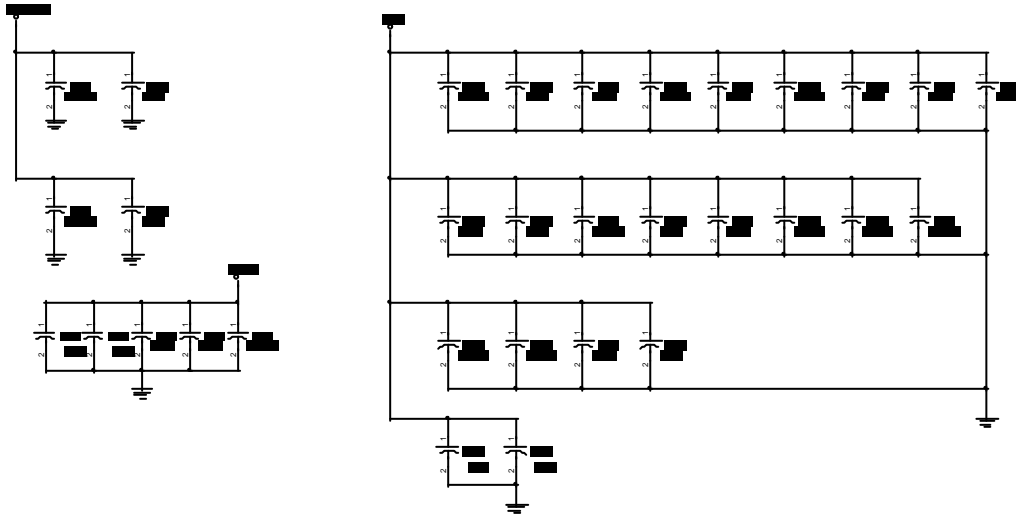





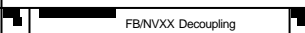
NV11 DECOUPLING

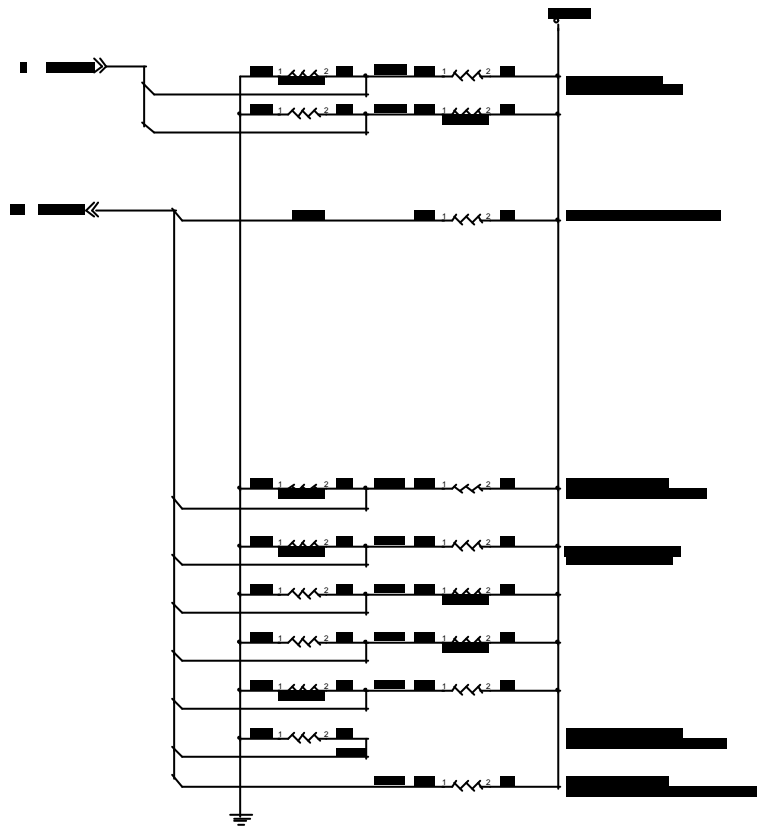


FB (MEMORY) DECOUPLING



FB/NVXX DECOUPLING

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FB/NVXX Decoupling	

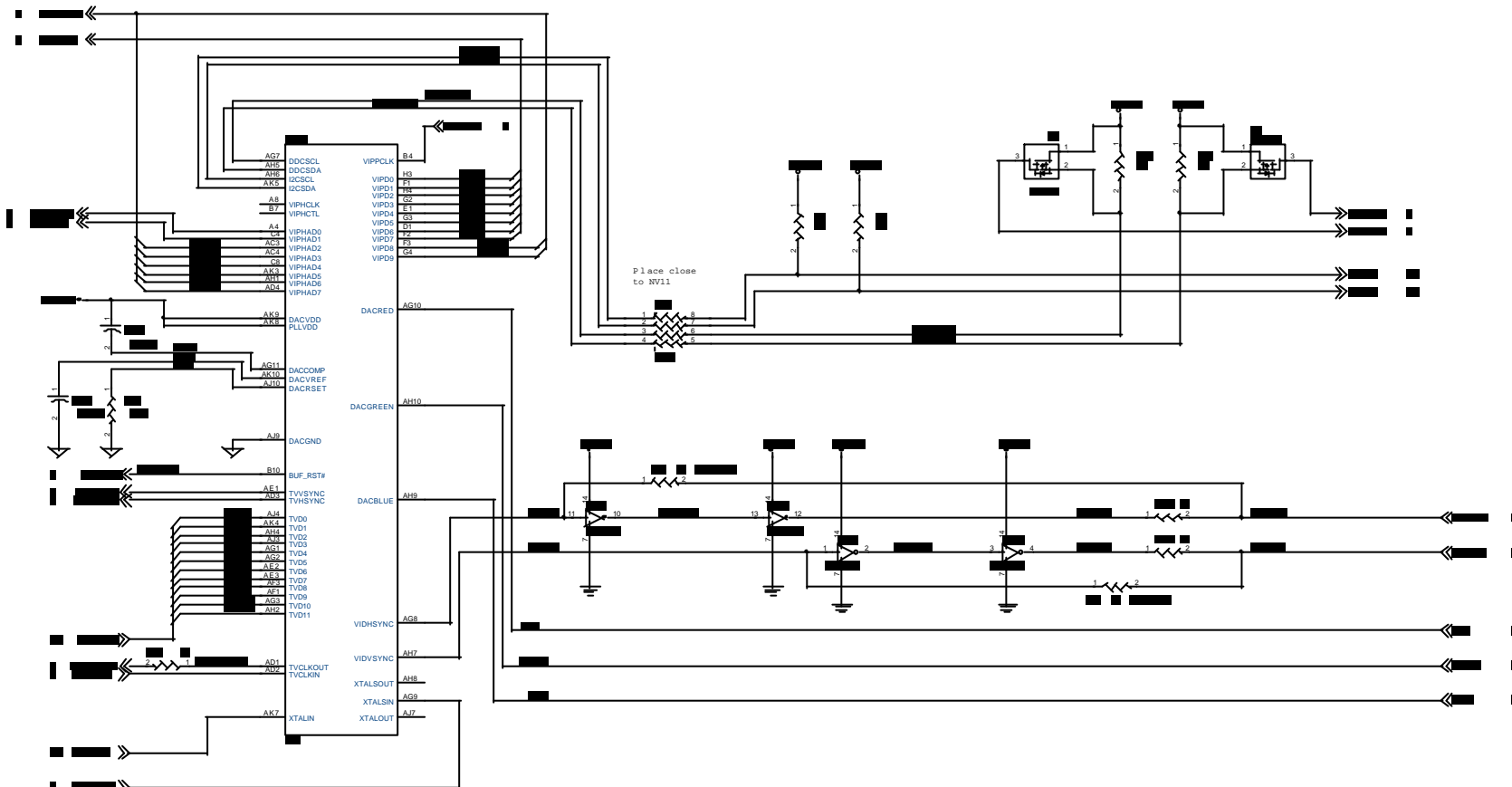


NVXX HOST STRAPS

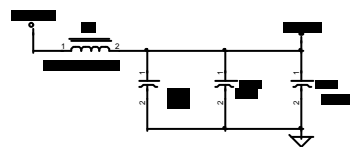


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NVXX BOOT STRAPS



Single point connection to DAC
output reference GND

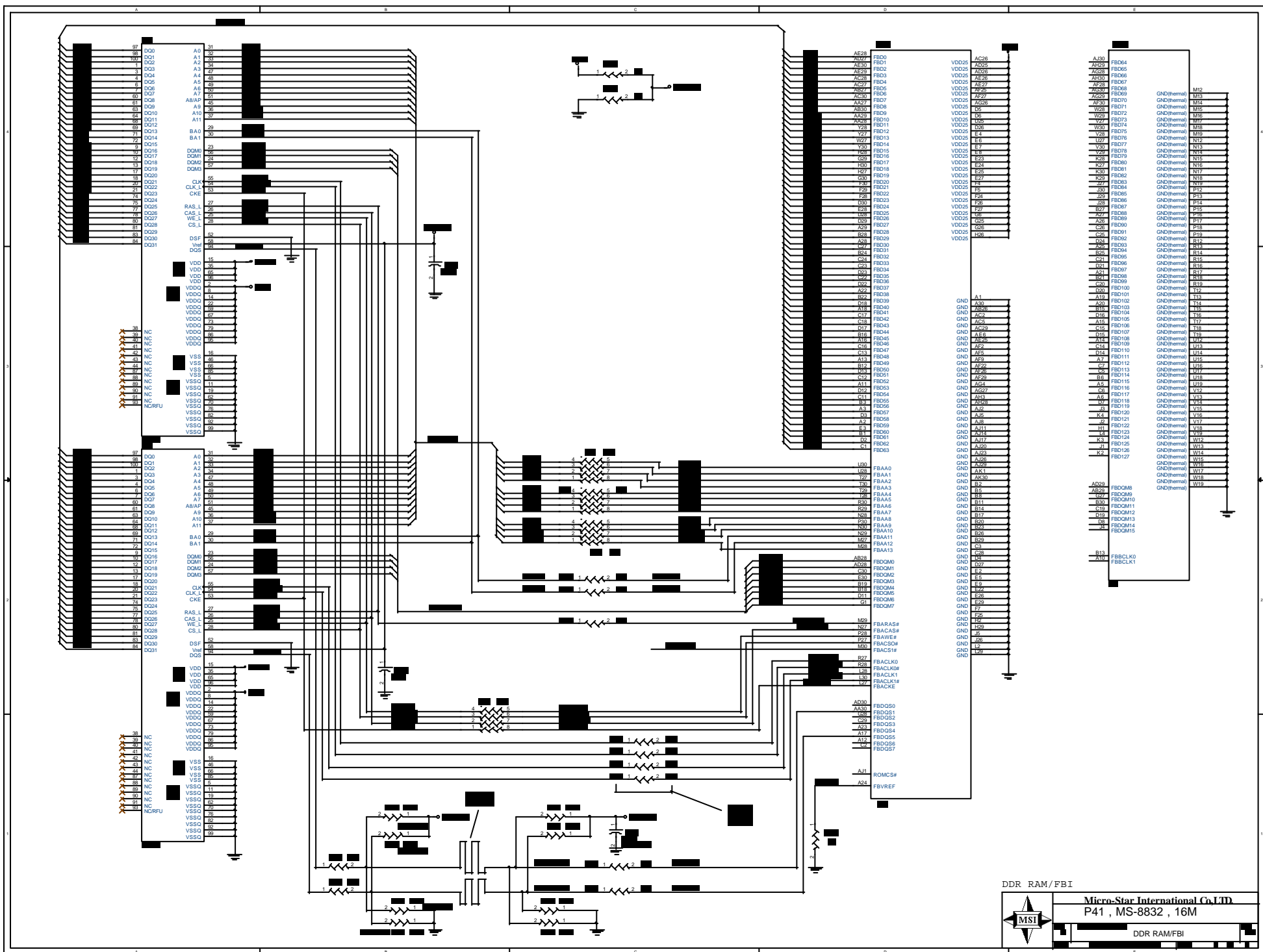


DAC and MEDIA PORT

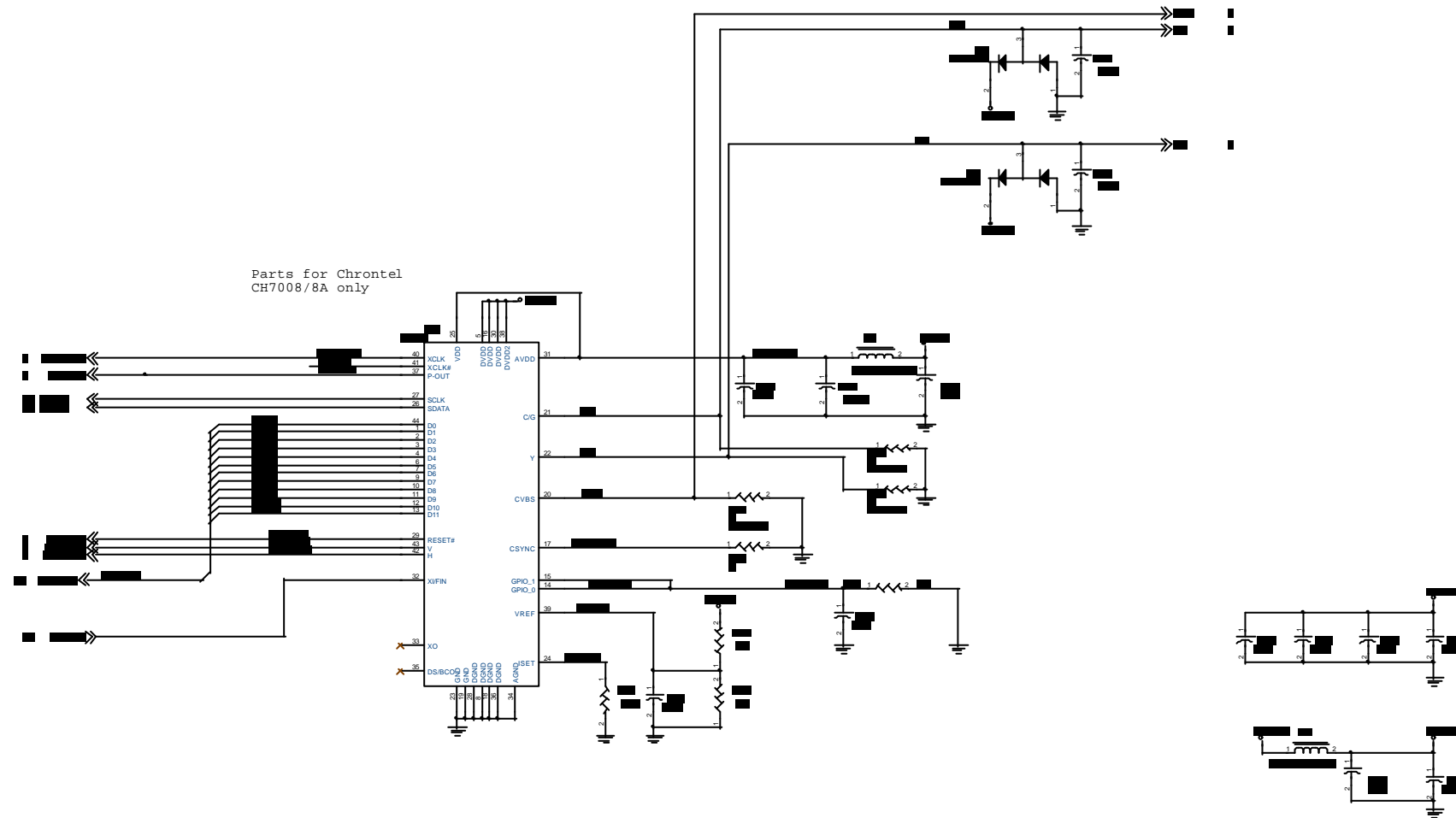


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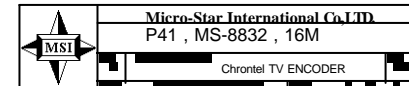
DAC and MEDIA PORT

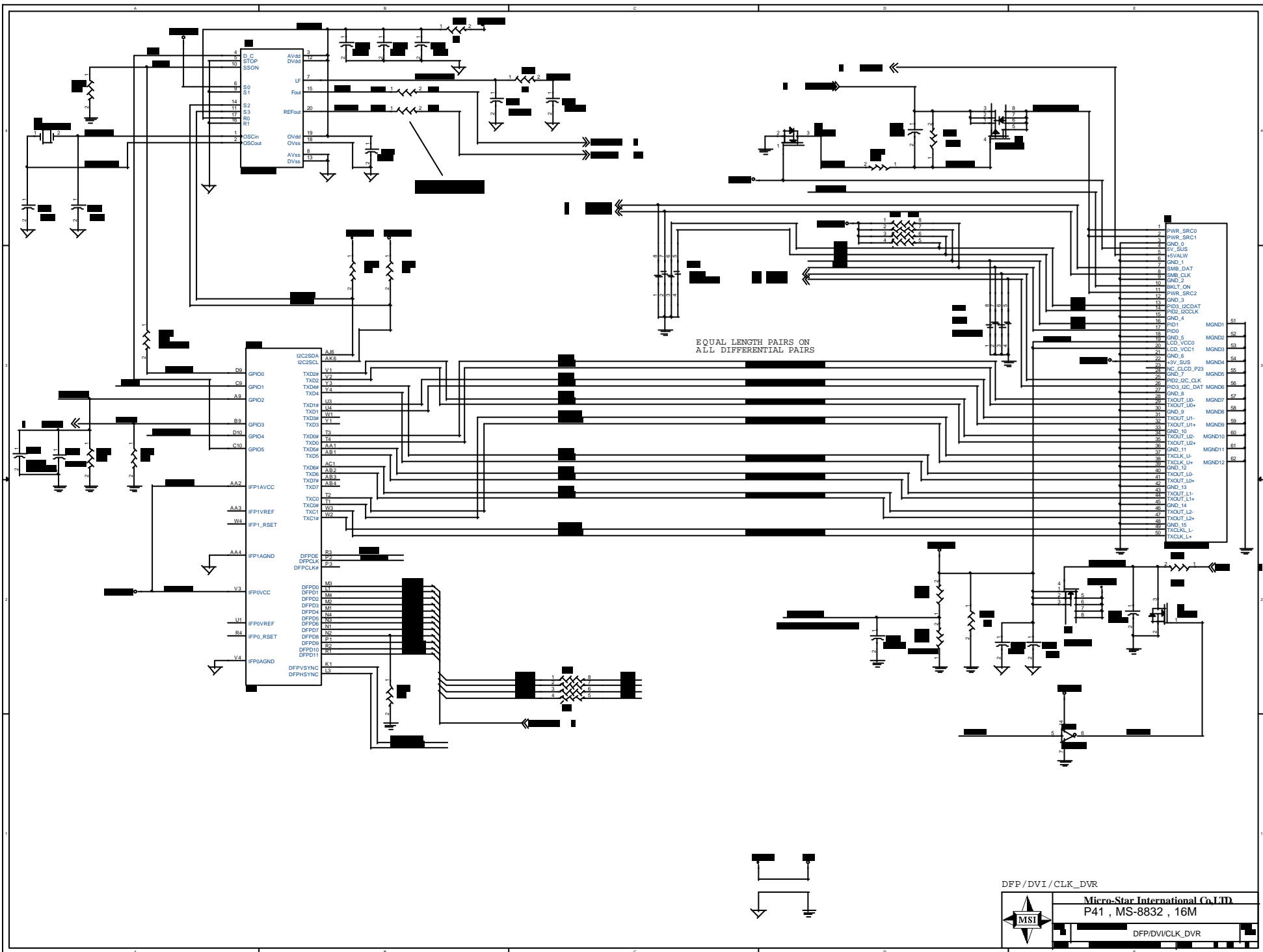


Parts for Chrontel
CH7008/8A only



Chrontel TV ENCODER





DFP/DVI/CLK_DVR



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DFP/DVI/CLK_DVR