GP107 128b GDDR5, 75W, PCB 5.7" DVI-D_DL (DP + DP) + HDMI (DP) + DP TABLE OF CONTENTS Description Page Description Table of Contents PS V: NVVDD Phase 3 Block Diagram MECH: Bracket/Thermal PCI Express MEMORY: GPU Partition A/B MEMORY: FBA[31:0] MEMORY: FBA[63:32] MEMORY: FBB[31:0] MEMORY: FBB[63:32] GPU FBVDDQ & 1V8 Decoupling GPU NVVDD & NVVDDS DECOUPLING IFPAB DVI-DL 11 12 IFPD DP 13 IFPC HDMI 14 IFPEF UNUSED MISC1: Fan, Thermal, JTAG, GPIO MISC2: ROM, XTAL, Straps Power Inputs and Filtering 18 Power Sequence PS: NV3V3 & NV12V 20 PSI: 1V8 21 PSII: 5V & 12V_FAN PS III: PEXVDD 22 PS IV: FBVDDQ PS V: NVVDD OVR2+1 25 PS V: NVVDD Phase 1,2 NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA NV_PN 600-1G210-BASE-SCH ALL NUDA DESIGN SECRECATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FLES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SERVARIALY, NATIONALS) AND EBBO PROVIDED 'AS 8'. THE INITIANALS OF AN EXPORT OF THE PROVIDED AS 18'. THE INITIANAL SO AND PROVIDED AS 18'. THE INITIANAL SO OTHER PROVIDED AS 18 PCB REV PG210-B00

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