PG210 B00 GP107 128b GDDR5, 75W, PCB 5.7" DVI-D_DL (DP + DP) + HDMI (DP) + DP TABLE OF CONTENTS V335-10 TABLE OF CONTENTS V335-11 TABLE OF CONTENTS V335-12 TABLE OF CONTENTS Page Page Page Description Description Description Page Description Change PCIE footprint for full pin Table of Contents Change J3 library and footprint No change Block Diagram Change J1 library and footprint PCI Express 13 Change J2 library and footprint MEMORY: GPU Partition A/B Remove 2pin Fan control MEMORY: FBA[31:0] 17 Change J6/L3/L11 library and footprint 20 Add co-lay LDO MEMORY: FBA[63:32] MEMORY: FBB[31:0] 23 Change to UP1537 circuit MEMORY: FBB[63:32] Add co-lay SY8288A 25 change Q9/Q11/L9/L10 library and footprint GPU FBVDDQ & 1V8 Decoupling GPU NVVDD & NVVDDS DECOUPLING change Q7/L8 library and footprint 10 26 IFPAB DVI-DL Add Logo LED circuit 12 IFPD DP 13 IFPC HDMI 14 IFPEF UNUSED MISC1: Fan, Thermal, JTAG, GPIO 15 16 MISC2: ROM, XTAL, Straps 17 Power Inputs and Filtering Power Sequence 19 PS: NV3V3 & NV12V PSI: 1V8 21 PSII: 5V & 12V_FAN 22 PS III: PEXVDD PS IV: FBVDDQ 23 PS V: NVVDD OVR2+1 24 25 PS V: NVVDD Phase 1,2 PS V: NVVDD Phase 3 26 27 MECH: Bracket/Thermal MICRO-STAR INT'L CO.,LTD MS-V335

Date: Saturday, September 10, 2016





















































