

P361 A00 Base Design

P361-A00, G92, 8Mx32/16Mx32 GDDR3
HDMI, D-SUB, DVI

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REV HISTORY (BASE ON V154 40)

- 2009/10/30
1. Remove TV-out circuit P13
 2. Add HDMI circuit P13
 3. Add D-SUBcircuit P11
 4. Remove DVI circuit(J3) P12

REV	VARIANT	N/P/N	ASSEMBLY
0	BASE	600-10361-base-000	P361 - BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU001	600-10361-0001-000	P361 G92-280B1 1024MB GDDR3 16Mx32 DVI-I+DVI-I
2	SKU002	600-10361-0002-000	P361 G92-280B1 512MB GDDR3 16Mx32 DVI-I+DVI-I
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4	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
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6	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
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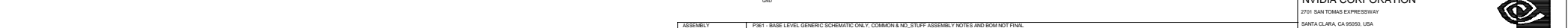
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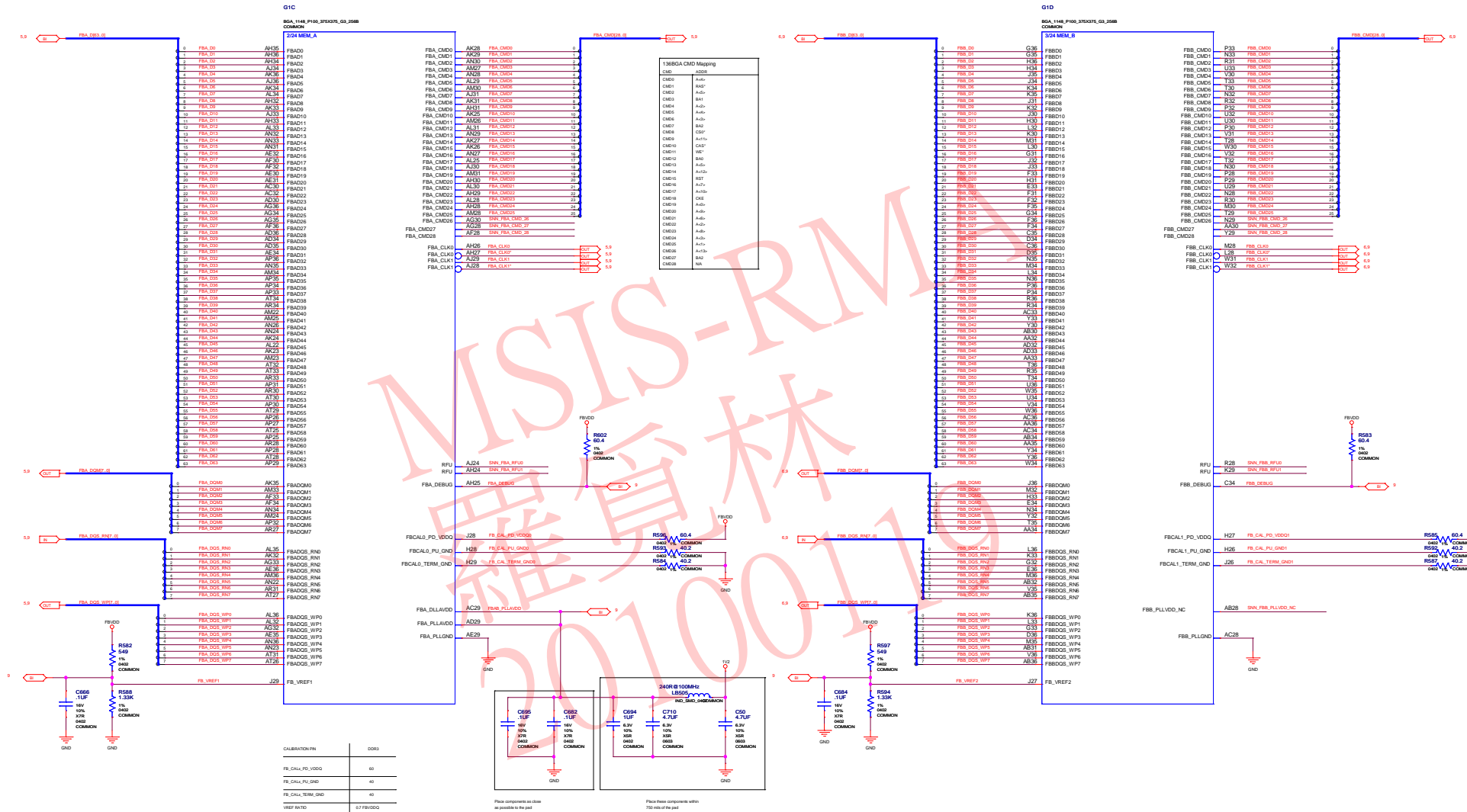
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ASSEMBLY	P361 - BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	MEMORY: GPU Partition A/B

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Place these components within
750 mils of the pad

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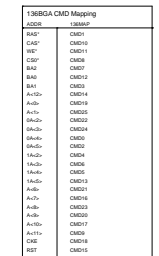


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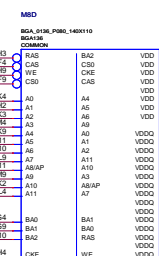
Clock Term **MUST BE PLACED** as close
as possible to the BGA memory on
the line **AFTER** the memory pin!

Minimize STUB length!

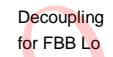


Click Team **MUST BE PLACED** as close as possible to the BGA memory on the line **AFTER** the memory pin

Minimize STUB length!




GDOR2: $V_{REF} = 0.70 \cdot FSVDDO$
 $1.41V = 2.0V \cdot 1.33K(549 \pm 1.33K)$



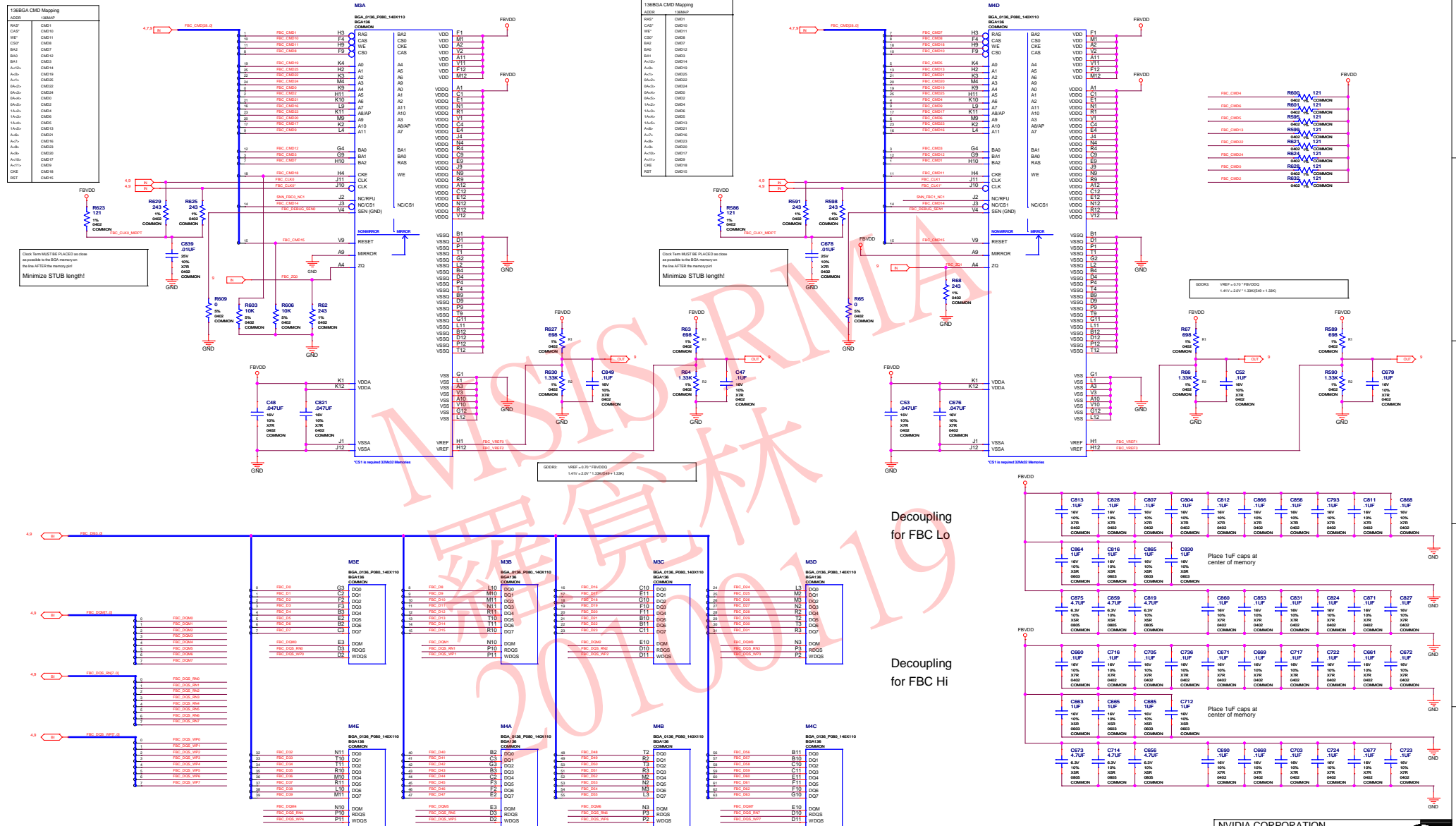
Decoupling for FBB Hi

ASSEMBLY	P361 - BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	FBB Partition

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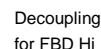


Page7: FBC Partition



Click Term **MUST BE PLACED** as close as possible to the SGA memory on the line **AFTER** the memory pin!

Minimize STUB length



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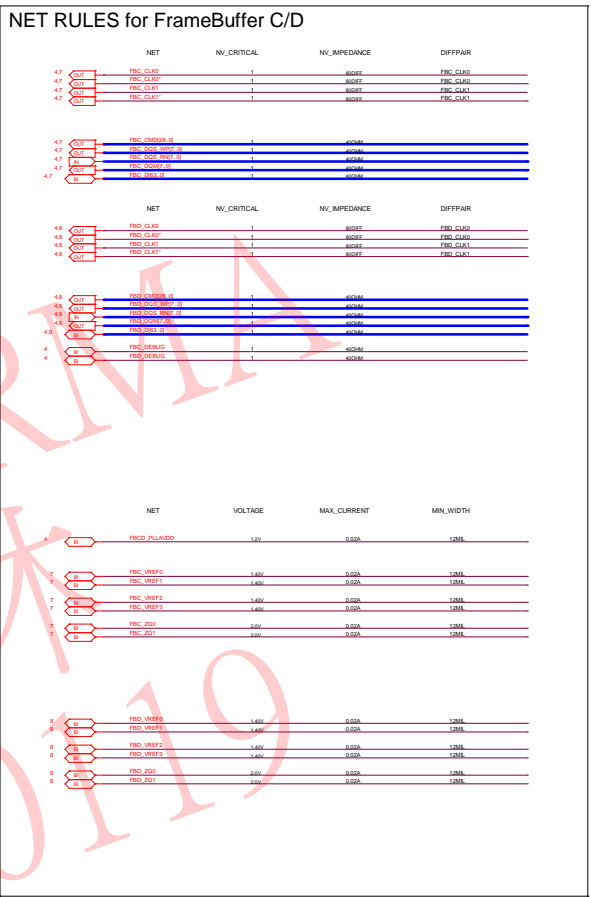
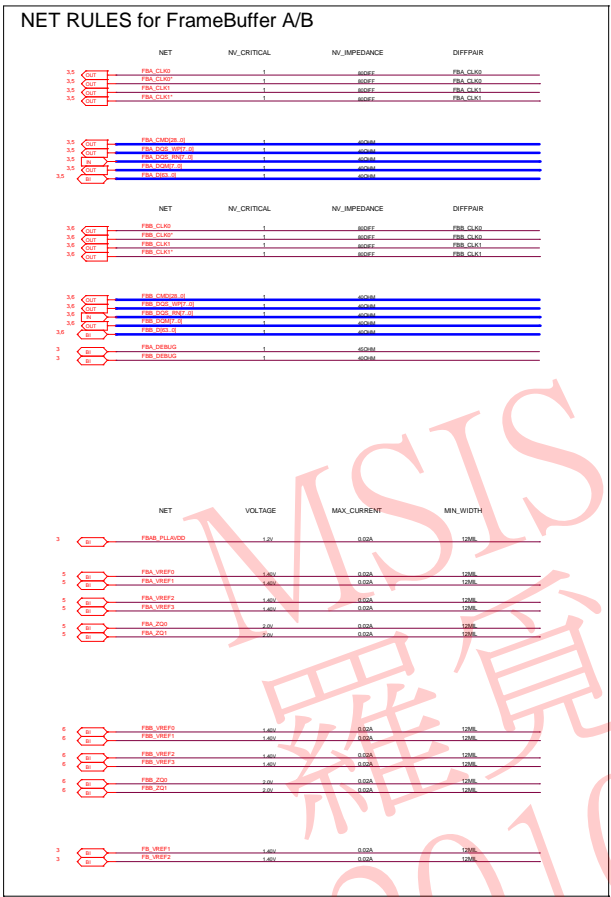


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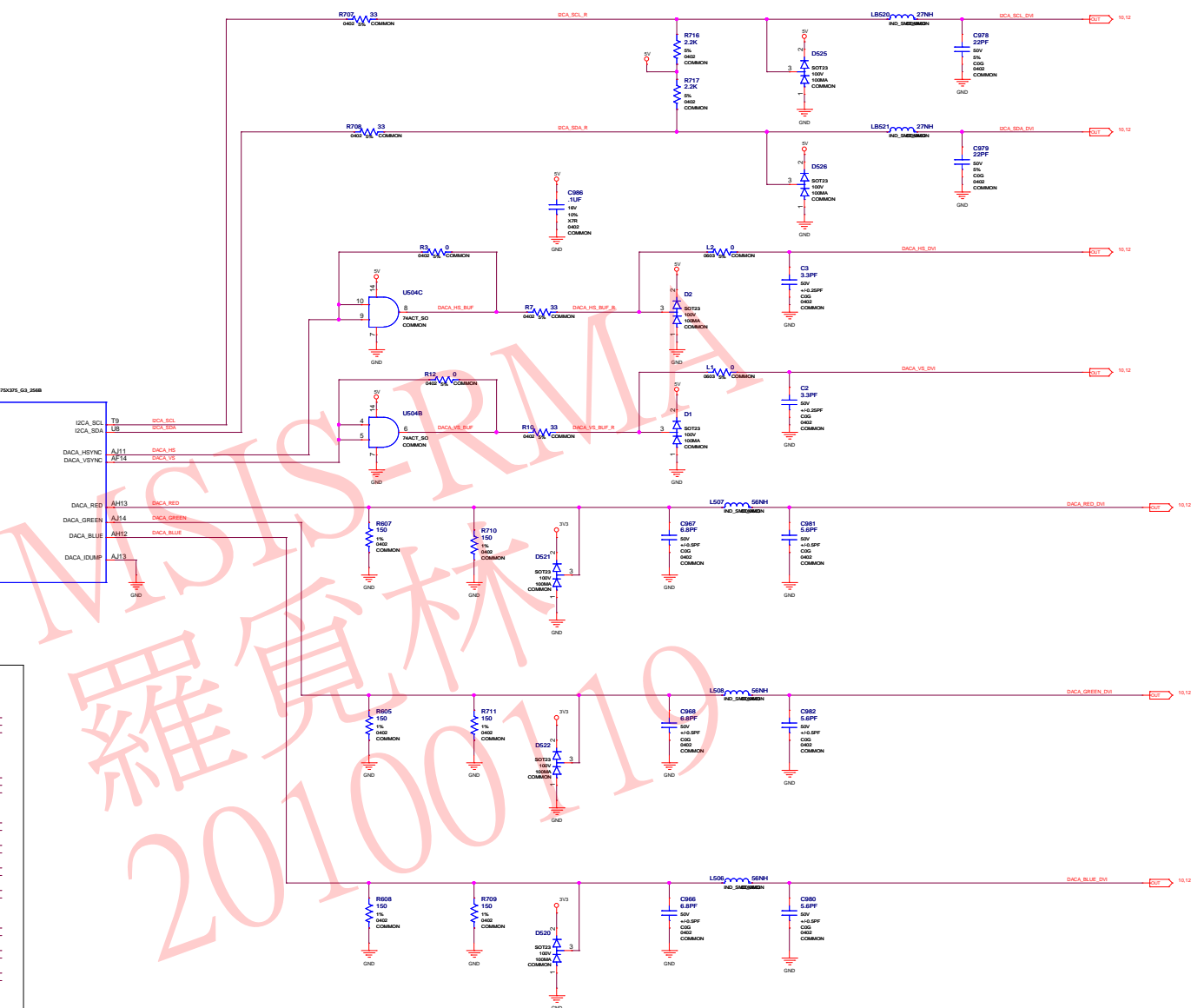
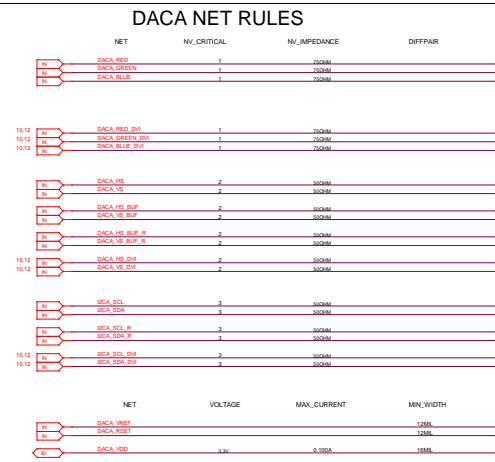
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PAGE DETAIL	FBD Partition




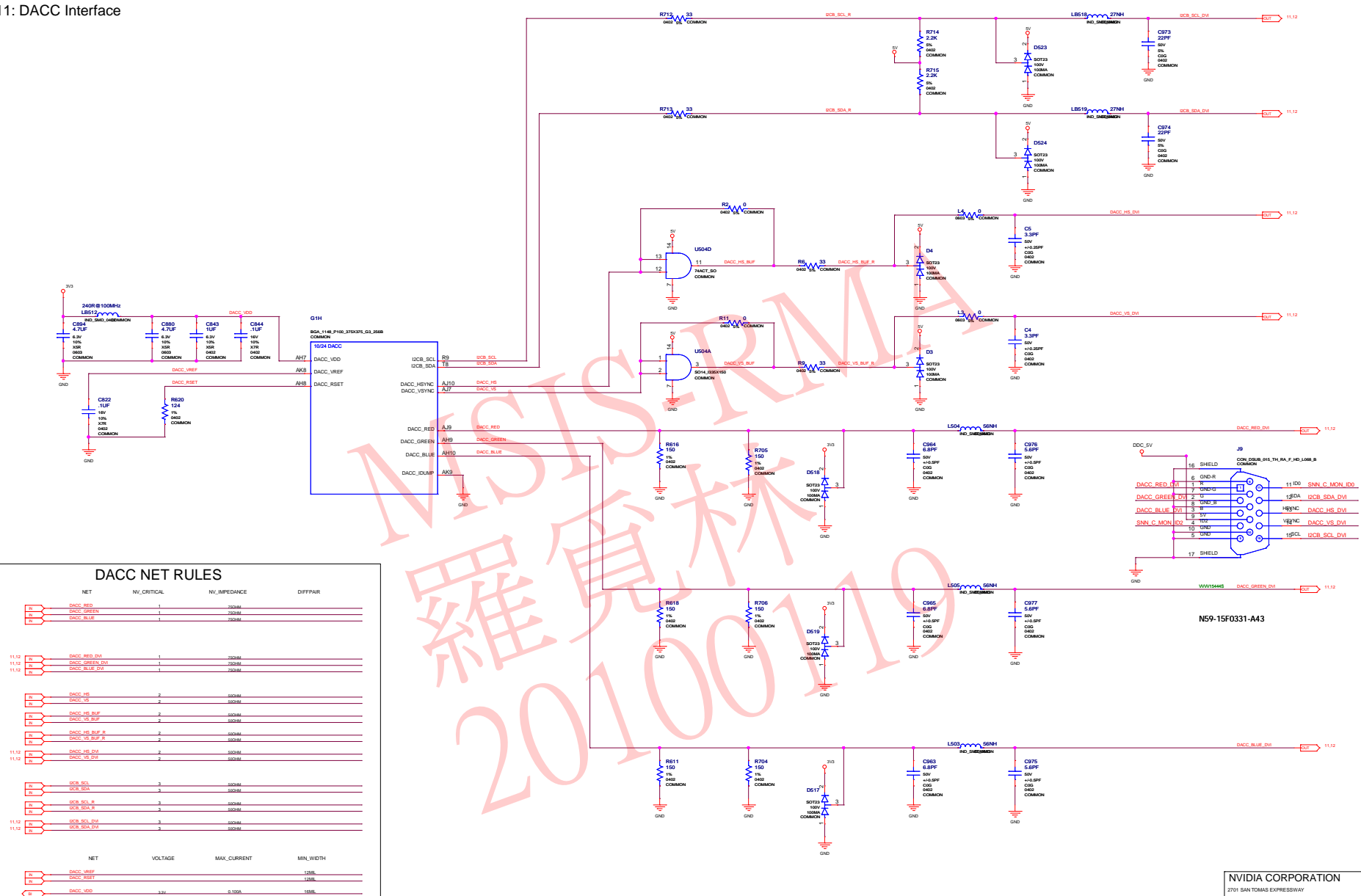
Note: FB traces on top and bottom layers are routed with 45ohm impedance for increasing spacing.
Internal FB traces are routed with 40ohm impedance.



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PAGE DETAIL	DACA Interface


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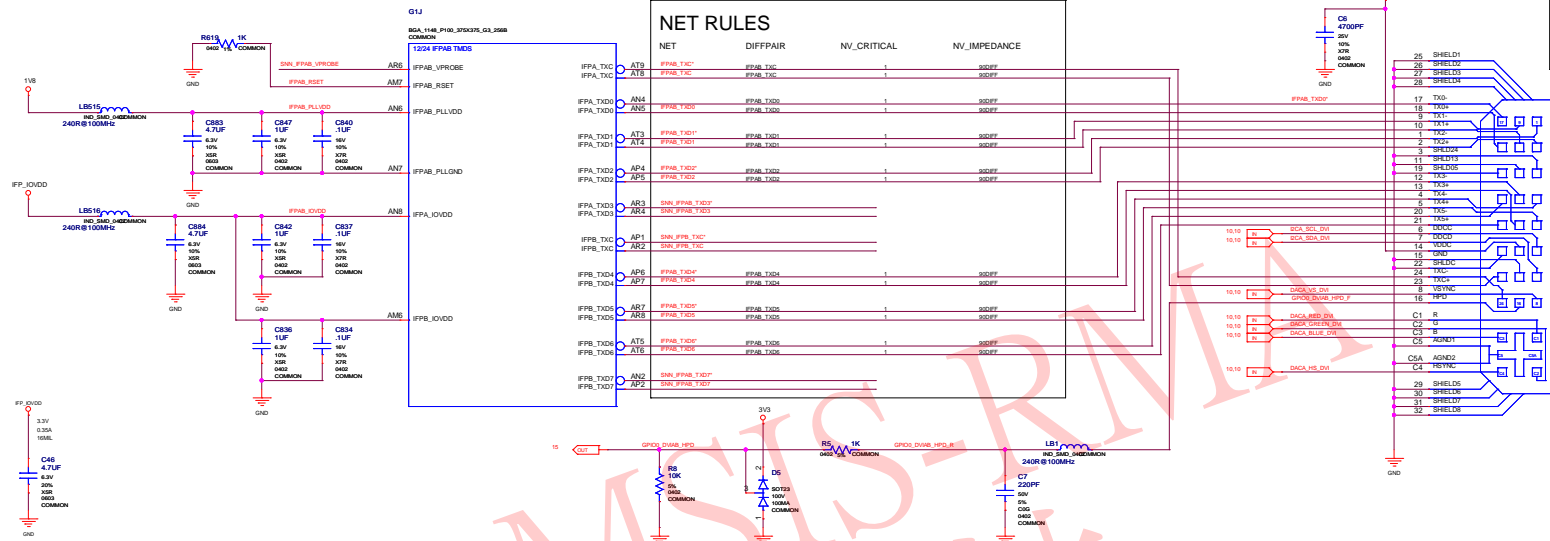
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PAGE DETAIL	DACC Interface

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IFPABCD NET RULES

NET	NV_CRITICAL	NV_IMPEDANCE	DIFFPAIR
IFPAB_RESET			120Ω
IFPAB_RESET			120Ω
IFPAB_DIN0_VPD_0	3	50ΩCM	
IFPAB_DIN0_VPD_0	3	50ΩCM	
IFPAB_DIN0_VPD_0	3	50ΩCM	
IFPAB_DIN0_VPD_0	3	50ΩCM	
IFPAB_DIN0_VPD_0	3	50ΩCM	
IFPAB_DIN0_VPD_0	3	50ΩCM	
NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
IFPAB_PLVDD0	1.6V	0.05A	100MIL
IFPAB_VDD0	3.3V	0.05A	100MIL
IFPAB_PLVDD0	1.6V	0.05A	100MIL
IFPAB_VDD0	3.3V	0.05A	100MIL

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PAGE DETAIL	IFP A/B and C/D Interface

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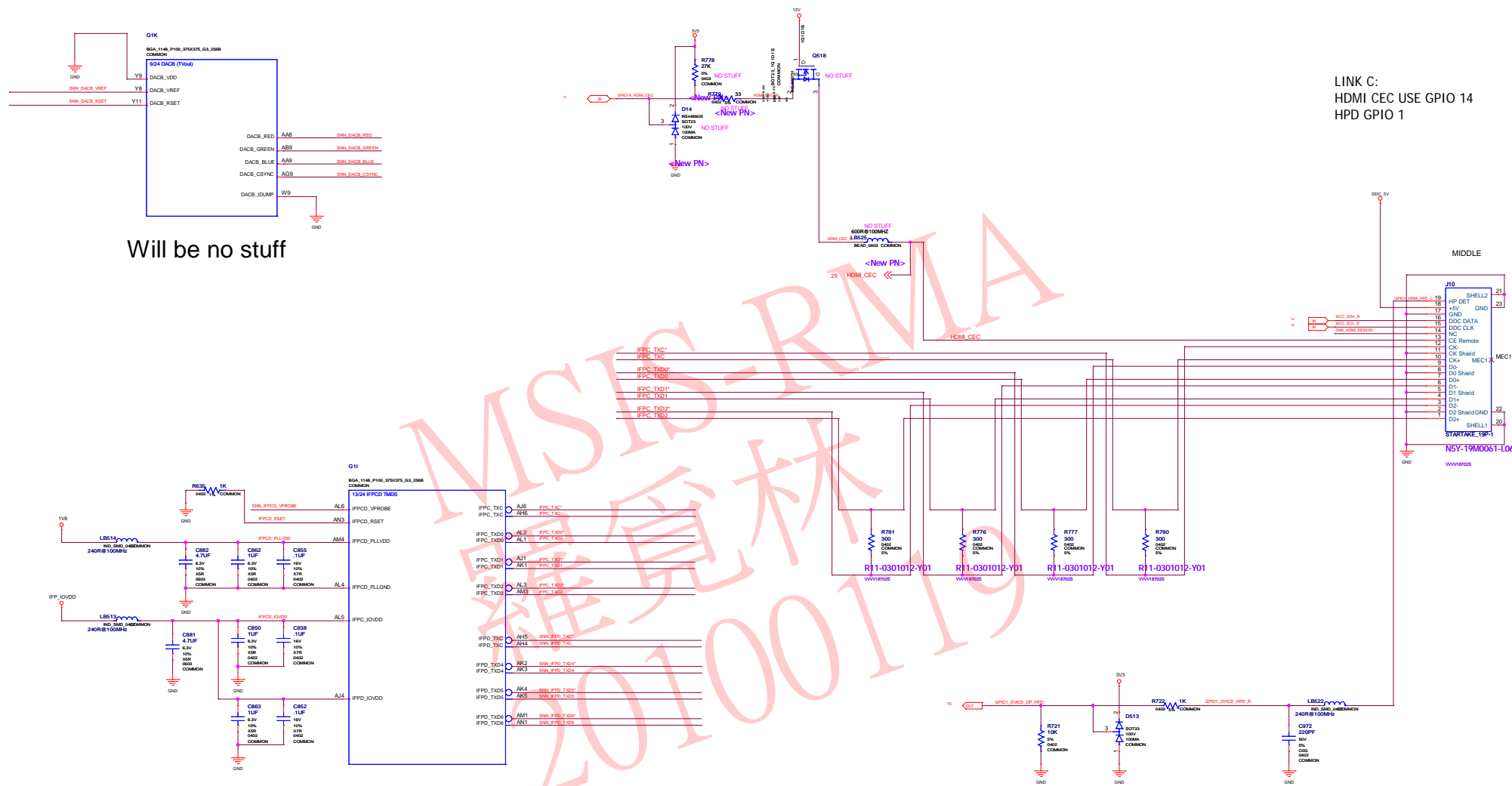
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LINK C:
HDMI CEC USE GPIO 14
HPD GPIO 1

Will be no stuff

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PAGE DETAIL	OACB Interface

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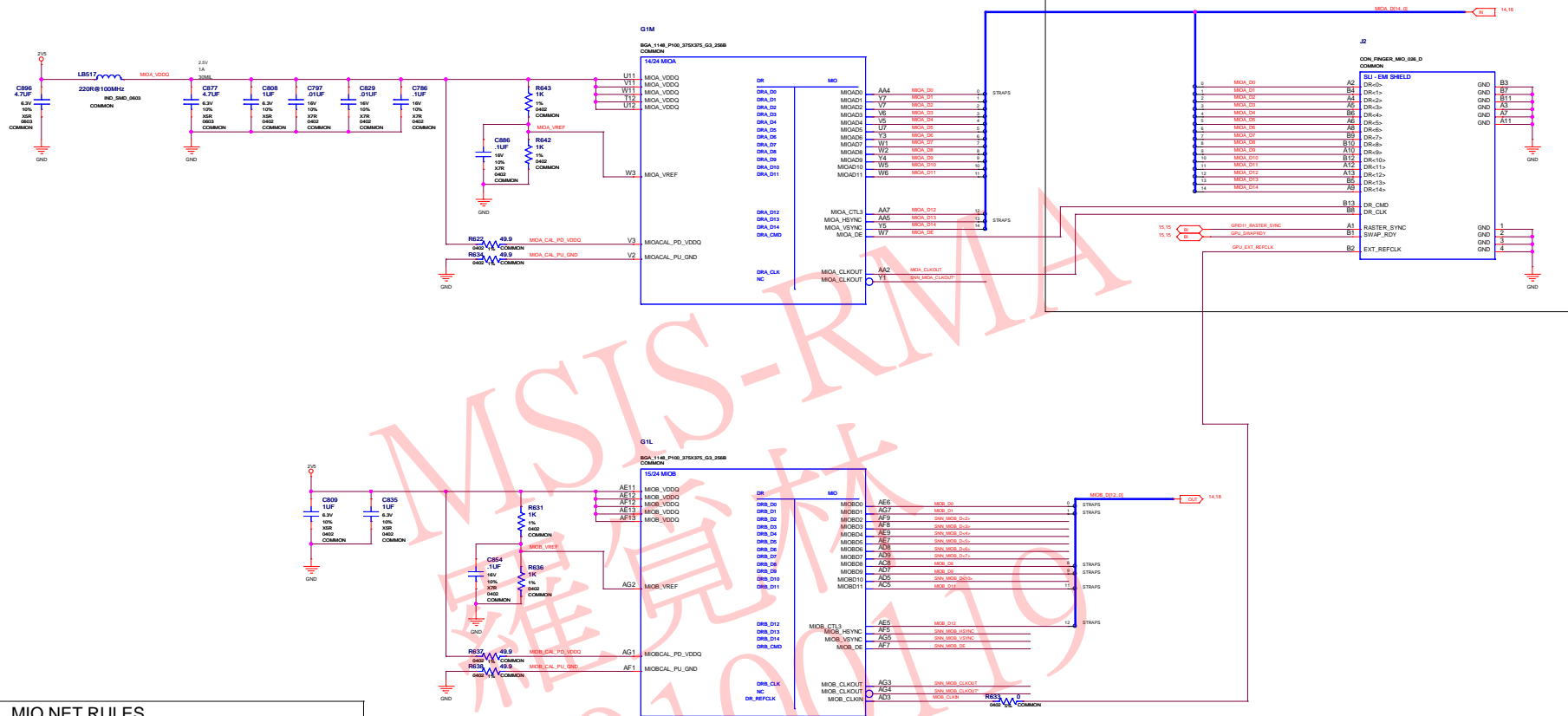
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MIO Feature Connector



MIO NET RULES

[illegible]

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PAGE DETAIL	Multi-use IO(MO) interface

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XTAL

(BUFRST/STEREO/SWAPRDY/CLAMP/TESTMODE)



GPIO	I/O	Function
0	IN	DVI Hotplug Detect South
1	IN	DVI Hotplug Detect North
2	N/A	Not used
3	N/A	Not used
4	OUT	Fan PWM Output
5	OUT	Voltage Select 0
6	OUT	Voltage Select 1
7	IN	Fan Tach Input
8	OUT	THERM_OVERT*
9	N/A	Not used
10	N/A	Not used
11	OUT	RASTER (SL) SYNC
12	N/A	Not used
13	N/A	Not used
14	N/A	Not used

ASSEMBLY	P361 - BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	MISC: GPIO, I2C, BIOS, PLL, and XTAL

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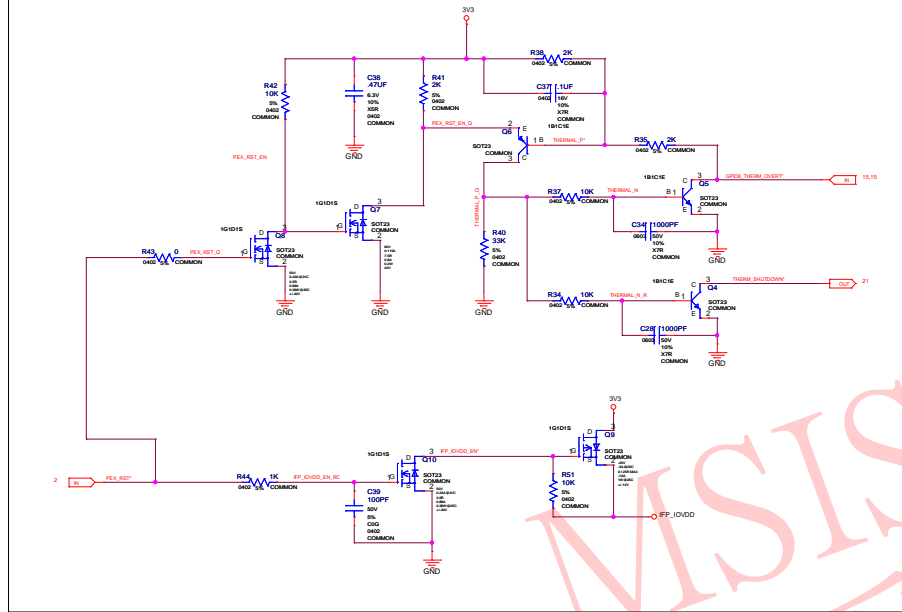
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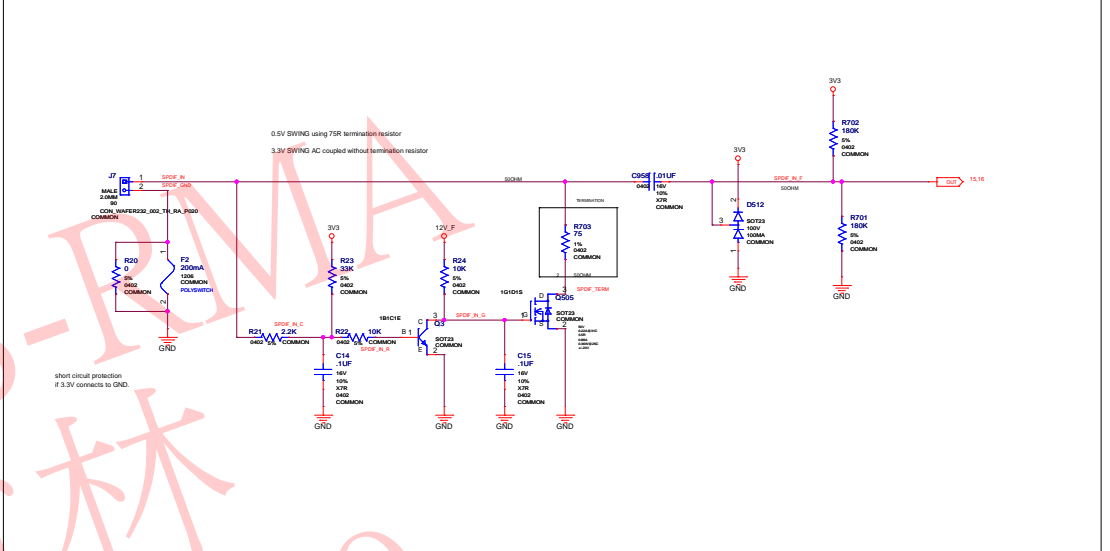
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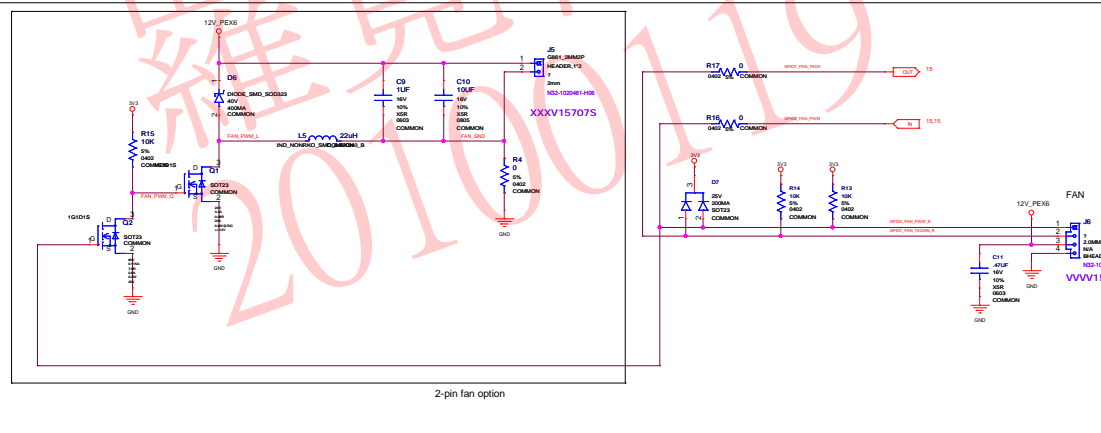
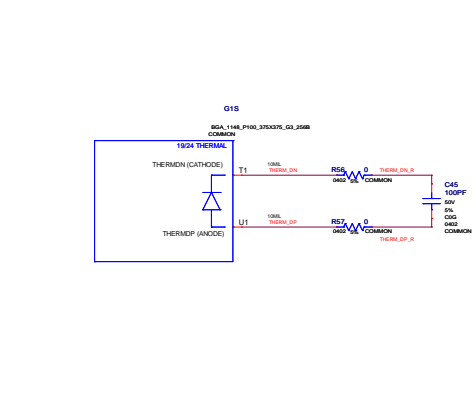
THERMAL PROTECTION/TMDS BACKDRIVE



SPDIF INPUT / DETECTION



THERMAL DIODE



ASSEMBLY P3B1 - BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO. STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL Thermal Control/Protection and SPDIF Input

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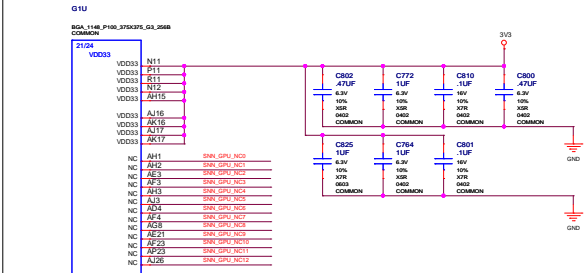
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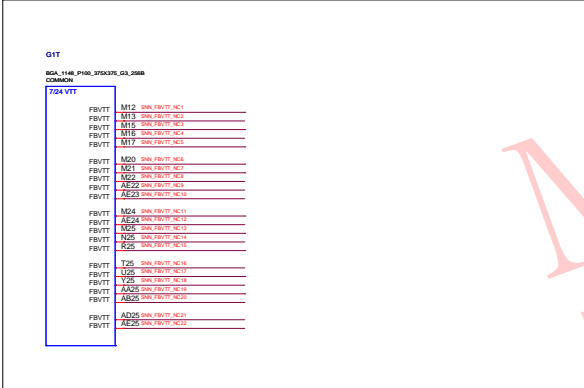
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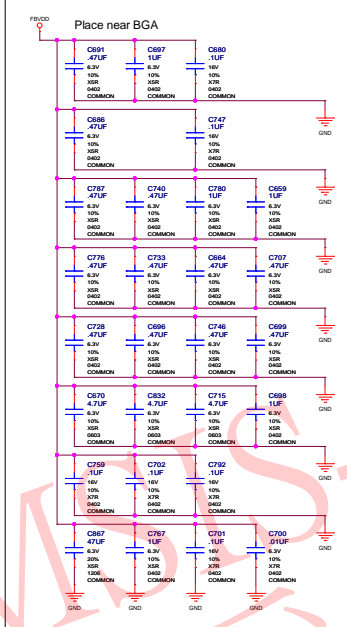
VDD33



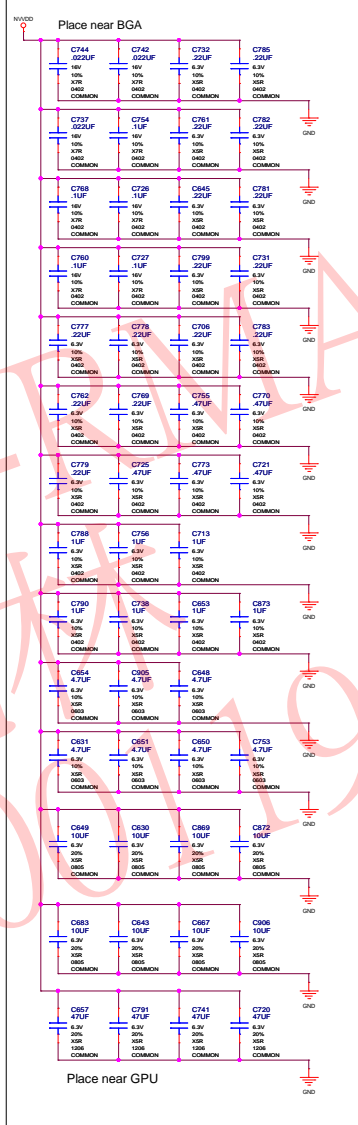
FBVTT



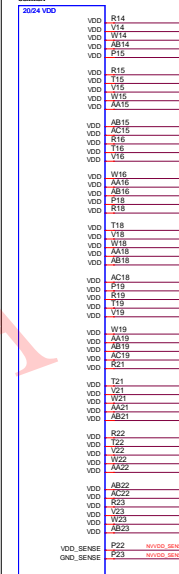
FBVDDQ



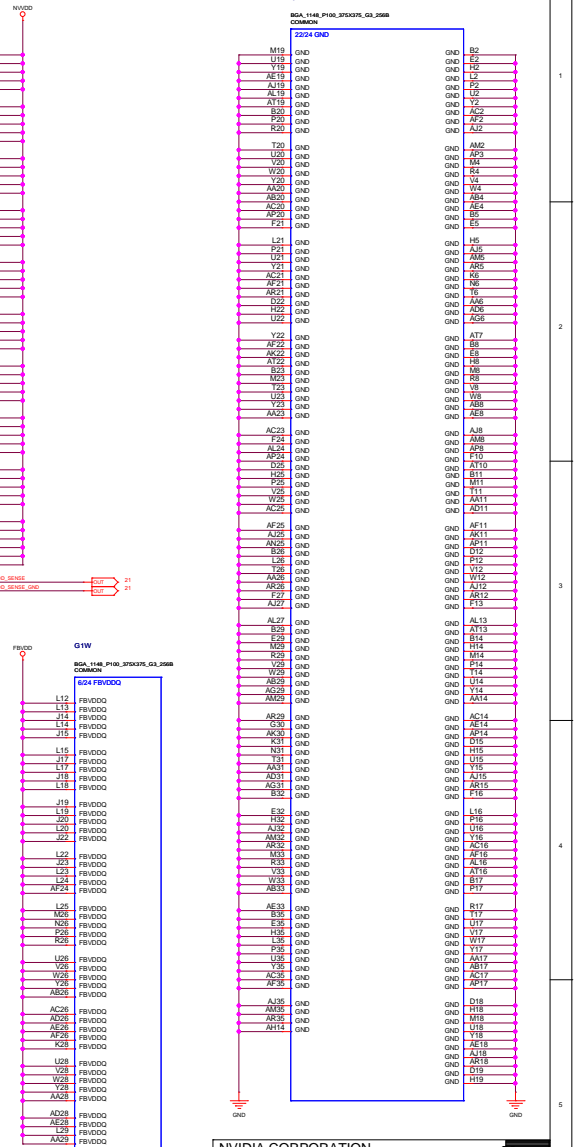
NVVDD



G1V



G1X



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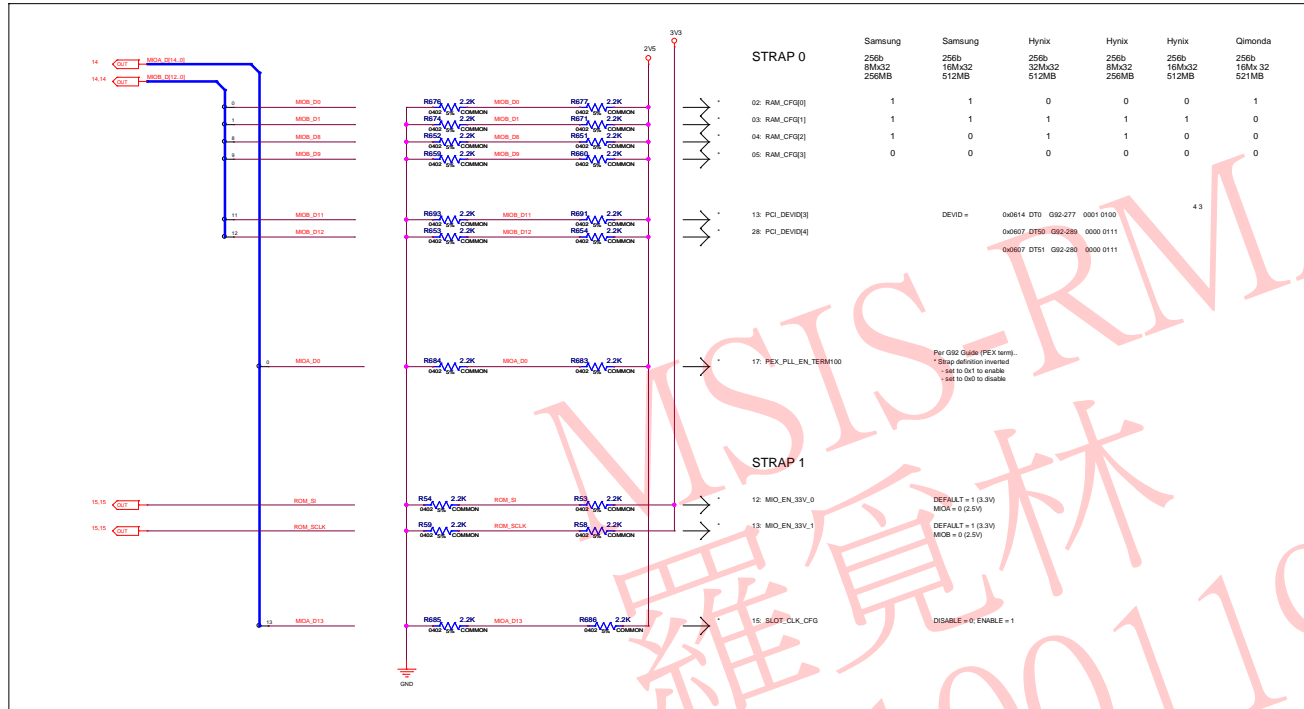
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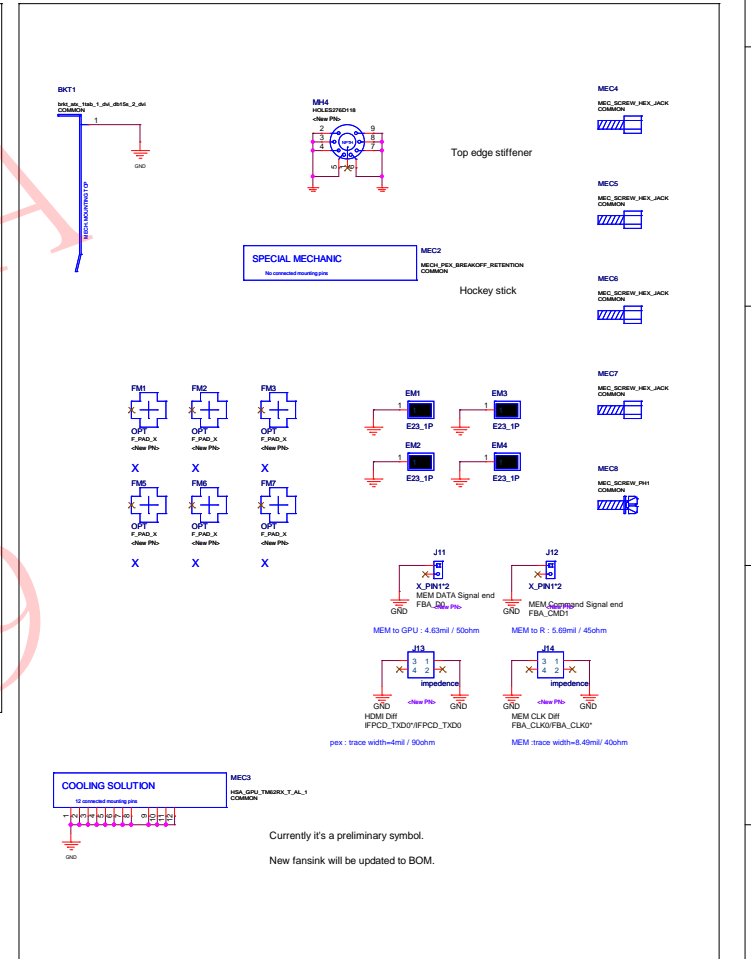
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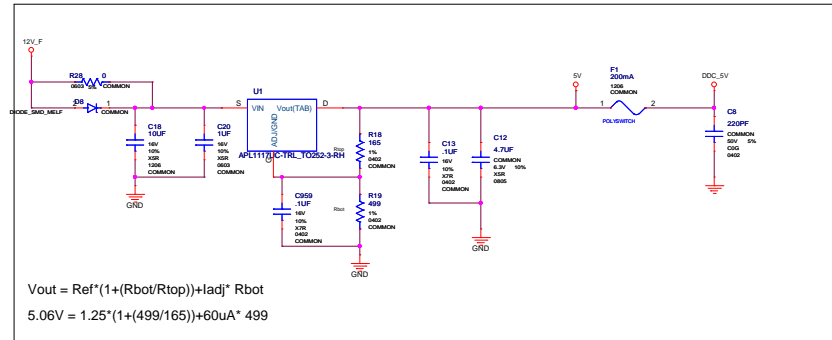
STRAPS



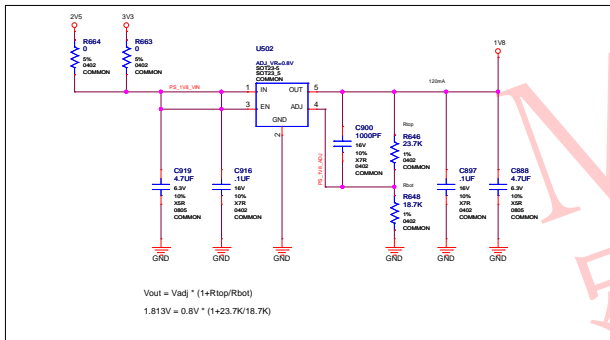
MECHANICAL



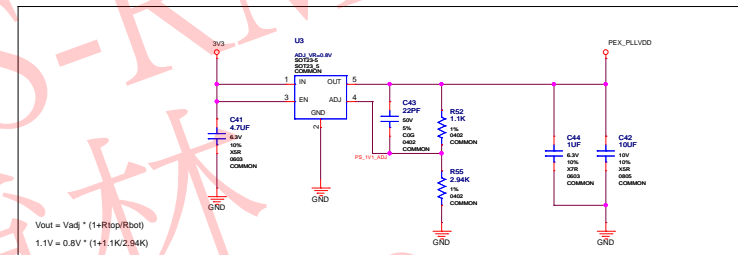
5V REGULATOR



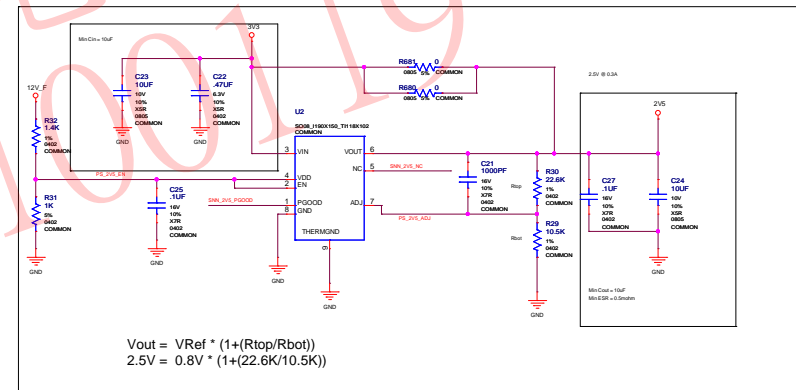
5V DDC



PEX_PLLVDD Optional



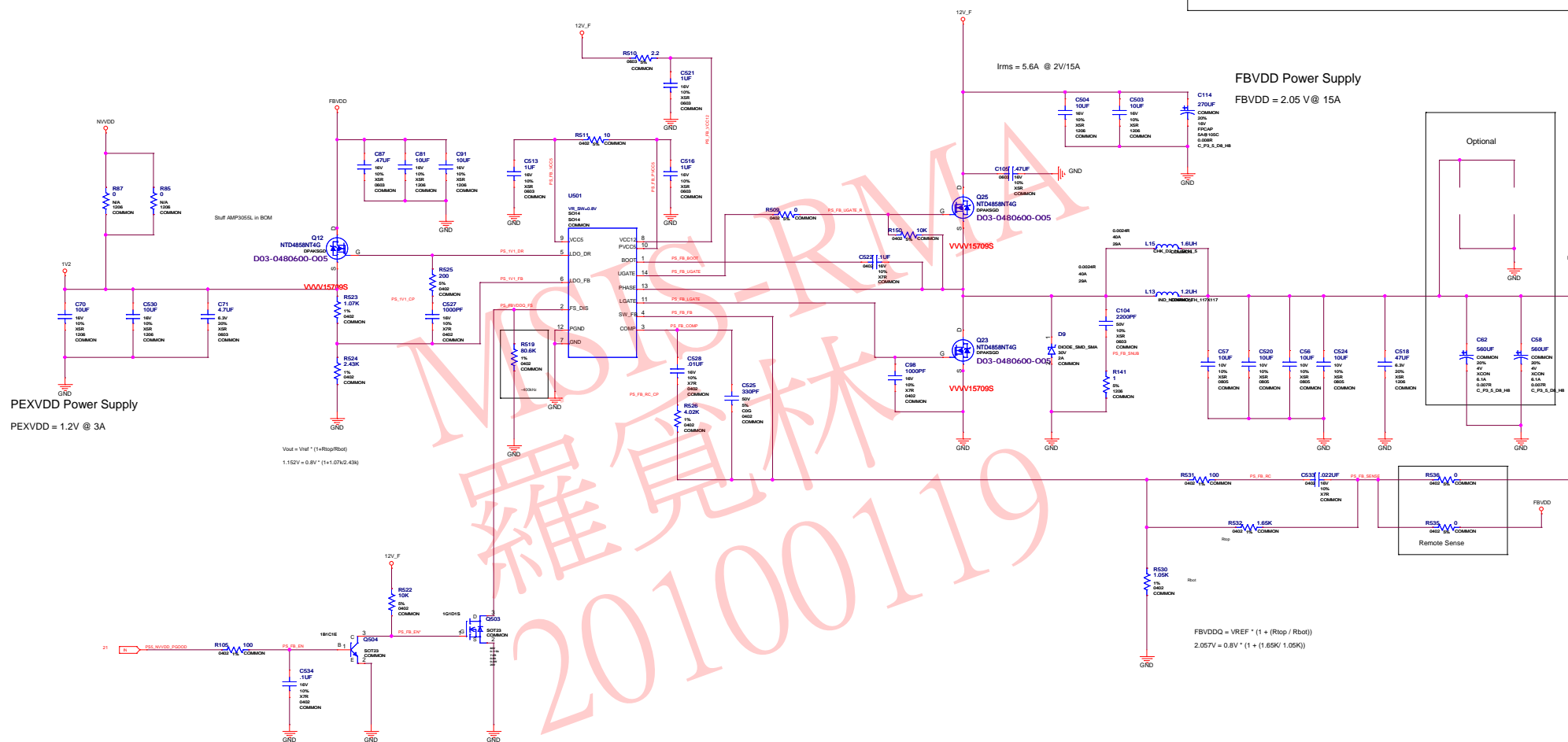
MIO_VDD 2.5V



NETNAME	MAX_CURRENT	MIN_LINE_WIDTH	VOLTAGE
DDC_5V	0.1A	138L	5V
5V	0.05A	138L	5V
PEX_PLLVDD	0.02A	138L	1.0V
TVDD	0.02A	138L	1.8V
2V5	0.0A	138L	2.5V



	NETNAME	MAX_CURRENT	MIN_LINE_WIDTH	VOLTAGE
FBVDD	FBVDD	15A	20MIL	1.8V
1V2	1V2	2A	10MIL	1.1V



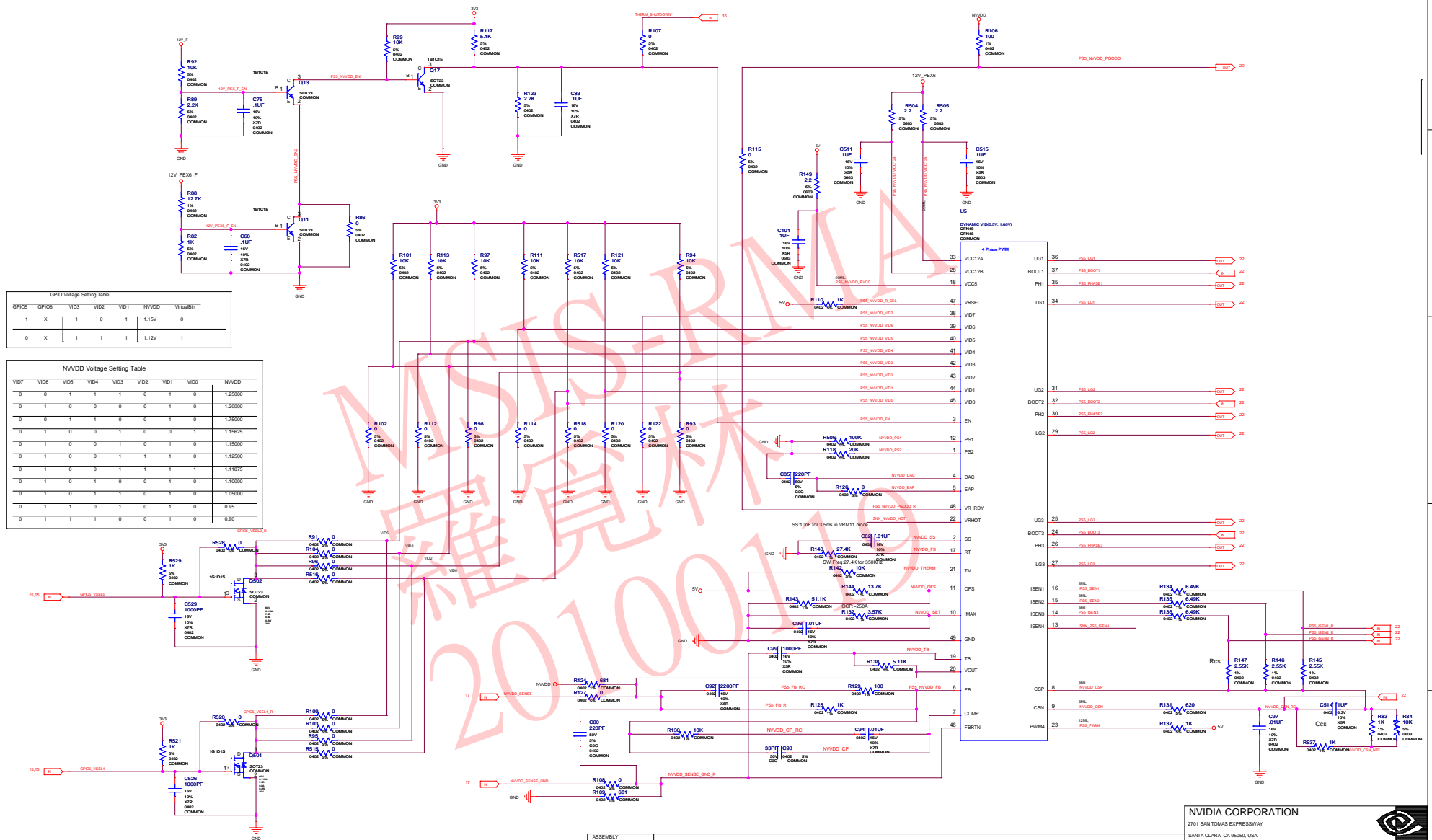
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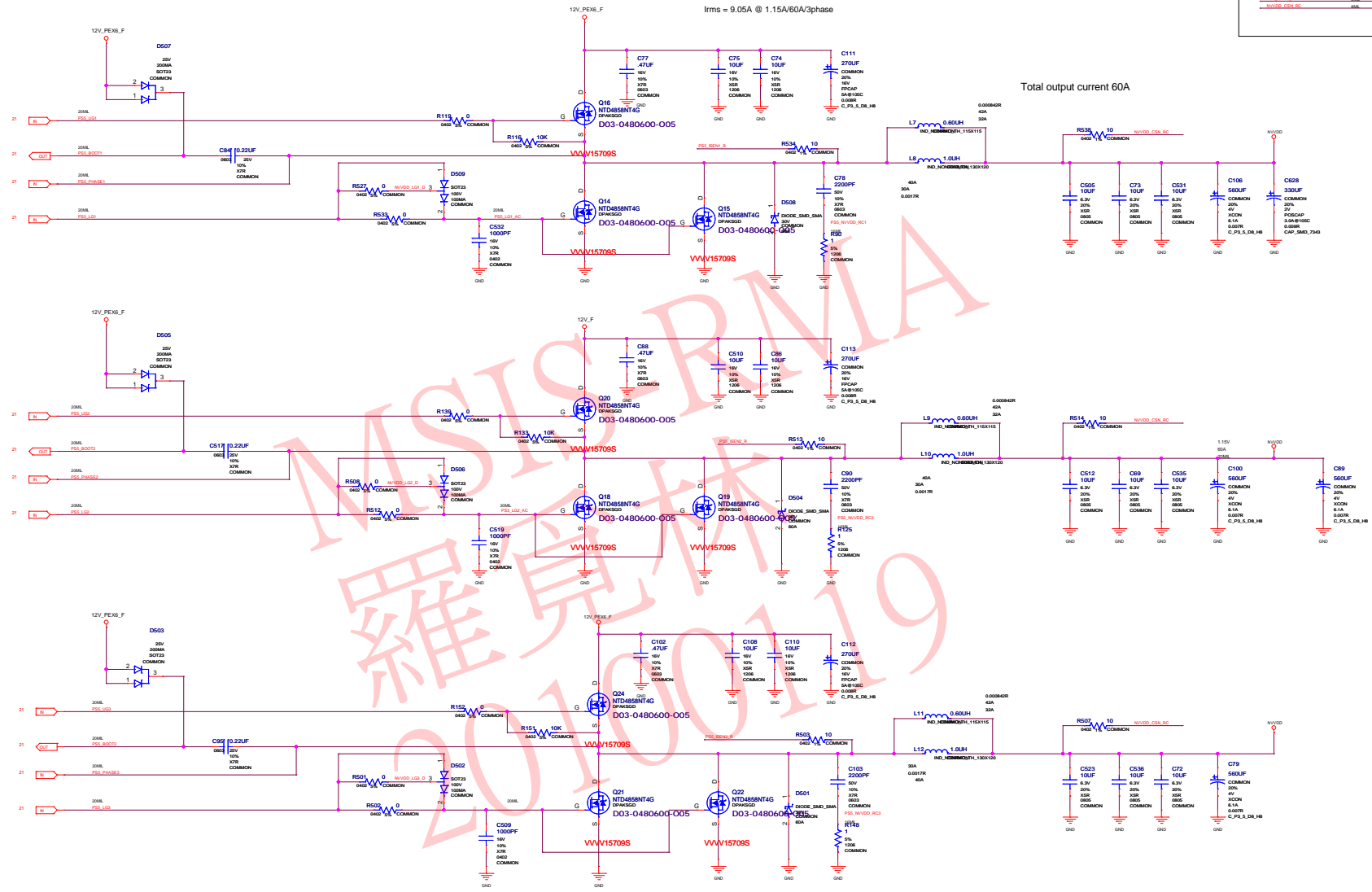
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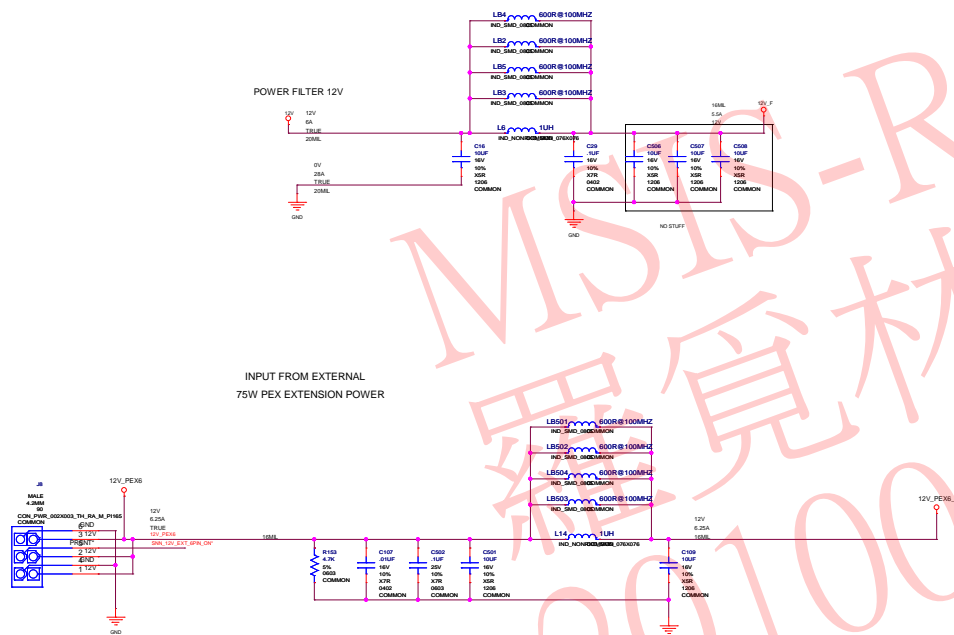


PSS_EEN1_R	数据	OUT	21
PSS_EEN2_R	数据	OUT	21
PSS_EEN3_R	数据	OUT	21
NYVD_CS_N_BC	数据	OUT	21

Total output current 60A

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ASSEMBLY	P361 - BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Power Supply: Filter of 3V3, 12V, 12V_PEX6

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