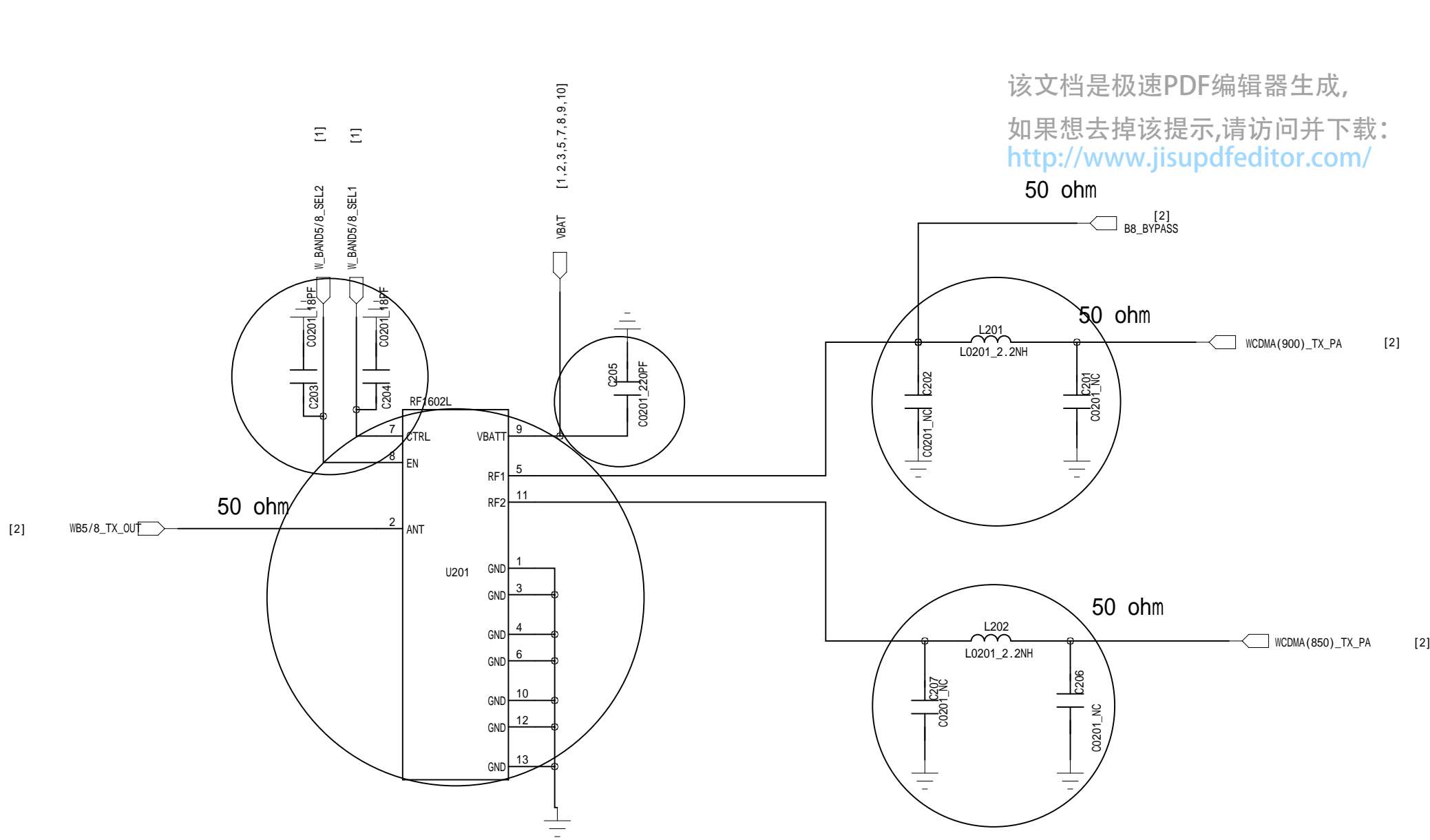
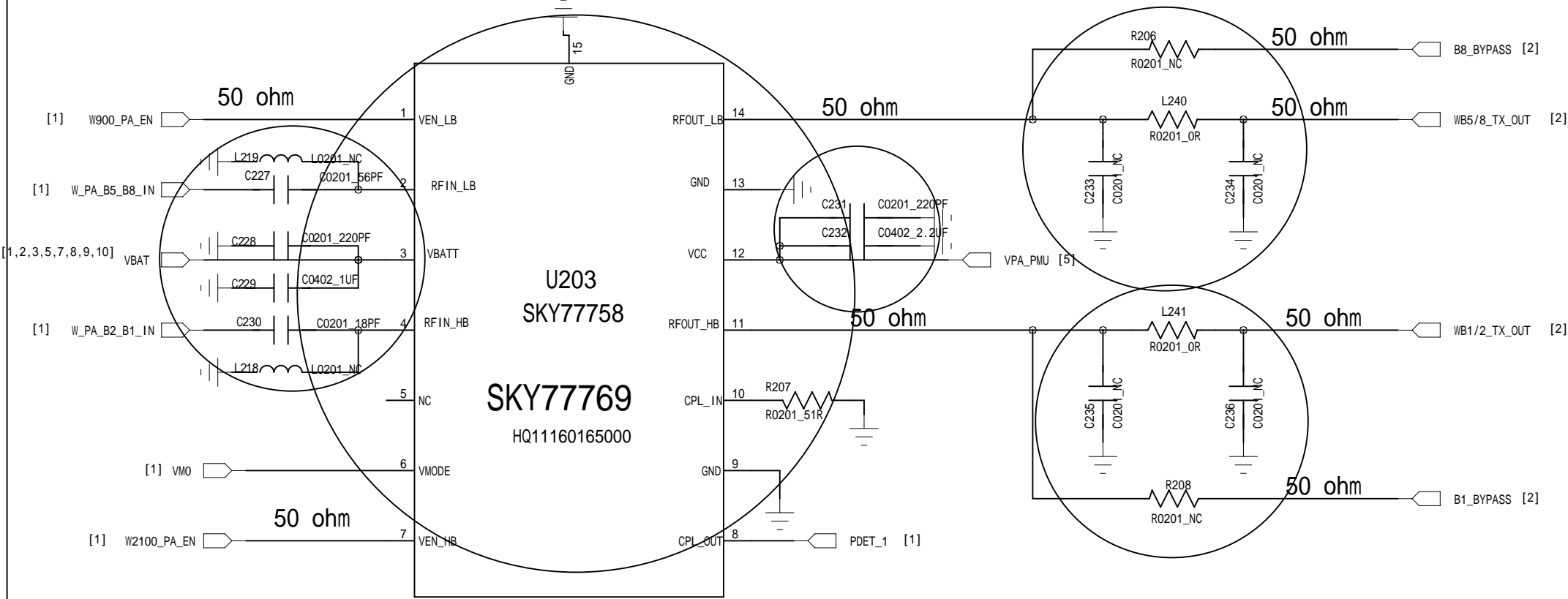
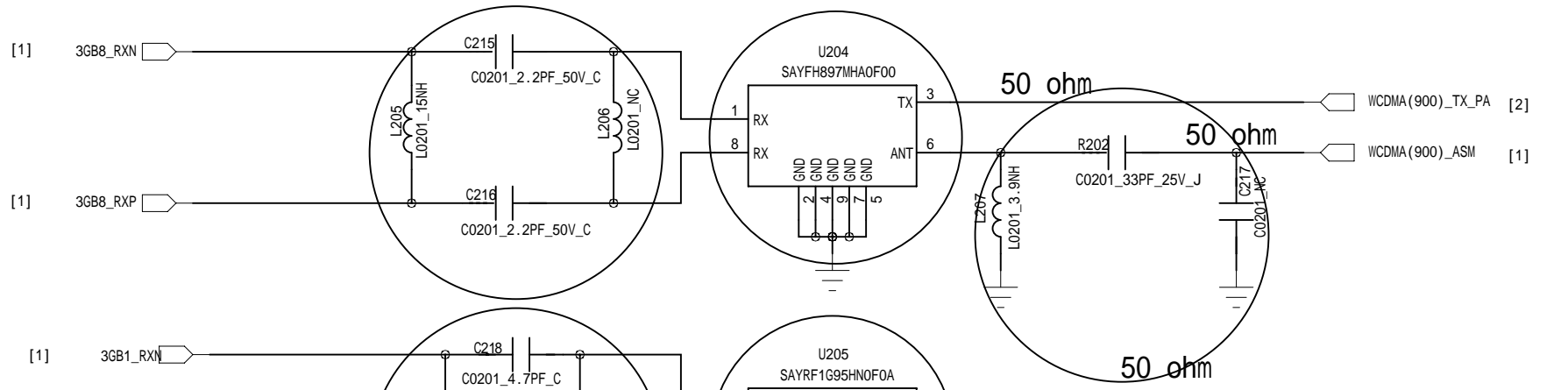


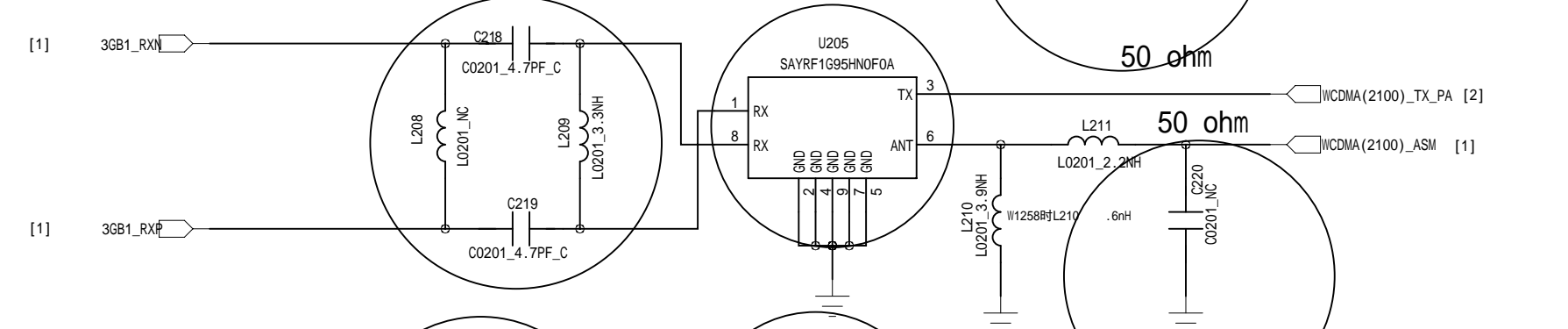
WCDMA Band1/2/5/8



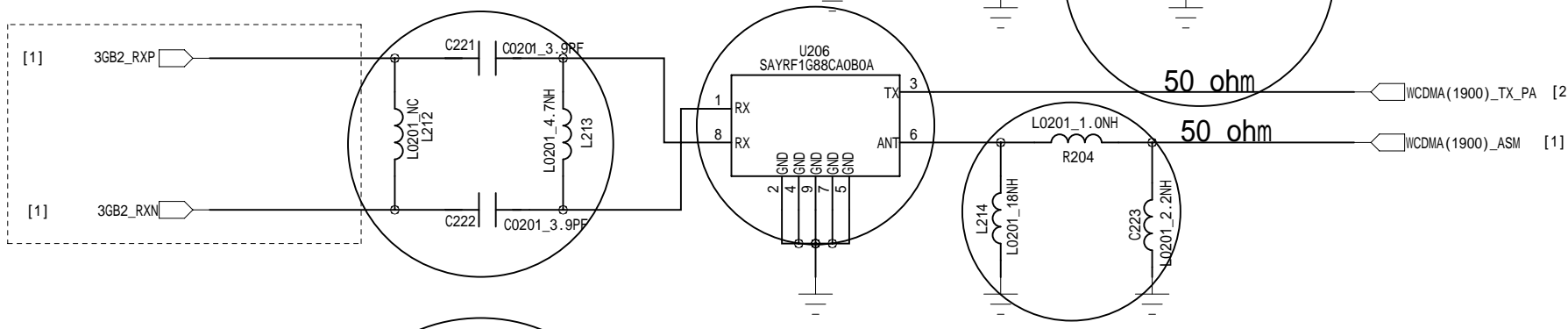
B8



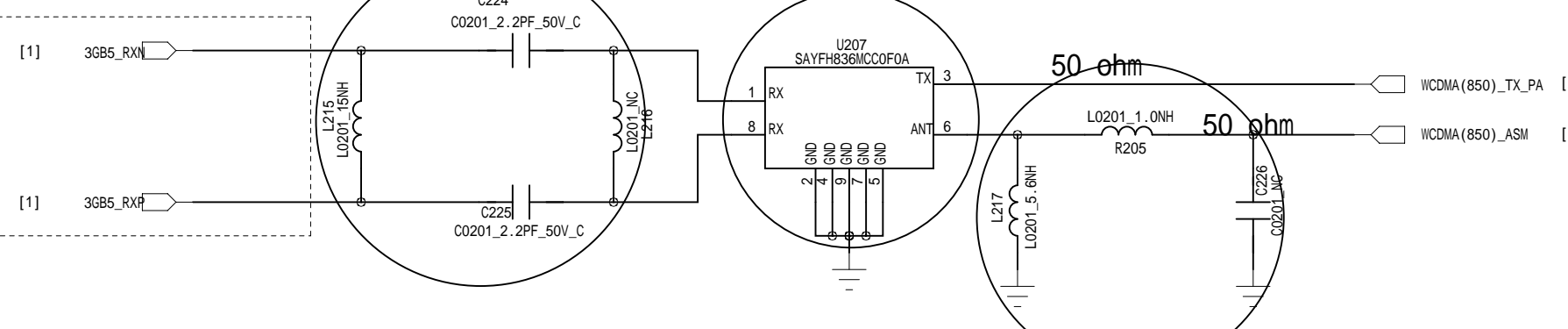
B1



B2



B5



DRAWN		DATED		TITLE:		VERSION:		SHEET:	
CHECKED		DATED							

Test Point

[1,2,5,7,8,9,10] VBAT

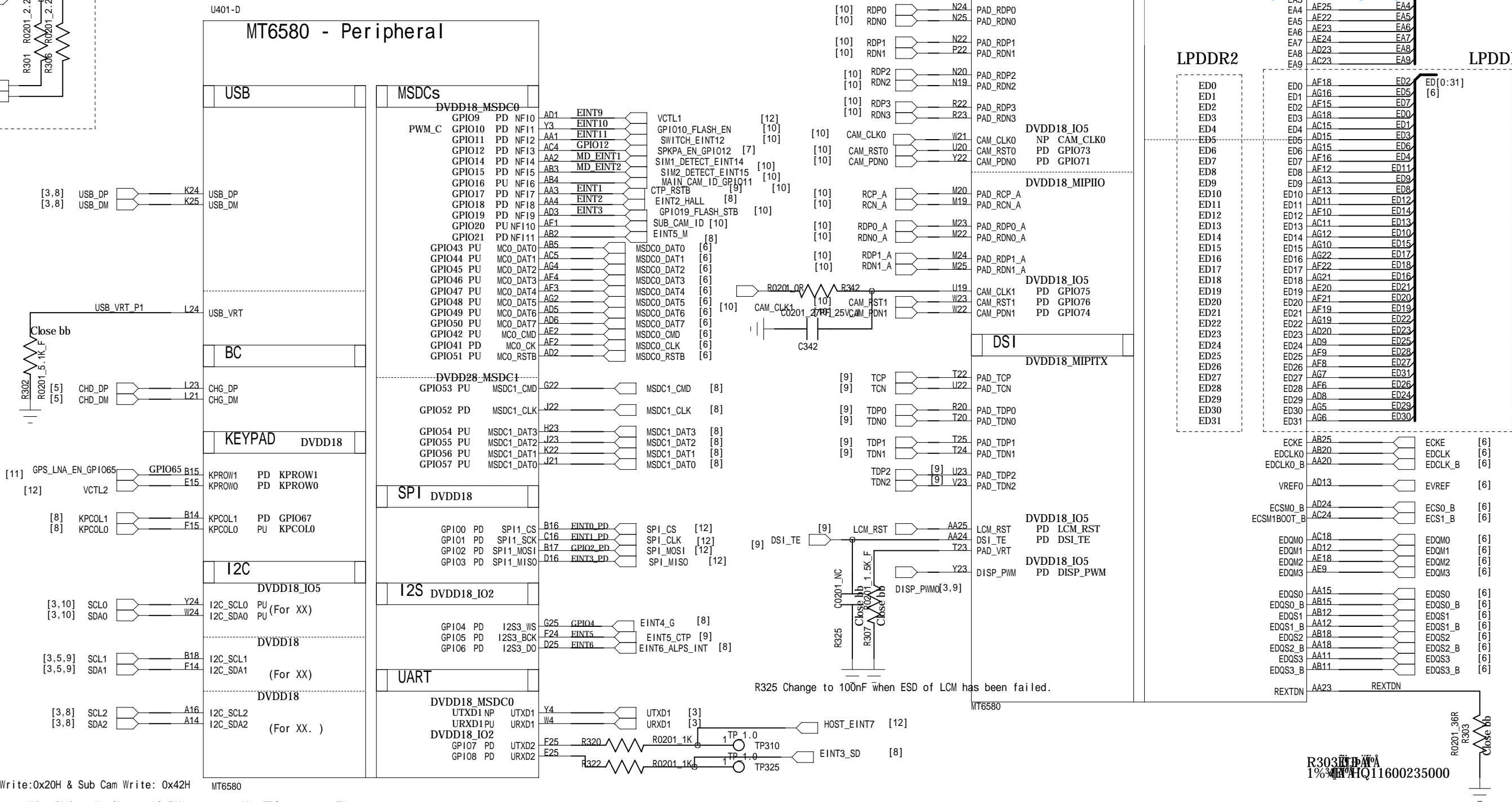
[3] UTXD1 R309 R0201 1K TP 1.0 TP313

[3] URXD1 R310 R0201 1K TP 1.0 TP314

[3,8] USB_DM TP 1.0 TP301

[3,8] USB_DP TP 1.0 TP308

[5,8] VCHG/USB_PWR TP 1.0 TP309



CONNSYS TCXO

TXC, 7L26002015?
Wait for new 6580 QVL

100K NTC----AP

[3] AUX_AP_NTC

V1018_PU1

[1, 3, 4, 5, 6, 7, 8, 9, 10, 12]

R0402_300K

R0402_10K

NTC301

NTC302

C301

100PF

50V

Close to BB IC

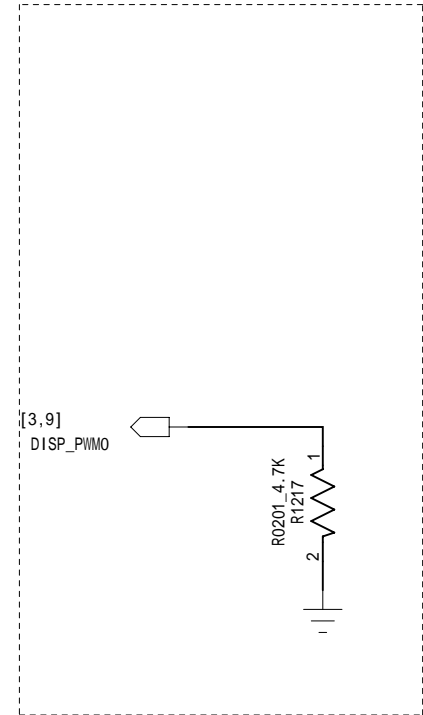
Close to BB IC

NTC301 Close to AP and keep 5~7mm close to IC, and located in the same layer

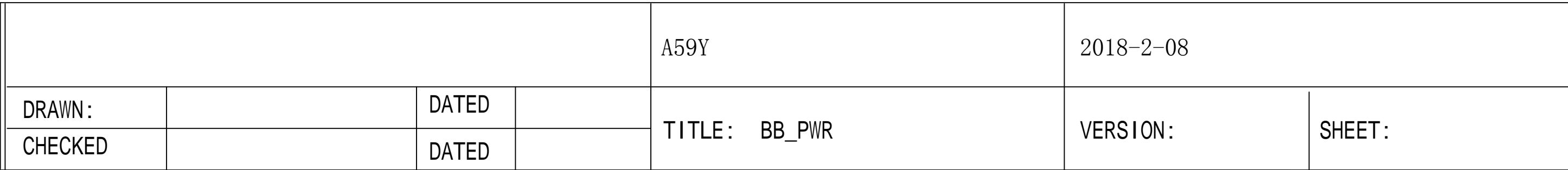
NTC302 Close to PA 3~5mm close to IC, and located in the same layer

Memory Trapping Table

Default eMMC
(BPBUS11, BPBUS10, PWRA_SPI0_CSN)
(0, x, x) : eMMC
(1, 0, 0) : LPDDR2 SLC Nand

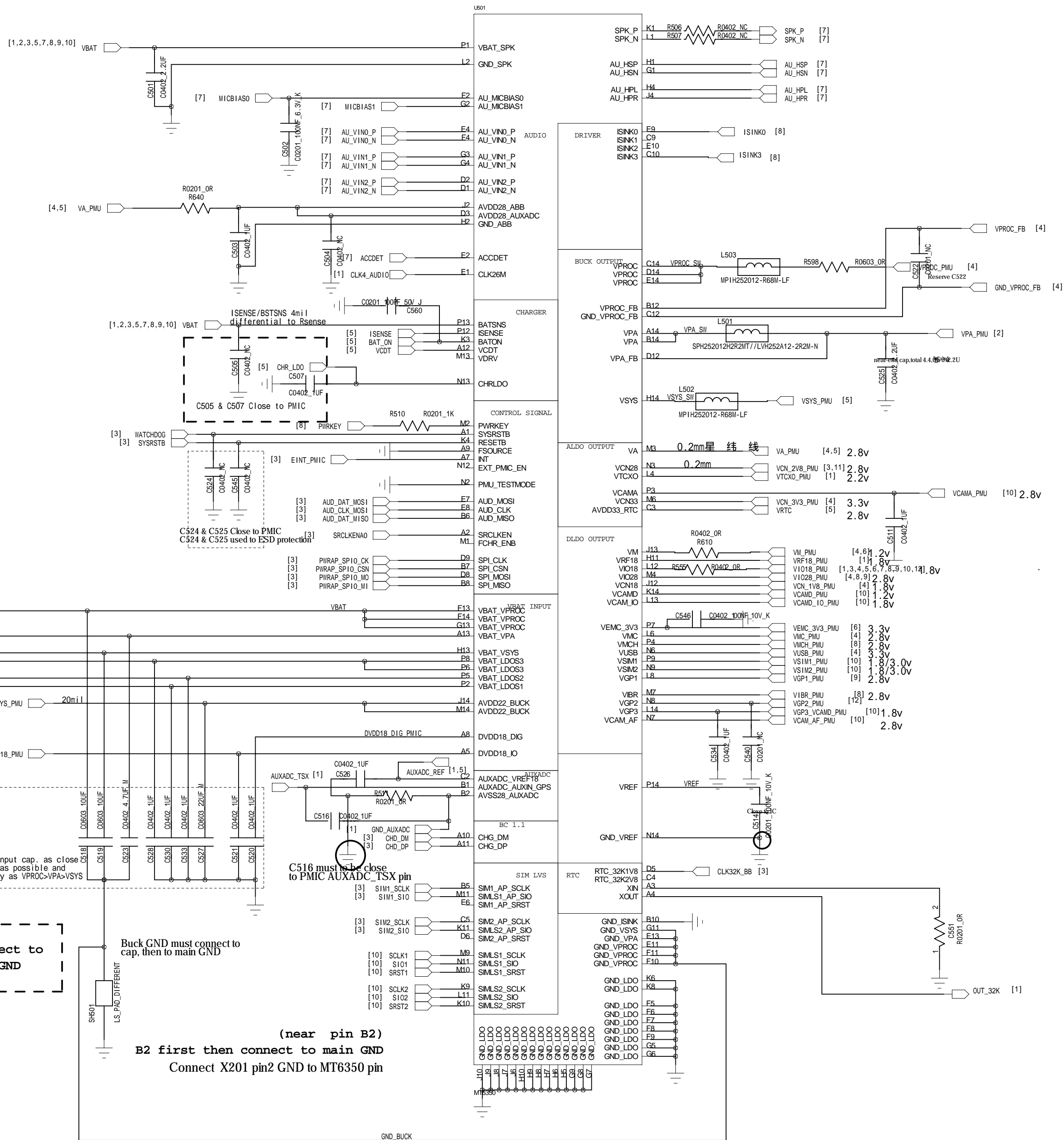
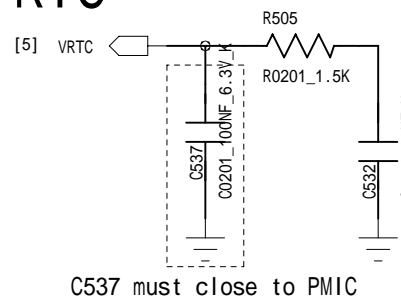


SHEET:



PMIC

RTC

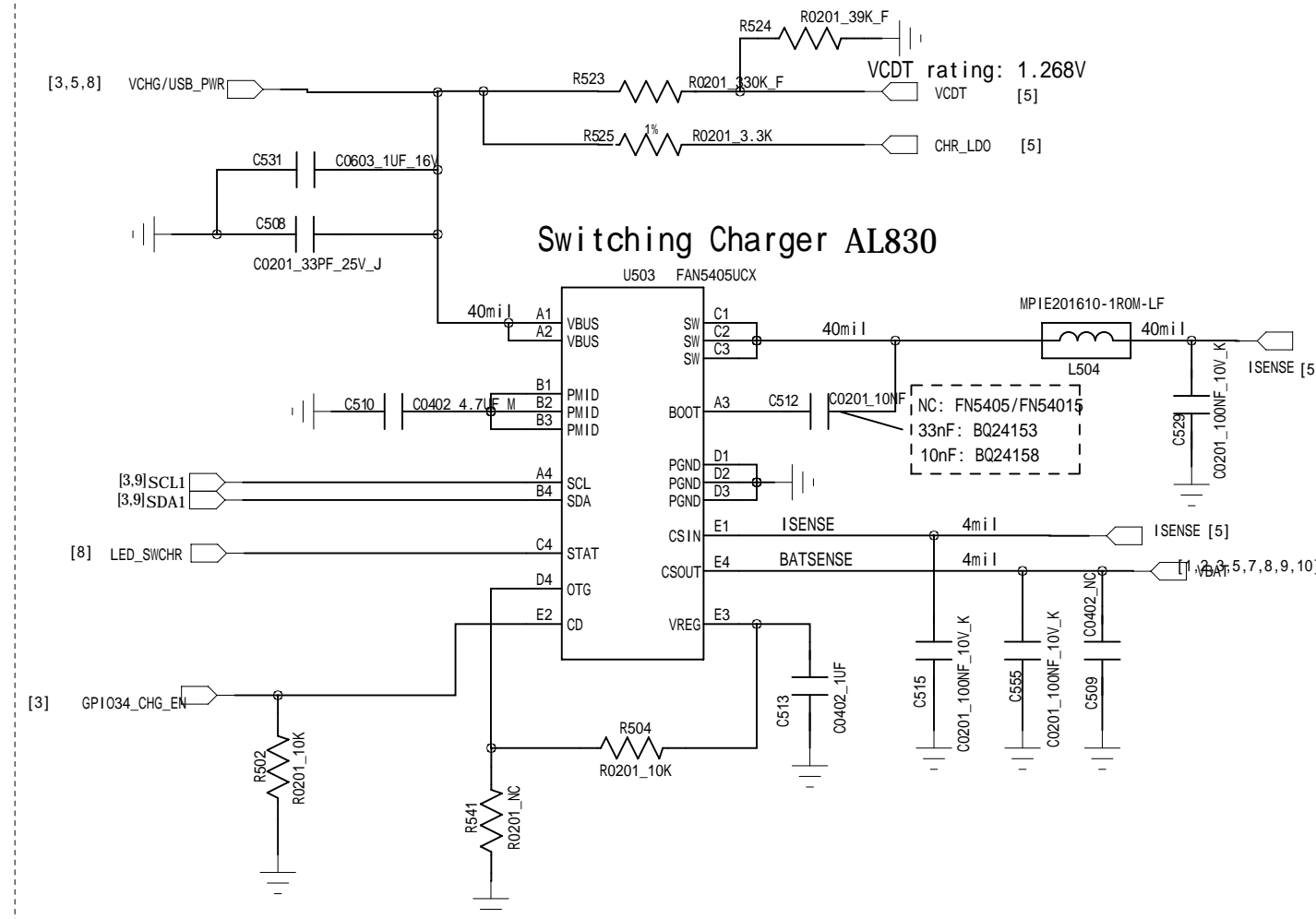
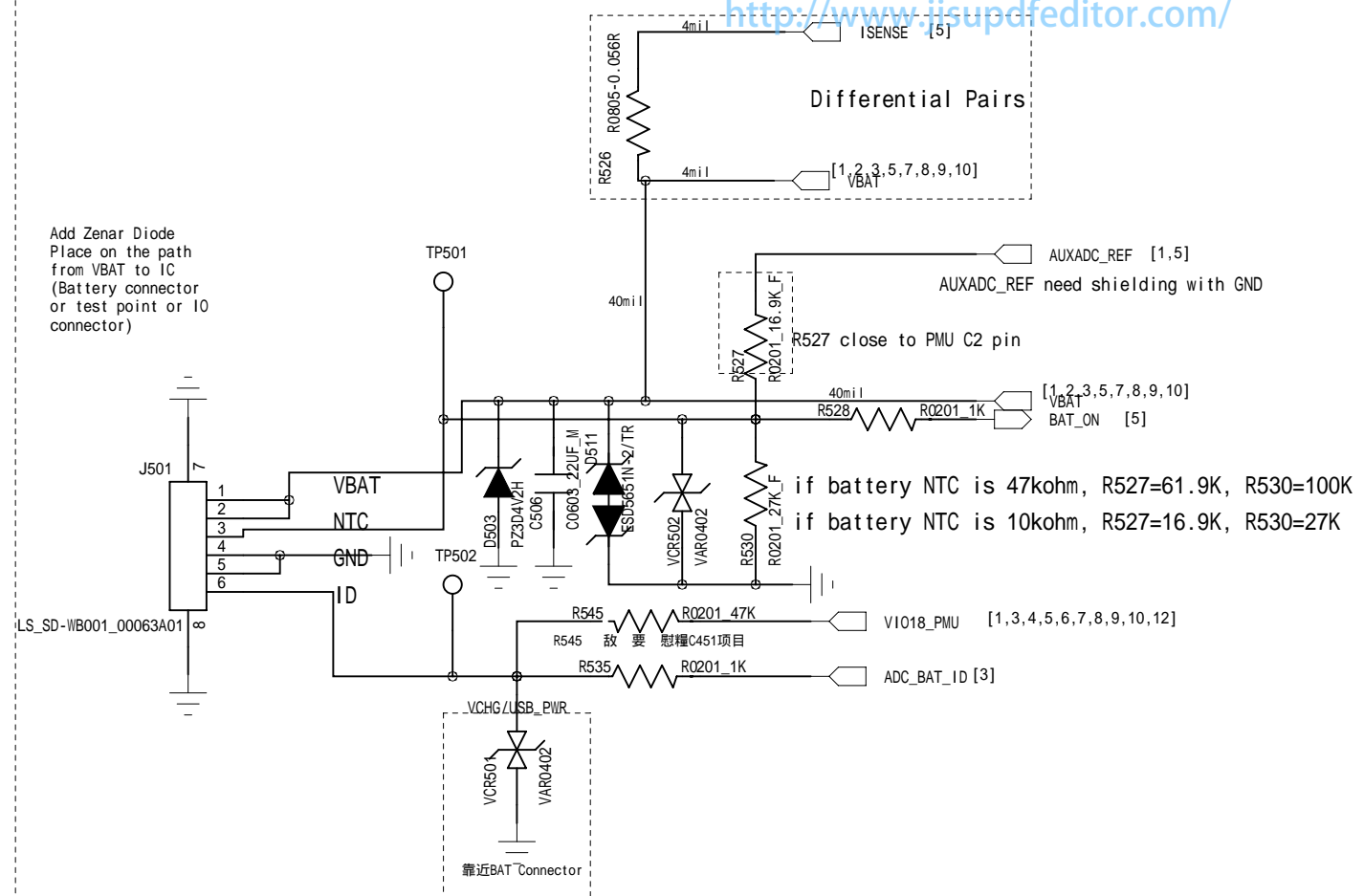


BATTERY CONNECTOR

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eMMC+LPDDR3

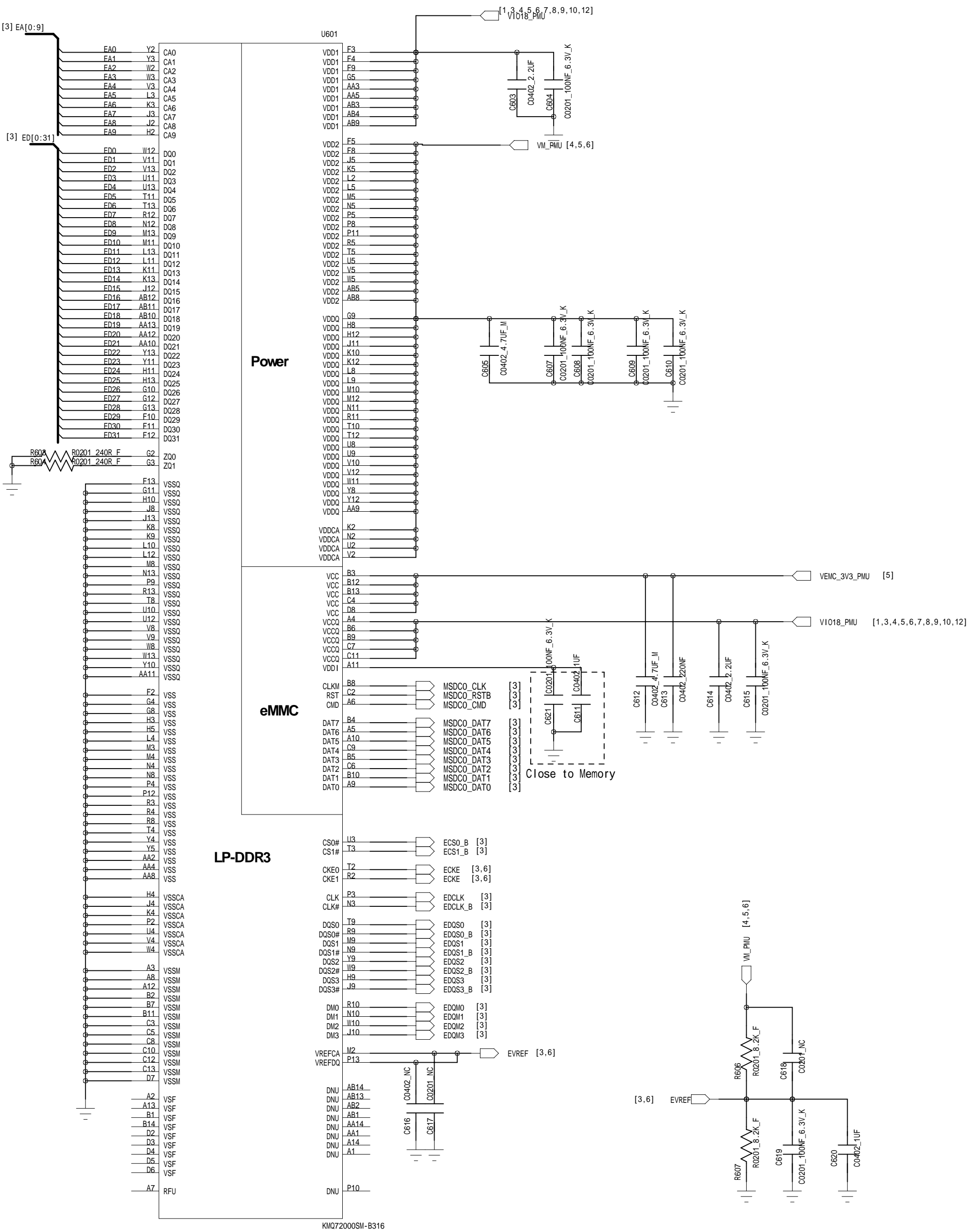
221 Ball, 0.5mm pitch

VDD1=1.8V

VDD2=1.20V

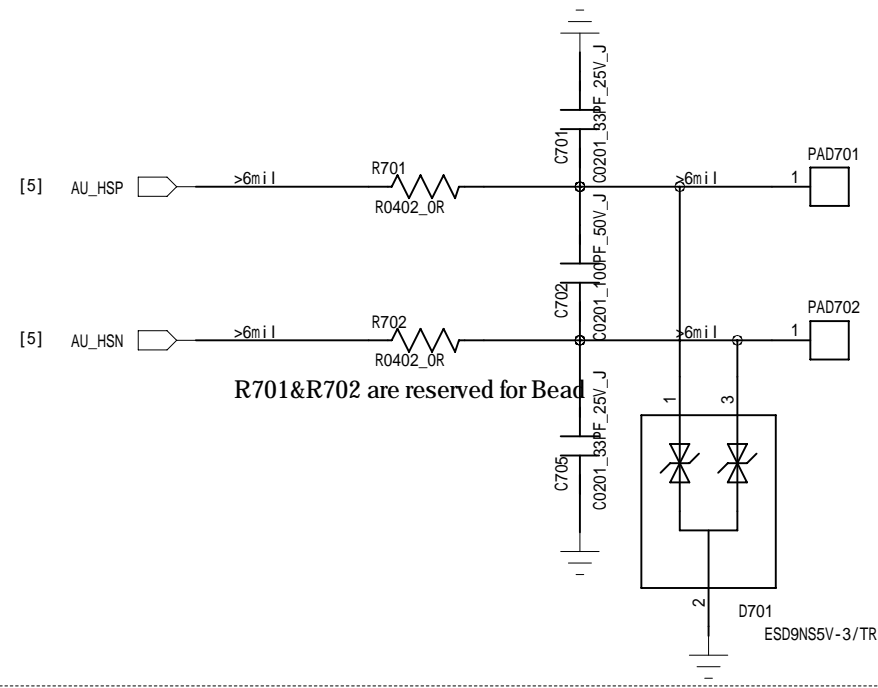
VDDCA=1.2V

VDDQ= 1.20V

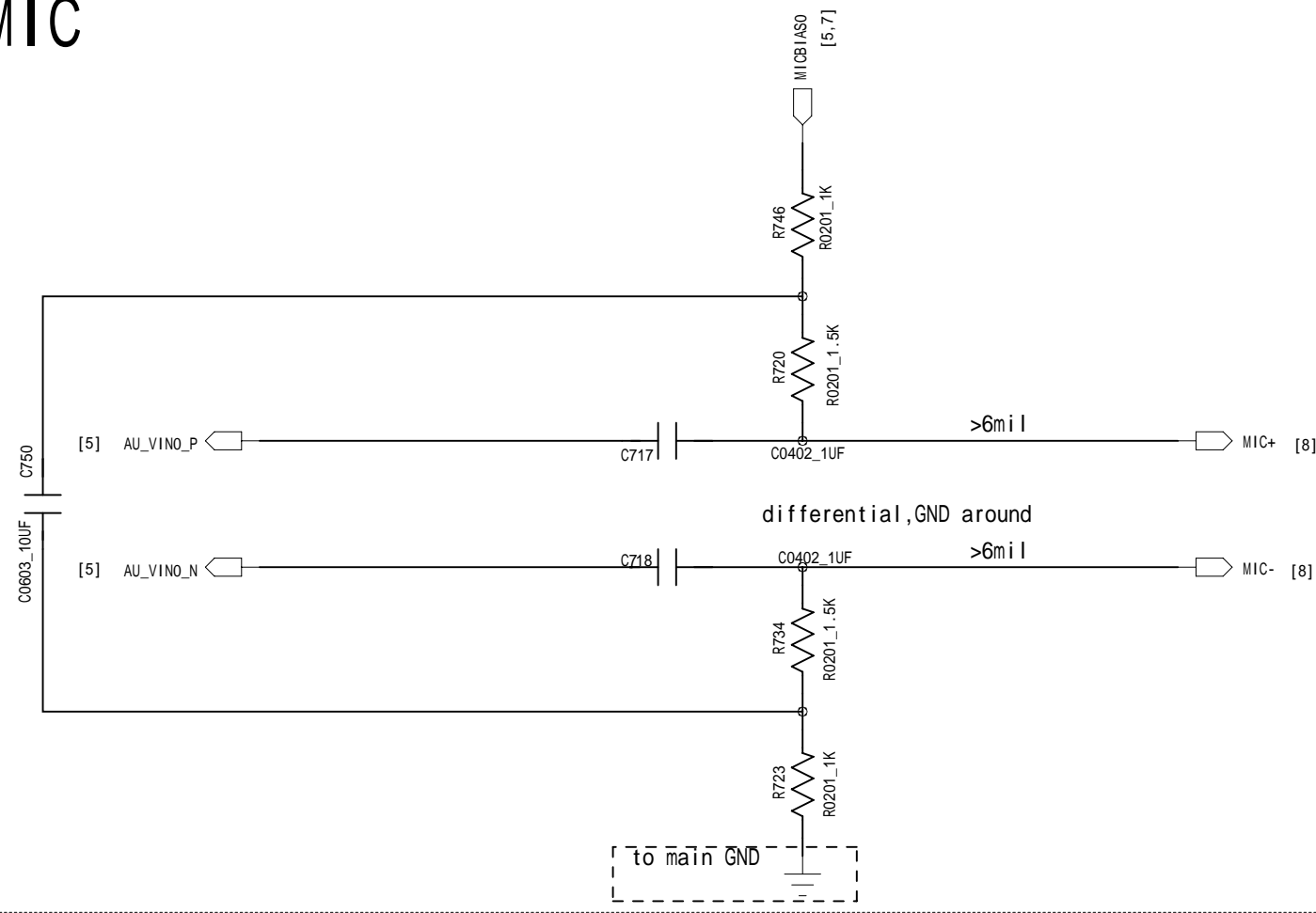


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CHECKED		DATED				

RECEIVER



MAIN MIC



EAR MIC

EAR MIC

Close to PMU

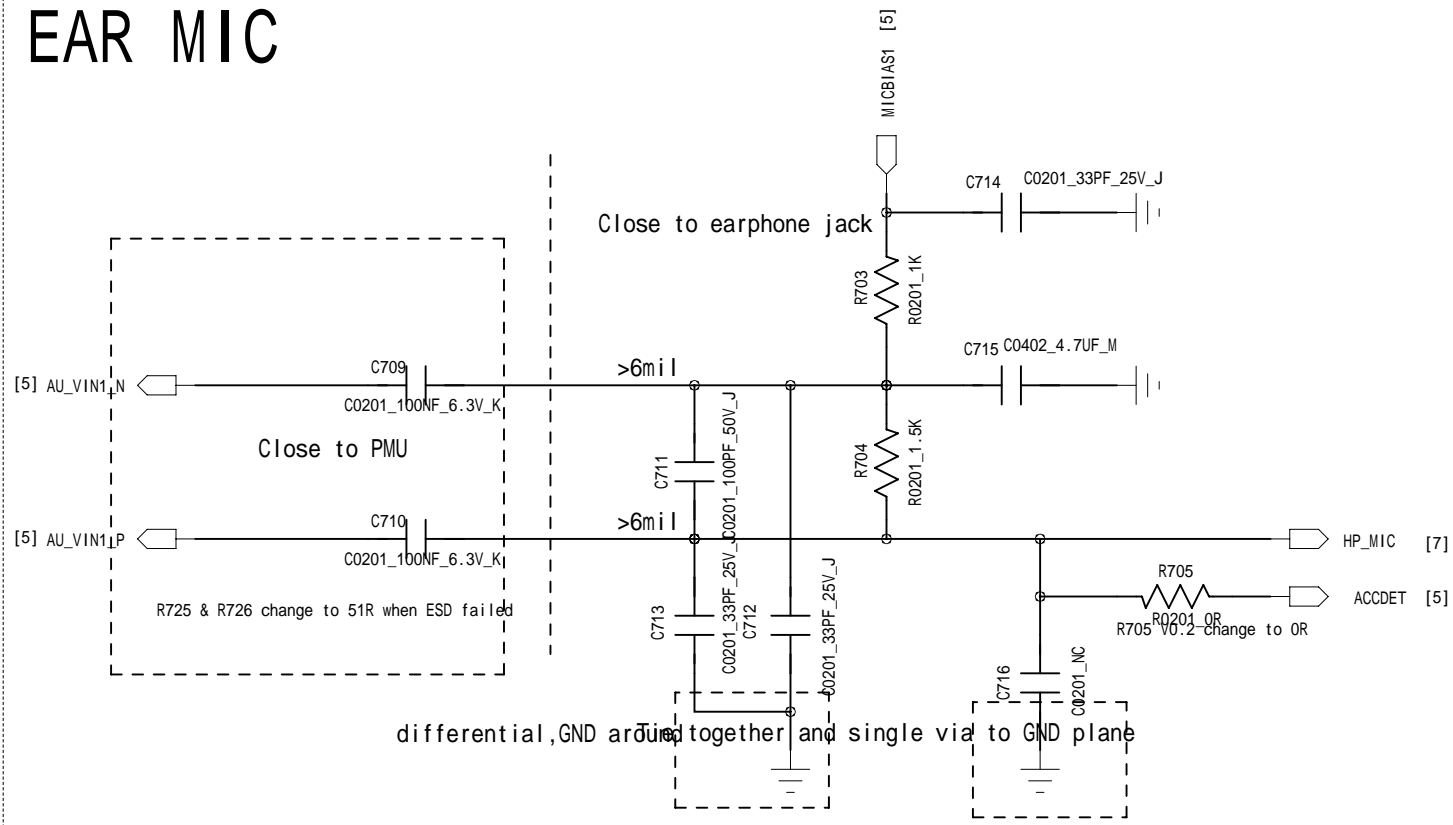
Close to earphone jack

HP_MIC [7]

ACCDET [5]

Notes:

- R725 & R726 change to 51R when ESD failed
- differential, GND are tied together and single via to GND plane



AUDIO PA

VBAT

R0402_OR
R730

C731

C0201_100NF_6.3V_K

C707

C0402_2.2UF

[5, 7] AU_HPL

C708

C0201_22NF

R719

R0201_OR

A1

C1

C729

C0201_22NF

R707

R0201_OR

C2

[3] SPKPA_EN_GP1012

R709

R0201_100K

C732

C0402_1UF

B2

VREF

U701

AW8145CSR

NC

A3

VOP

A2

VDD

B3

P_GND

A_GND

B

[5] SPK_P

[5] SPK_N

B0603_PZ1608U221
B701

B0603_PZ1608U221
B702

C742

C0201_1NF

C757

C0201_100PF_5MV_J

C719

C0201_33PF_25V_J

C743

C0201_1NF

VCR704

VAR0402_NC

VCR705

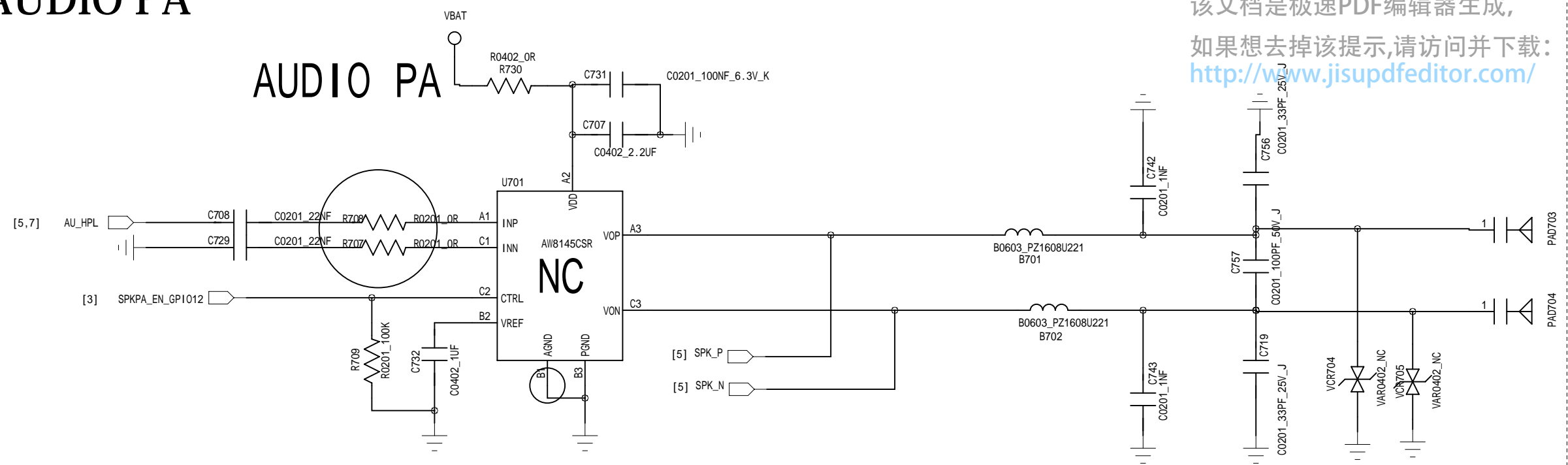
VAR0402_NC

VAR0402_NC

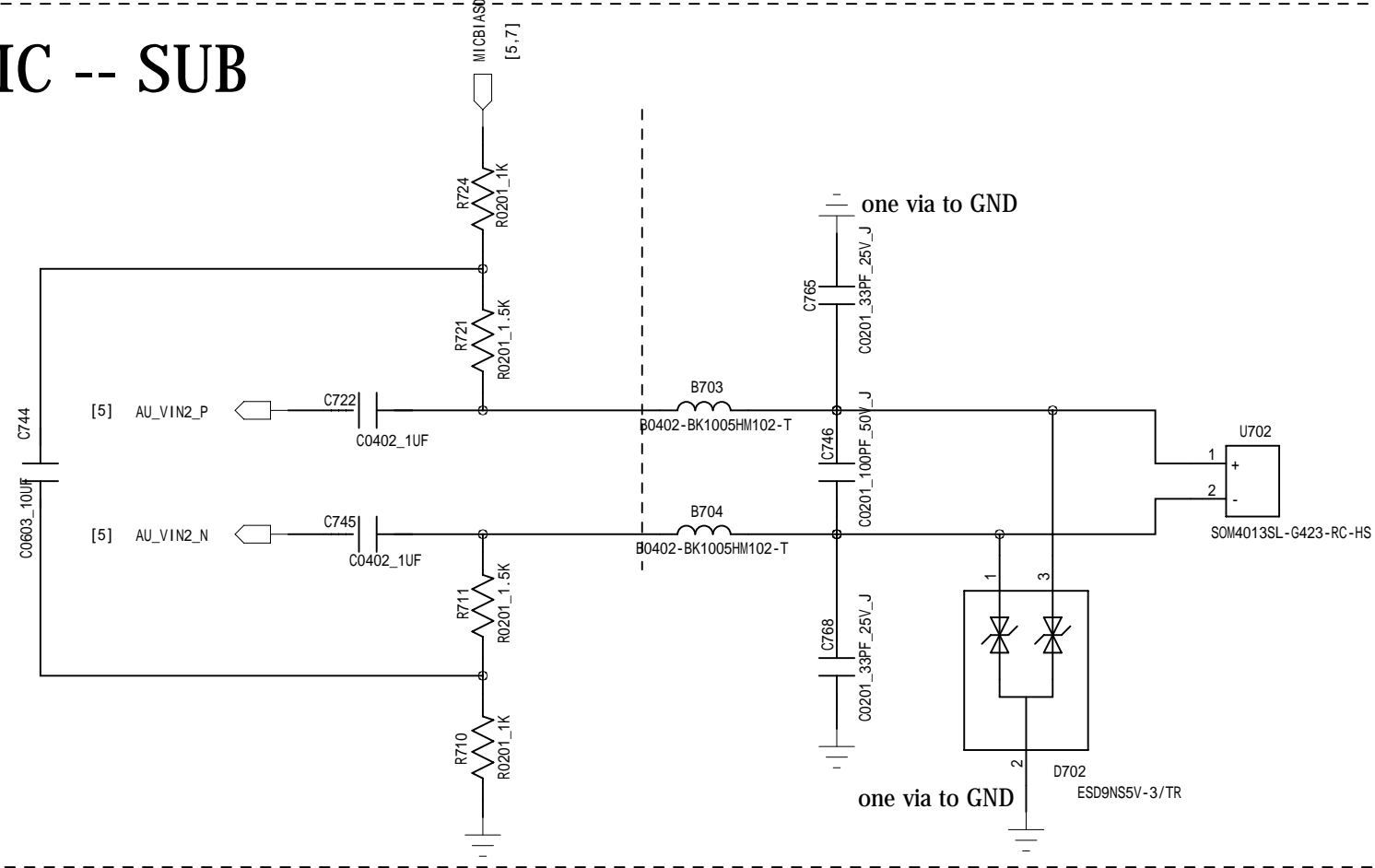
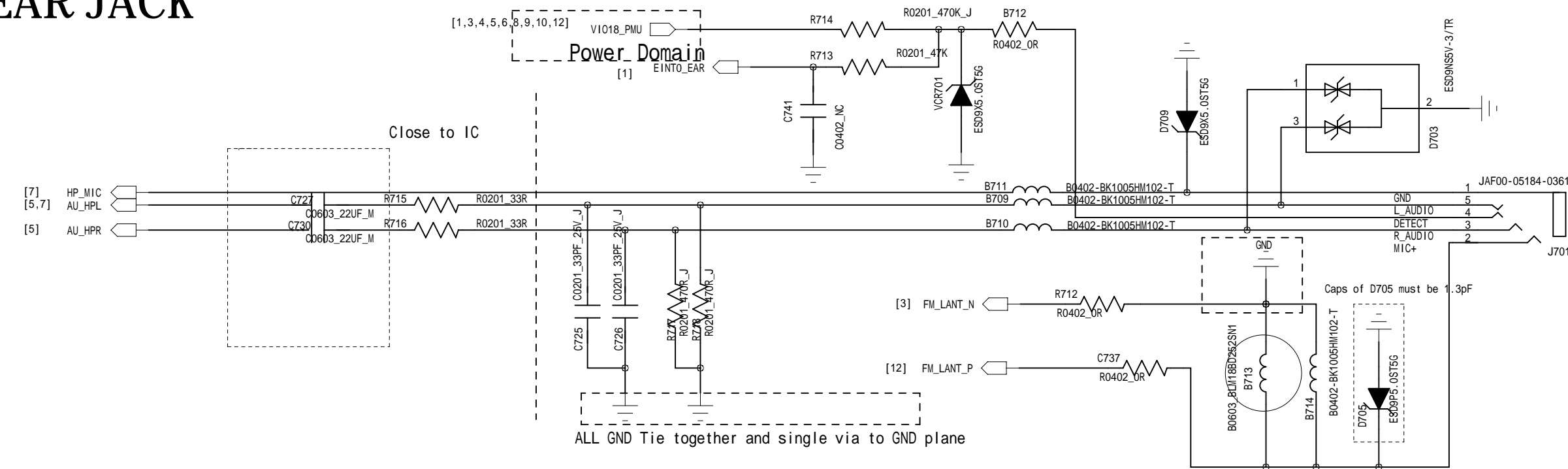
PA0703

PA0704

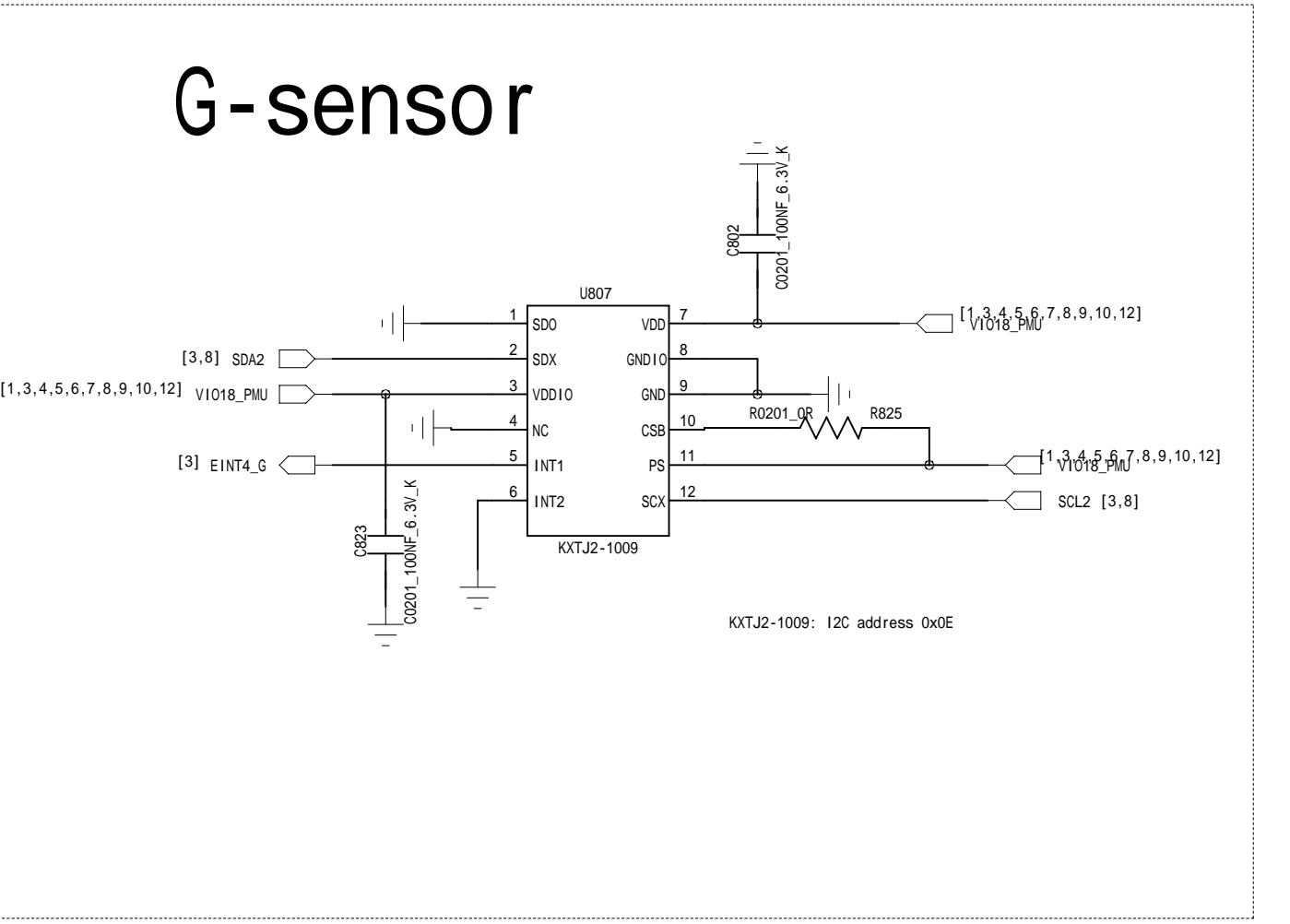
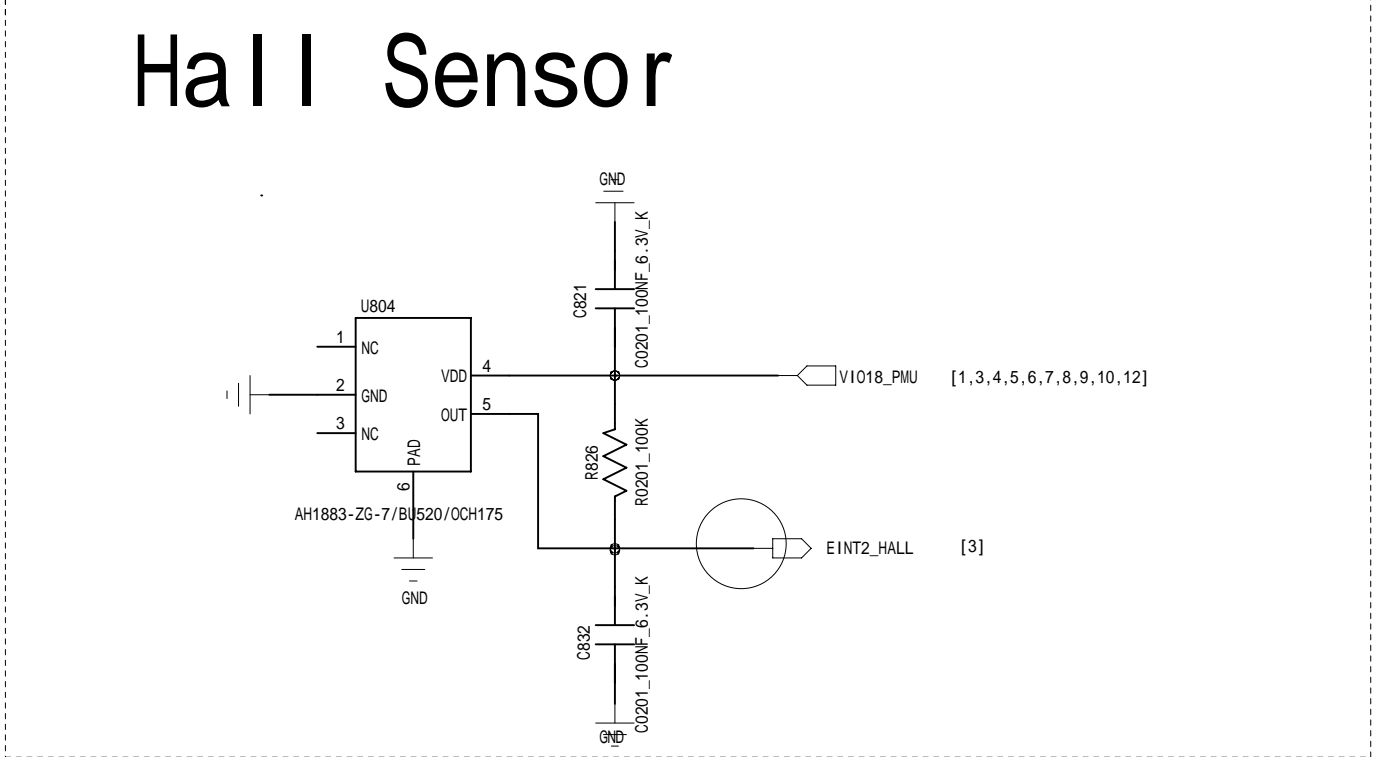
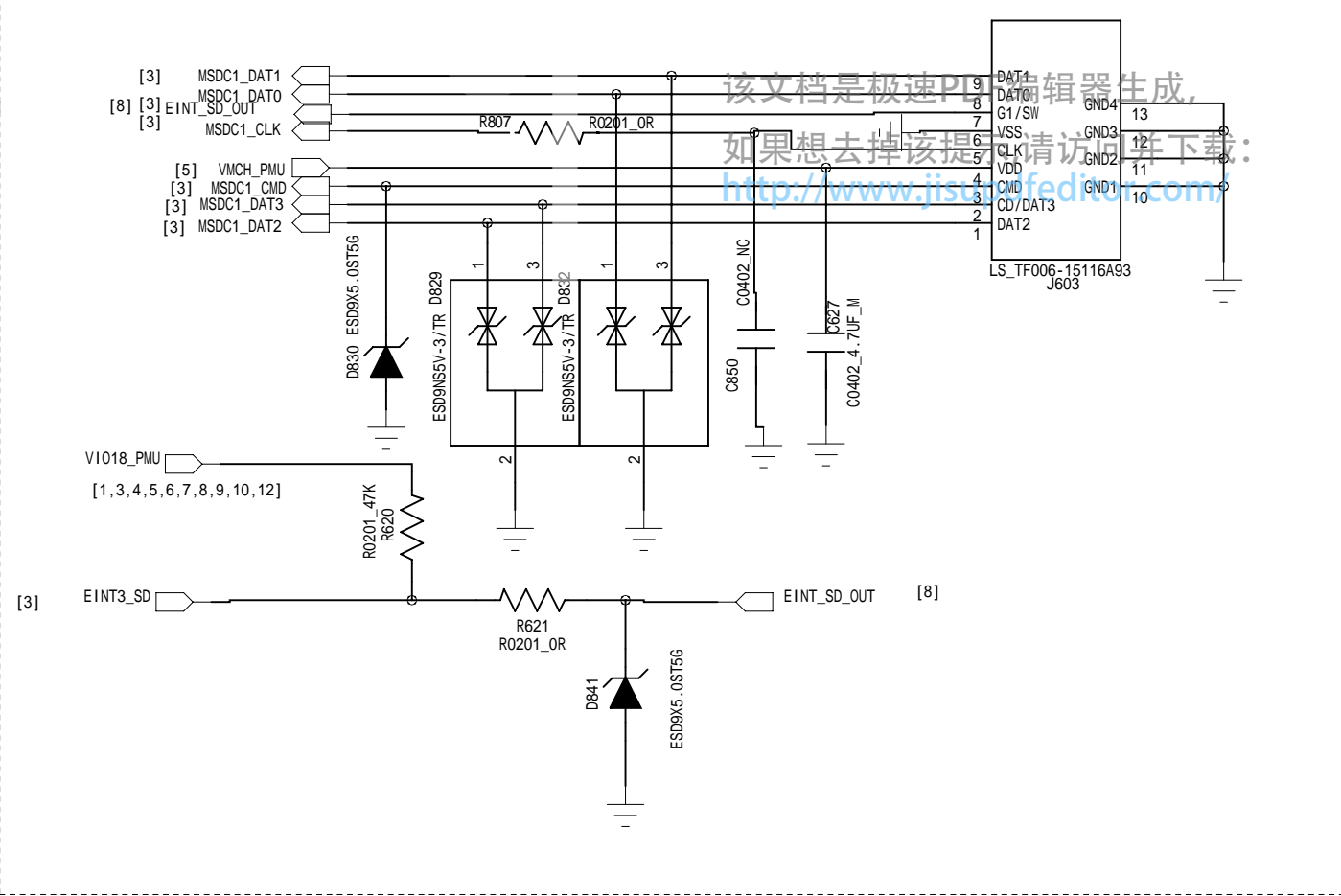
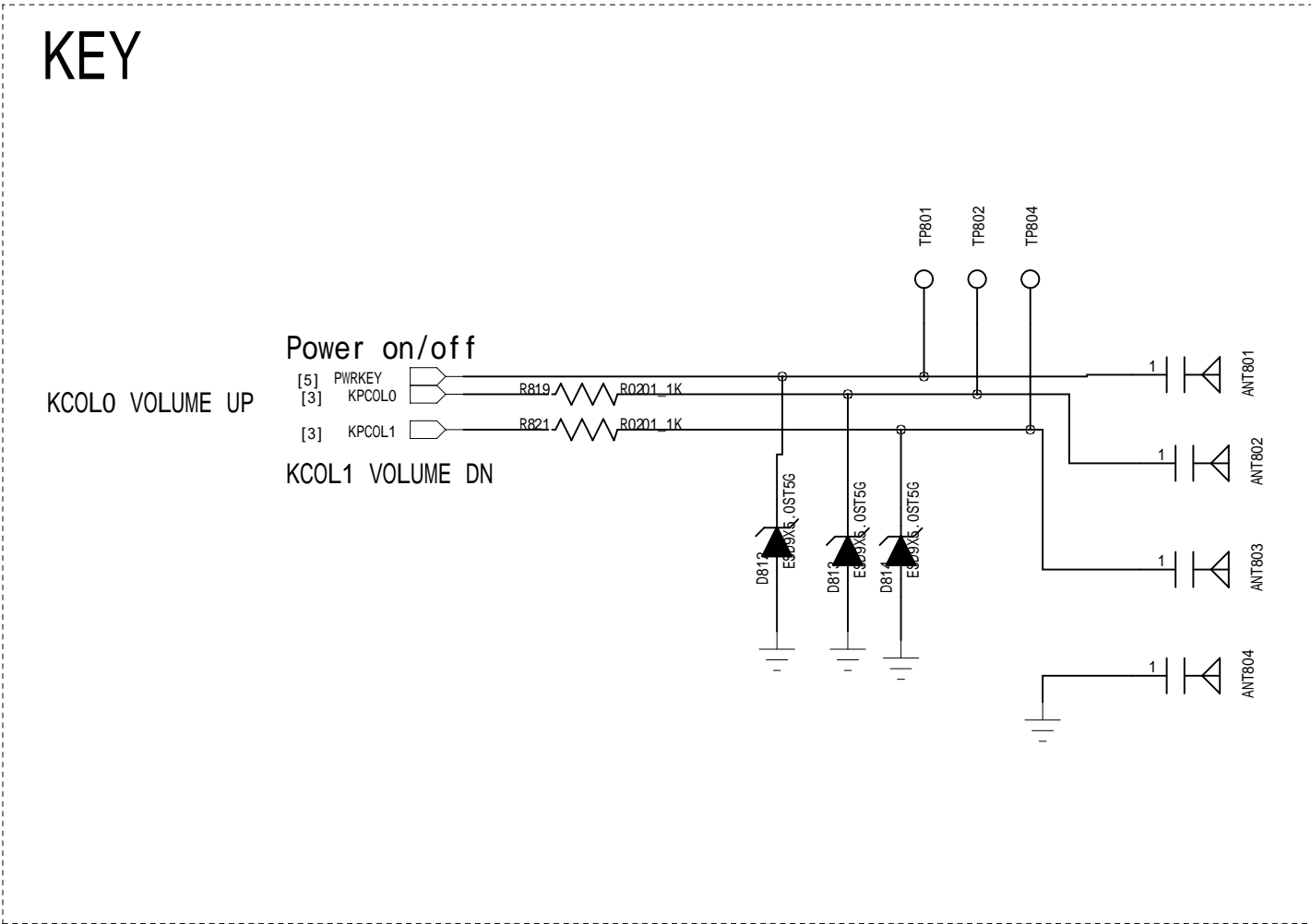
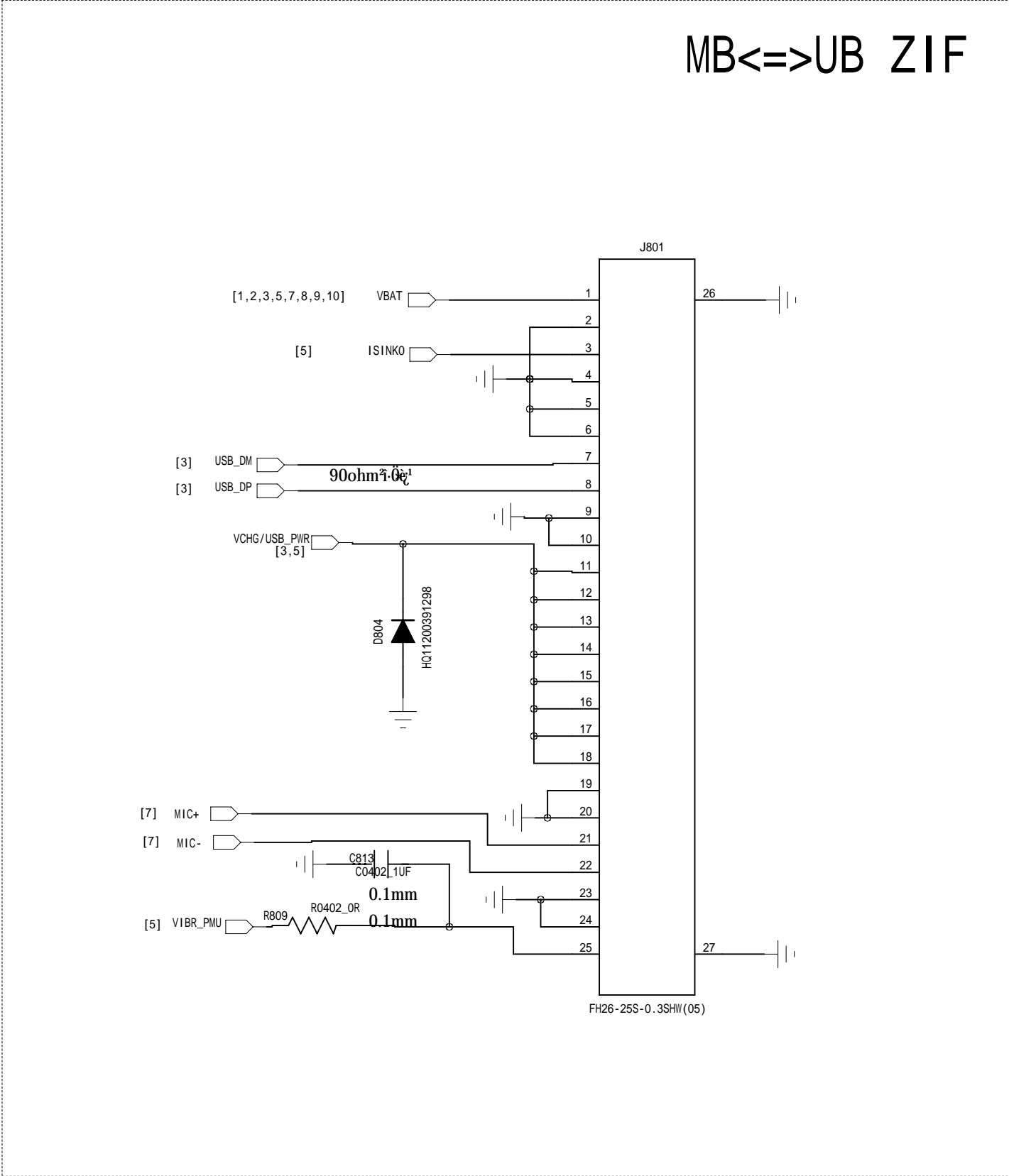
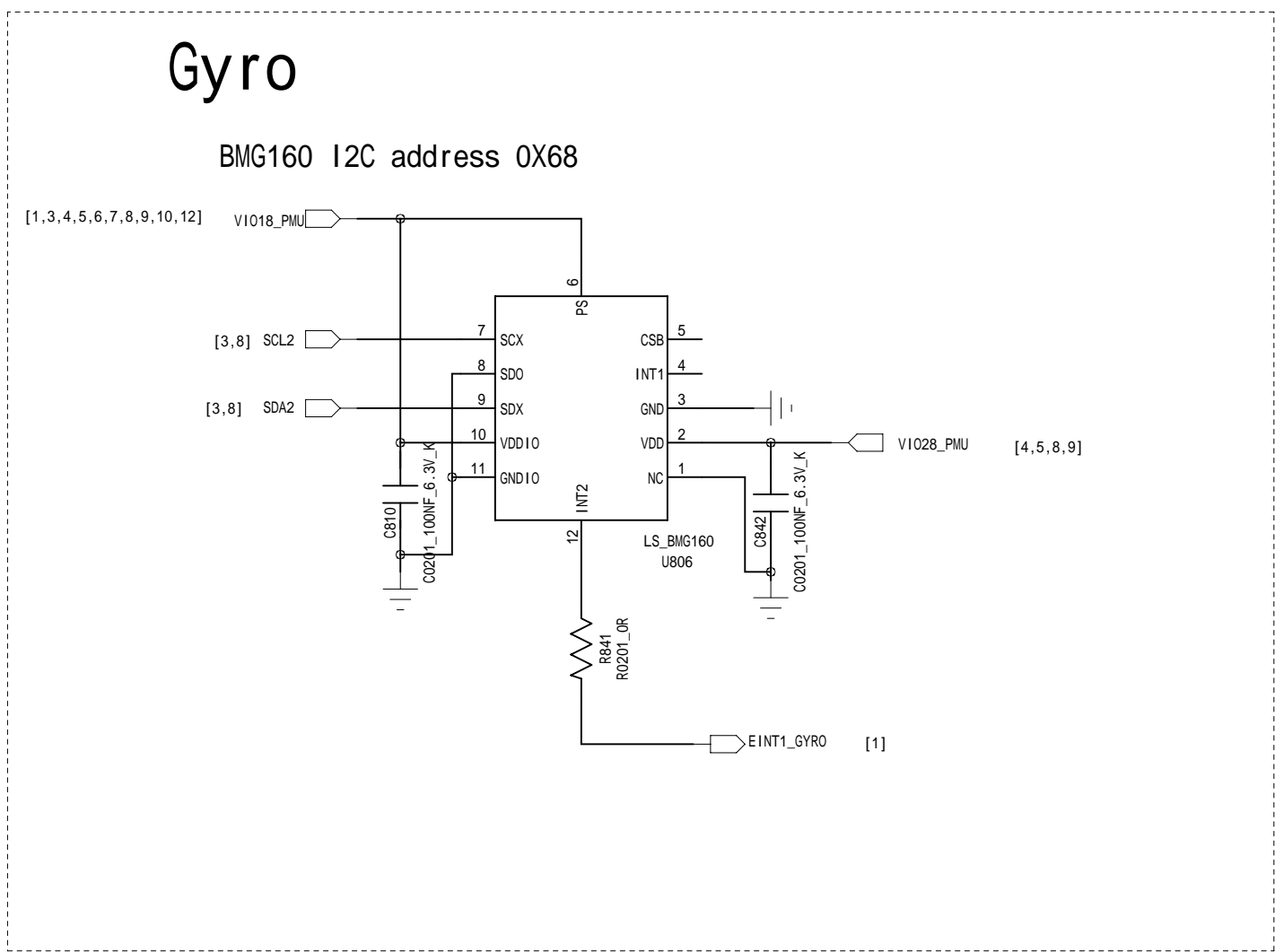
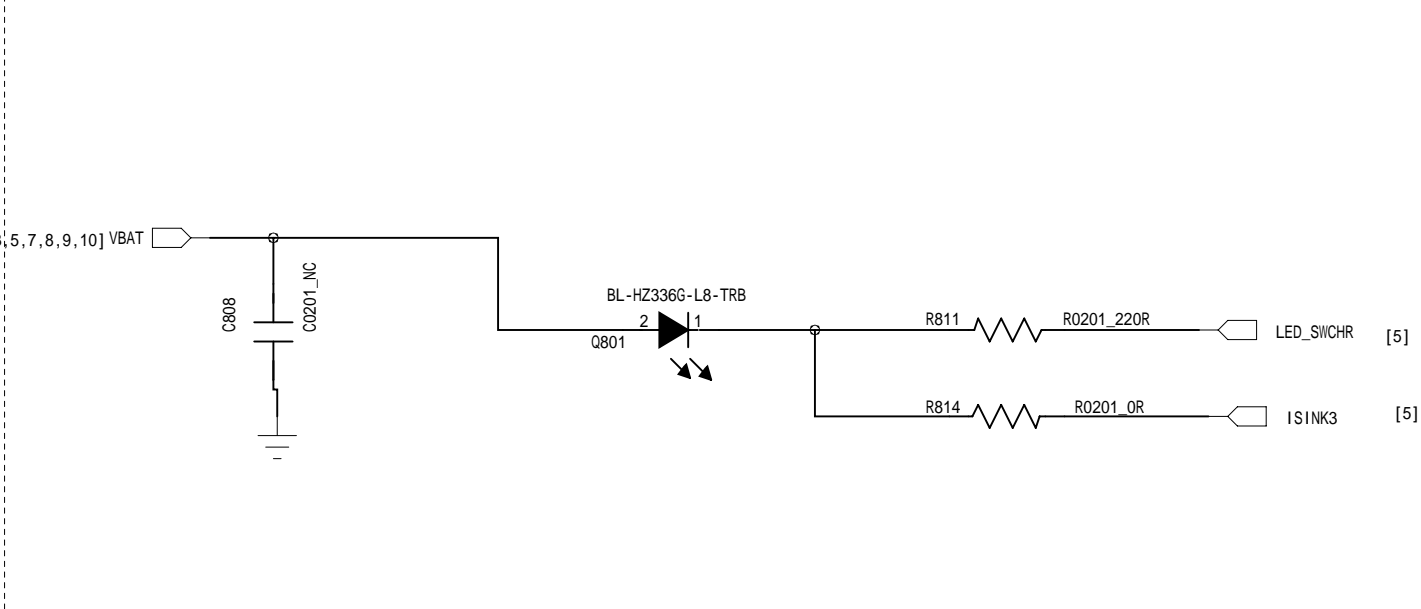
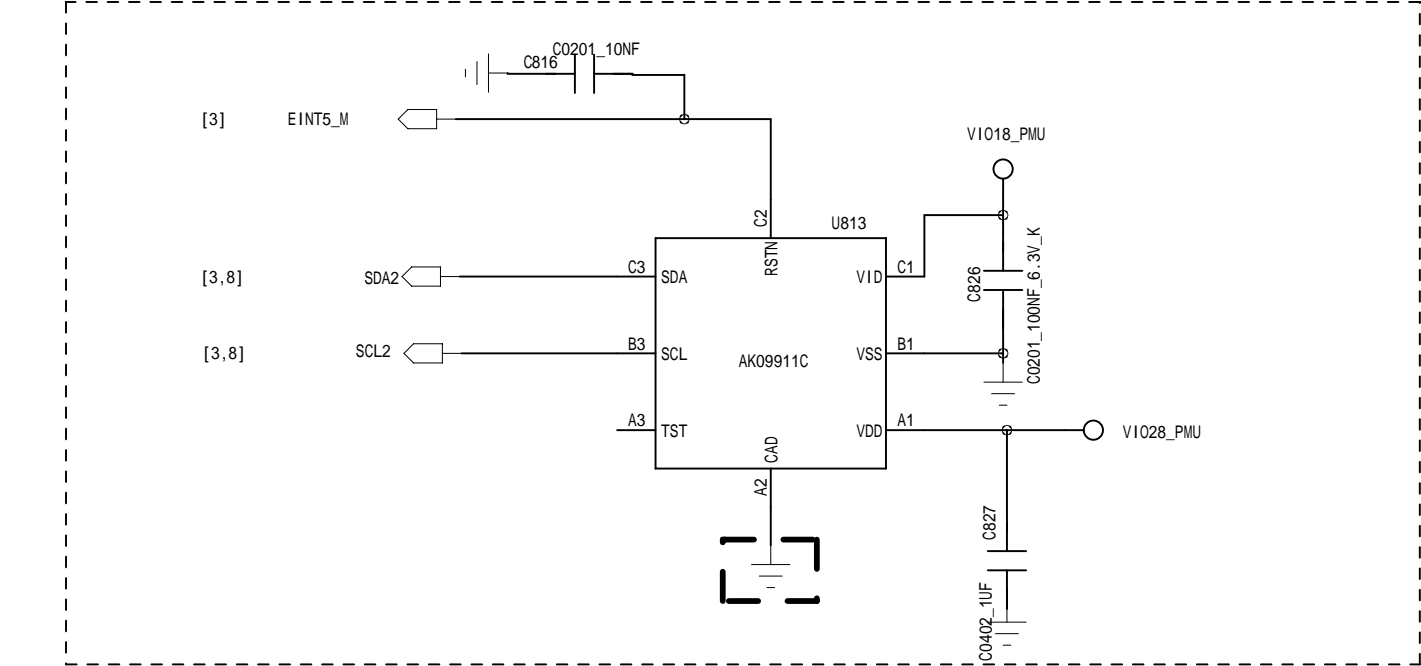
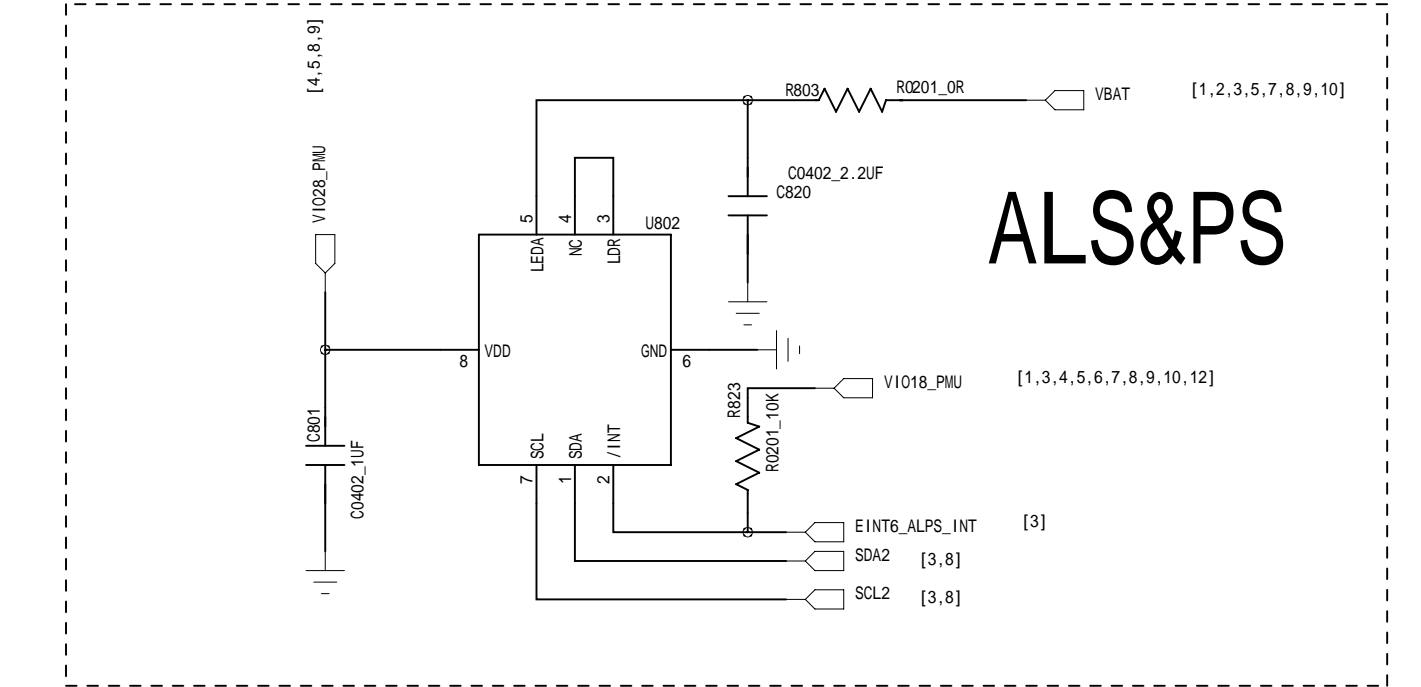
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MIC -- SUB

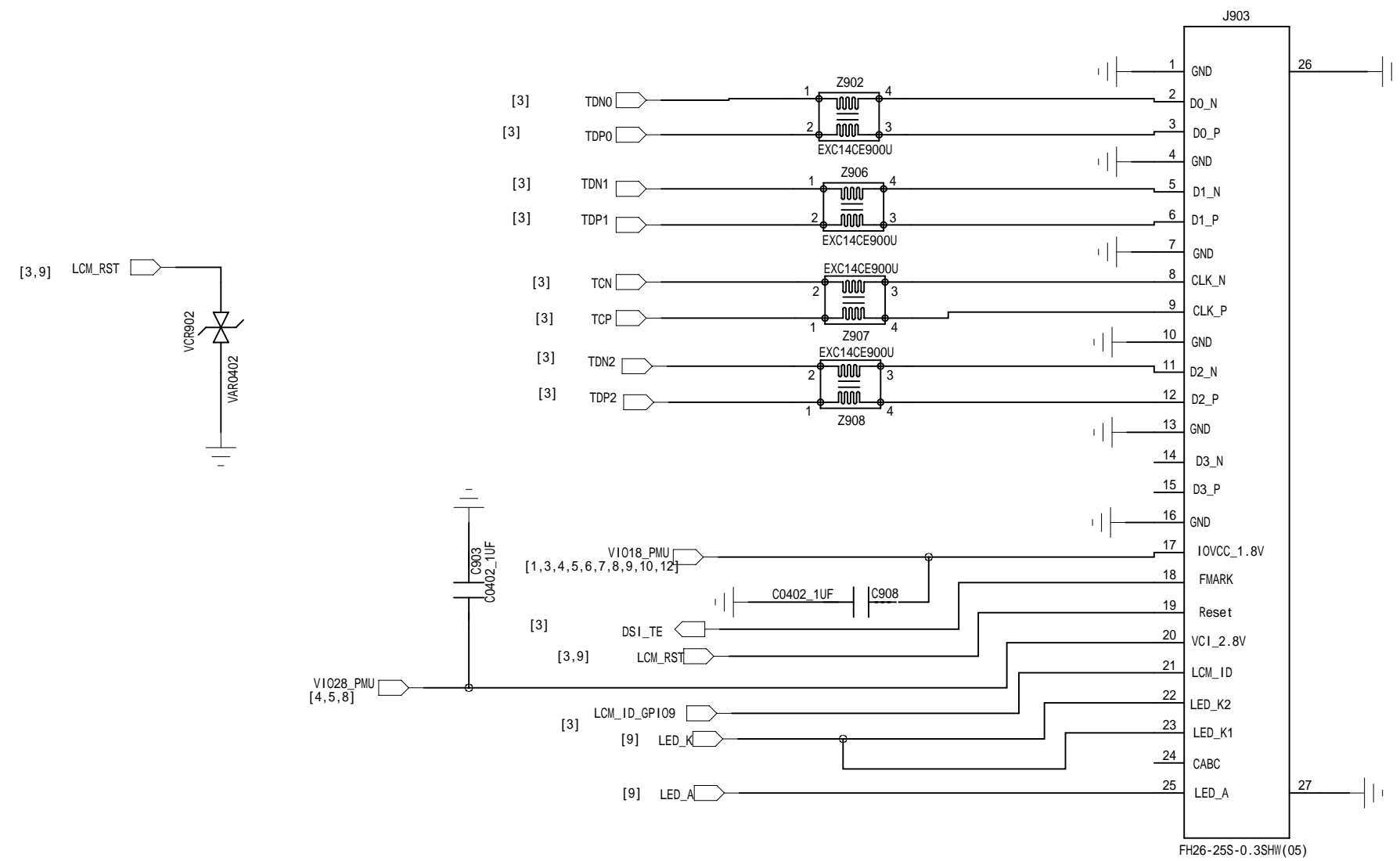
[illegible]

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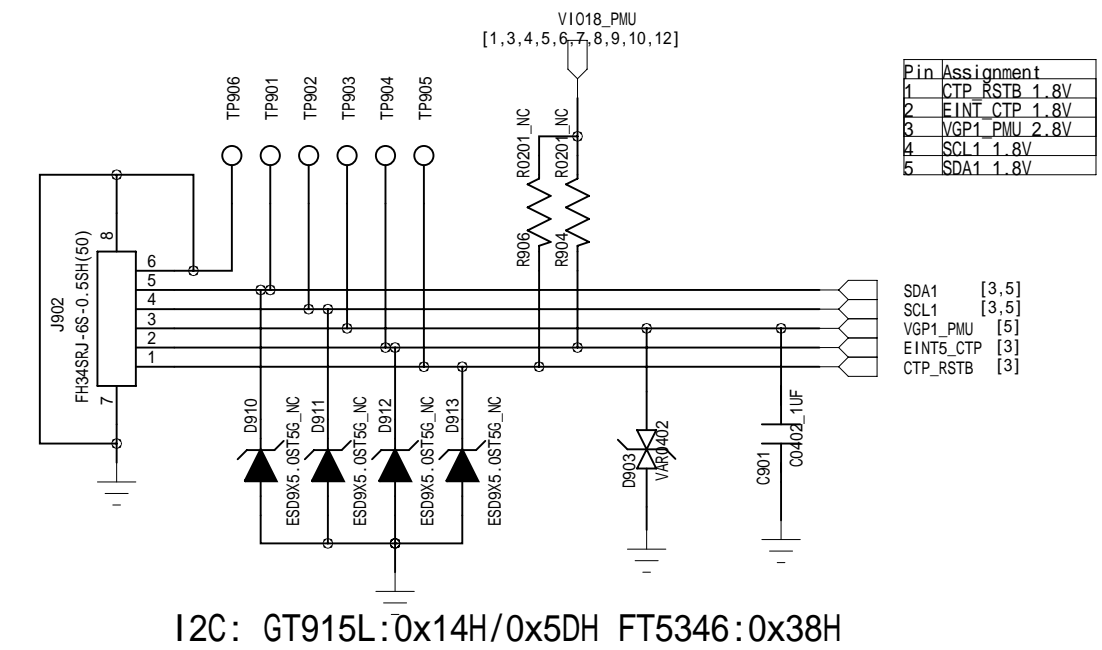


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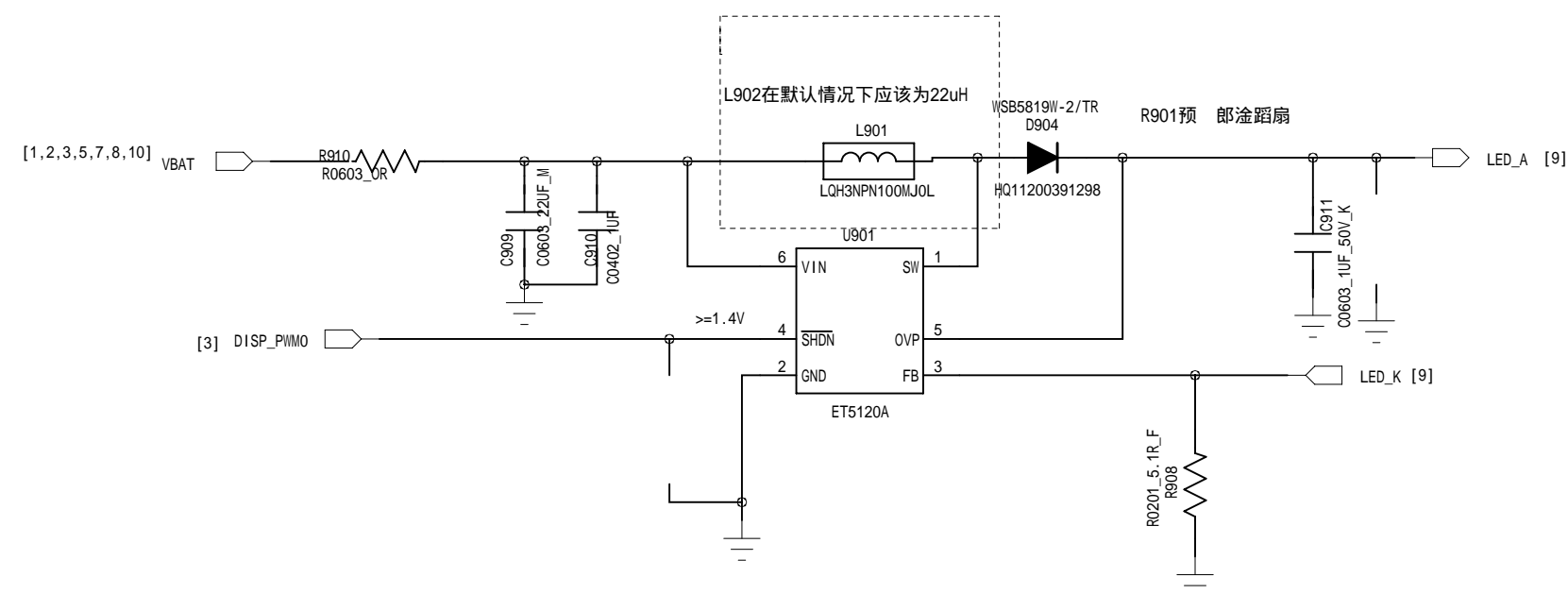
LCM



CTP COF Interface

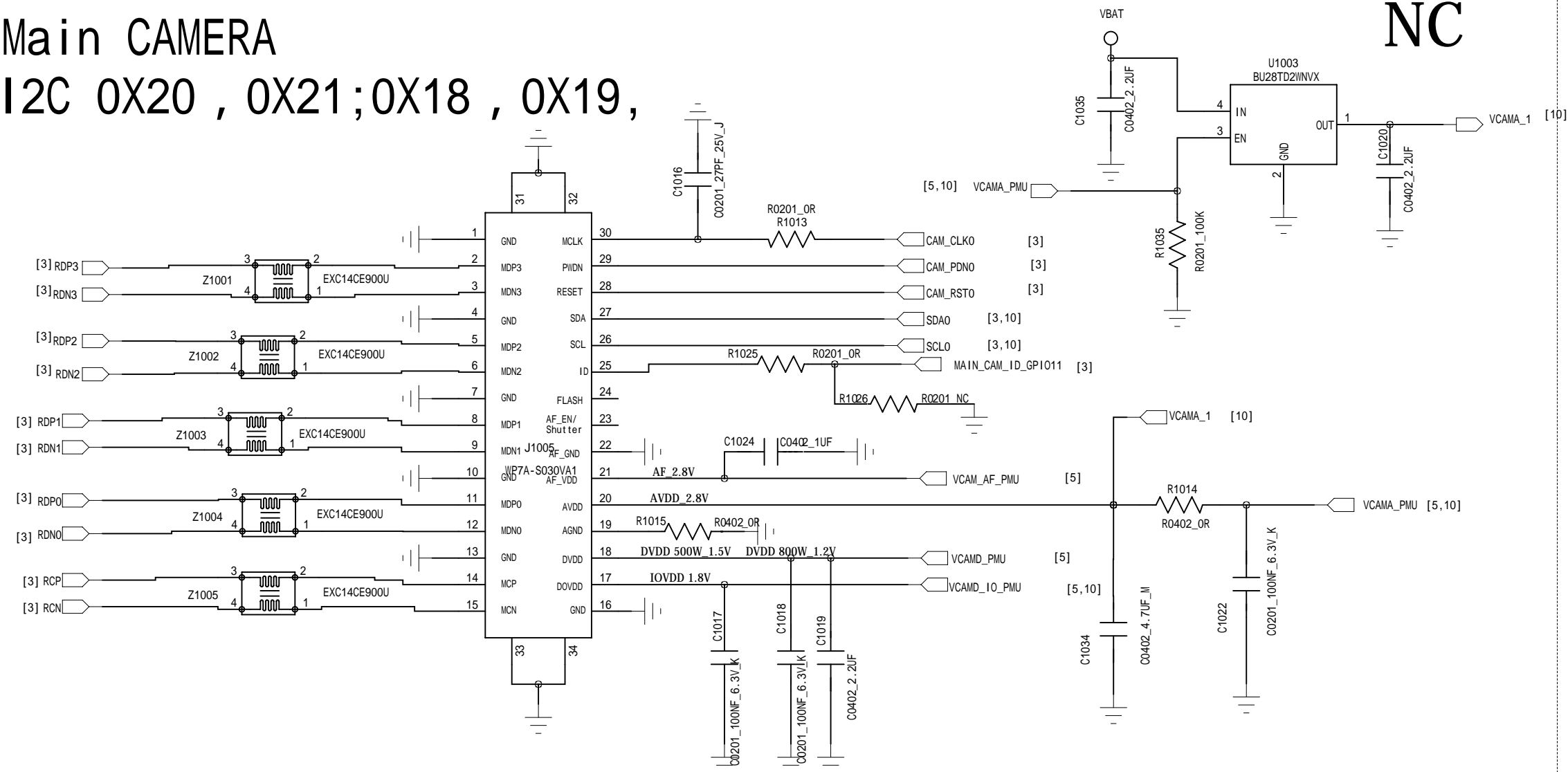


LCD BL Driver



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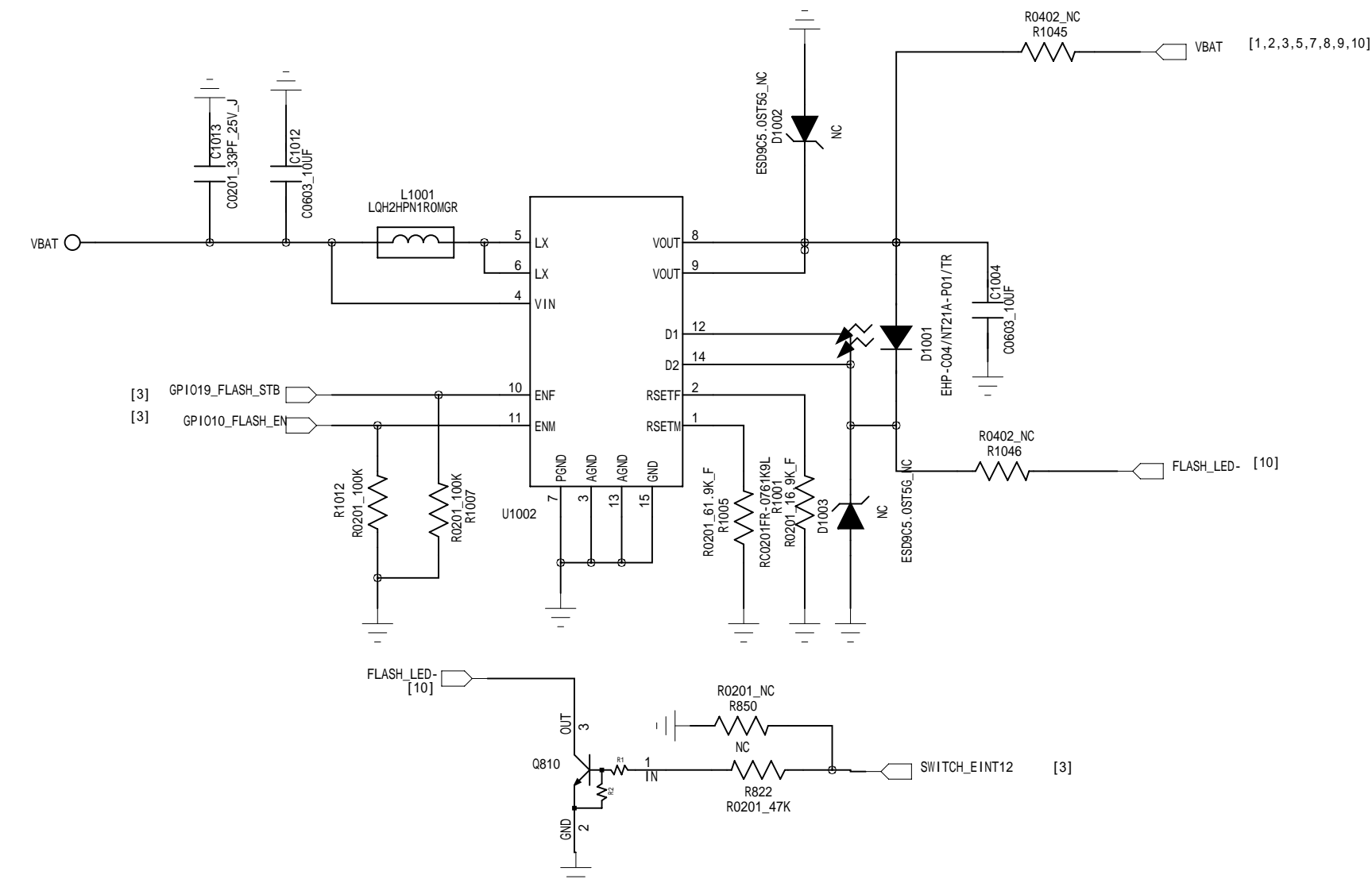
Main CAMERA
I2C 0X20 , 0X21;0X18 , 0X19,



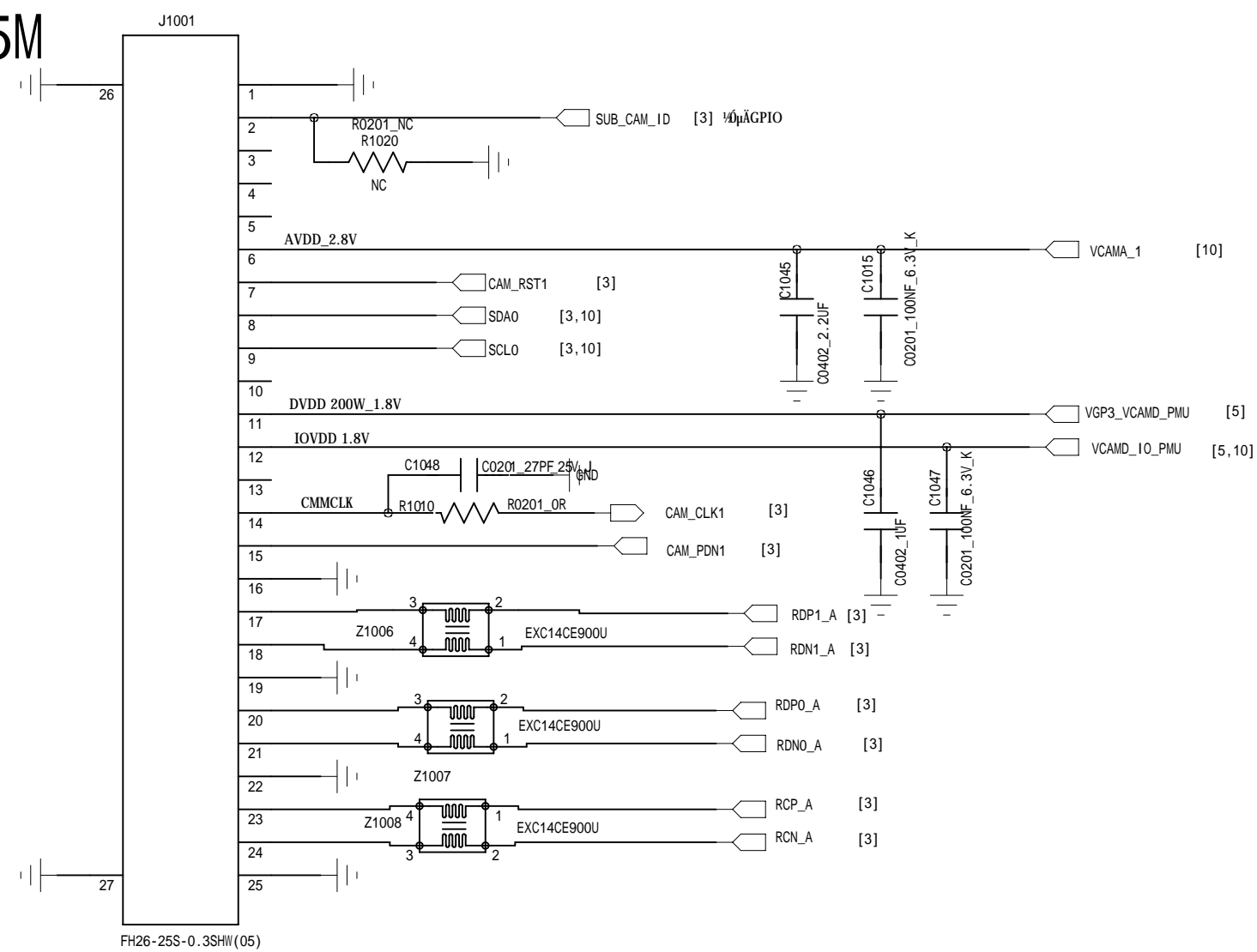
Flash LED Driver

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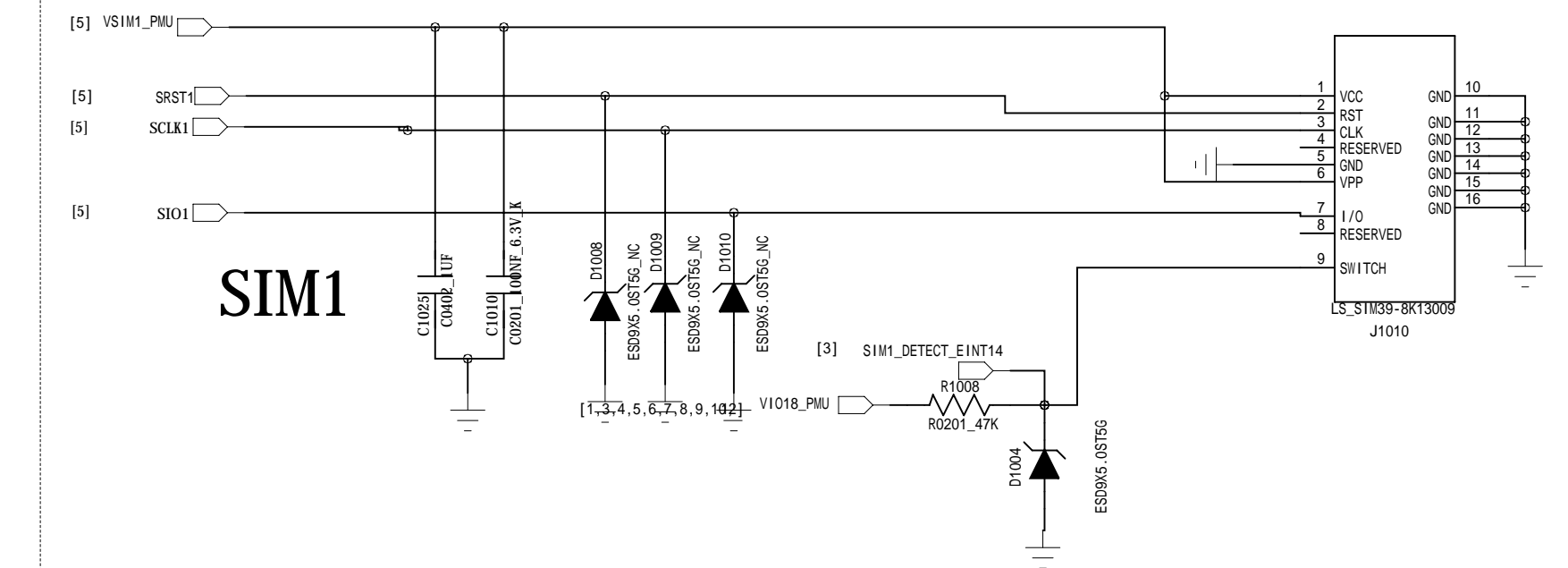
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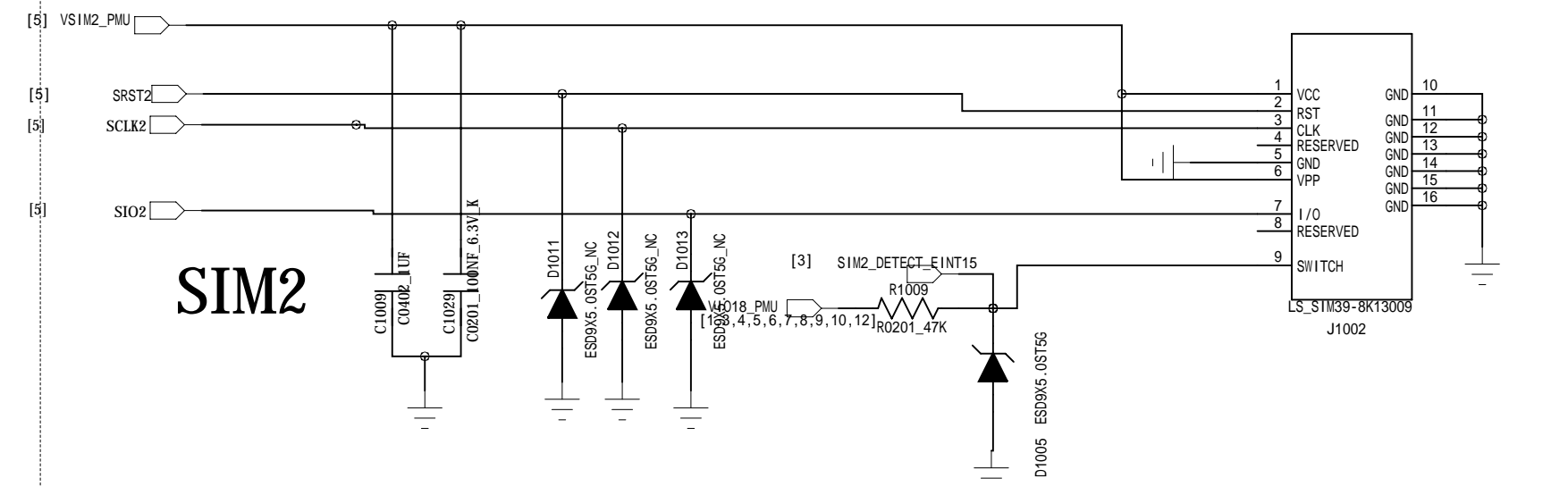
Sub Camera 2M/5M



SIM1

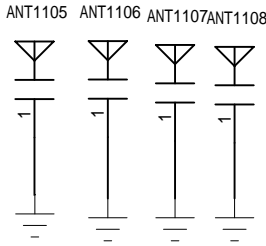


SIM2

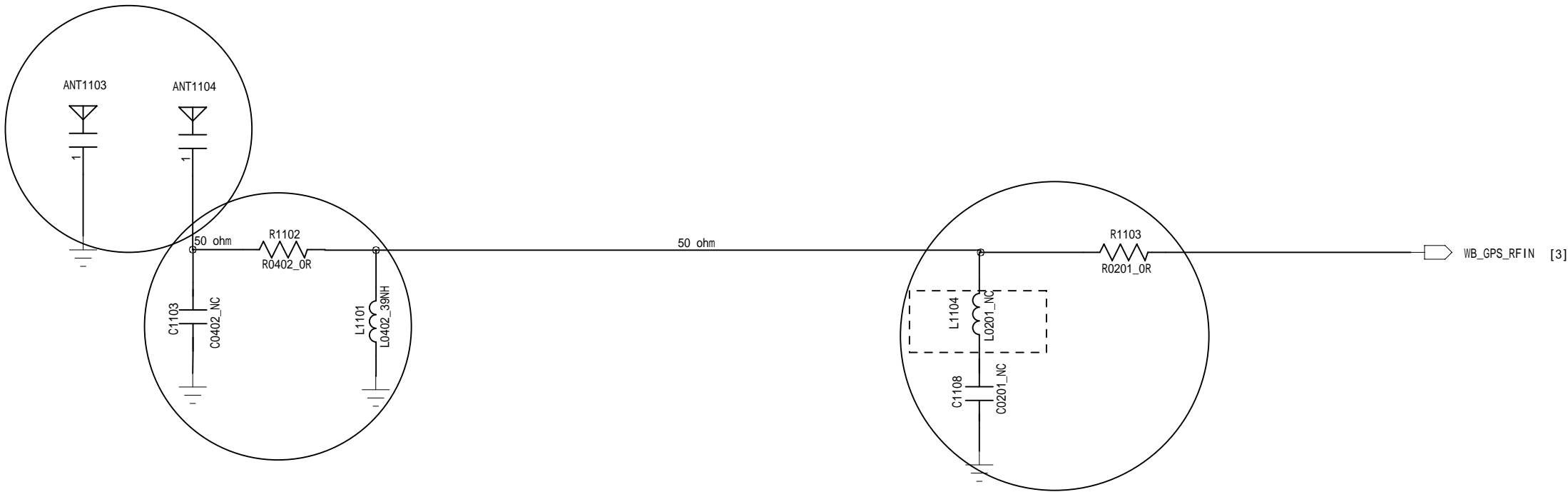


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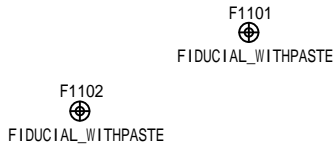
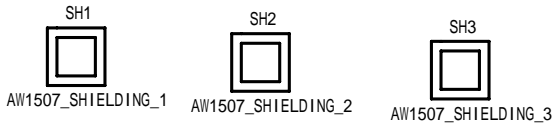
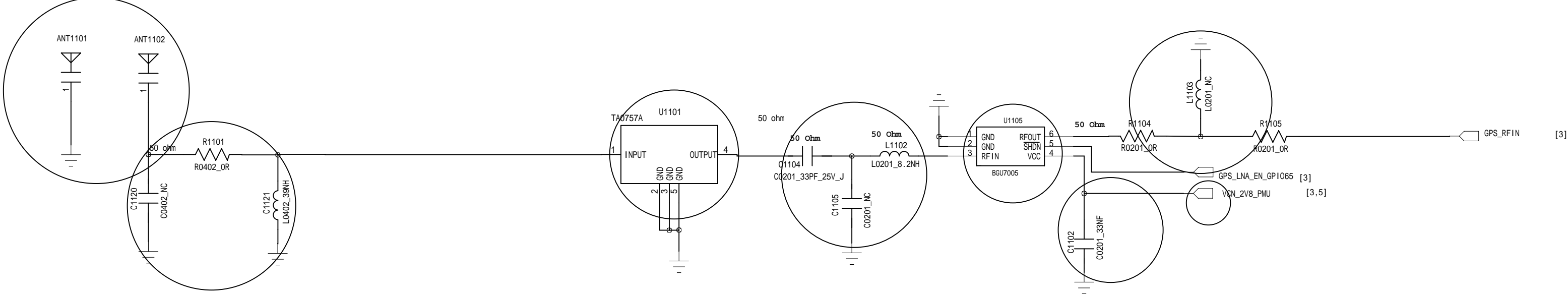
GPS/BT/WIFI



WIFI

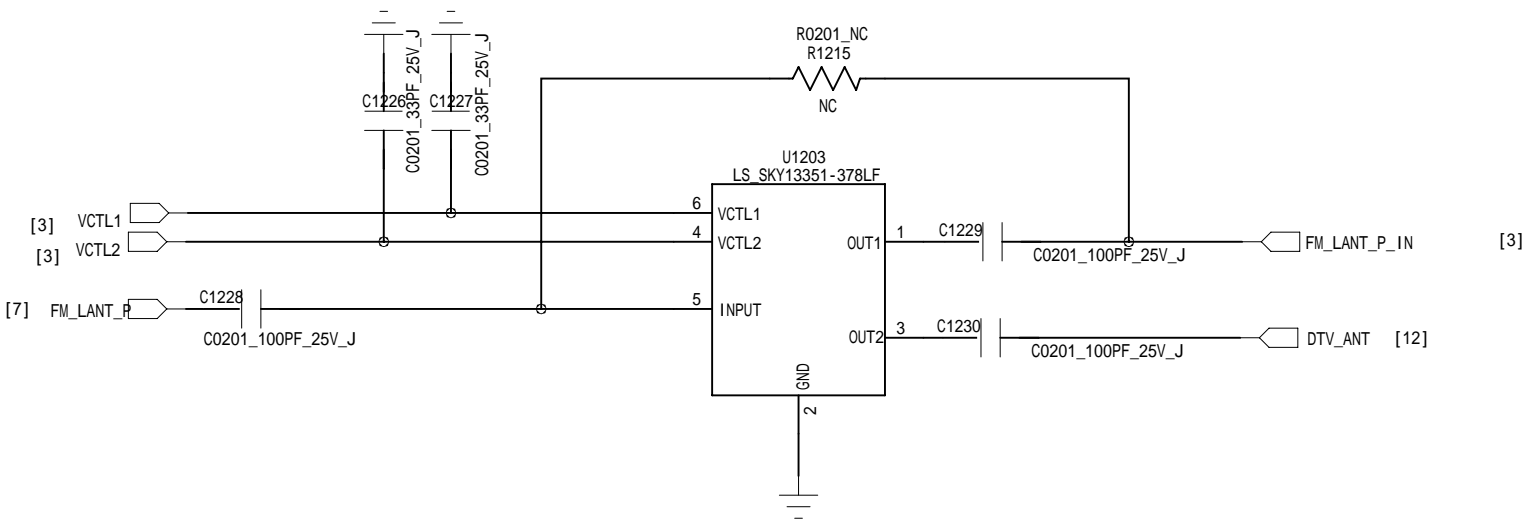


GPS



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CHECKED		DATED					

RF MAIN



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