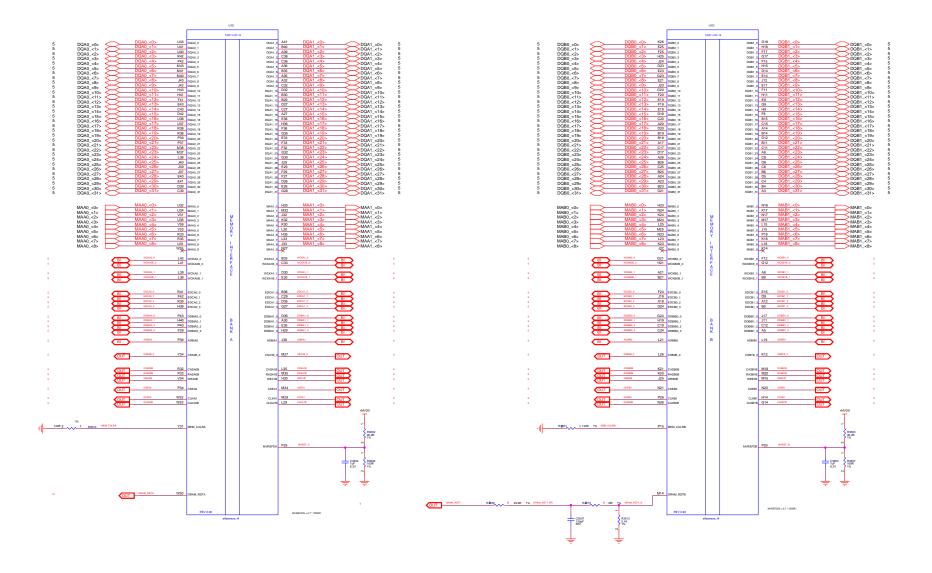


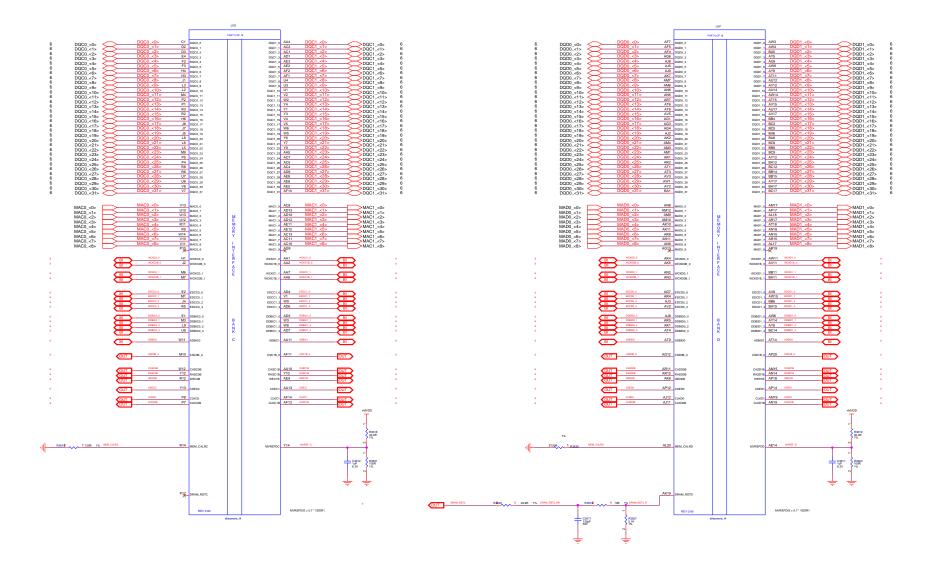


(3) ELLESMERE MEM INTERFACE CH A/B

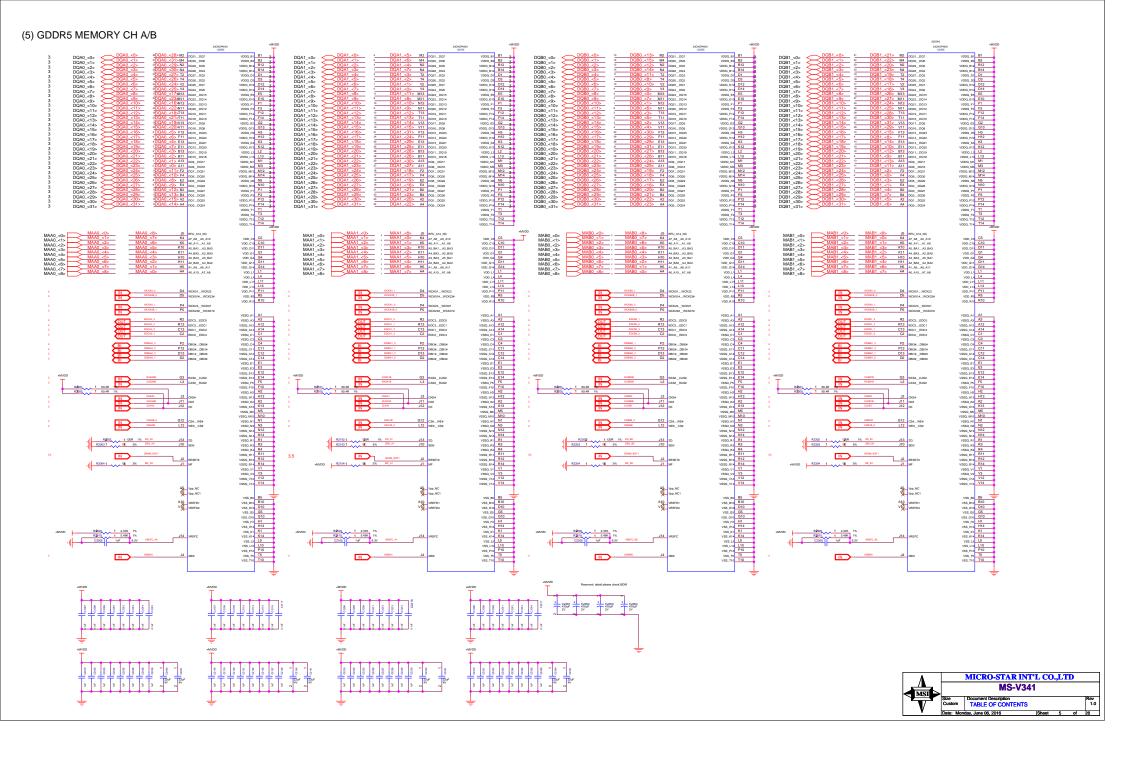


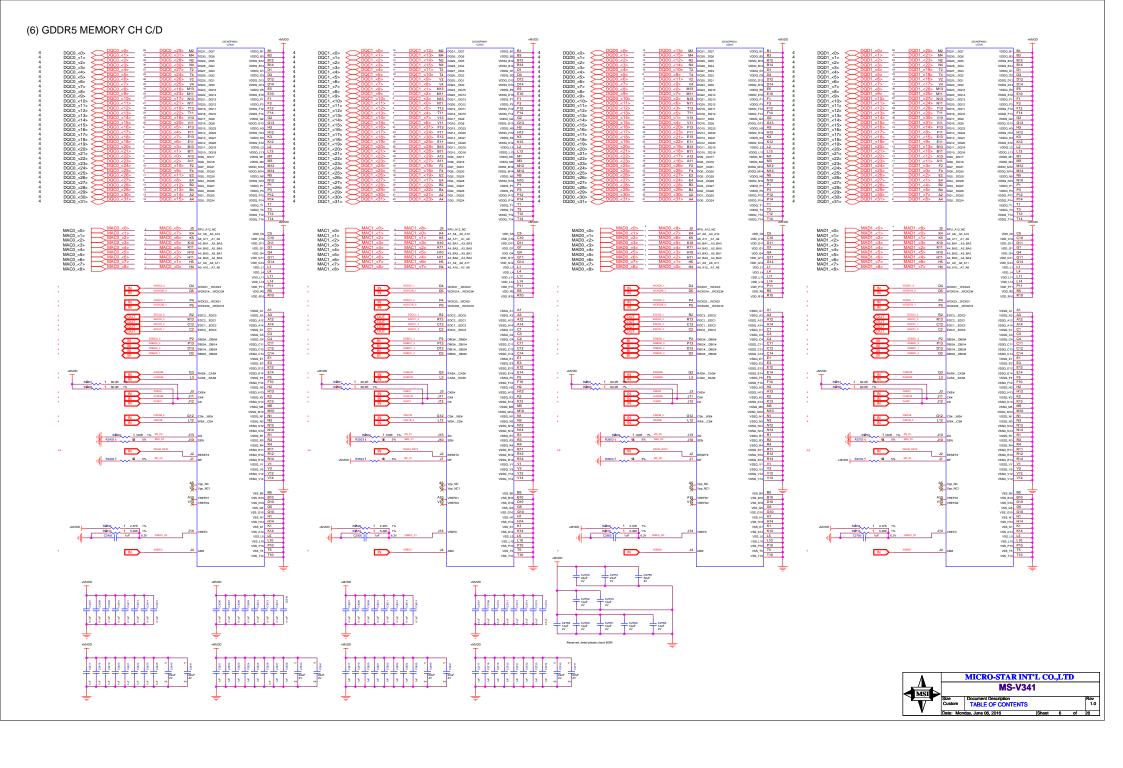


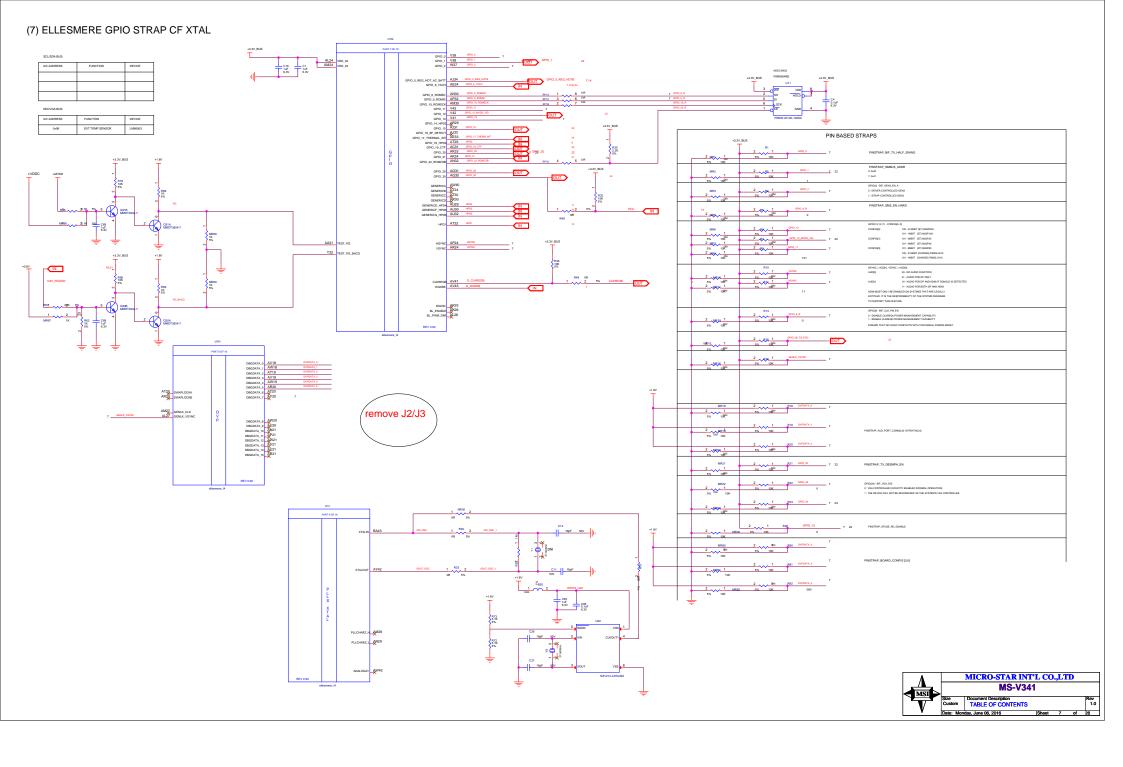
(4) ELLESMERE MEM INTERFACE CH C/D

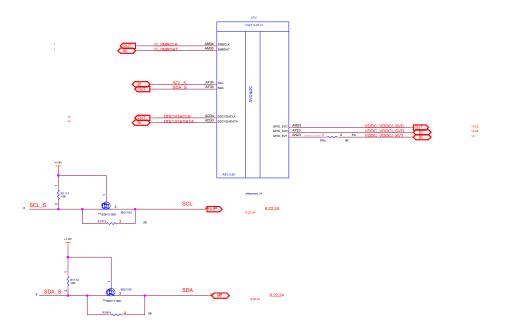






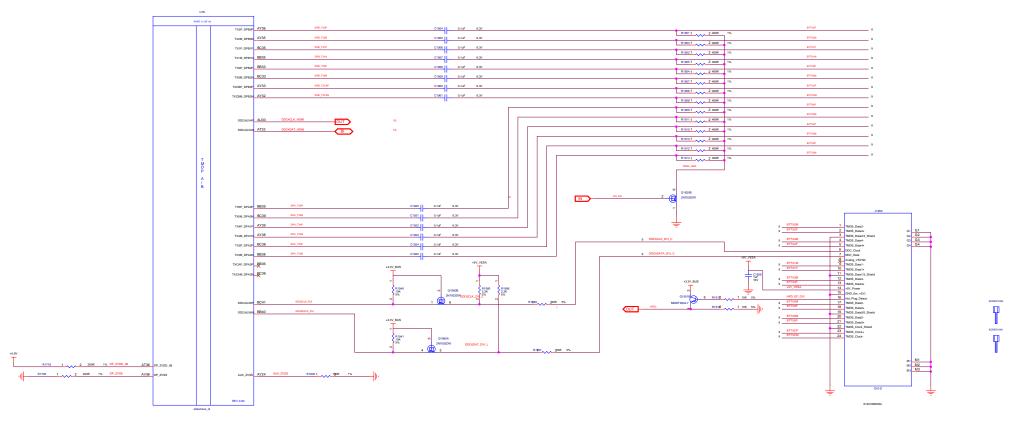




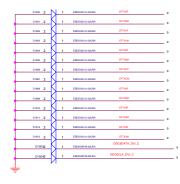




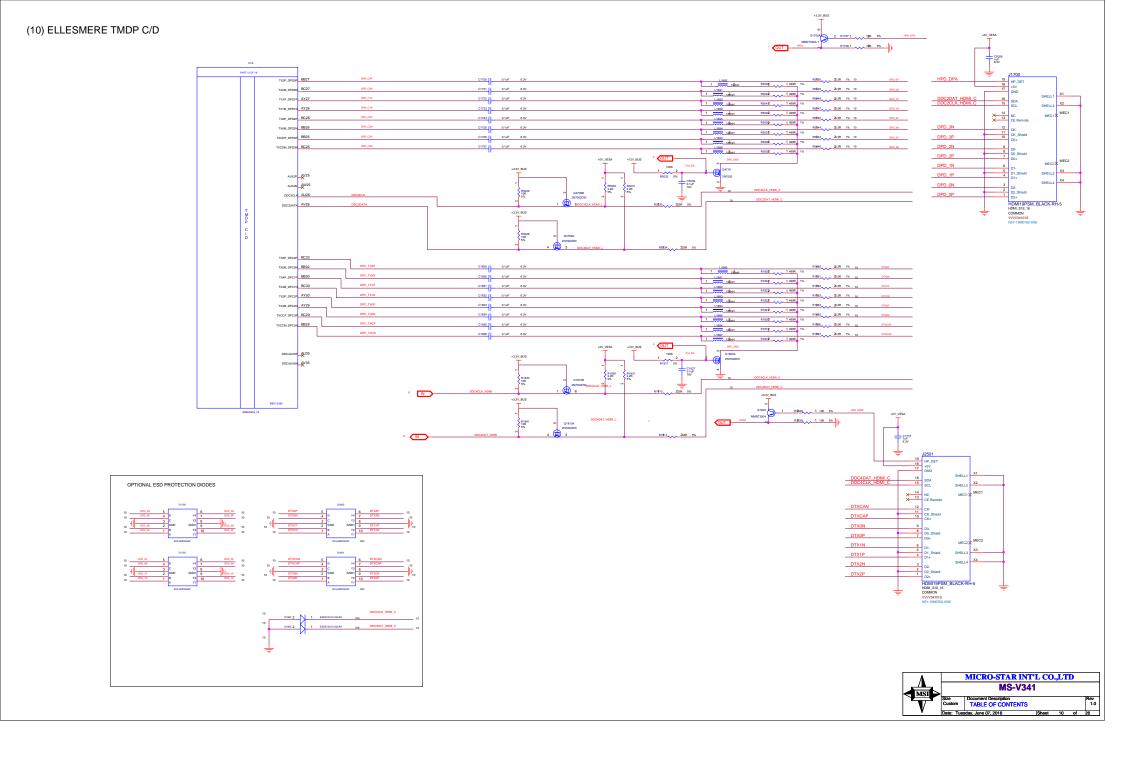
(9) ELLESMERE TMDP A/B

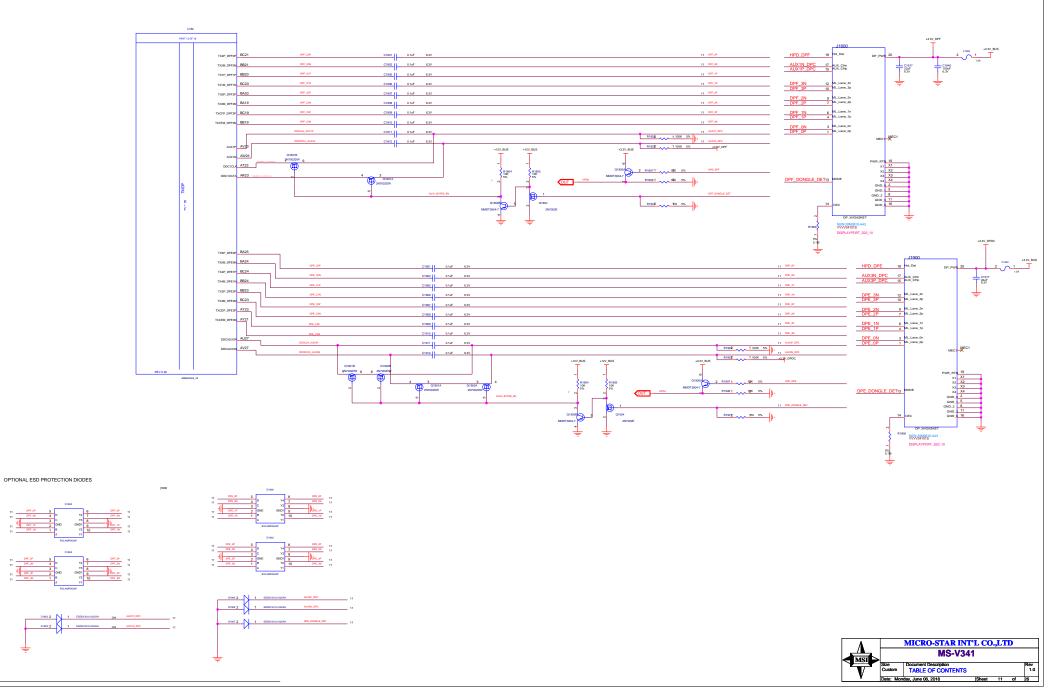


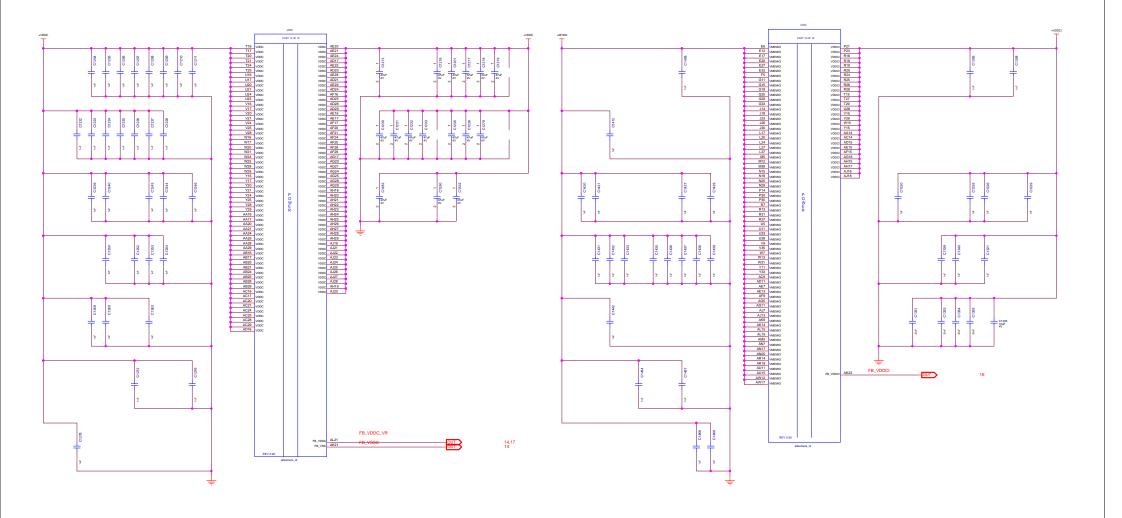
OPTIONAL ESD PROTECTION DIODES



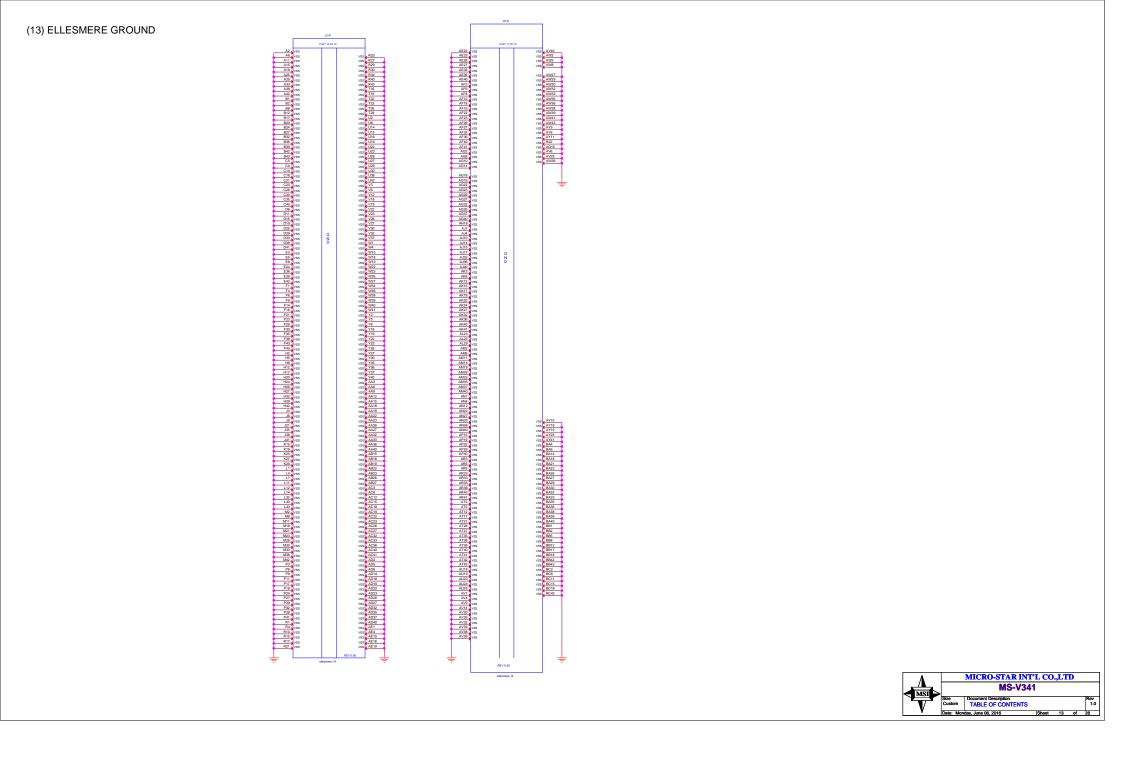
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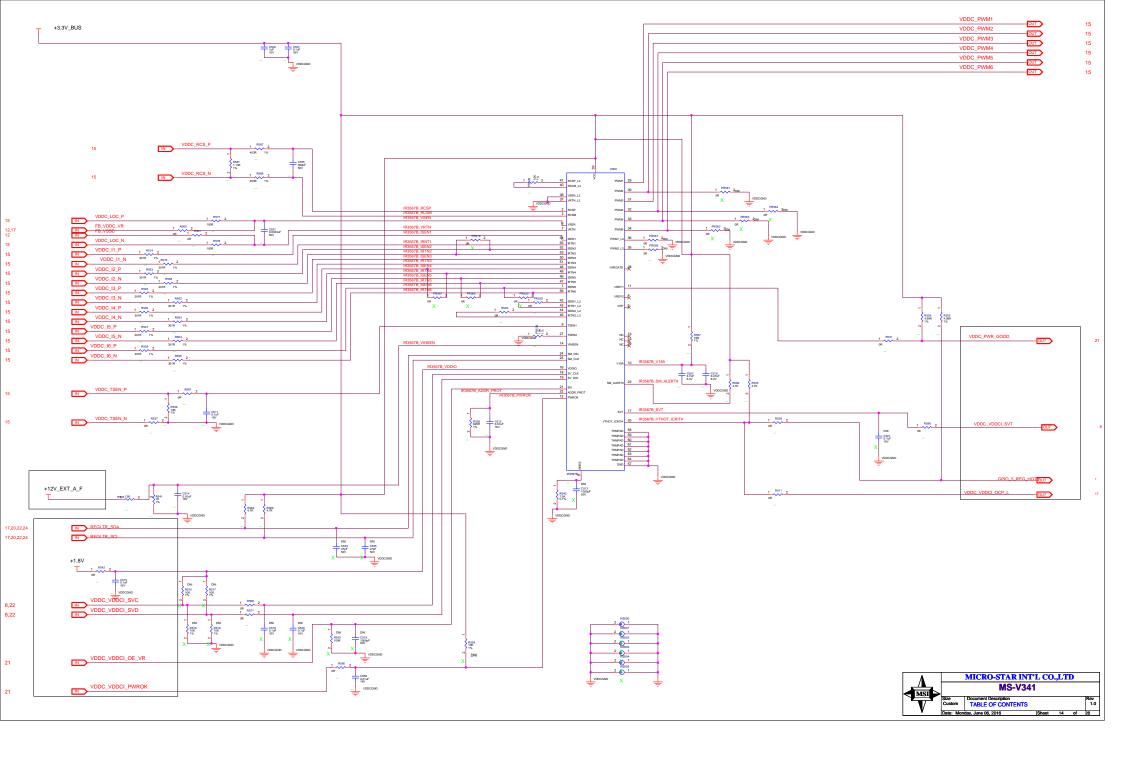


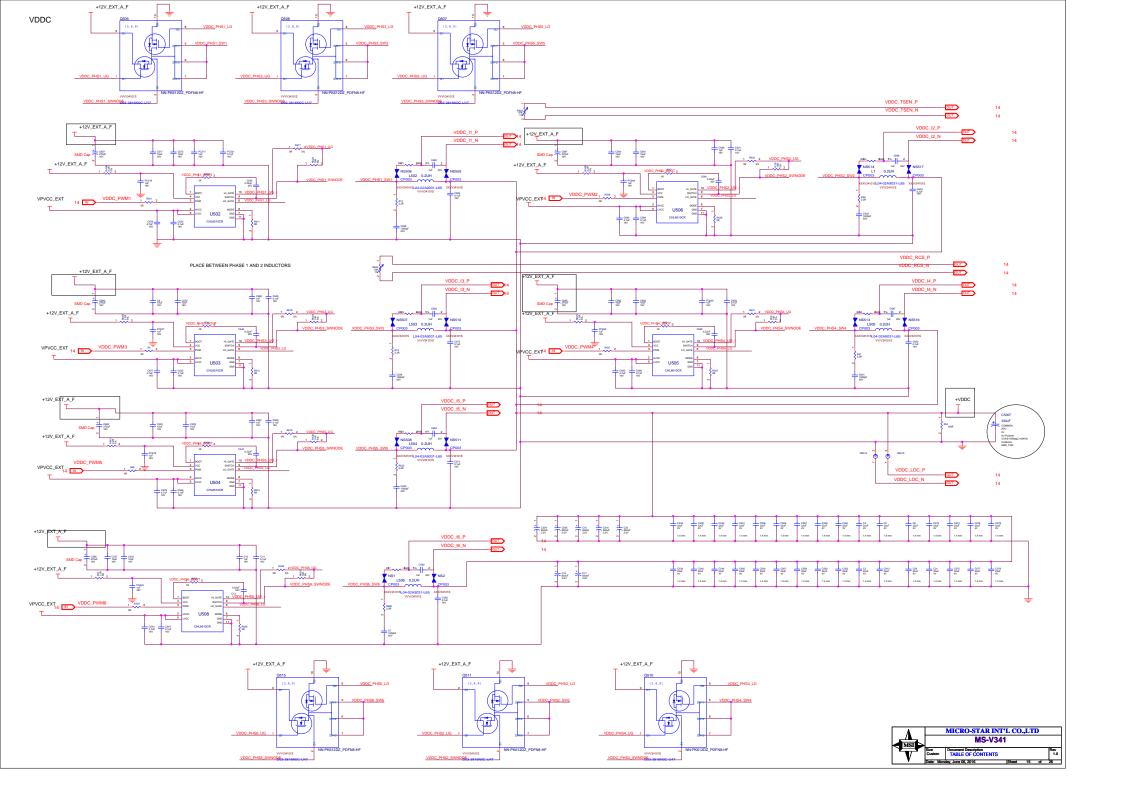


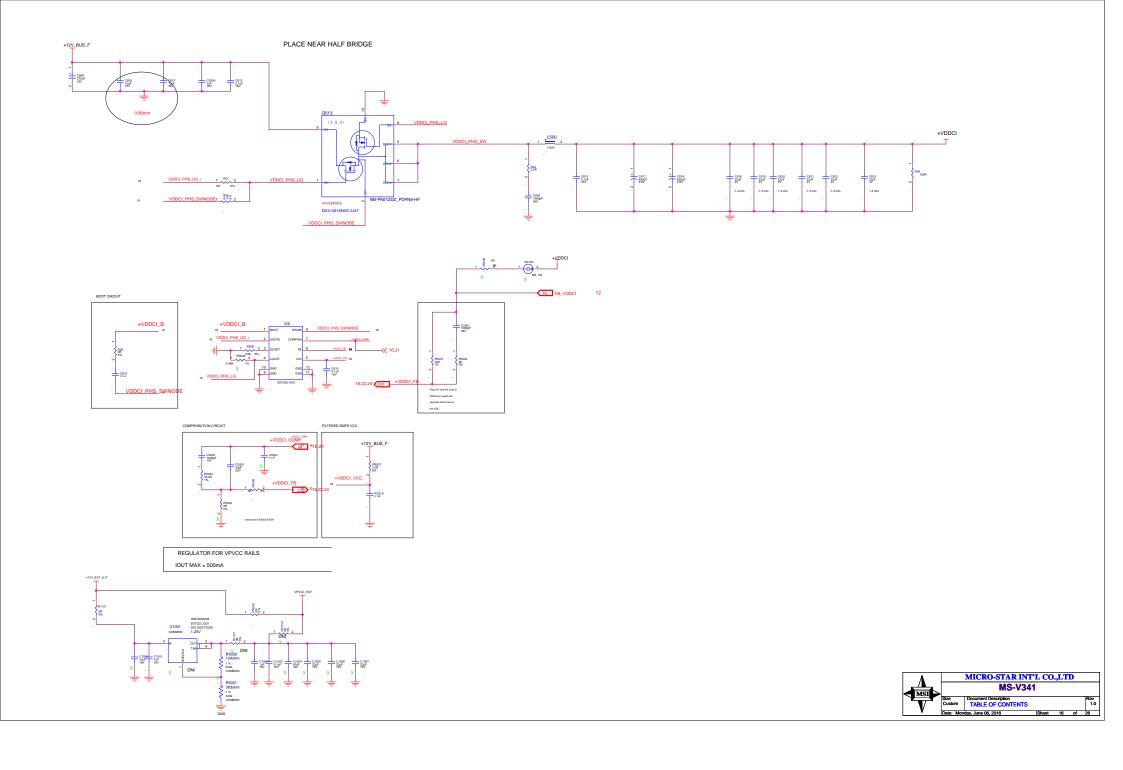


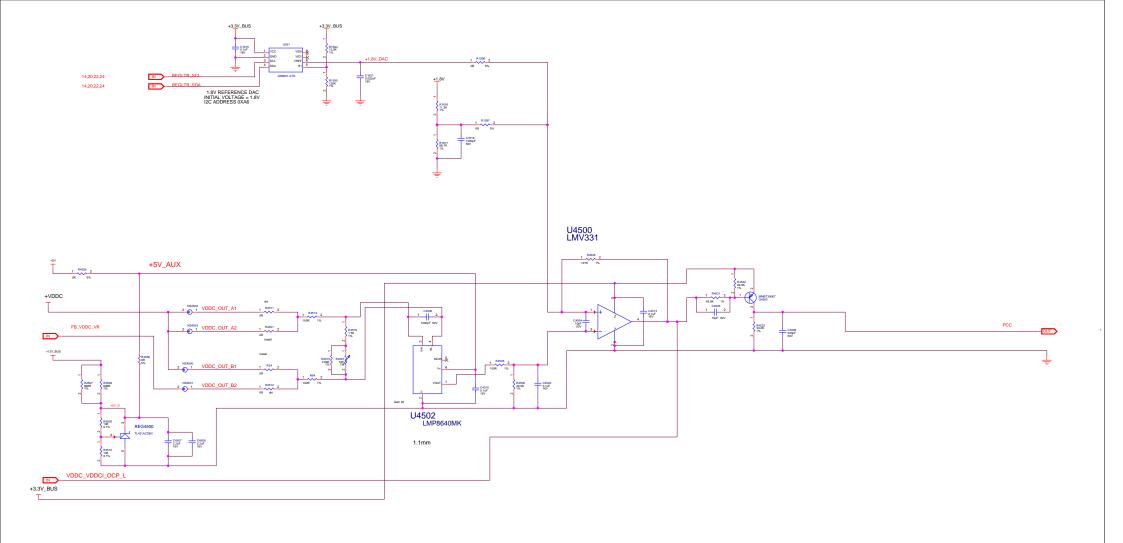




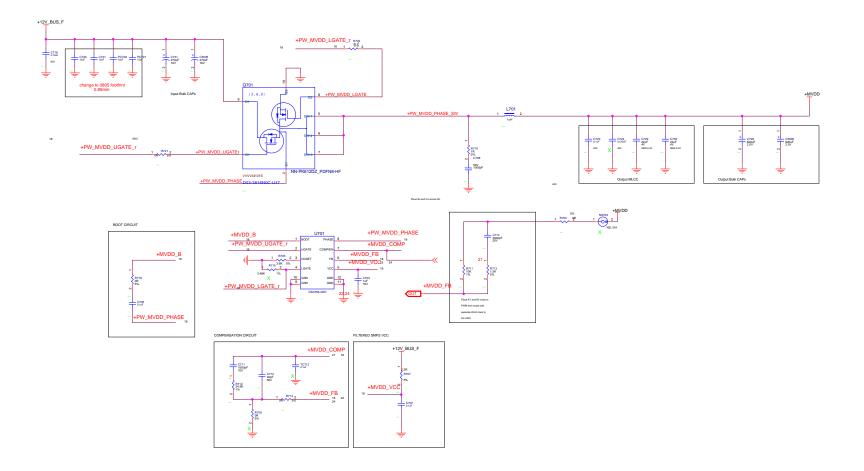




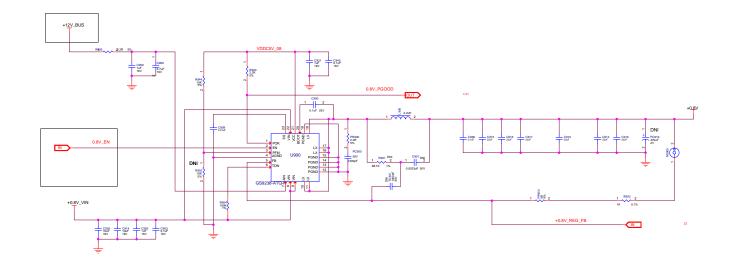




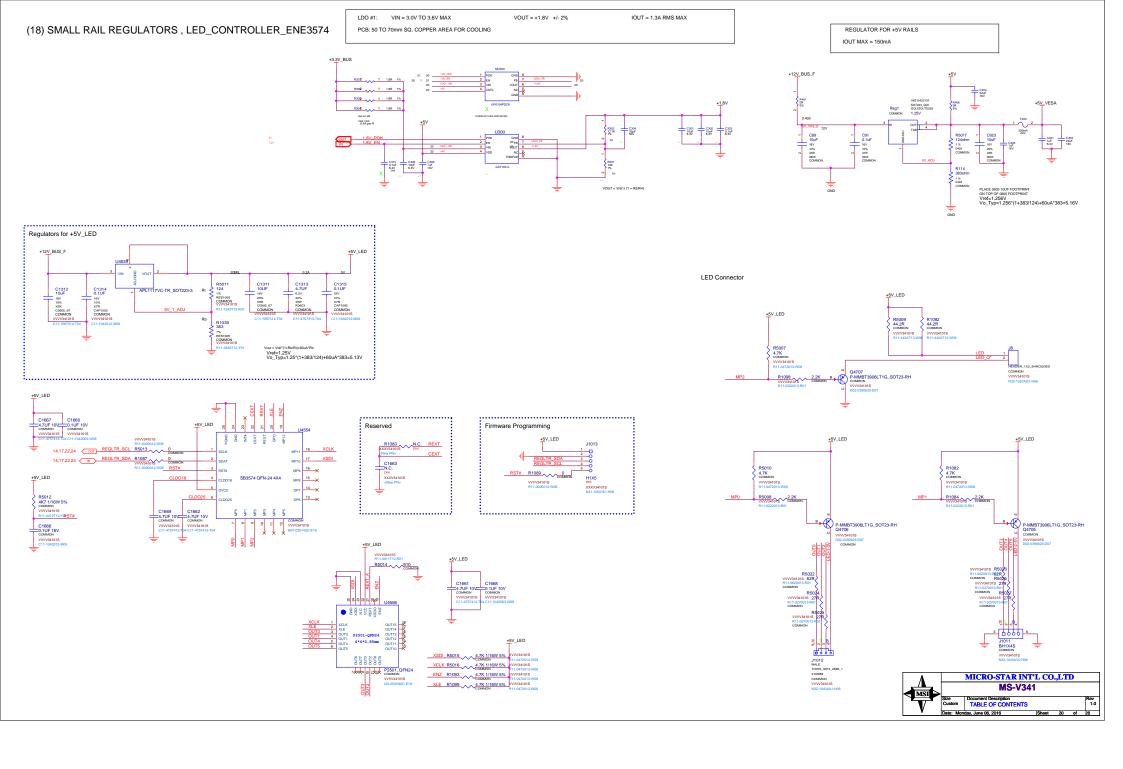


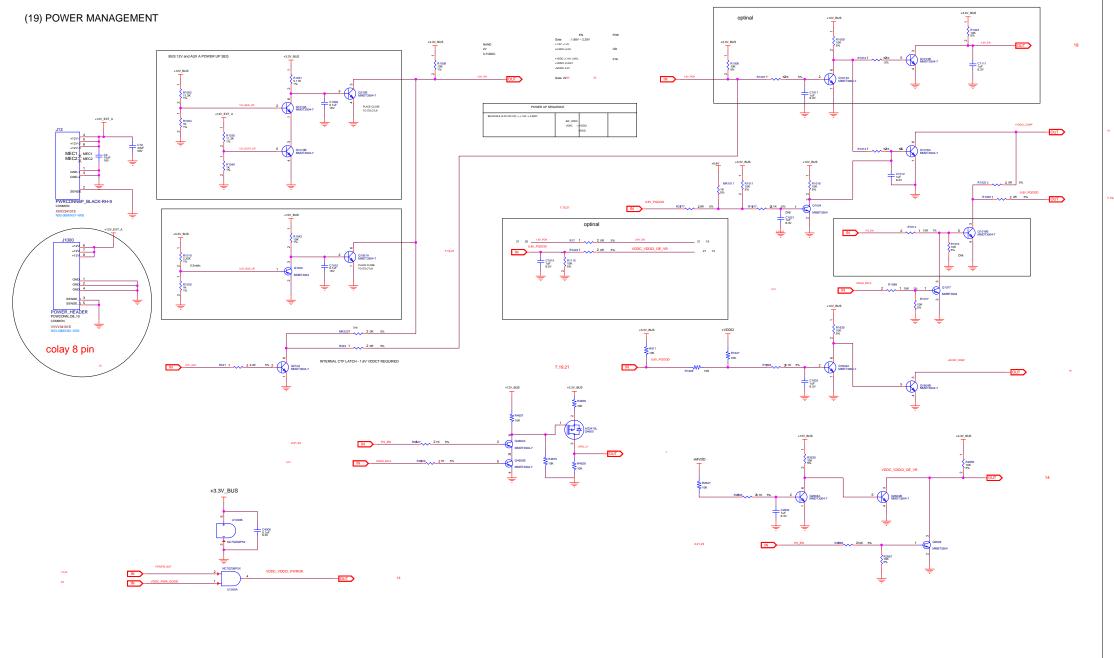


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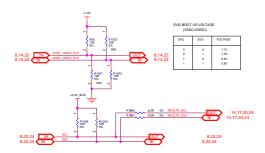


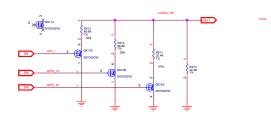
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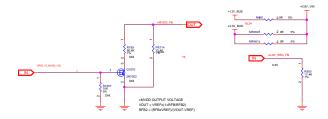




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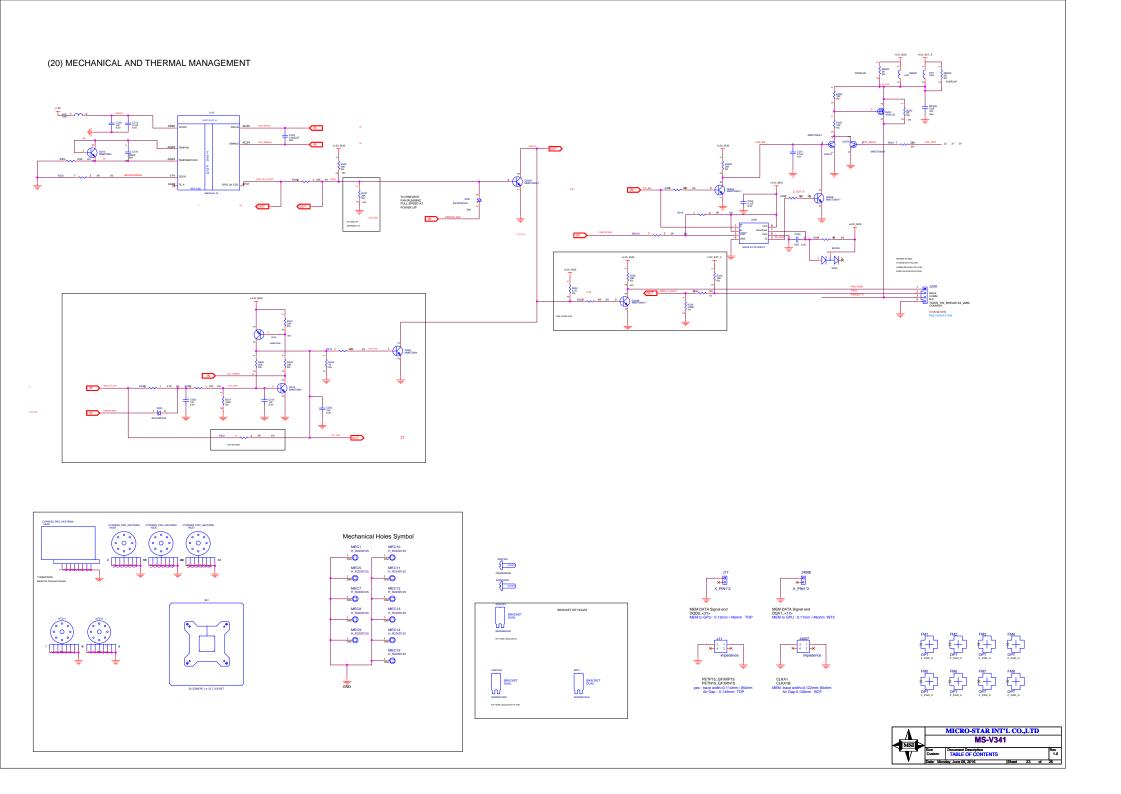


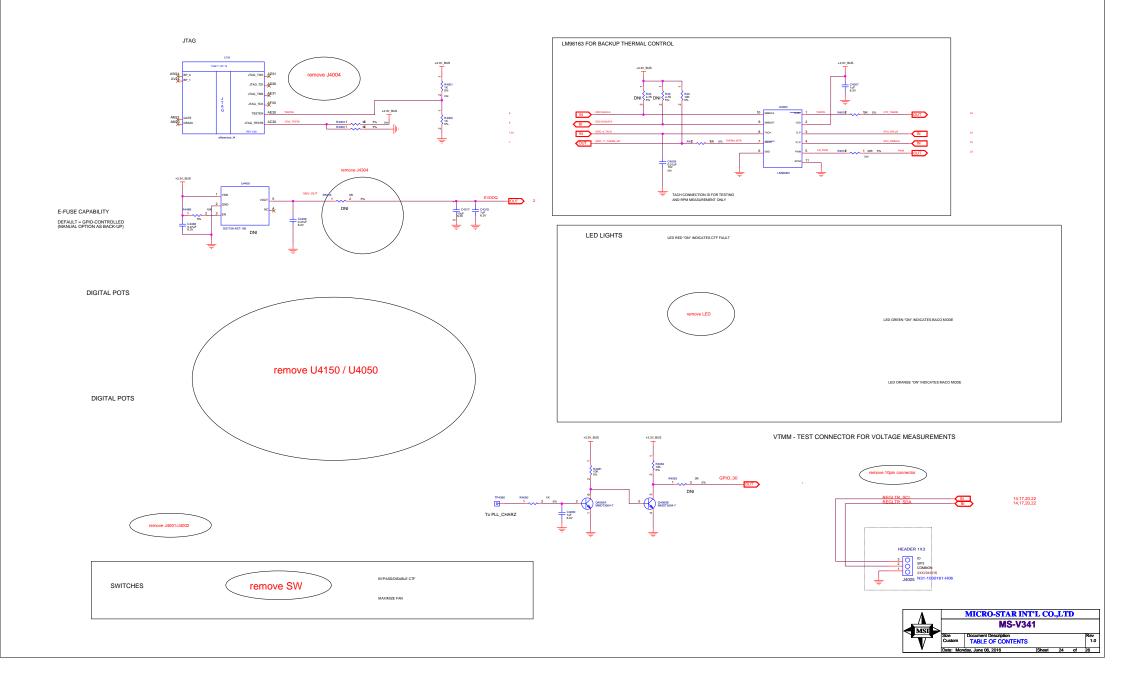


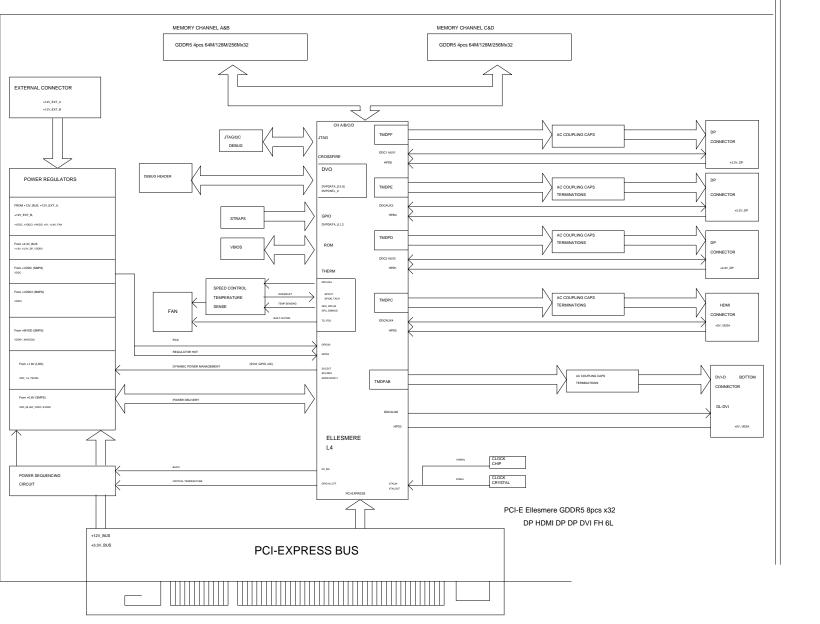














0 00A separation

609 to record 2. update VDDC driver seq

- add series resistor R1880 -R1887 - add pull down inductor L1880 -L1887

Page7: remove J2,J3 Page9: enable DVI

Page10: DP change to HDMI Page15: change dual-N MOS

Page16: change dual-N MOS

Page18: change dual-N MOS

Page20: Add LED circuit

Page21: colay 8pin power connecter

Page24: remove debug circuit

