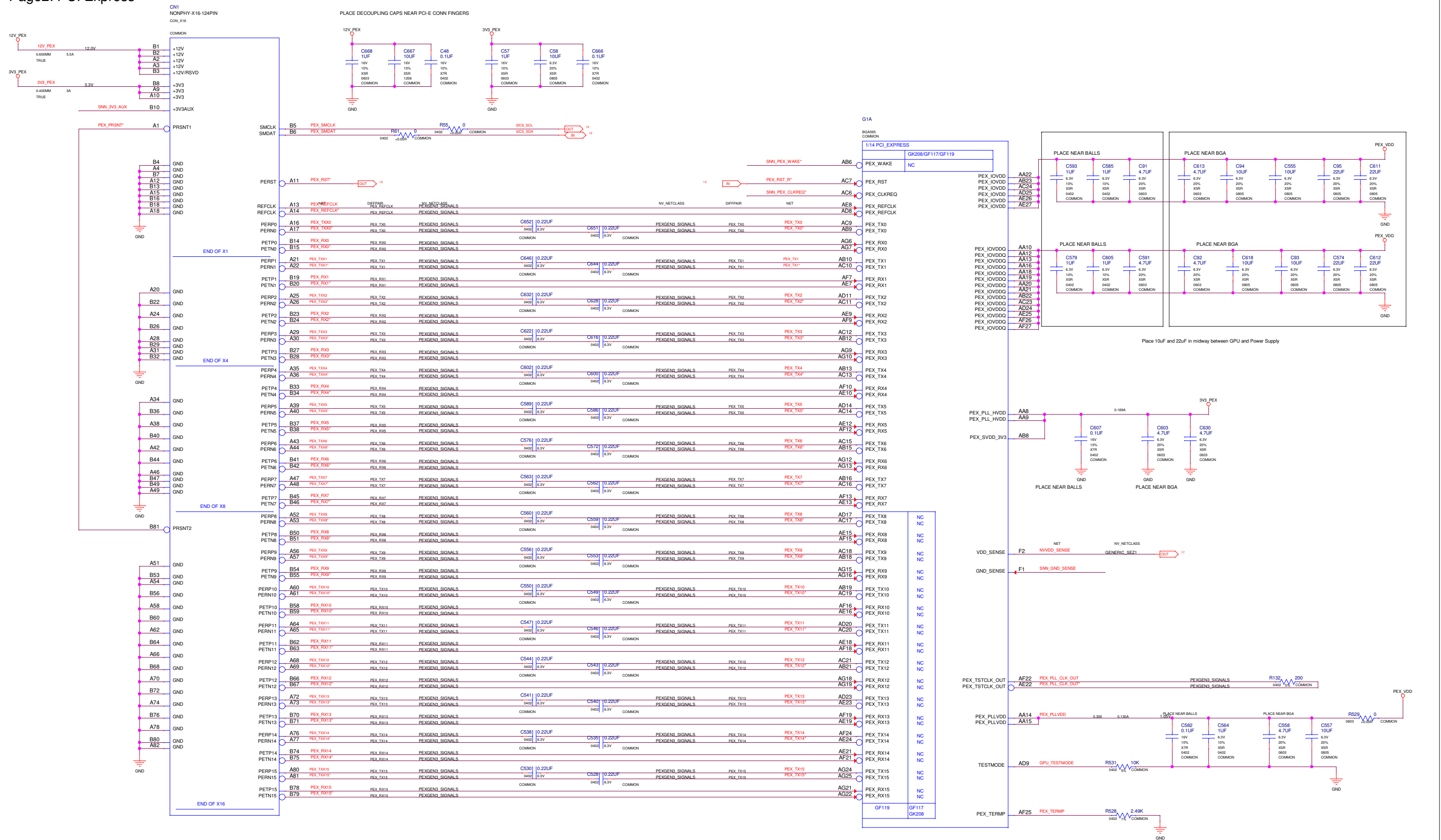
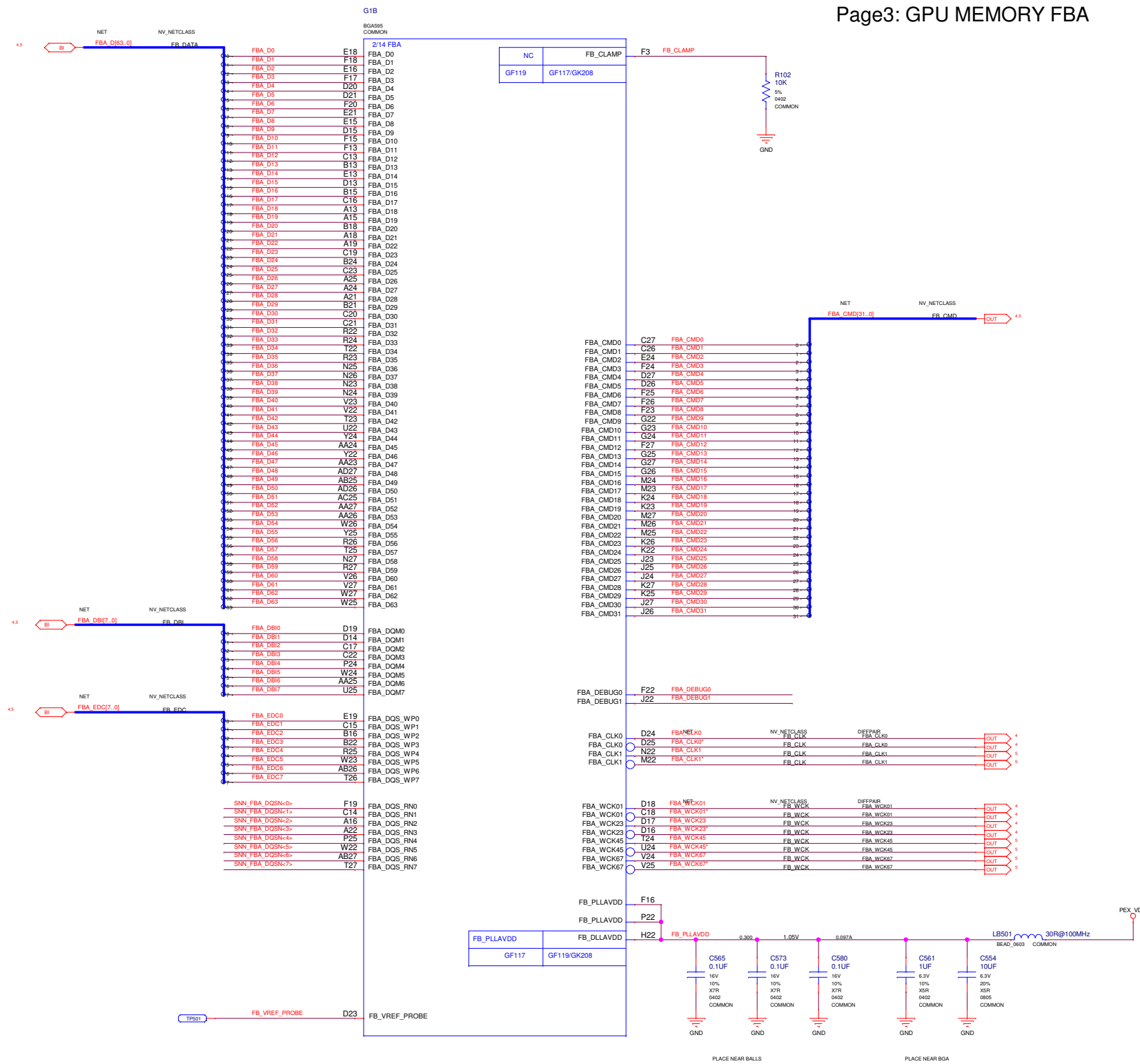


P2131_A00

GK208 GDDR5 X16 64BIT
DVI-I/DVI-D/SlimVGA + HDMI/DP + VGA

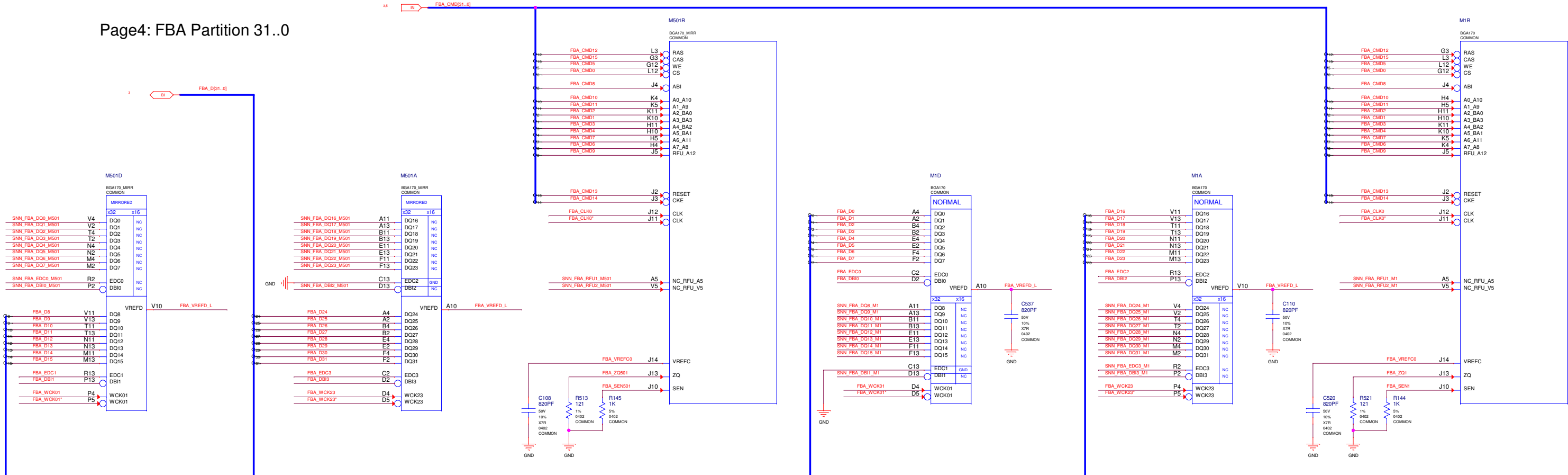
1)R150 Place inside U503



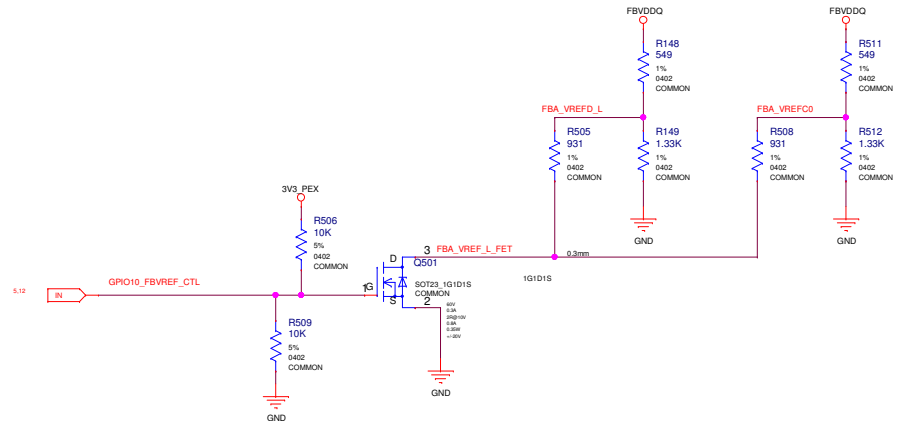
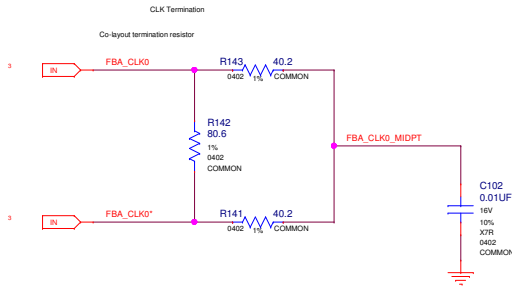
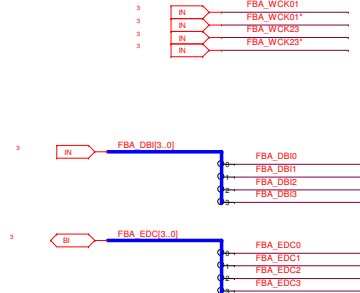


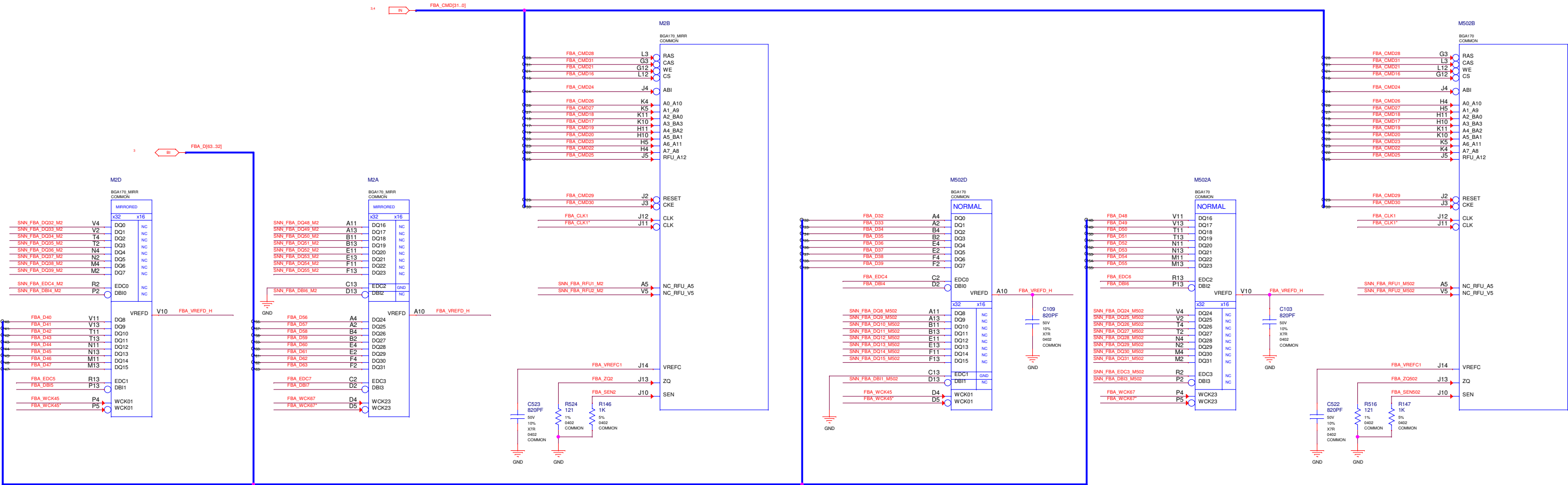
CMD	31.0	63.32
CMD0	C5'	
CMD1	A2, BA2	
CMD2	A2, BA0	
CMD3	A4, BA2	
CMD4	A0, BA1	
CMD5	WE'	
CMD6	A7, AB	
CMD7	A6, A11	
CMD8	AB'	
CMD9	A12, RFU	
CMD10	A0, A10	
CMD11	A1, A9	
CMD12	RA5'	
CMD13	RB1'	
CMD14	CKE'	
CMD15	CAS'	
CMD16		C5'
CMD17		A2, BA3
CMD18		A2, BA0
CMD19		A4, BA2
CMD20		A0, BA1
CMD21		WE'
CMD22		A7, AB
CMD23		A6, A11
CMD24		AB'
CMD25		A12, RFU
CMD26		A0, A10
CMD27		A1, A9
CMD28		RA5'
CMD29		RB1'
CMD30		CKE'
CMD31		CAS'

Page4: FBA Partition 31..0

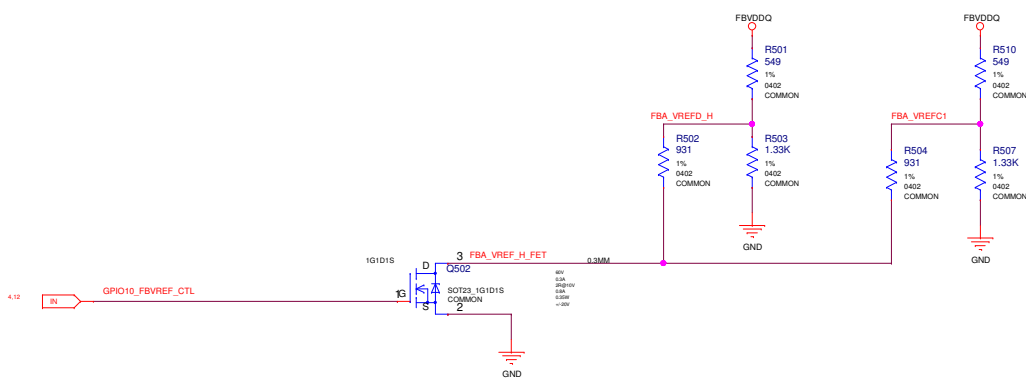
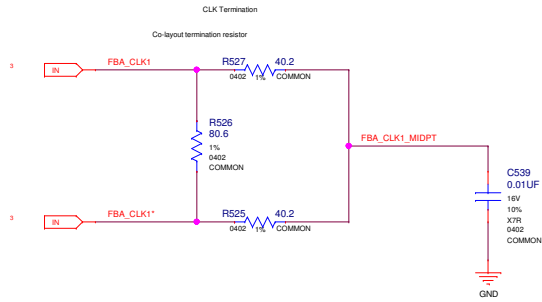
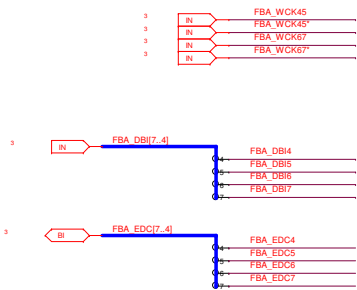


CMO	31.0	63.32
CM00	C5'	
CM01	A3, BA3	
CM02	A2, BA0	
CM03	A4, BA2	
CM04	A5, BA1	
CM05	WE'	
CM06	A7, A8	
CM07	AB, A11	
CM08	AB'	
CM09	A12, RFU	
CM10	A0, A10	
CM11	A1, A9	
CM12	RA2'	
CM13	R5T'	
CM14	CKE'	
CM15	CAS'	
CM16		C5'
CM17		A3, BA3
CM18		A2, BA0
CM19		A4, BA2
CM20		A5, BA1
CM21		WE'
CM22		A7, A8
CM23		AB, A11
CM24		AB'
CM25		A12, RFU
CM26		A0, A10
CM27		A1, A9
CM28		RA2'
CM29		R5T'
CM30		CKE'
CM31		CAS'



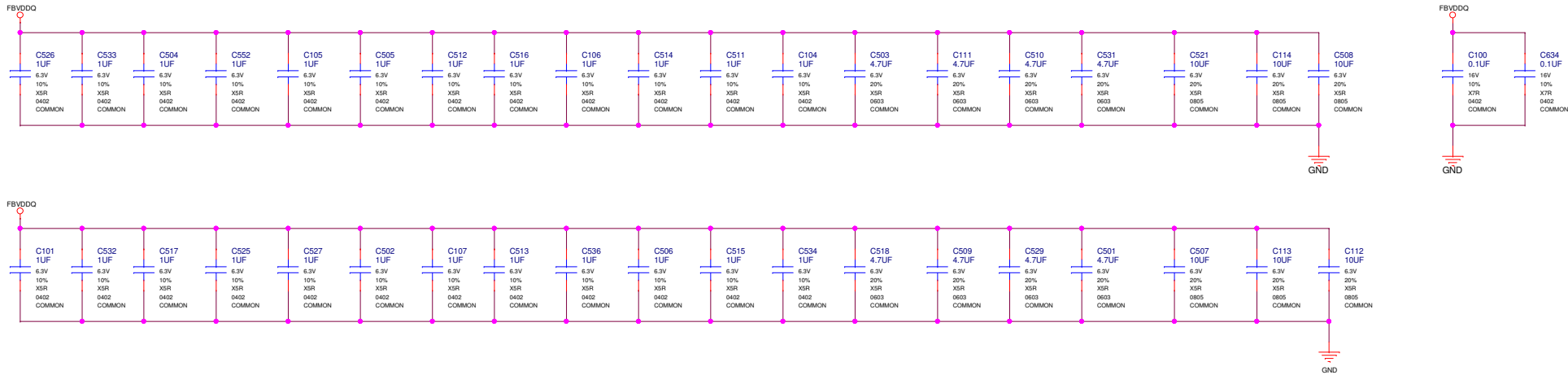


CMD	31..0	63..32
CM00	CS*	
CM01	A3_BA3	
CM02	A2_BA0	
CM03	A4_BA2	
CM04	A5_BA1	
CM05	WE*	
CM06	A7_A8	
CM07	A6_A11	
CM08	AB*	
CM09	A12_RFU	
CM010	A0_A10	
CM011	A1_A9	
CM012	RAS*	
CM013	RESET*	
CM014	CKE*	
CM015	CAS*	
CM016		CS*
CM017		A3_BA3
CM018		A2_BA0
CM019		A4_BA2
CM020		A5_BA1
CM021		WE*
CM022		A7_A8
CM023		A6_A11
CM024		AB*
CM025		A12_RFU
CM026		A0_A10
CM027		A1_A9
CM028		RAS*
CM029		RESET*
CM030		CKE*
CM031		CAS*



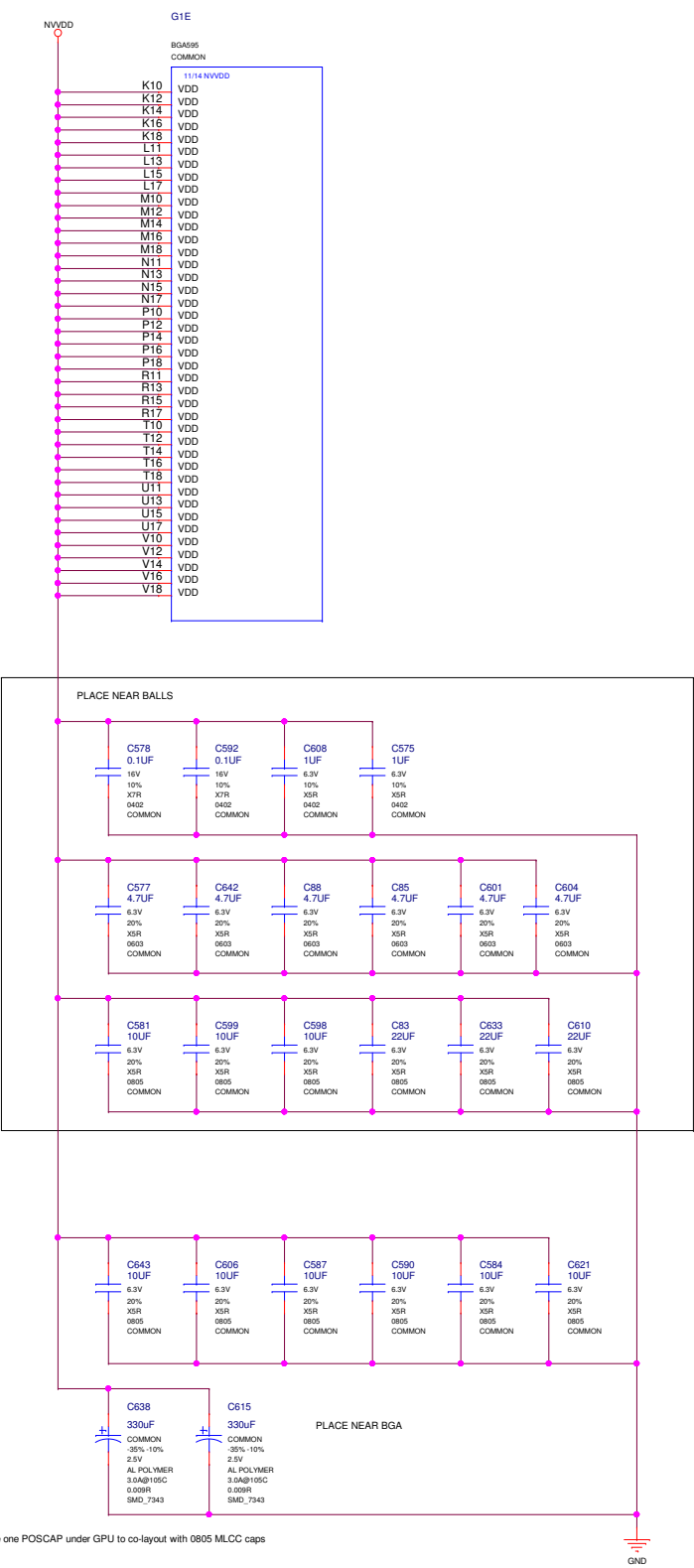
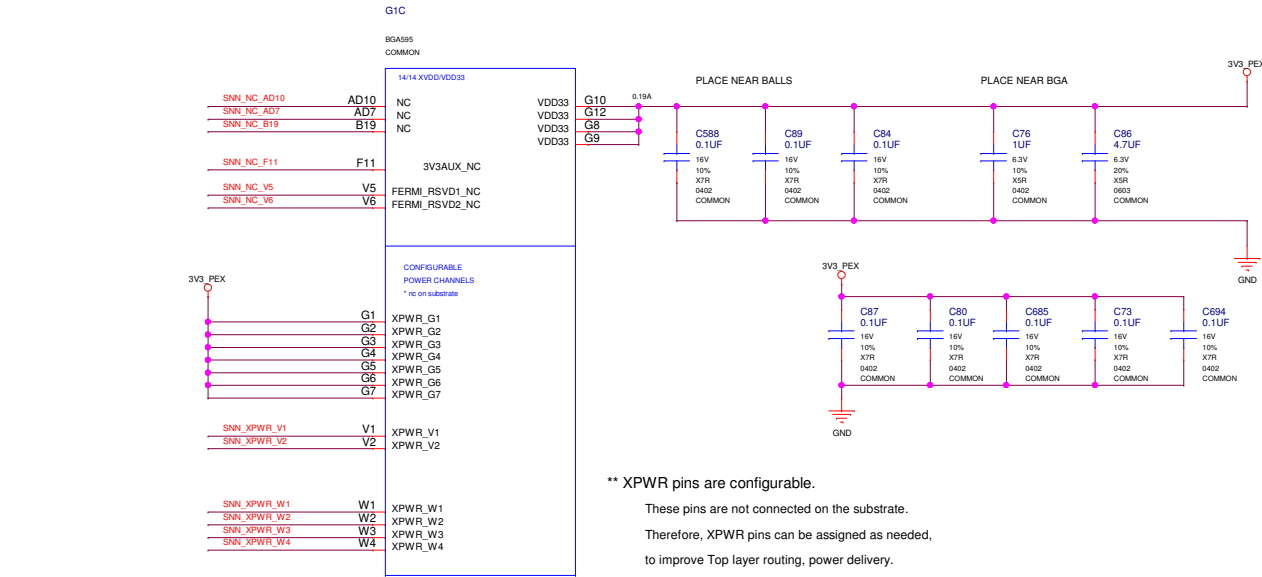
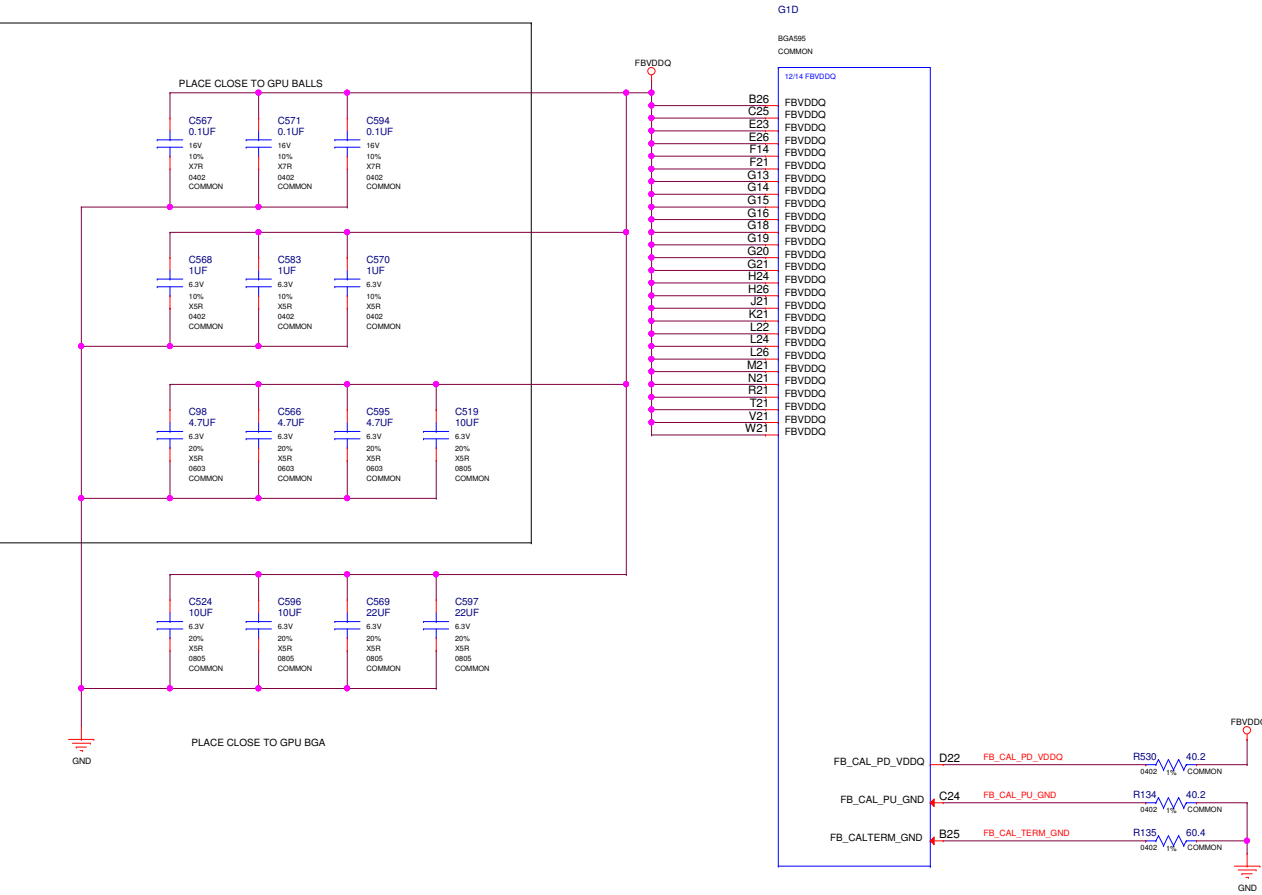
DECOUPLING FOR M1 and M501 DEVICE

DECOUPLING FOR M2 and M502 DEVICE

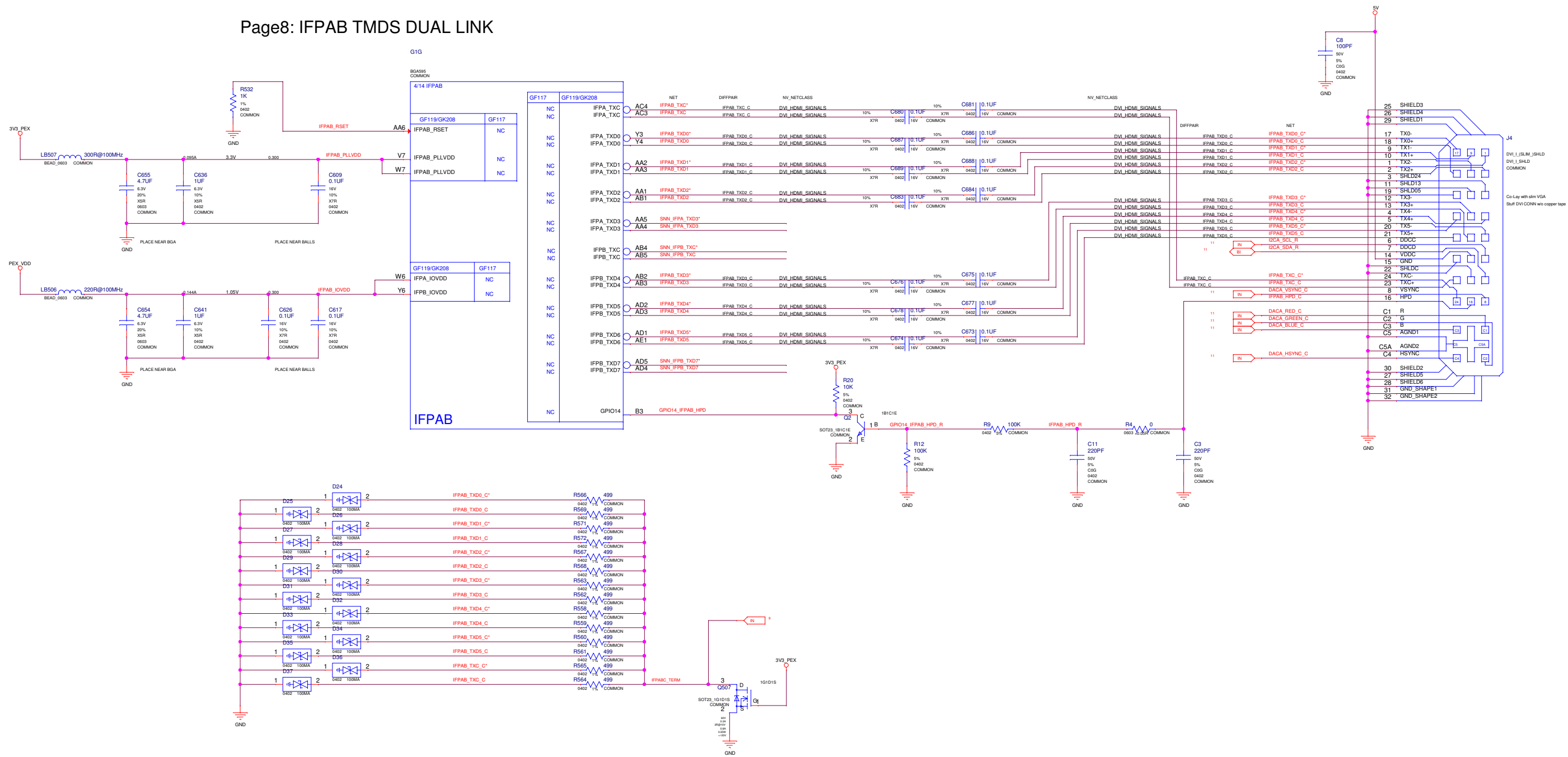


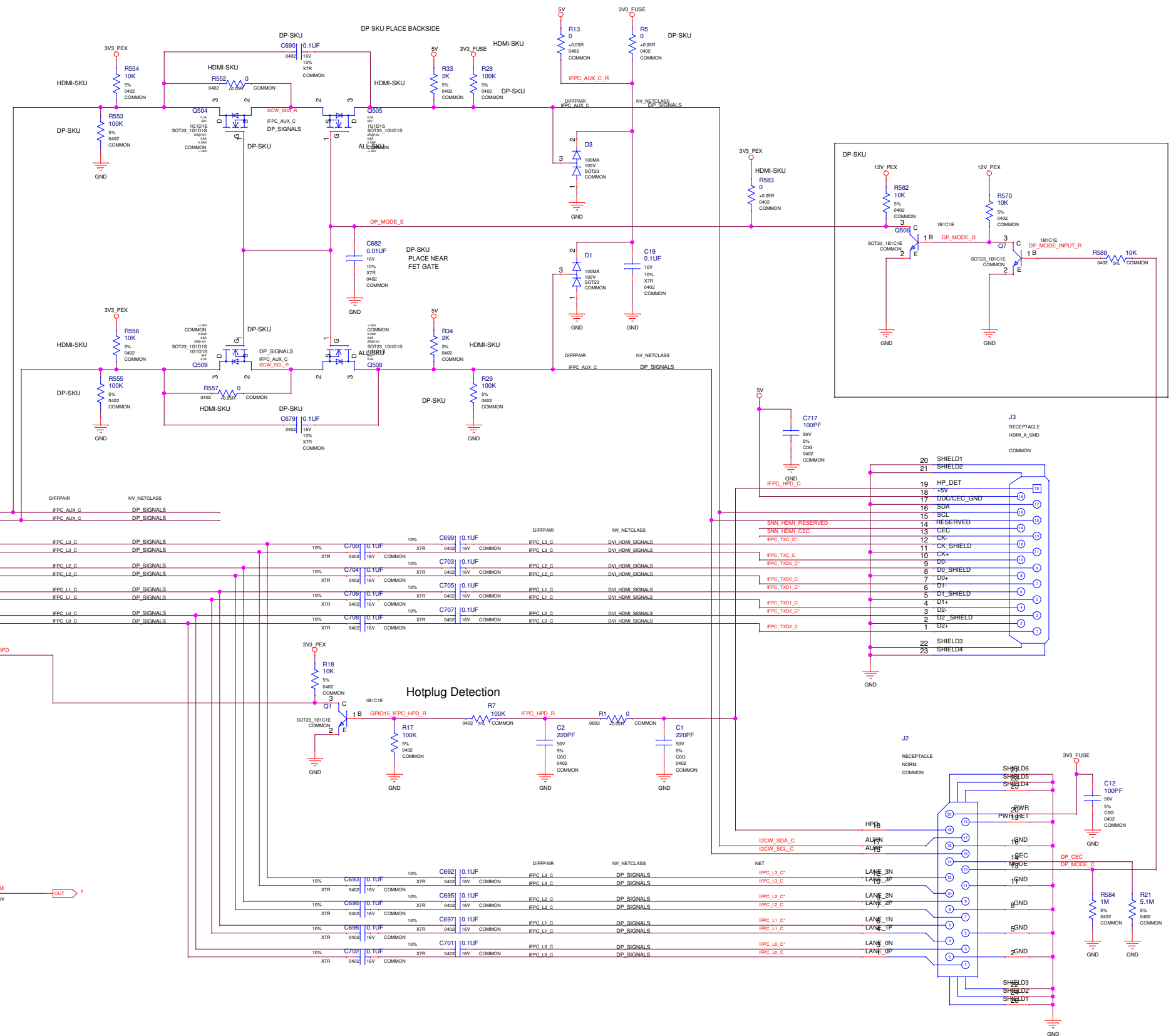
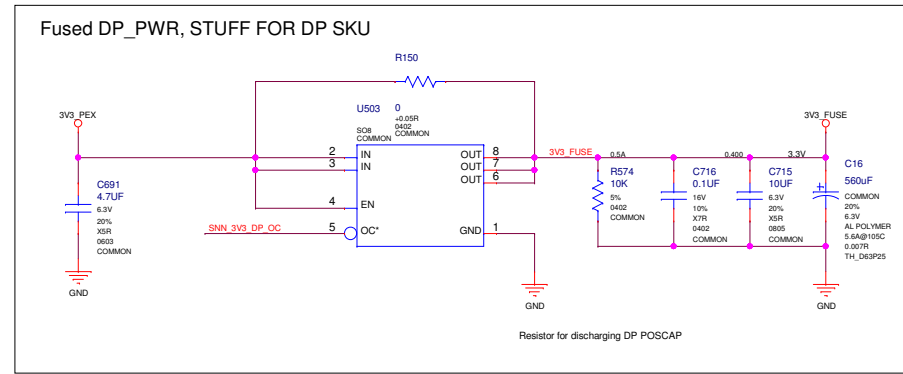
PLACE 1uF CAPS CLOSEST TO THE MEMORY DEVICES

PLACE LARGER CAPACITORS SLIGHTLY FARTHER AWAY

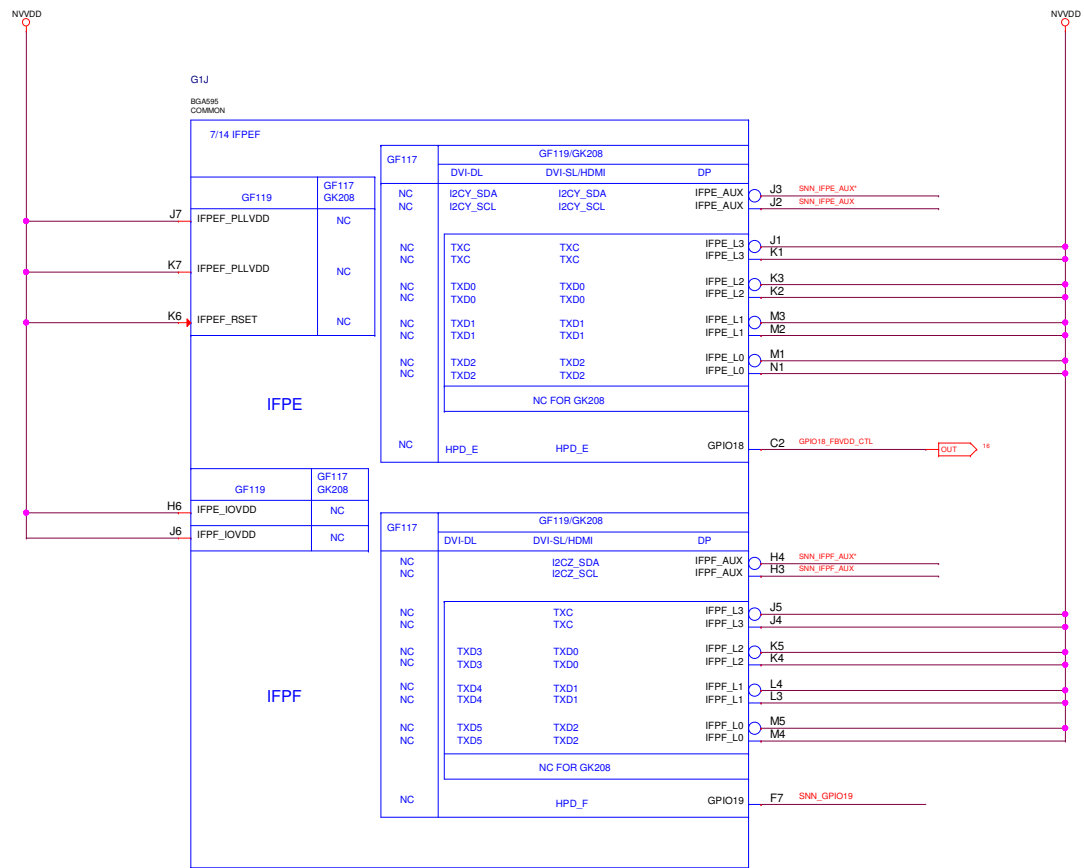
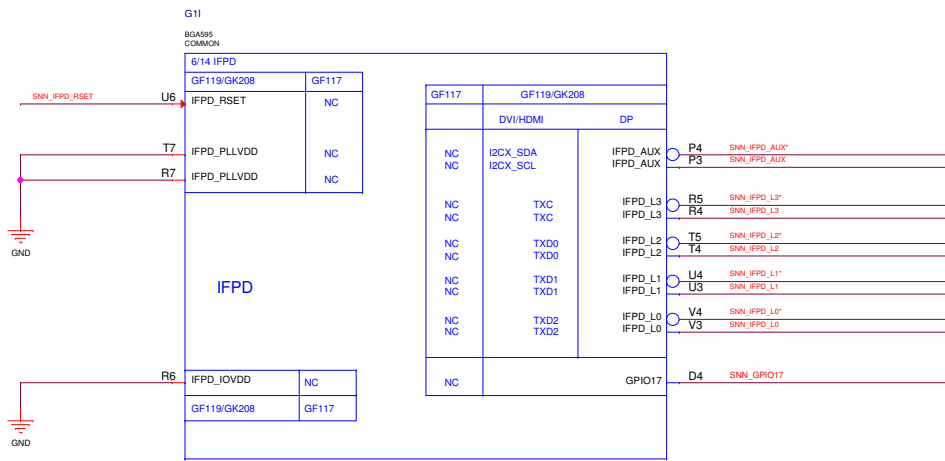


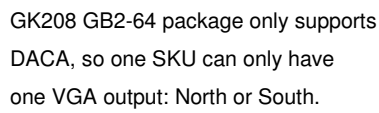
Page8: IFPAB TMDS DUAL LINK

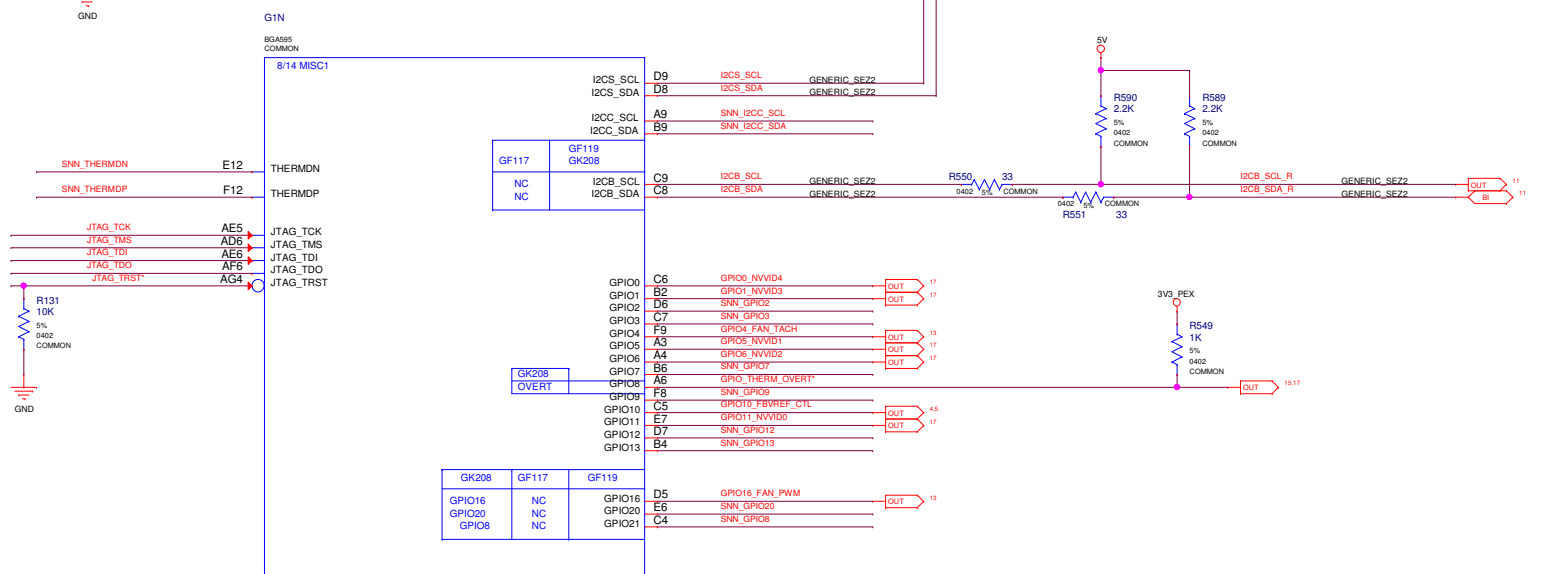
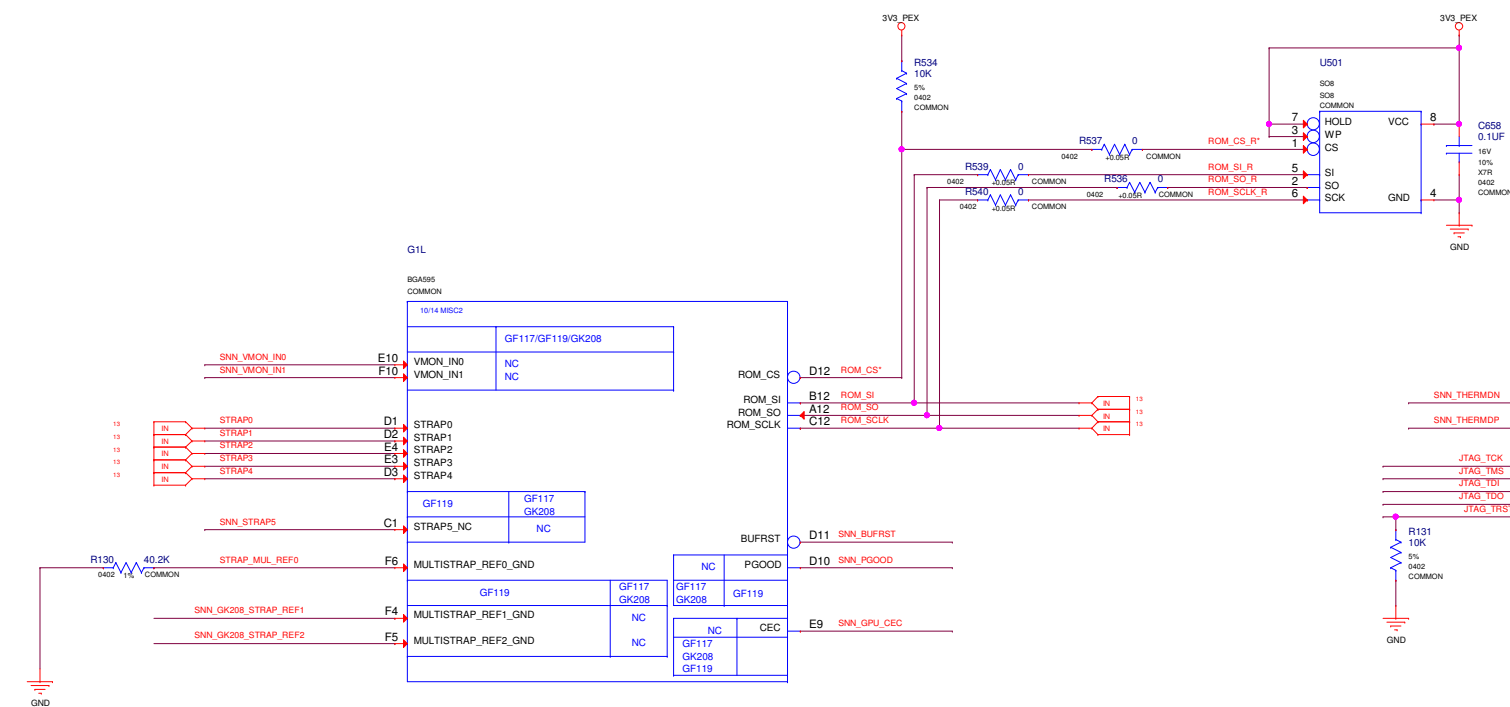
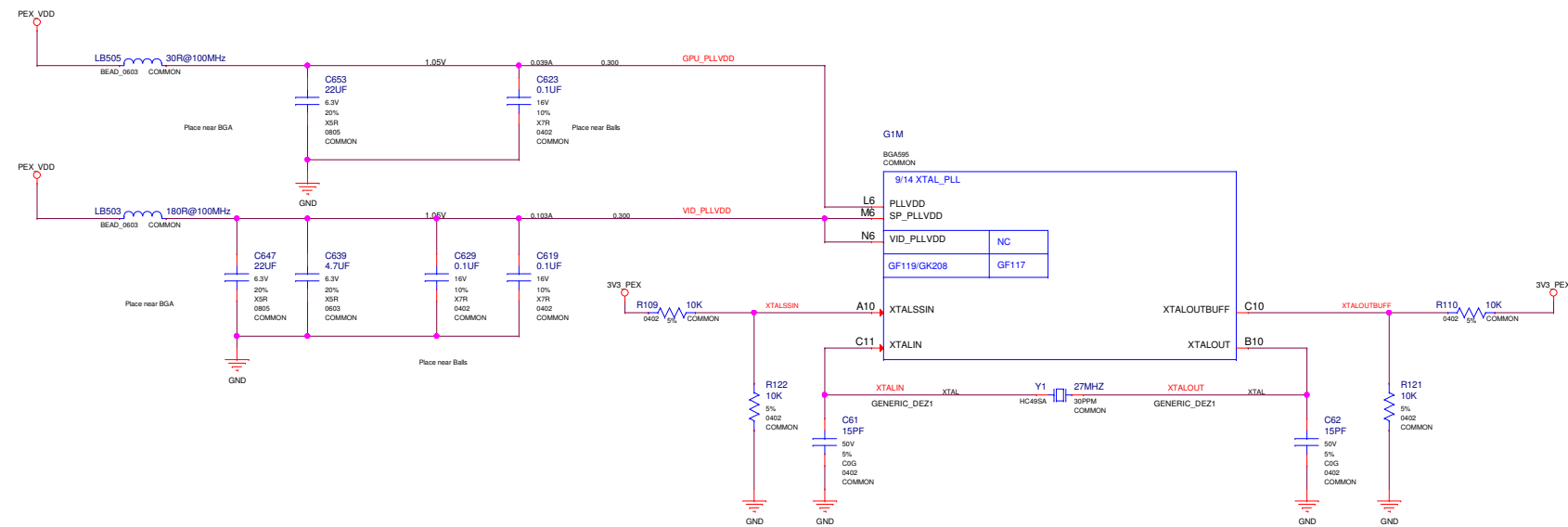




Page10: UNUSED IFPD,IFPE & IFPF







Smart Fan

XTALSSIN	XTALOUTBUFF	Inverted PWM %
PU	PU	66 (33% HIGH)
PU	PD	50 (50% HIGH)
PD	PU	33 (66% HIGH)
PD	PD	0 (100% HIGH)

