

P1071: GF108, 1024MB x16 DDR3, DVI+HDMI+VGA

Page 1: Overview

Page 2: Board Block Diagram

Page 3: PCI Express Interface

Page 4: GPU Frame Buffer

Page 5: FBA Partition

Page 6: FBB Partition

Page 7: Memory Decoupling

Page 8: DACA VGA

Page 9: DACB VGA Header

Page 10: TMDS Interface

Page 11: HDMI Interface

Page 12: Unused Interfaces, NVVDD Decoupling

Page 13: XTAL, ROM, JTAG, Misc

Page 14: Straps, Thermal, Mechanical

Page 15: Power Supply I: Misc Power, Thermal Shutdown

Page 16: Power Supply II: FBVDDQ

Page 17: Power Supply III: NVVDD

REV	VARIANT	NVPIN	ASSEMBLY
B	BASE	600-11071-BASE-SCH	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU0001	600-11071-0001-100	GF108-300-A1 625/1250/900MHz 1024MB/128bit DDR3, DVI-DL+HDMI+VGA
2	SKU0002	600-11071-0002-100	GF108-300-A1 650/1300/900MHz 1024MB/128bit DDR3, DVI-DL+HDMI+VGA
3	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
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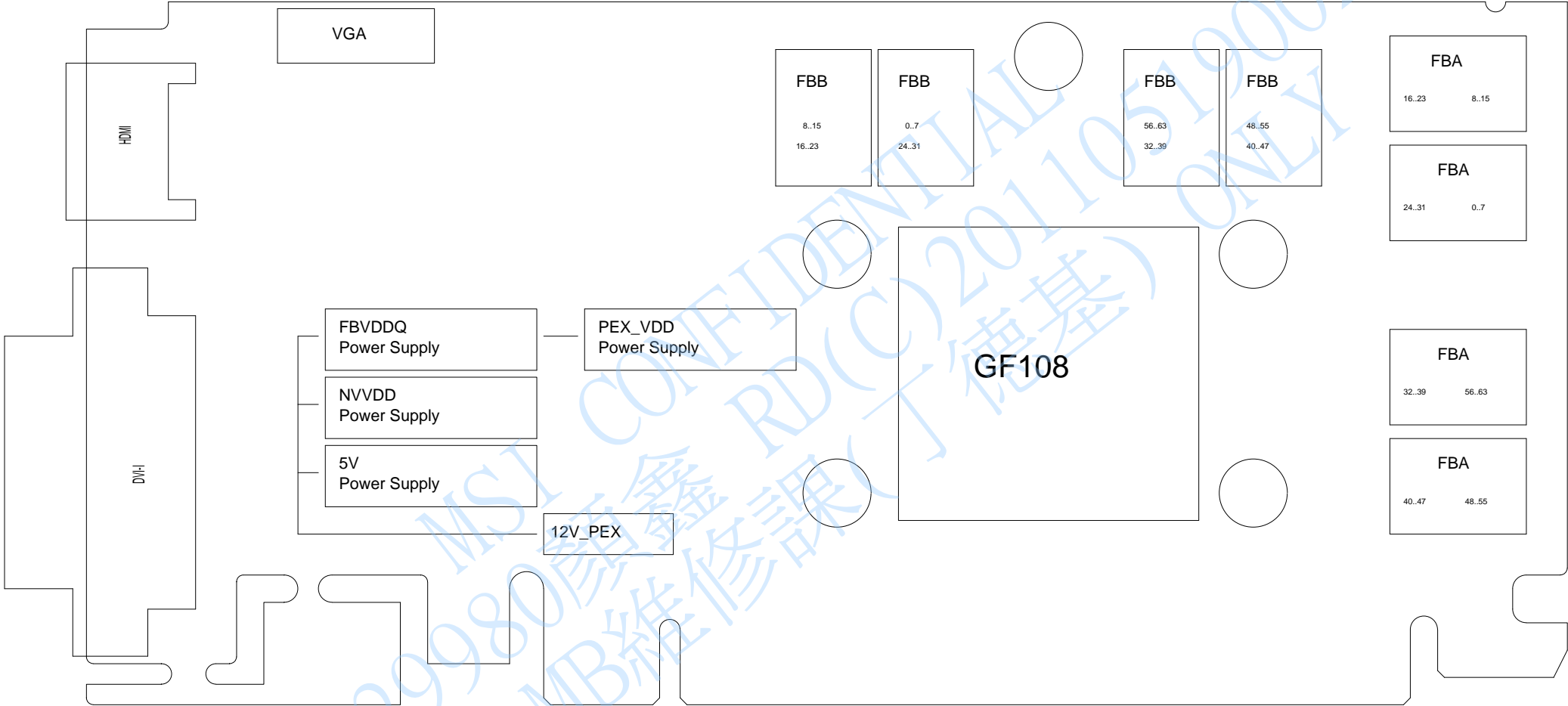
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Overview

NVIDIA CORPORATION			
2701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95050, USA			
NV_P/N	600-11071-BASE-SCH		
PCB REV	P1071-A01	PAGE	
BOM REV	A	DATE	01-APR-2010



Block Diagram

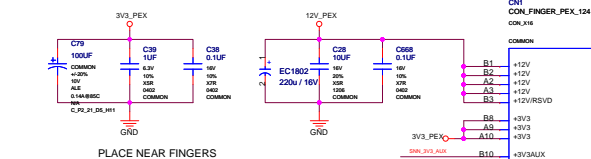


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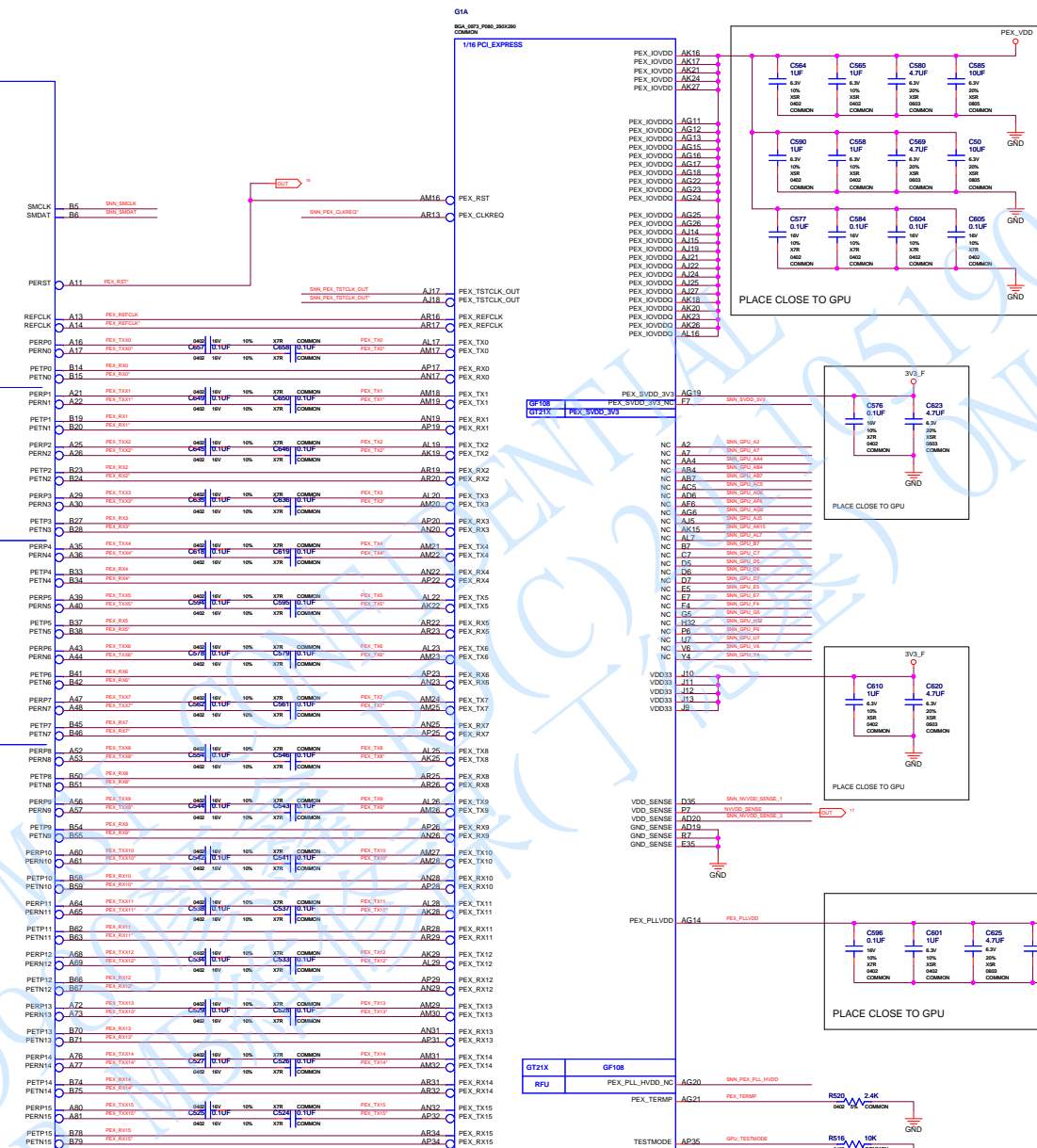
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO STUFF ASSEMBLY NOTES AND BOARD NOT FINAL
PAGE DETAIL	Board Block Diagram

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SANTA CLARA, CA 95050, USA			
NV_PN	600-11071-BASE-SCH		
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BOX REV	A	DATE	01-APR-2010

PCI Express Interface




Net Name		DIFF_PAIR	CRITICAL	IMPEDANCE
IN	RES_120	RES_120	2	1000Ω
IN	RES_121	RES_121	2	1000Ω
IN	RES_122	RES_122	2	1000Ω
IN	RES_123	RES_123	2	1000Ω
IN	RES_124	RES_124	2	1000Ω
IN	RES_125	RES_125	2	1000Ω
IN	RES_126	RES_126	2	1000Ω
IN	RES_127	RES_127	2	1000Ω
IN	RES_128	RES_128	2	1000Ω
IN	RES_129	RES_129	2	1000Ω
IN	RES_130	RES_130	2	1000Ω
IN	RES_131	RES_131	2	1000Ω
IN	RES_132	RES_132	2	1000Ω
IN	RES_133	RES_133	2	1000Ω
IN	RES_134	RES_134	2	1000Ω
IN	RES_135	RES_135	2	1000Ω
IN	RES_136	RES_136	2	1000Ω
IN	RES_137	RES_137	2	1000Ω
IN	RES_138	RES_138	2	1000Ω
IN	RES_139	RES_139	2	1000Ω
IN	RES_140	RES_140	2	1000Ω
IN	RES_141	RES_141	2	1000Ω
IN	RES_142	RES_142	2	1000Ω
IN	RES_143	RES_143	2	1000Ω
IN	RES_144	RES_144	2	1000Ω
IN	RES_145	RES_145	2	1000Ω
IN	RES_146	RES_146	2	1000Ω
IN	RES_147	RES_147	2	1000Ω
IN	RES_148	RES_148	2	1000Ω
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IN	RES_151	RES_151	2	1000Ω
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IN	RES_154	RES_154	2	1000Ω
IN	RES_155	RES_155	2	1000Ω
IN	RES_156	RES_156	2	1000Ω
IN	RES_157	RES_157	2	1000Ω
IN	RES_158	RES_158	2	1000Ω
IN	RES_159	RES_159	2	1000Ω
IN	RES_160	RES_160	2	1000Ω
IN	RES_161	RES_161	2	1000Ω
IN	RES_162	RES_162	2	1000Ω
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IN	RES_169	RES_169	2	1000Ω
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IN	RES_180	RES_180	2	1000Ω
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IN	RES_183	RES_183	2	1000Ω
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IN	RES_187	RES_187	2	1000Ω
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IN	RES_189	RES_189	2	1000Ω
IN	RES_190	RES_190	2	1000Ω
IN	RES_191	RES_191	2	1000Ω
IN	RES_192	RES_192	2	1000Ω
IN	RES_193	RES_193	2	1000Ω
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IN	RES_195	RES_195	2	1000Ω
IN	RES_196	RES_196	2	1000Ω
IN	RES_197	RES_197	2	1000Ω
IN	RES_198	RES_198	2	1000Ω
IN	RES_199	RES_199	2	1000Ω
IN	RES_200	RES_200	2	1000Ω
IN	RES_201	RES_201	2	1000Ω
IN	RES_202	RES_202	2	1000Ω
IN	RES_203	RES_203	2	1000Ω
IN	RES_204	RES_204	2	1000Ω
IN	RES_205	RES_205	2	1000Ω
IN	RES_206	RES_206	2	1000Ω
IN	RES_207	RES_207	2	1000Ω



Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
IN PEX_PSRST*	12MIL		
IN PEX_TEMP	12MIL		
IN GPU_TESTMODE	12MIL		
IN PEX_FLLVDD	12MIL	1.05V	0.145A

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BOM REV	A		DATE	01-APR-2010



GPU Frame Buffer

G10

SDA_0071, FBA_20K030

COMMON

216 FBA

FBA_001

FBA_002

FBA_003

FBA_004

FBA_005

FBA_006

FBA_007

FBA_008

FBA_009

FBA_010

FBA_011

FBA_012

FBA_013

FBA_014

FBA_015

FBA_016

FBA_017

FBA_018

FBA_019

FBA_020

FBA_021

FBA_022

FBA_023

FBA_024

FBA_025

FBA_026

FBA_027

FBA_028

FBA_029

FBA_030

FBA_031

FBA_032

FBA_033

FBA_034

FBA_035

FBA_036

FBA_037

FBA_038

FBA_039

FBA_040

FBA_041

FBA_042

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FBA_070

FBA_071

FBA_072

FBA_073

FBA_074

FBA_075

FBA_076

FBA_077

FBA_078

FBA_079

FBA_080

FBA_081

FBA_082

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FBA_313

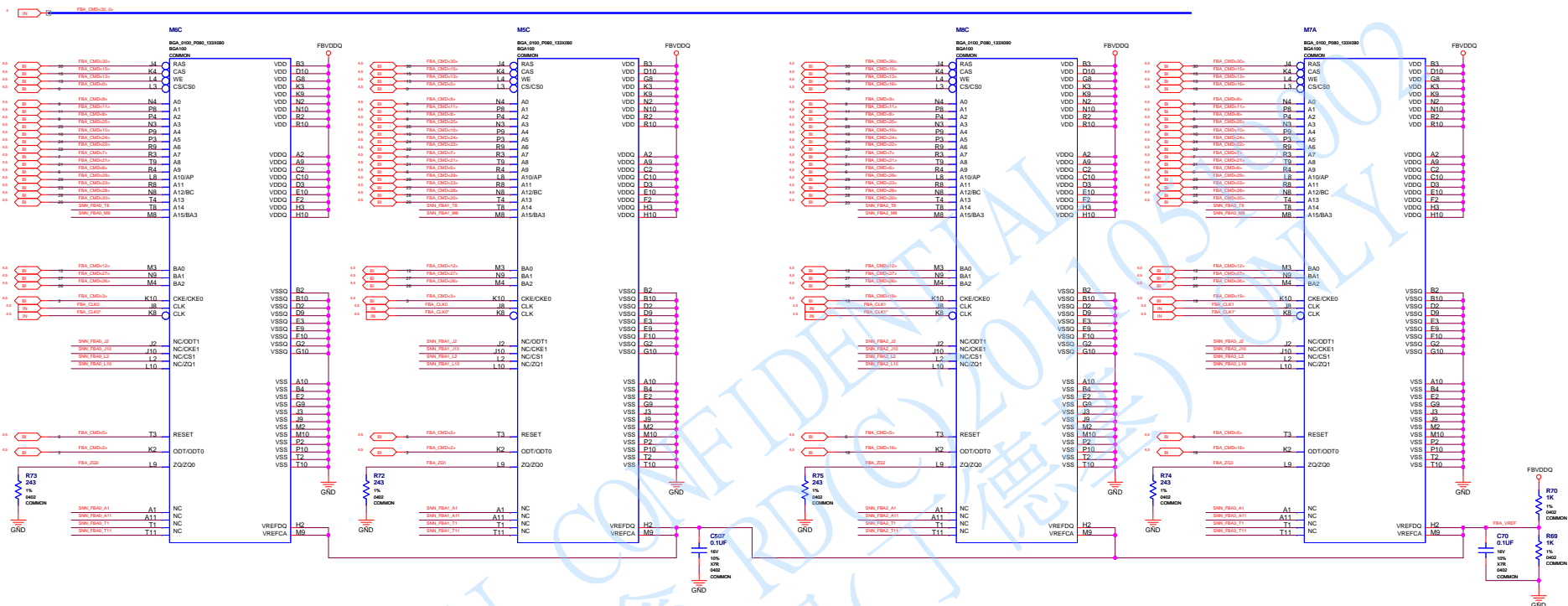
FBA_314

FBA_315

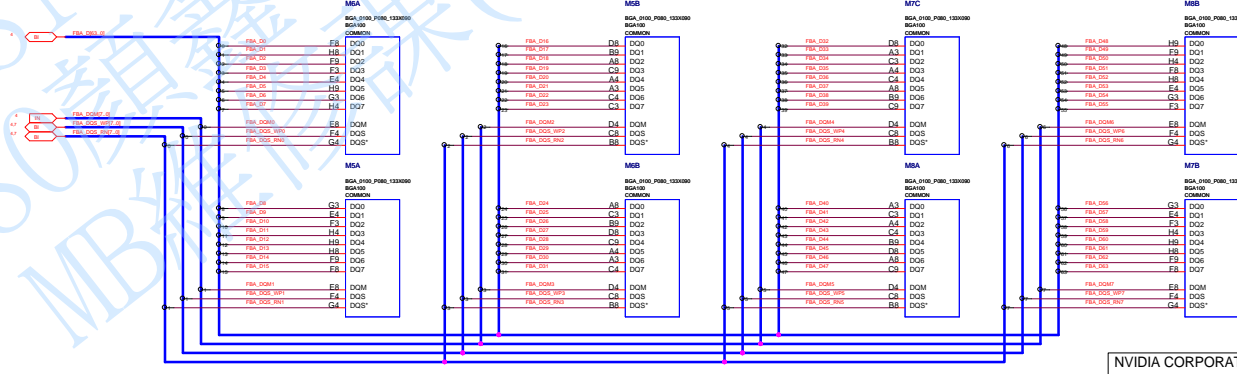
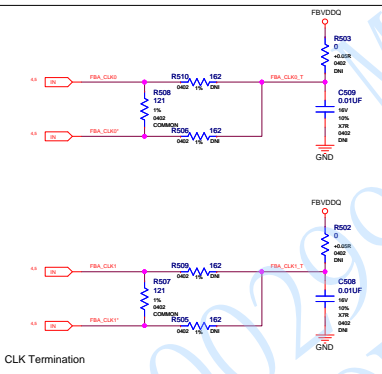
FBA

FBA Partition

Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
FBA_CLK0-T		0.9V	
FBA_CLK1-T		0.9V	
FBA_VREF			
FBA_Z00			
FBA_Z01			
FBA_Z02			
FBA_Z03			



CMD	+0.3V	+0.6V
CM00	C00	
CM01	C01	
CM02	C02	
CM03	C03	
CM04	C04	
CM05	C05	
CM06	C06	
CM07	C07	
CM08	C08	
CM09	C09	
CM10	C10	
CM11	C11	
CM12	C12	
CM13	C13	
CM14	C14	
CM15	C15	
CM16	C16	
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CM99	C99	
CM100	C100	



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INV_PN

600-11071-BASE-SCH

PCB REV

P1071-001

DATE

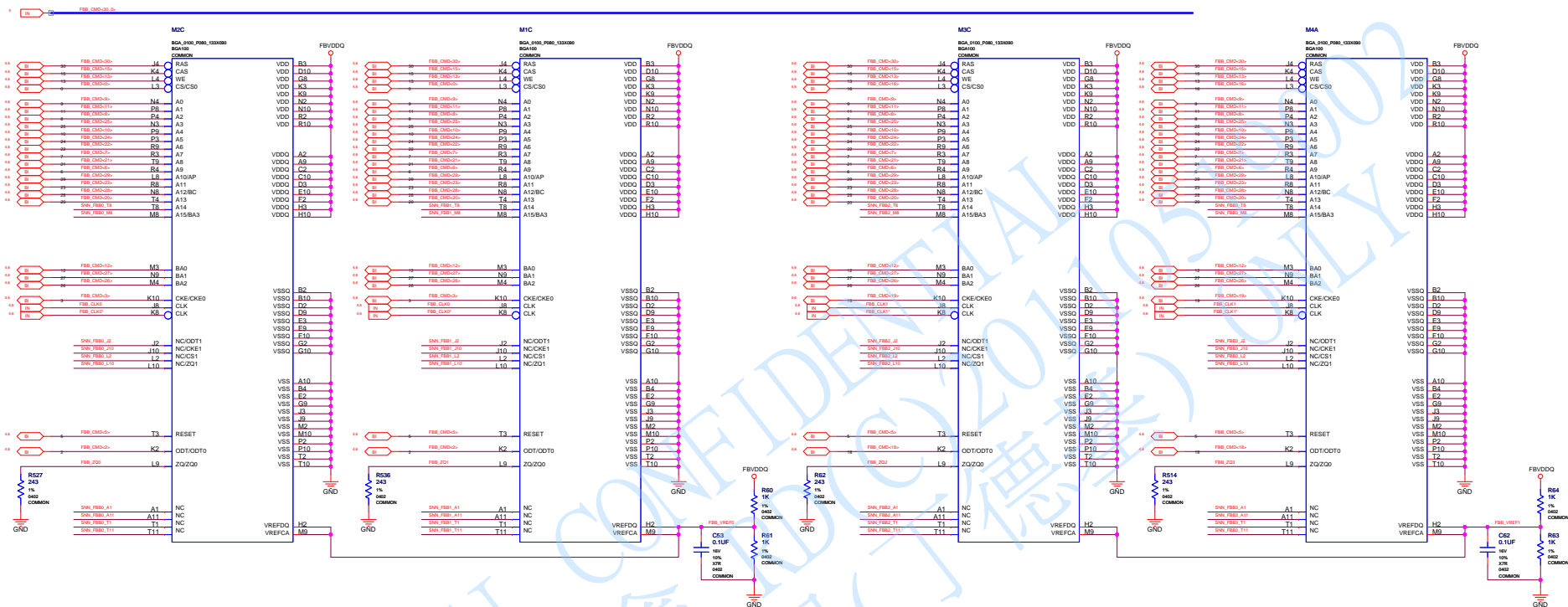
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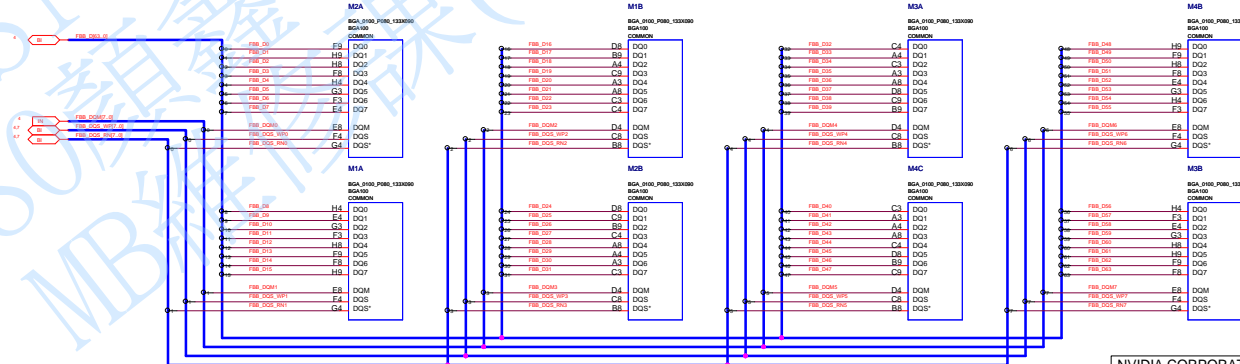
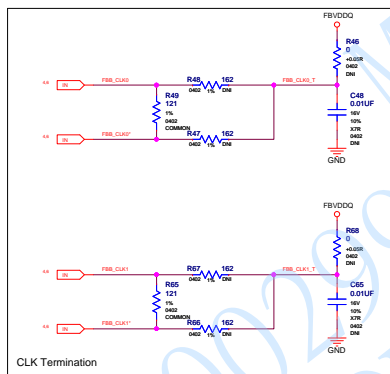
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO. 01/07/01 ASSEMBLY NOTES AND BOARD NOT FINAL
PAGE DETAIL	FBA Partition


FBB Partition

	Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
IN	FBS_CLK0_T		0.75V	
IN	FBS_CLK0_T		0.75V	
IN	FBS_VREF0	1.00MIL		
IN	FBS_VREF1	1.00MIL		
IN	FBS_Z00	1.00MIL		
IN	FBS_Z01	1.00MIL		
IN	FBS_Z02	1.00MIL		
IN	FBS_Z03	1.00MIL		



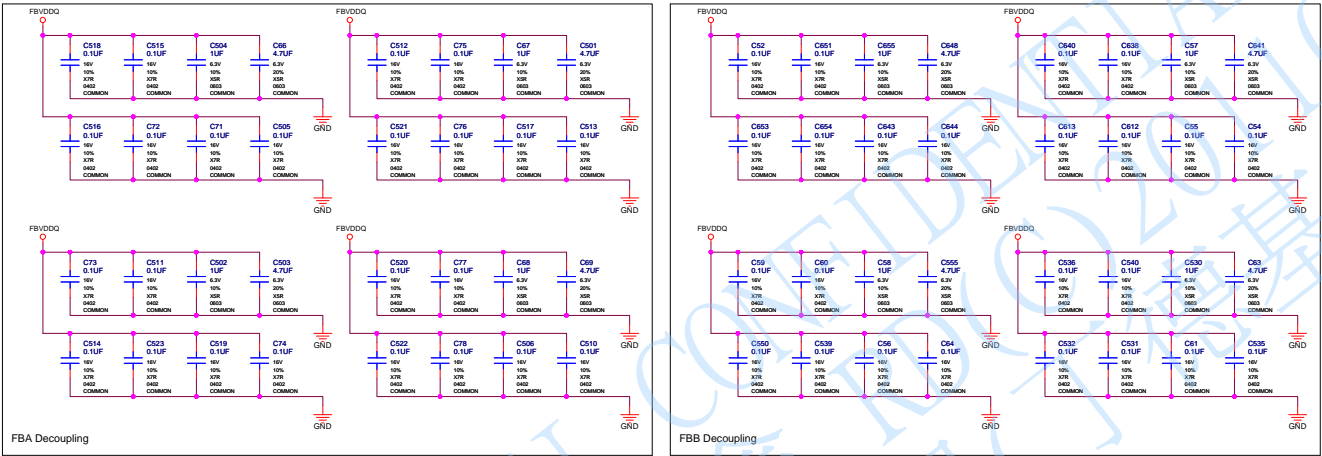
CMD Mapping		
CMD	$\leq 31\%$	$\leq 63\%$
CMD0	CMD	
CMD1		
CMD2	CMD	
CMD3	CMD	
CMD4		
CMD5	SET	SET
CMD6	A0	A0
CMD7	A2	A2
CMD8	A3	A2
CMD9	A4	A2
CMD10	A1	A1
CMD11		
CMD12	BAG	BAG
CMD13	W0	W0
CMD14		
CMD15	CAP	CAP
CMD16		
CMD17		
CMD18		CMD
CMD19		CMD
CMD20	A13	A13
CMD21	A8	A8
CMD22	A6	A6
CMD23	A11	A11
CMD24	A5	A5
CMD25	A3	A3
CMD26	BAG	BAG
CMD27	BAG	BAG
CMD28	A12	A12
CMD29	A10	A10
CMD30	RAG	RAG



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Memory Decoupling



Net Name		DIFF_PAIR	CRITICAL	IMPEDANCE
FBA_DQS_VDDQ	FBA_DQS0	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS1	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS2	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS3	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS4	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS5	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS6	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS7	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS8	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS9	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS10	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS11	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS12	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS13	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS14	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS15	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS16	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS17	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS18	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS19	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS20	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS21	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS22	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS23	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS24	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS25	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS26	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS27	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS28	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS29	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS30	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS31	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS32	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS33	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS34	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS35	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS36	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS37	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS38	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS39	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS40	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS41	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS42	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS43	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS44	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS45	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS46	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS47	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS48	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS49	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS50	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS51	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS52	+	NOSET	
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FBA_DQS_VDDQ	FBA_DQS55	+	NOSET	
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FBA_DQS_VDDQ	FBA_DQS57	+	NOSET	
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FBA_DQS_VDDQ	FBA_DQS59	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS60	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS61	+	NOSET	
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FBA_DQS_VDDQ	FBA_DQS64	+	NOSET	
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FBA_DQS_VDDQ	FBA_DQS87	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS88	+	NOSET	
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FBA_DQS_VDDQ	FBA_DQS90	+	NOSET	
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FBA_DQS_VDDQ	FBA_DQS93	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS94	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS95	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS96	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS97	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS98	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS99	+	NOSET	
FBA_DQS_VDDQ	FBA_DQS100	+	NOSET	

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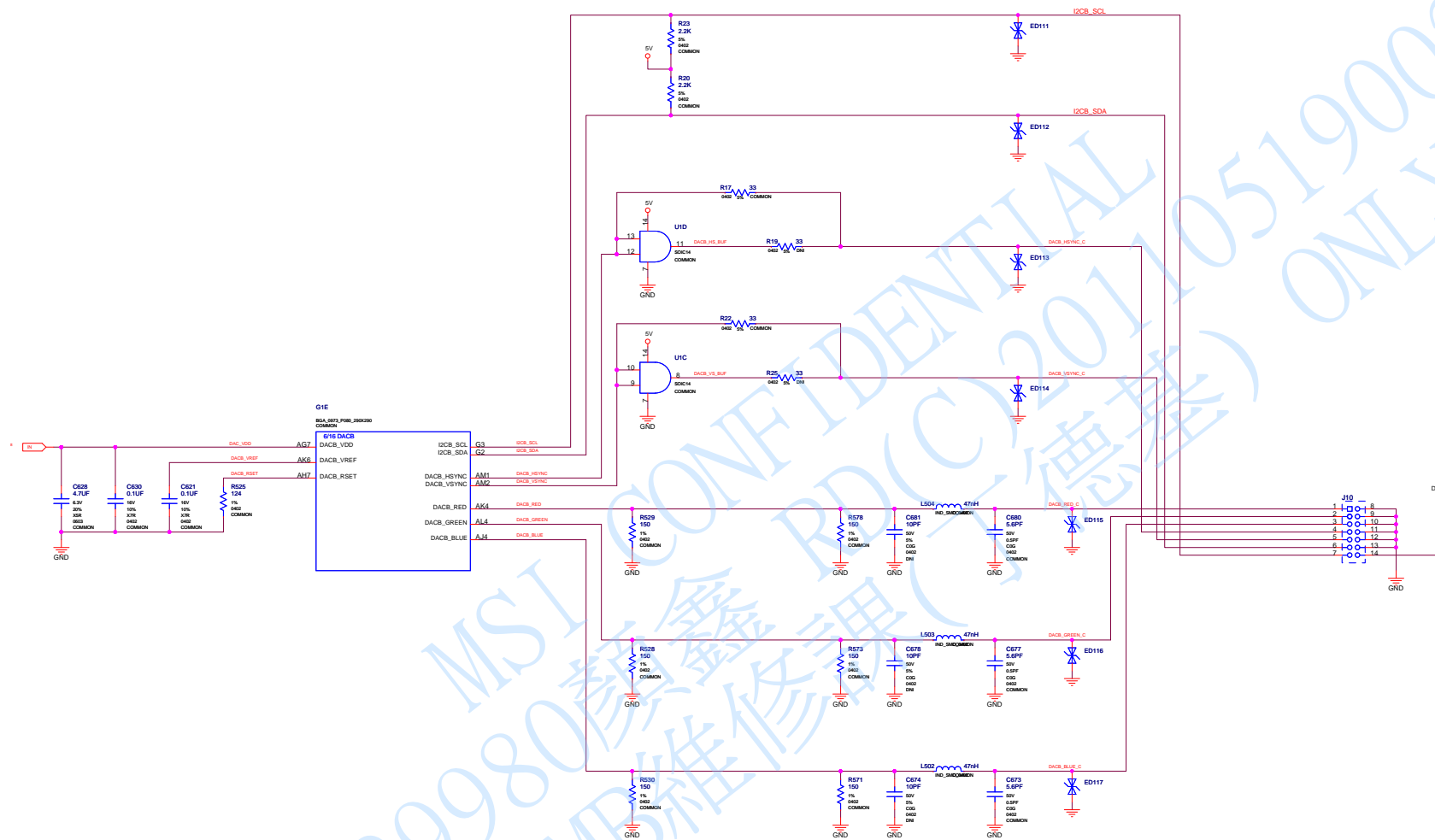
NVIDIA CORPORATION
2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA

NV_PN	600-11071-BASE-SCH		
PCB REV	P1071-A01	PAGE	
BOX REV	A	DATE	01-APR-2010

DACA VGA

Net Name		DIFF_PAIR	CRITICAL	IMPEDANCE
1	DACA_RED	1		50OHM
2	DACA_GREEN	1		50OHM
3	DACA_BLUE	1		50OHM
4	DACA_HS_BUF	2		50OHM
5	DACA_VSYNC	2		50OHM
6	DACA_SCL	1		50OHM
7	DACA_SDA	1		50OHM
8	DACA_VREF	1		50OHM
9	DACA_RST	1		50OHM
10	DACA_HS_BUF	2		50OHM
11	DACA_VSYNC	2		50OHM
12	DACA_SCL	1		50OHM
13	DACA_SDA	1		50OHM
14	DACA_VREF	1		50OHM
15	DACA_RST	1		50OHM
16	DACA_HS_BUF	2		50OHM
17	DACA_VSYNC	2		50OHM
18	DACA_SCL	1		50OHM
19	DACA_SDA	1		50OHM
20	DACA_VREF	1		50OHM
21	DACA_RST	1		50OHM
22	DACA_HS_BUF	2		50OHM
23	DACA_VSYNC	2		50OHM
24	DACA_SCL	1		50OHM
25	DACA_SDA	1		50OHM
26	DACA_VREF	1		50OHM
27	DACA_RST	1		50OHM
28	DACA_HS_BUF	2		50OHM
29	DACA_VSYNC	2		50OHM
30	DACA_SCL	1		50OHM
31	DACA_SDA	1		50OHM
32	DACA_VREF	1		50OHM
33	DACA_RST	1		50OHM
34	DACA_HS_BUF	2		50OHM
35	DACA_VSYNC	2		50OHM
36	DACA_SCL	1		50OHM
37	DACA_SDA	1		50OHM
38	DACA_VREF	1		50OHM
39	DACA_RST	1		50OHM
40	DACA_HS_BUF	2		50OHM
41	DACA_VSYNC	2		50OHM
42	DACA_SCL	1		50OHM
43	DACA_SDA	1		50OHM
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45	DACA_RST	1		50OHM
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47	DACA_VSYNC	2		50OHM
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63	DACA_RST	1		50OHM
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67	DACA_SDA	1		50OHM
68	DACA_VREF	1		50OHM
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71	DACA_VSYNC	2		50OHM
72	DACA_SCL	1		50OHM
73	DACA_SDA	1		50OHM
74	DACA_VREF	1		50OHM
75	DACA_RST	1		50OHM
76	DACA_HS_BUF	2		50OHM
77	DACA_VSYNC	2		50OHM
78	DACA_SCL	1		50OHM
79	DACA_SDA	1		50OHM
80	DACA_VREF	1		50OHM
81	DACA_RST	1		50OHM
82	DACA_HS_BUF	2		50OHM
83	DACA_VSYNC	2		50OHM
84	DACA_SCL	1		50OHM
85	DACA_SDA	1		50OHM
86	DACA_VREF	1		50OHM
87	DACA_RST	1		50OHM
88	DACA_HS_BUF	2		50OHM
89	DACA_VSYNC	2		50OHM
90	DACA_SCL	1		50OHM
91	DACA_SDA	1		50OHM
92	DACA_VREF	1		50OHM
93	DACA_RST	1		50OHM
94	DACA_HS_BUF	2		50OHM
95	DACA_VSYNC	2		50OHM
96	DACA_SCL	1		50OHM
97	DACA_SDA	1		50OHM
98	DACA_VREF	1		50OHM
99	DACA_RST	1		50OHM
100	DACA_HS_BUF	2		50OHM
101	DACA_VSYNC	2		50OHM
102	DACA_SCL	1		50OHM
103	DACA_SDA	1		50OHM
104	DACA_VREF	1		50OHM
105	DACA_RST	1		50OHM
106	DACA_HS_BUF	2		50OHM
107	DACA_VSYNC	2		50OHM
108	DACA_SCL	1		50OHM
109	DACA_SDA	1		50OHM
110	DACA_VREF	1		50OHM
111	DACA_RST	1		50OHM
112	DACA_HS_BUF	2		50OHM
113	DACA_VSYNC	2		50OHM
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115	DACA_SDA	1		50OHM
116	DACA_VREF	1		50OHM
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121	DACA_SDA	1		50OHM
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124	DACA_HS_BUF	2		50OHM
125	DACA_VSYNC	2		50OHM
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127	DACA_SDA	1		50OHM
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130	DACA_HS_BUF	2		50OHM
131	DACA_VSYNC	2		50OHM
132	DACA_SCL	1		50OHM
133	DACA_SDA	1		50OHM
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135	DACA_RST	1		50OHM
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137	DACA_VSYNC	2		50OHM
138	DACA_SCL	1		50OHM
139	DACA_SDA	1		50OHM
140	DACA_VREF	1		50OHM
141	DACA_RST	1		50OHM
142	DACA_HS_BUF	2		50OHM
143	DACA_VSYNC	2		50OHM
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145	DACA_SDA	1		50OHM
146	DACA_VREF	1		50OHM
147	DACA_RST	1		50OHM
148	DACA_HS_BUF	2		50OHM
149	DACA_VSYNC	2		50OHM
150	DACA_SCL	1		50OHM
151	DACA_SDA	1		50OHM
152	DACA_VREF	1		50OHM
153	DACA_RST	1		50OHM
154	DACA_HS_BUF	2		50OHM
155	DACA_VSYNC	2		50OHM
156	DACA_SCL	1		50OHM
157	DACA_SDA	1		50OHM
158	DACA_VREF	1		50OHM
159	DACA_RST	1		50OHM
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163	DACA_SDA	1		50OHM
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167	DACA_VSYNC	2		50OHM
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176	DACA_VREF	1		50OHM
177	DACA_RST	1		50OHM
178	DACA_HS_BUF	2		50OHM
179	DACA_VSYNC	2		50OHM
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181	DACA_SDA	1		50OHM
182	DACA_VREF	1		50OHM
183	DACA_RST	1		50OHM
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185	DACA_VSYNC	2		50OHM
186	DACA_SCL	1		50OHM
187	DACA_SDA	1		50OHM
188	DACA_VREF	1		50OHM
189	DACA_RST	1		50OHM
190	DACA_HS_BUF	2		50OHM
191	DACA_VSYNC	2		50OHM
192	DACA_SCL	1		50OHM
193	DACA_SDA	1		50OHM
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195	DACA_RST	1		50OHM
196	DACA_HS_BUF	2		50OHM
197	DACA_VSYNC	2		50OHM
198	DACA_SCL	1		50OHM
199	DACA_SDA	1		50OHM
200	DACA_VREF	1		50OHM
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203	DACA_VSYNC	2		50OHM
204	DACA_SCL	1		50OHM
205	DACA_SDA	1		50OHM
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207	DACA_RST	1		50OHM
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215	DACA_VSYNC	2		50OHM
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243	DACA_RST	1		50OHM
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247	DACA_SDA	1		50OHM
248	DACA_VREF	1		50OHM
249	DACA_RST	1		50OHM
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253	DACA_SDA	1		50OHM
254	DACA_VREF	1		50OHM
255	DACA_RST	1		50OHM
256	DACA_HS_BUF	2		50OHM
257	DACA_VSYNC	2		50OHM
258	DACA_SCL	1		50OHM
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265	DACA_SDA	1		50OHM
266	DACA_VREF	1		50OHM
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273	DACA_RST	1		50OHM
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279	DACA_RST	1		50OHM
280	DACA_HS_BUF	2		50OHM
281	DACA_VSYNC	2		50OHM
282	DACA_SCL	1		50OHM
283	DACA_SDA	1		50OHM
284	DACA_VREF	1		50OHM
285	DACA_RST	1		50OHM
286	DACA_HS_BUF	2		50OHM
287	DACA_VSYNC	2		50OHM
288	DACA_SCL	1		50OHM
289	DACA_SDA	1		50OHM
290	DACA_VREF	1		50OHM
291	DACA_RST	1		50OHM
292	DACA_HS_BUF	2		50OHM
293	DACA_VSYNC	2		50OHM
294	DACA_SCL	1		50OHM
295	DACA_SDA	1		50OHM
296	DACA_VREF	1		50OHM
297	DACA_RST	1		50OHM
298	DACA_HS_BUF	2		50OHM
299	DACA_VSYNC	2		50OHM
300	DACA_SCL	1		50OHM
301	DACA_SDA	1		50OHM
302	DACA_VREF	1		50OHM
303	DACA_RST	1		50OHM
304	DACA_HS_BUF	2		50OHM
305	DACA_VSYNC	2		50OHM
306	DACA_SCL	1		50OHM
307	DACA_SDA	1		50OHM
308	DACA_VREF	1		50OHM
309	DACA_RST	1		50OHM
310	DACA_HS_BUF	2		50OHM
311	DACA_VSYNC	2		50OHM
312	DACA_SCL	1		50OHM
313	DACA_SDA	1		50OHM
314	DACA_VREF	1		50OHM
315	DACA_RST	1		50OHM
316	DACA_HS_BUF	2		50OHM
317	DACA_VSYNC	2		50OHM
318	DACA_SCL	1		50OHM
319	DACA_SDA	1		50OH

DACB VGA Header



Net Name		DIFF_PAIR	CRITICAL	IMPEDANCE
IN	DACB_RED		1	50OHMS
IN	DACB_RED_C		1	50OHMS
IN	DACB_GREEN		1	50OHMS
IN	DACB_GREEN_C		1	50OHMS
IN	DACB_BLUE		1	50OHMS
IN	DACB_BLUE_C		1	50OHMS
IN	DACB_HYSTING		2	50OHMS
IN	DACB_HS_BUF		2	50OHMS
IN	DACB_HYSTING_C		2	50OHMS
IN	DACB_HYSTING_2		2	50OHMS
IN	DACB_HS_BUF_2		2	50OHMS
IN	DACB_HYSTING_3		2	50OHMS
IN	DACB_HYSTING_4		2	50OHMS

Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
IN DQB_SCL			
IN DQB_SDA			
IN DACB_VREF	125MIL		
IN DACB_RST	125MIL		

SPECIAL MECHANIC
No connected mounting pins

MEC3

NVIDIA CORPORATION

2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA



NV_PN	600-11071-BASE-SCH
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PCB REV	P1071-AD1
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BOM REV	1
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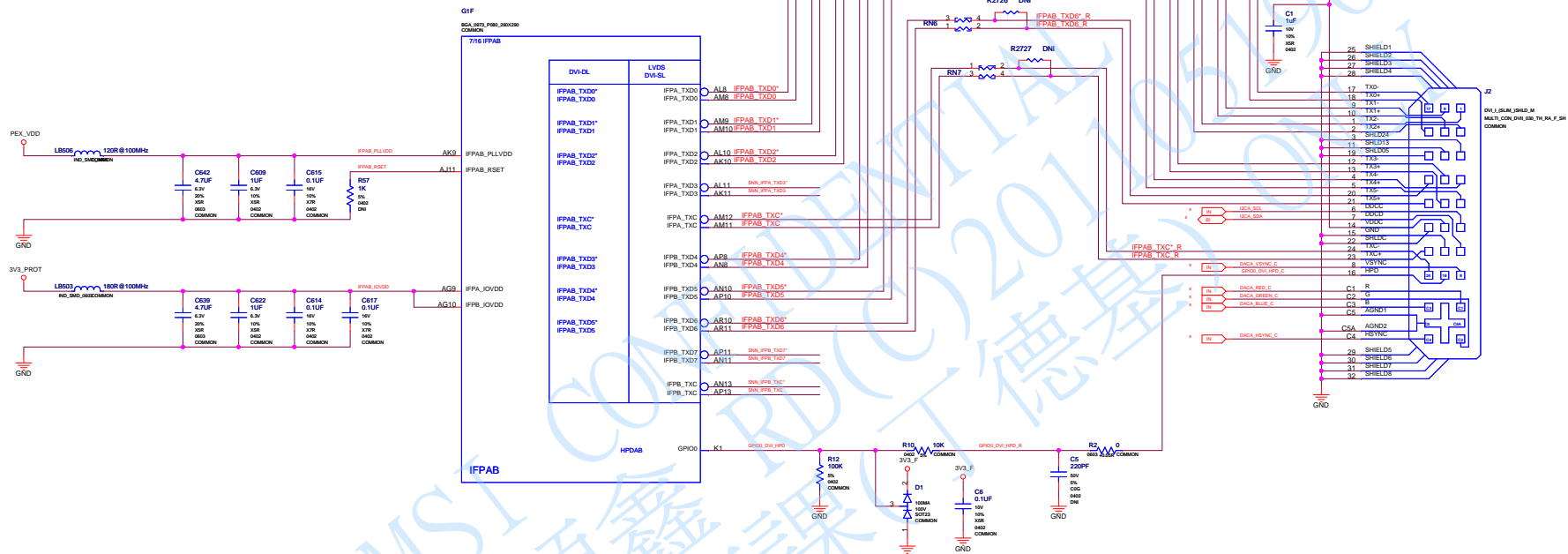
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DATE	01-APR-2010
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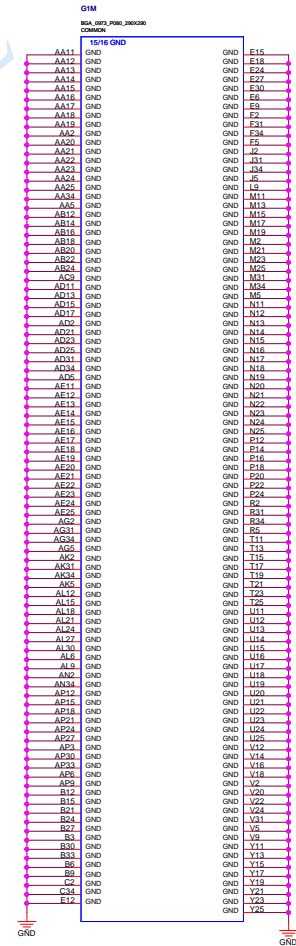
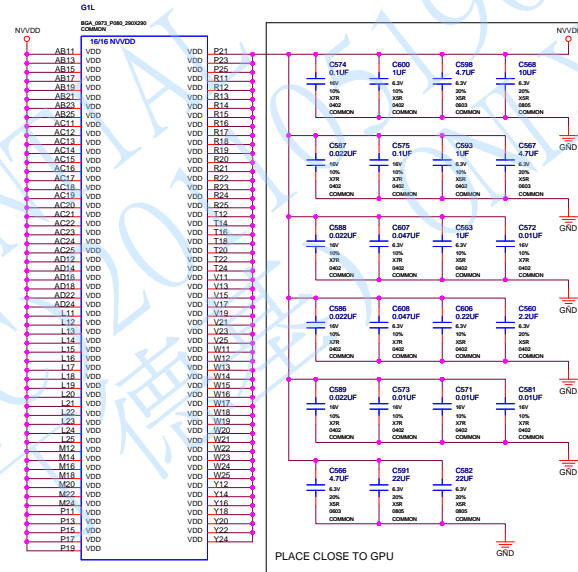
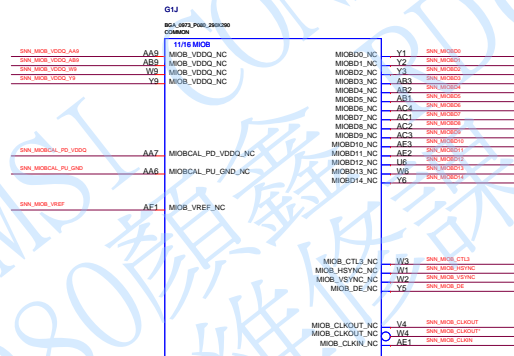
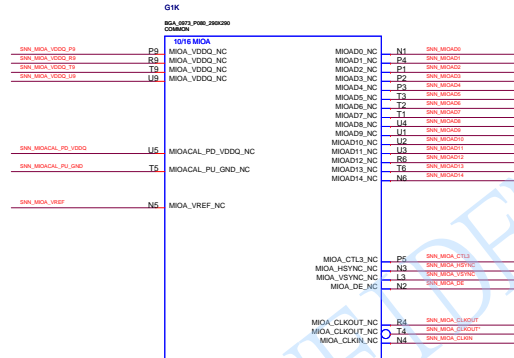
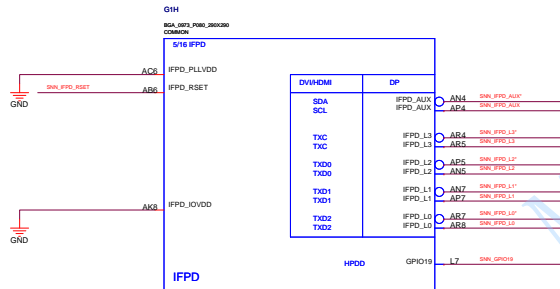
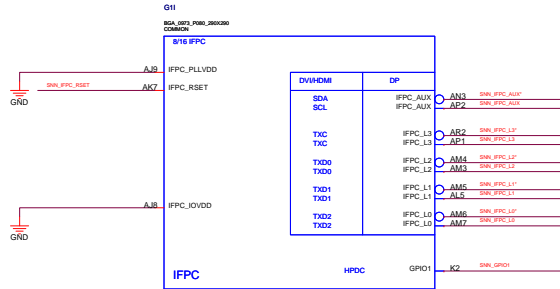
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACB VGA Header

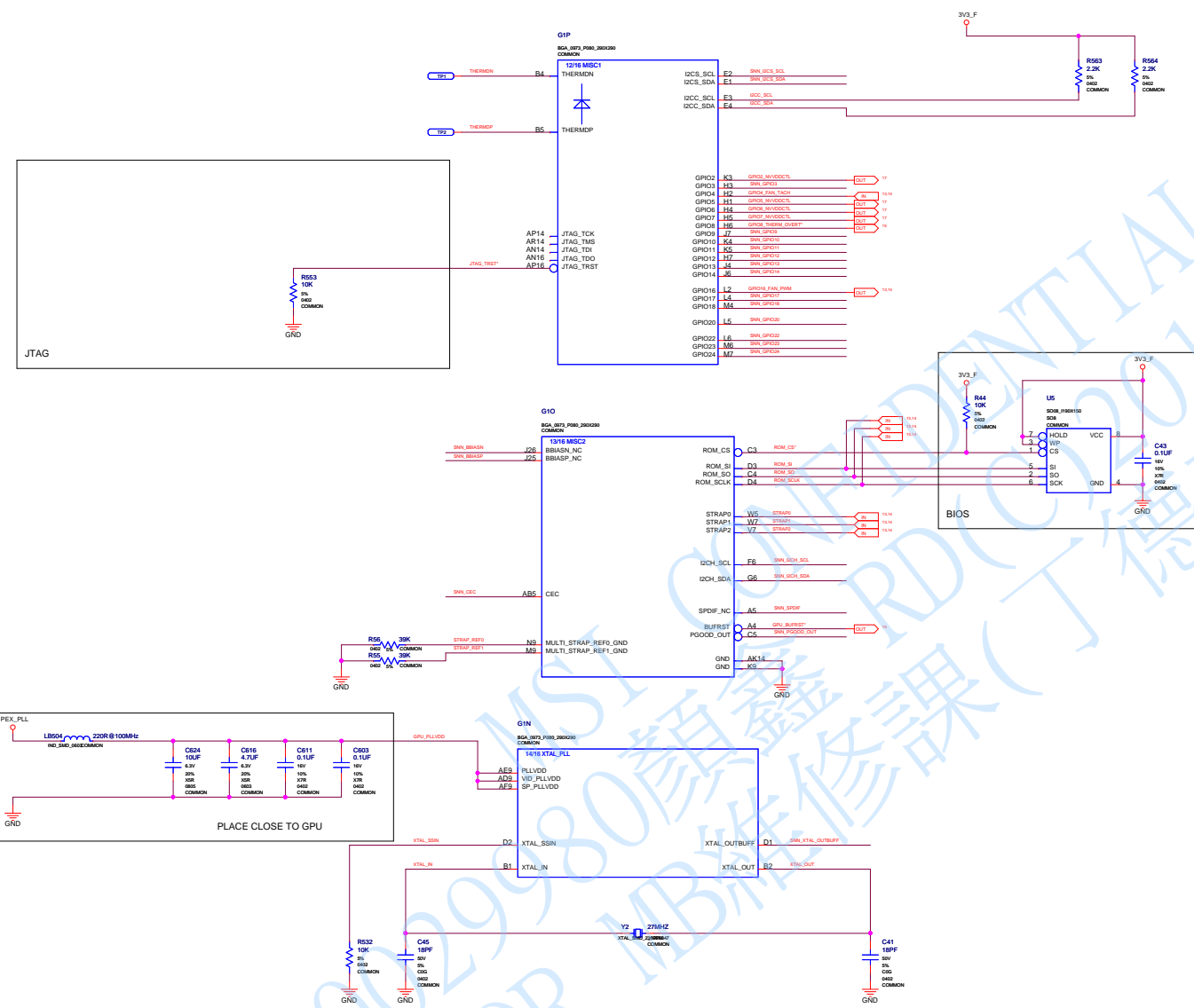
TMDS Interface

[illegible]

Unused Interfaces, NVVDD Decoupling



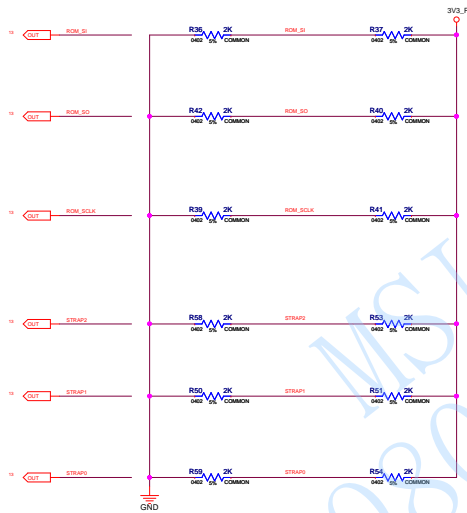
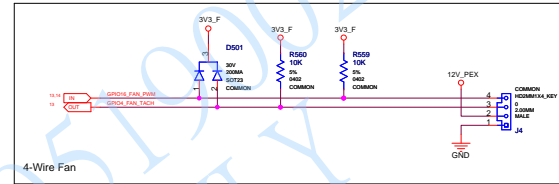
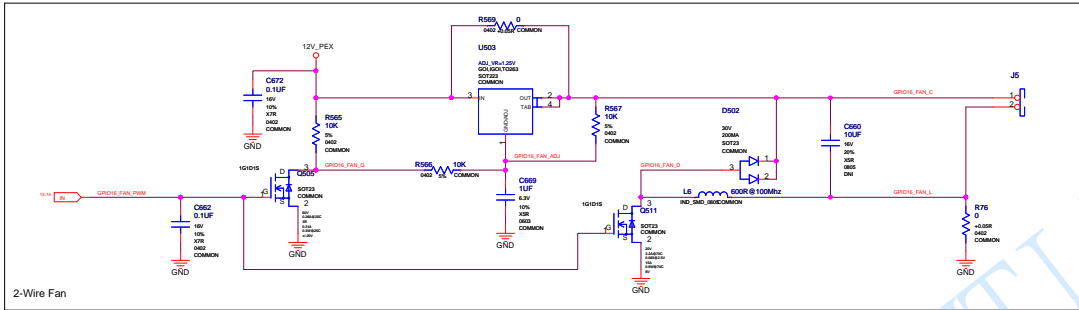
XTAL, ROM, JTAG, Misc



Net Name		DIFF_PAIR	CRITICAL	IMPEDANCE
IN	XTAL_IN	XTAL	1	50ohm
OUT	XTAL_OUT	XTAL	1	50ohm

Net Name		MIN_WIDTH	VOLTAGE	MAX_CURRENT
IN	XTAL_SSN			
IN	THERM0N	10mil		
IN	THERM0P	10mil		
IN	JTAG_TCK			
IN	JTAG_TMS			
IN	JTAG_TDI			
IN	JTAG_TDO			
IN	JTAG_TRST			
IN	STRAP_REF0			
IN	STRAP_REF1			
IN	BIOS_CS			
IN	BIOS_SCLK			
IN	BIOS_SCS_B			
IN	BIOS_SCLK_R			
IN	BIOS_VDD	10mil	3.3V	
IN	GPIO2			
IN	GPIO3			
IN	GPIO4			
IN	GPIO5			
IN	GPIO6			
IN	GPIO7			
IN	GPIO8			
IN	GPIO9			
IN	GPIO10			
IN	GPIO11			
IN	GPIO12			
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Straps, Thermal, Mechanical

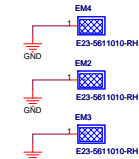
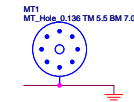
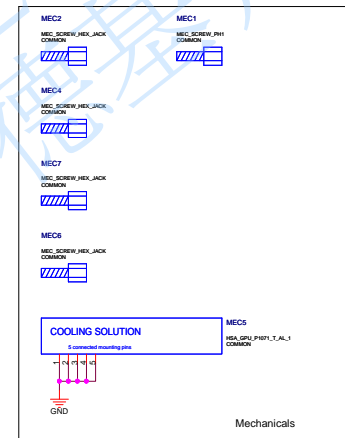



GF108 Straps

Bit Signal	Values
------------	--------

03	RANCFQ2	0000	0	Disable	0
02	RANCFQ2	0001	0	Monitor	0
01	RANCFQ2	0011	0	Shake	0
01	RANCFQ2	0010	0	Clamping	0
		0100	0		0
		0101	0		0
		0111	0		0
03	ICL4_417	0	0	Disable	0
		1	0	Enable	0
02	FB00	0	0	Default	0
01	SMB_ACT_ADR0	0	0	CoE	0
		1	0	SiOC	0
00	VGA_DEVICE	0	0	Disable	0
		1	0	Enable	0
03	PCI_DEV_ID_EXT	0	0	CF100-300-01	0
		1	0	CF100-300-01	0
02	SUB_VENDOR	0	0	NU805	0
		1	0	805	0
01	SLOT_CLK_CFG	0	0	Disable	0
		1	0	Enable	0
00	PEX_FLL_EN_TERM100	0	0	Disable	0
		1	0	Enable	0
03	PCI_DEVQ2	0000	000	CF100-300-01	0
02	PCI_DEVQ2	0001	000	CF100-300-01	0
01	PCI_DEVQ2	0010	000		0
01	PCI_DEVQ2	0011	000		0
		0100	000		0
		0101	000		0
		0111	000		0
03	SIGD_FADCFG_LIST_WDRQ	0000	0000	DSGTOP_DEFAULT	000
		0001	0000	MOBILE_DEFAULT	000
		0010	0000	MOBILE_NTHRS_LAMP	100
		0011	0000	MOBILE_NTHRS_LAMP	100
		0100	0000	MOBILE_NTHRS_LAMP	110
		0101	0000	MOBILE_NTHRS_VHAMP	110
		0110	0000	MOBILE_NTHRS_VHAMP	110
		0111	0000	MOBILE_NTHRS_VHAMP	110

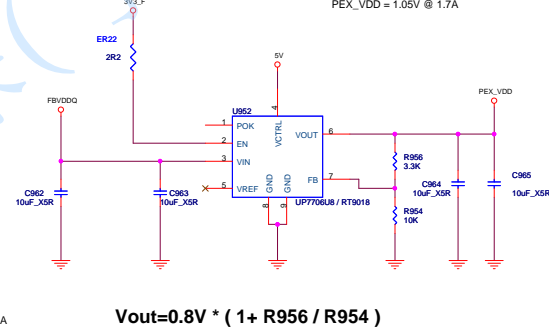
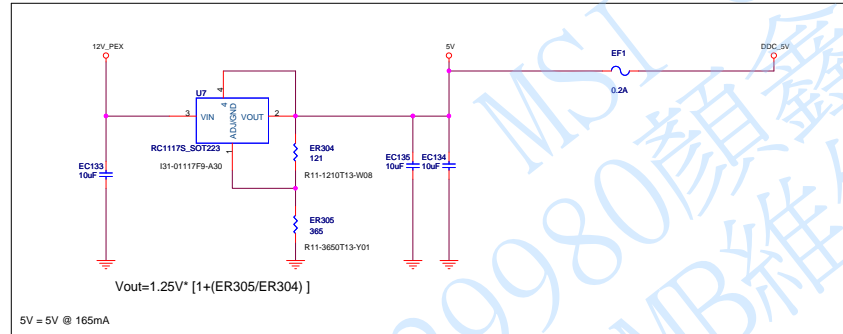
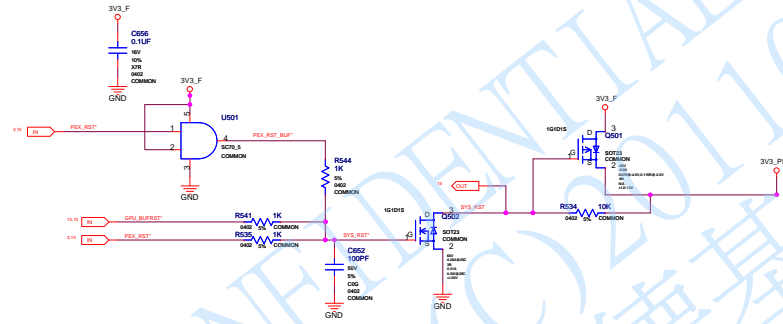
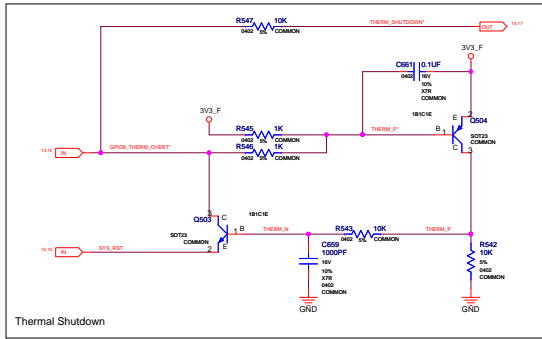
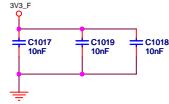
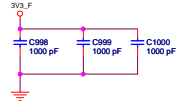
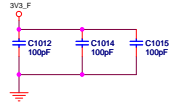
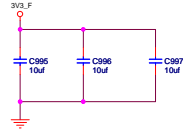
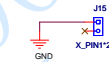
Multilevel Straps	
5K 5W GRD	0000
10K 5W GRD	0001
15K 5W GRD	0010
20K 5W GRD	0011
25K 5W GRD	0100
30K 5W GRD	0101
35K 5W GRD	0110
40K 5W GRD	0111
5K 5V VCC	1000
10K 5V VCC	1001
15K 5V VCC	1010
20K 5V VCC	1011
25K 5V VCC	1100
30K 5V VCC	1101
35K 5V VCC	1110
40K 5V VCC	1111



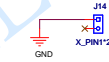
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PCB REV	P1071-A01	PAGE	
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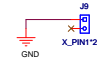
Power Supply I: Misc Power, Thermal Shutdown

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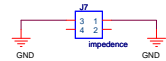
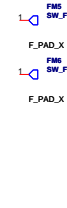
Top
Single Ended_Memory CMD
6.6929 mil
40 ohm +/- 10%



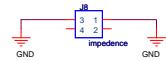
Int3
Single Ended_RGB
4.76377 mil
50 ohm +/- 10%



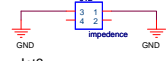
Bottom
Single Ended_Memory Data
3.937 mil
53 ohm +/- 10%



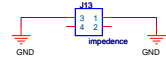
Top
Differential_PCIE BUS
3.94 / 4.72 mil
90 ohm +/- 10%




Int3
Differential_DVI
3.94 / 5.98 mil
90 ohm +/- 10%



Differential_Memory Clock
4.33 / 4.13 mil
80 ohm +/- 10%



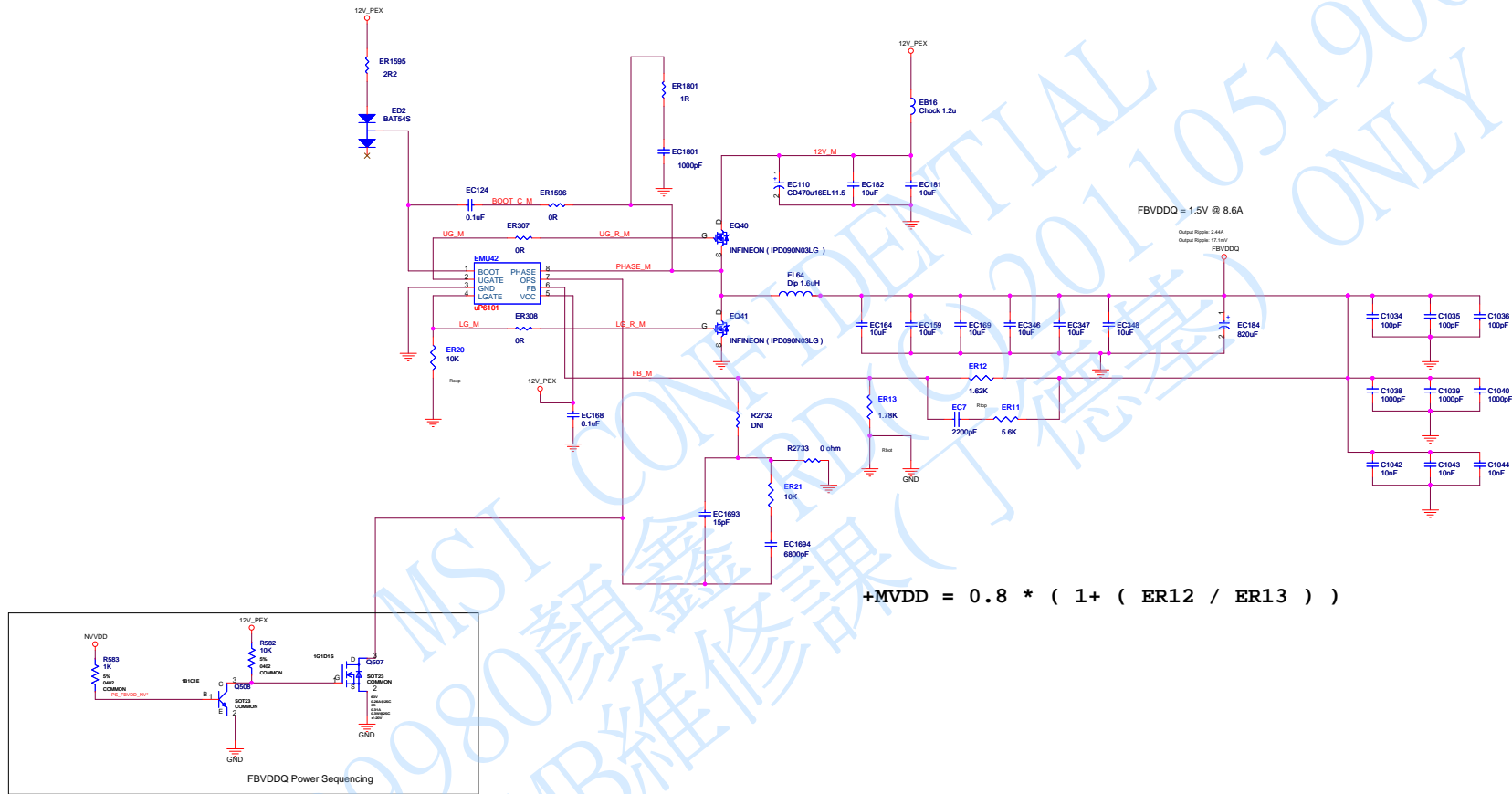
Bottom
Differential_PCIE BUS
3.94 / 4.72 mil
90 ohm +/- 10%

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NV_PN		600-11071-BASE-SCH	
PCB REV	P1071-AD1	PAGE	
BOM REV	A	DATE	01-APR-2010

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Power Supply II: FBVDDQ

	Net Name	MIN. WIDTH	VOLTAGE	MAX. CURRENT
28	PS_FVDD0_VDD	1.0000		
29	PS_FVDD0_EN	1.0000		
30	PS_FVDD0_RST			
31				
32	PS_FVDD0_RST0	1.0000		
33	PS_FVDD0_RST0_PS	1.0000		
34	PS_FVDD0_FB			
35	PS_FVDD0_FB_RST			
36	PS_FVDD0_CP_RST			
37				
38	PS_FVDD0_VDD	1.0000		
39	PS_FVDD0_VDD_0	1.0000		
40	PS_FVDD0_L0	1.0000		
41	PS_FVDD0_POWER			
42	PS_FVDD0_SNDR			
	FVDD0	1.0000	1.200	0.0A


$$+MVDD = 0.8 * (1 + (ER12 / ER13))$$

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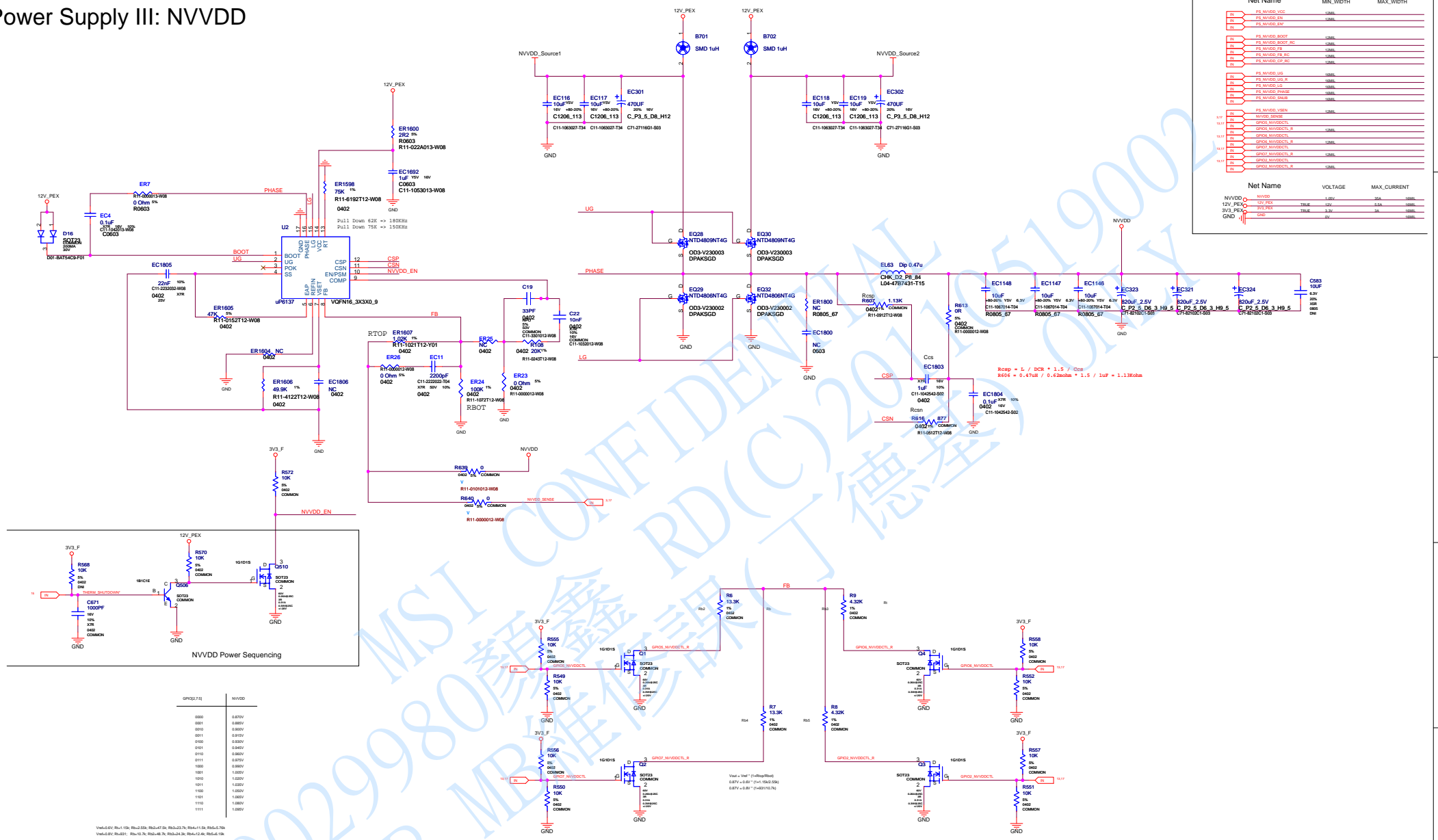
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BOM REV	A	DATE	01-APR-2010

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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Power Supply II: FBVDDQ

Power Supply III: NVVDD



Net Name		MIN_WIDTH	MAX_WIDTH
(H)	PS_NVIDEO_VDD	12mil	
(H)	PS_NVIDEO_EN	12mil	
(H)	PS_NVIDEO_STB	12mil	
(H)	PS_NVIDEO_RESET	12mil	
(H)	PS_NVIDEO_RESET_B0	12mil	
(H)	PS_NVIDEO_FB	12mil	
(H)	PS_NVIDEO_FB_0C	12mil	
(H)	PS_NVIDEO_CP_0C	12mil	
(H)	PS_NVIDEO_LAS	12mil	
(H)	PS_NVIDEO_LAS_B	12mil	
(H)	PS_NVIDEO_A0	12mil	
(H)	PS_NVIDEO_P0REQ	12mil	
(H)	PS_NVIDEO_STROB	12mil	
(H)	PS_NVIDEO_VIDEN	12mil	
8.3.7	NPVIDE_VDDEN	12mil	
8.3.7	GRF0D_NVIDE0EN1	12mil	
8.3.7	GRF0D_NVIDE0EN2	12mil	
8.3.7	GRF0D_NVIDE0EN3	12mil	
8.3.7	GRF0D_NVIDE0EN4	12mil	
8.3.7	GRF0D_NVIDE0EN5	12mil	
8.3.7	GRF0D_NVIDE0EN6	12mil	
8.3.7	GRF0D_NVIDE0EN7	12mil	

Net Name		VOLTAGE	MAX_CURRENT
NVIDEO	NVIDEO	1.2V	5A
12V_PEX0	12V_PEX	1.8V	5.5A
20V_PEX0	20V_PEX	1.8V	5A
GND	GND	0V	

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