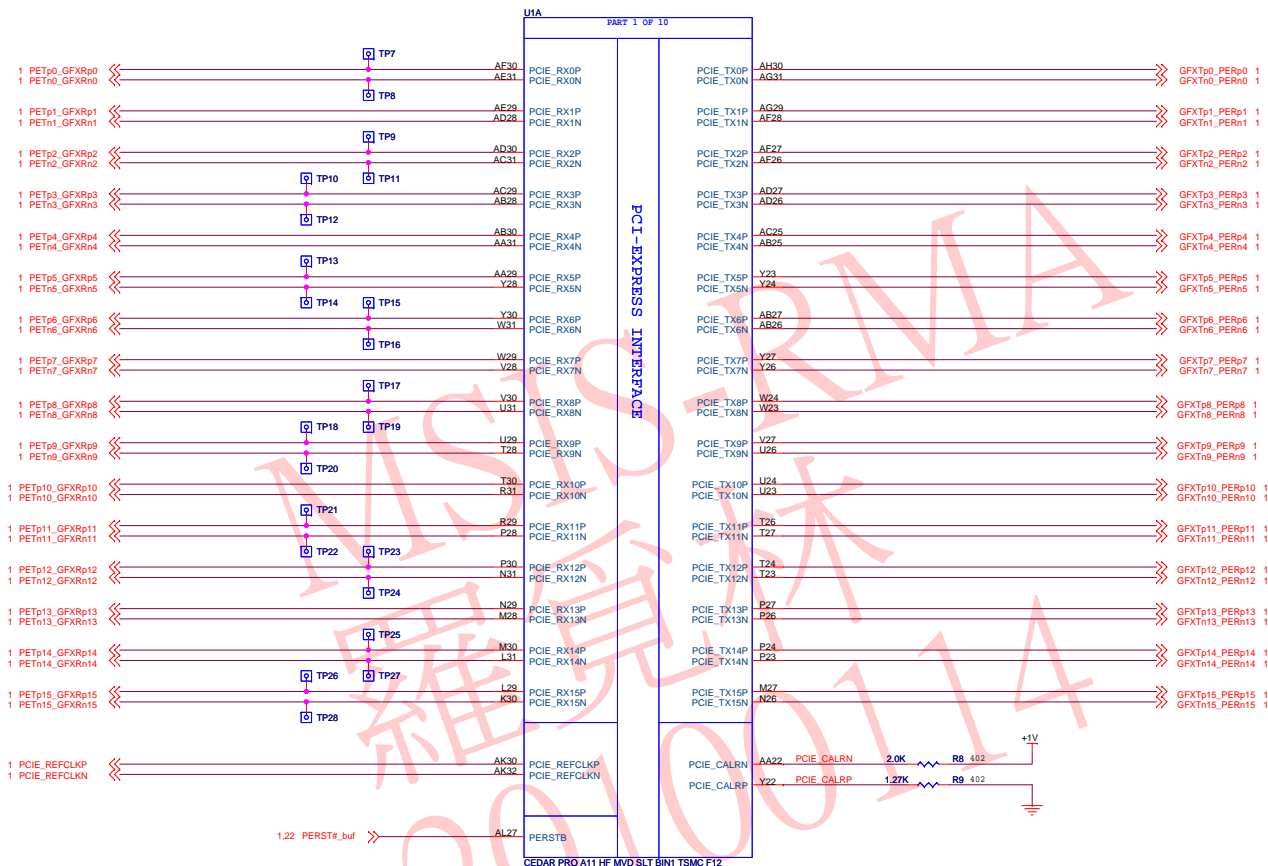
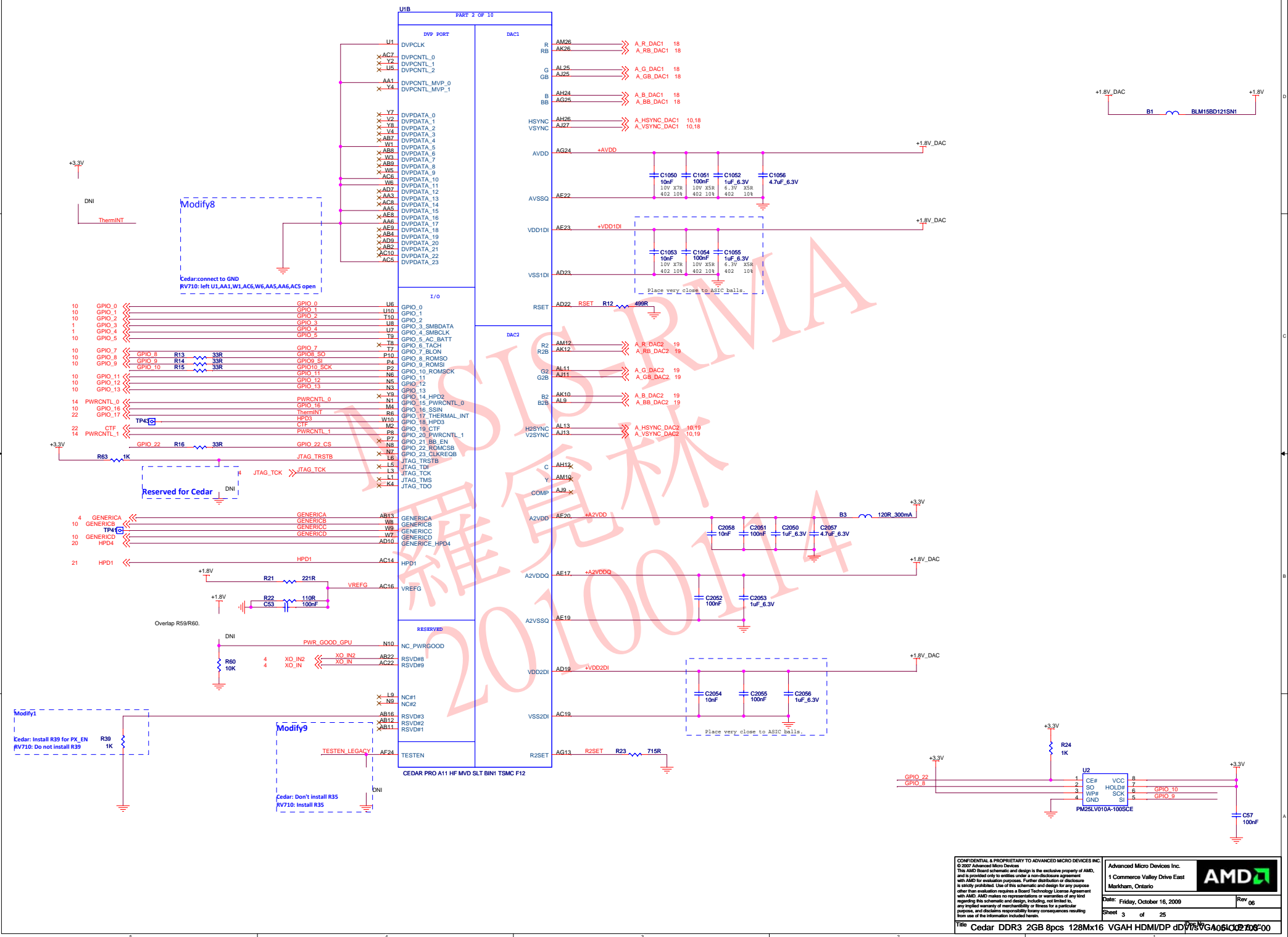


(2) Cedar PCIE Interface

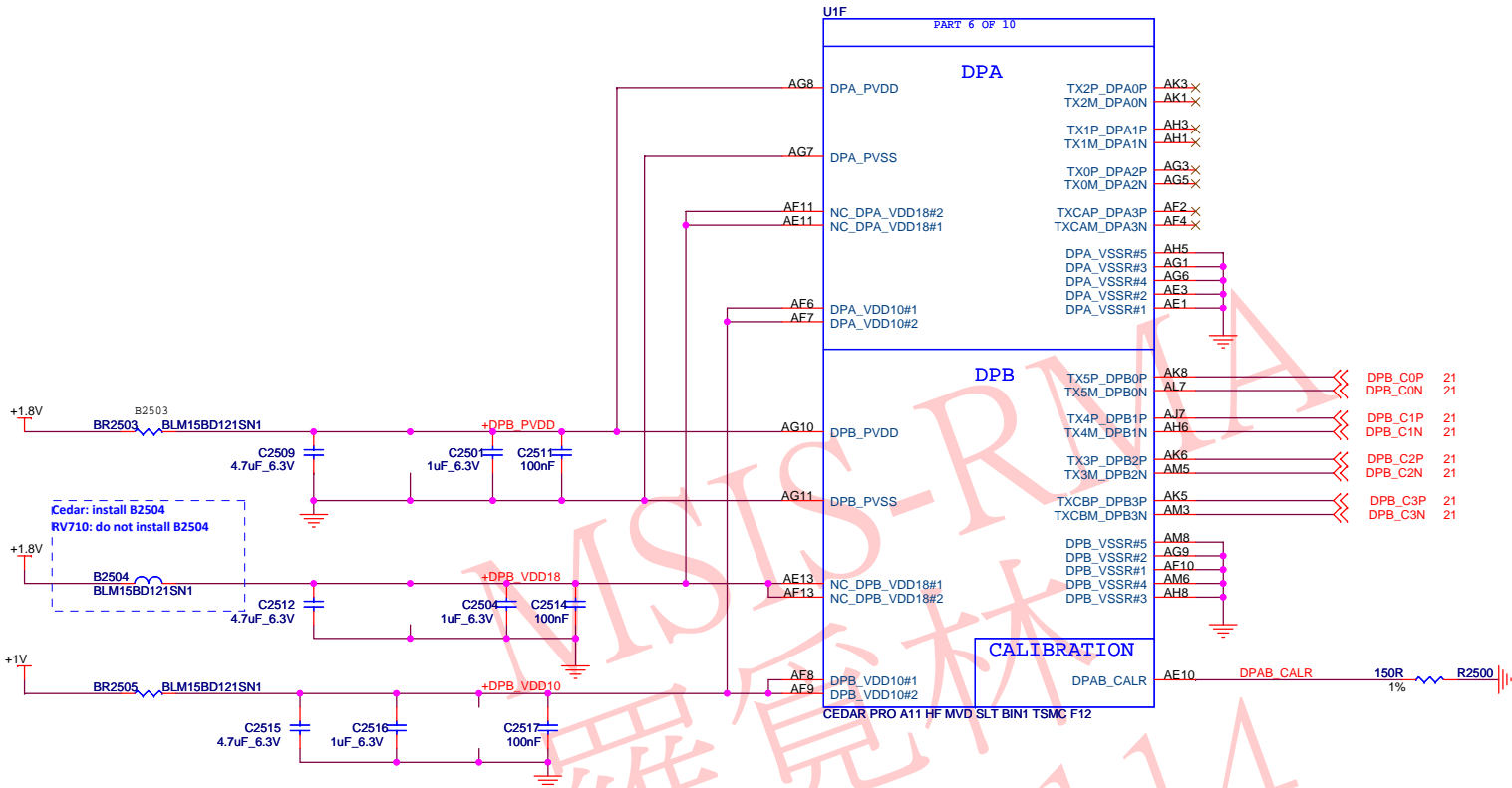
NOTE: some of the PCIE testpoints will be available through via on traces.



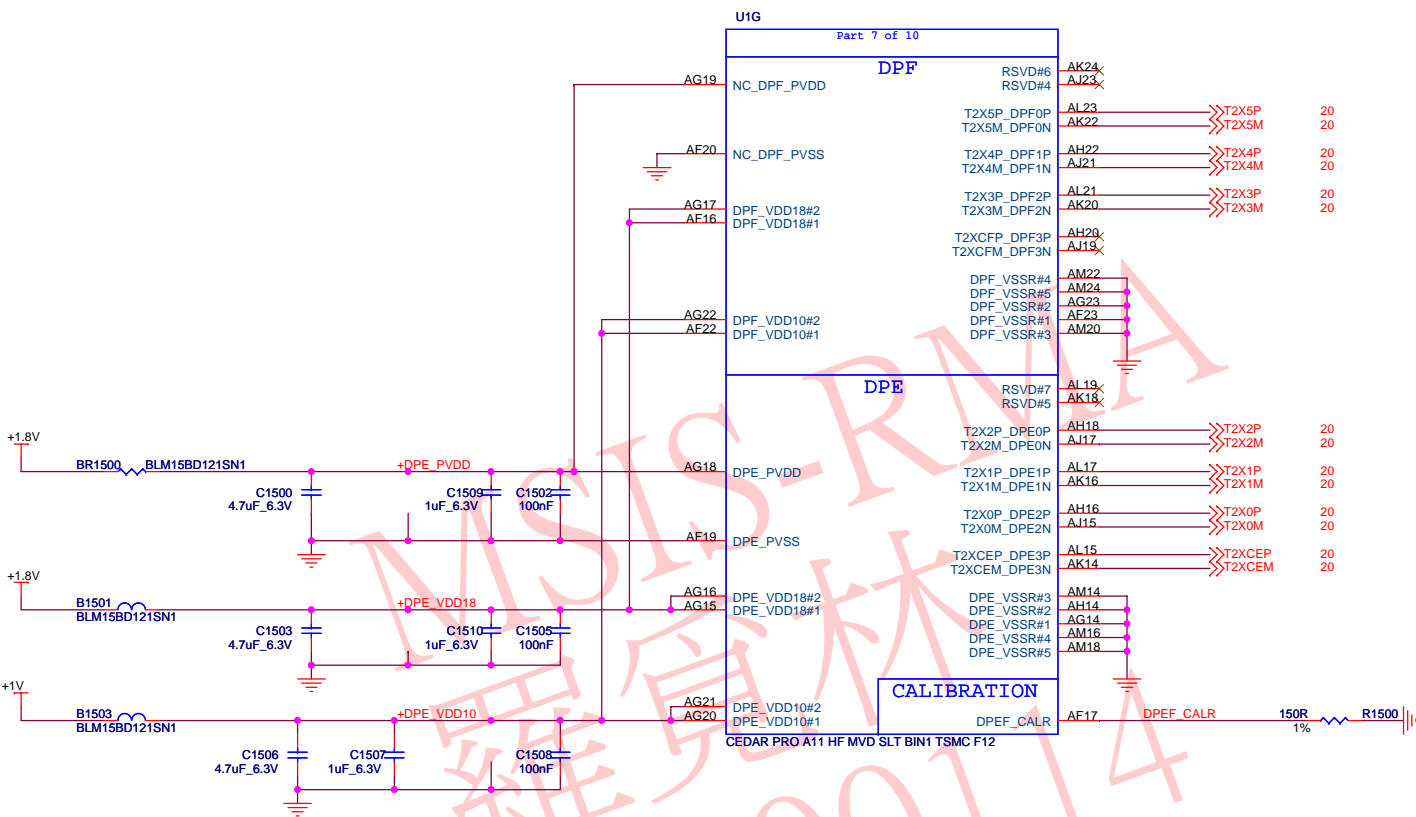
(3) Cedar Main



TMDP INTERFACE



LVTMDP INTERFACE



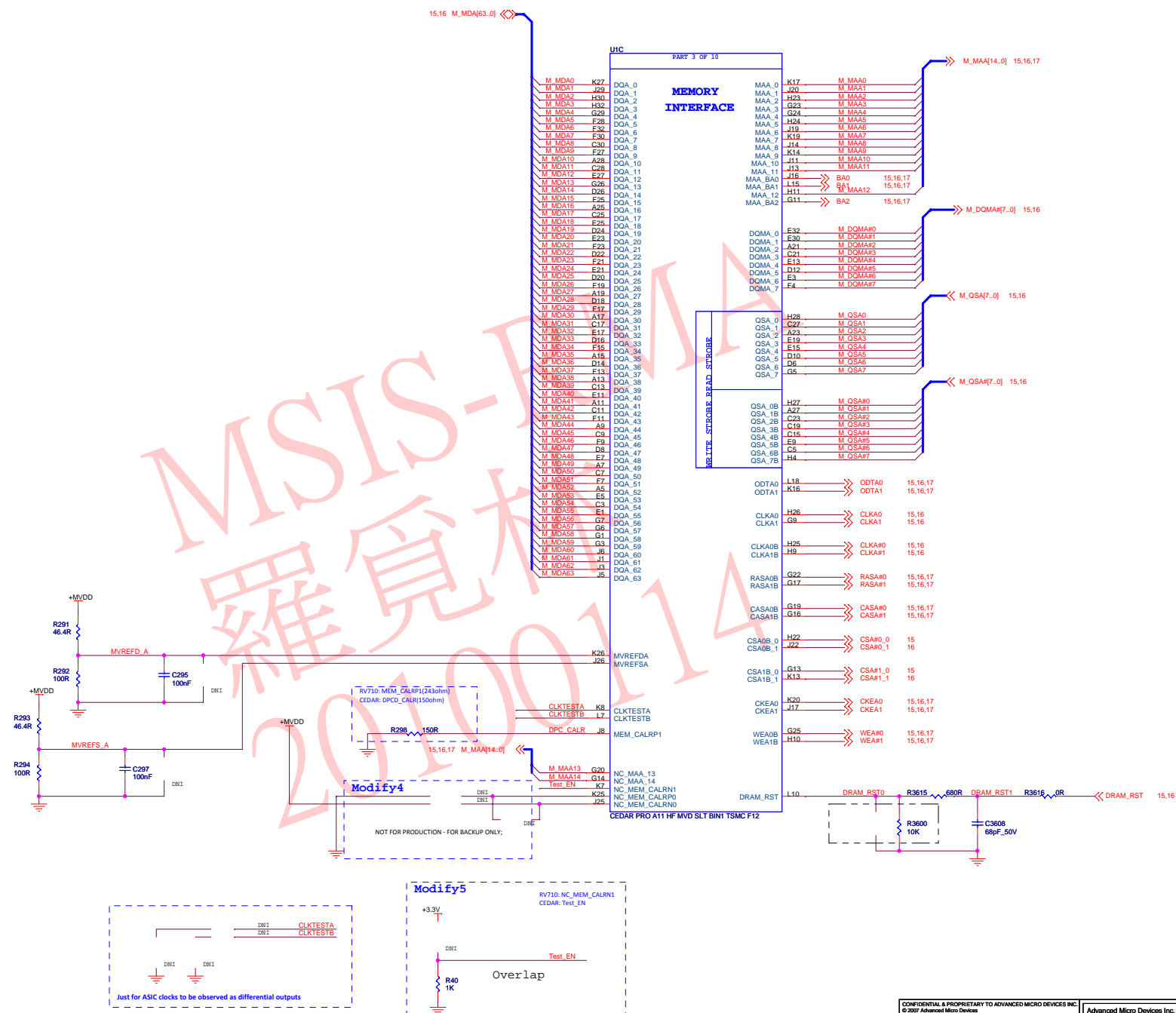
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MEMORY INTERFACE



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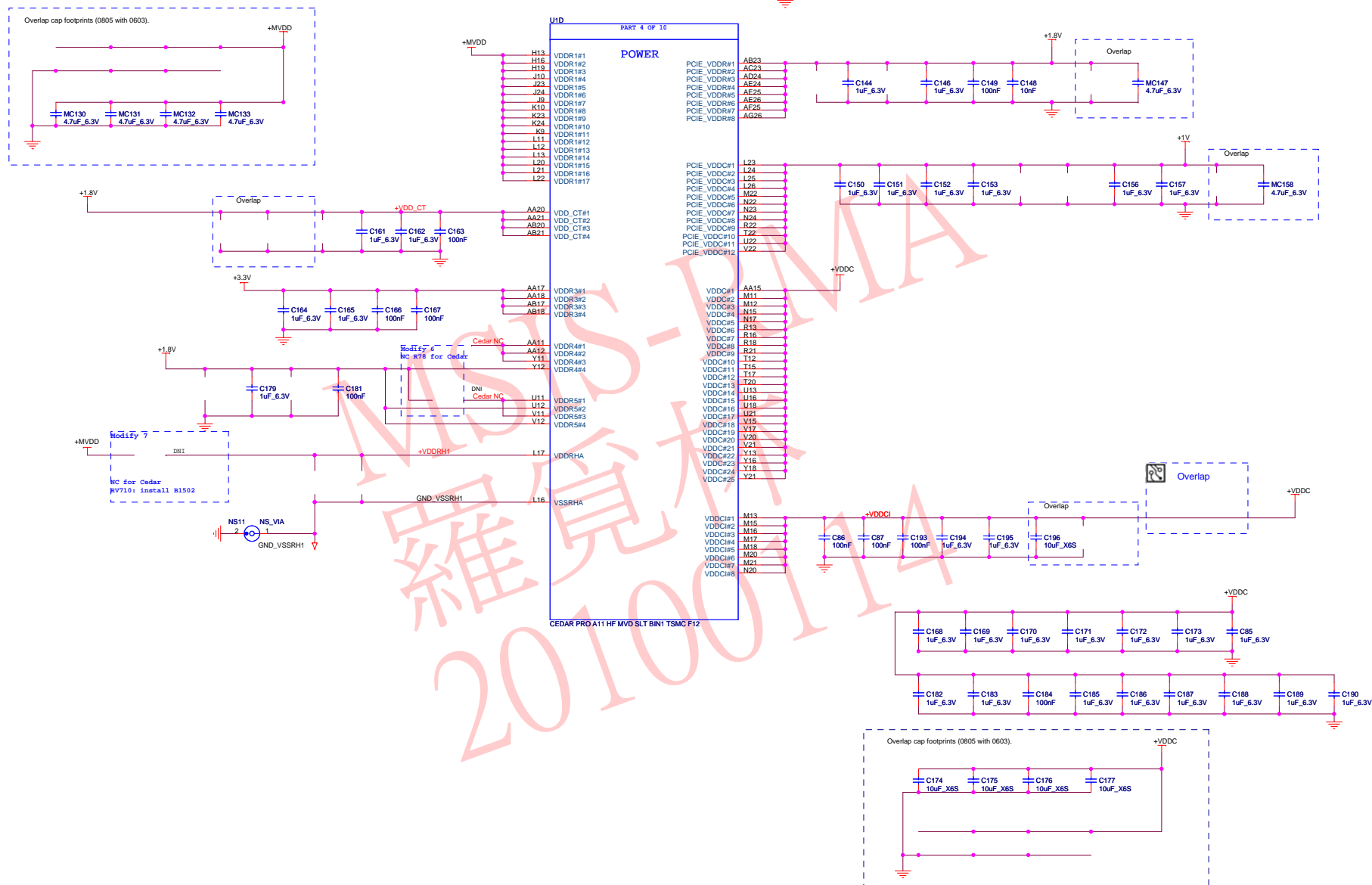
Date: Friday, October 16, 2009


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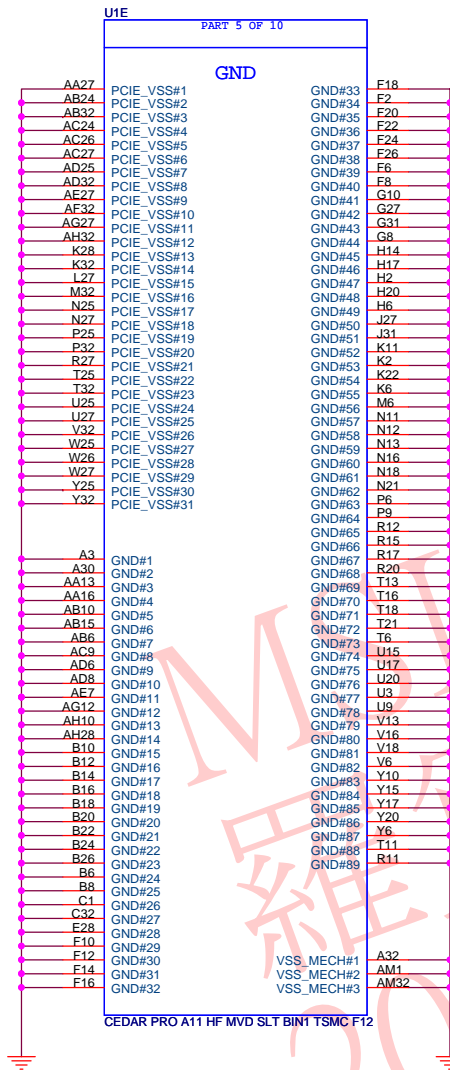
(08) Cedar Power



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<p>Title: <u>Cedar DDR3 2GB 8pcs 128Mb16 VG4H CD/DP dV/GS/A061002000</u></p>	

Title	Cedar DDR3 2GB 8pcs 128Mx16	VGAH HDMI/DP dDVI/VGA061002008-00
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(09) Cedar GND



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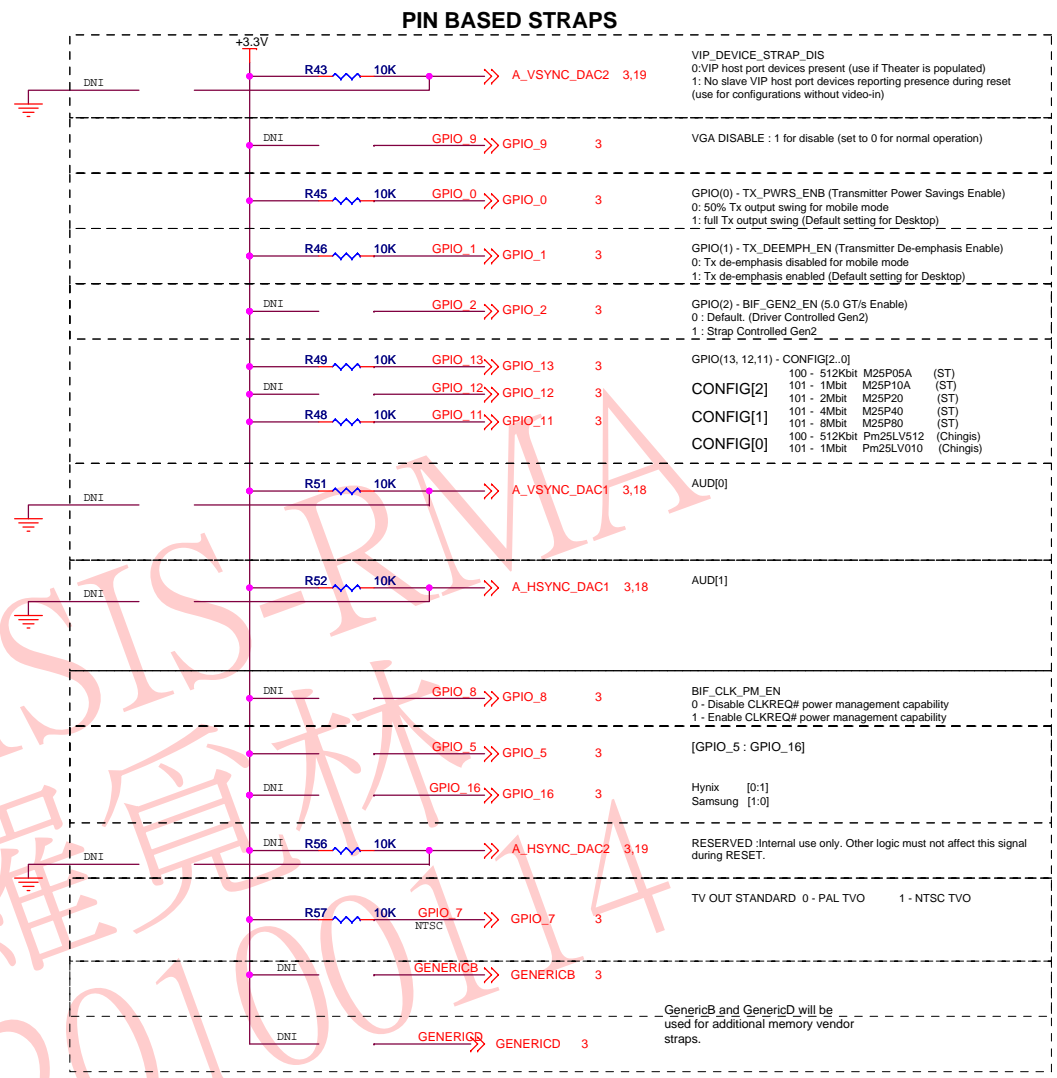
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Title Cedar DDR3 2GB 8pcs 128Mx16 VGAH HDMI/DP dDV7/SVGA/15.5" LCD/15.5" DVI

(10) Cedar STRAPS



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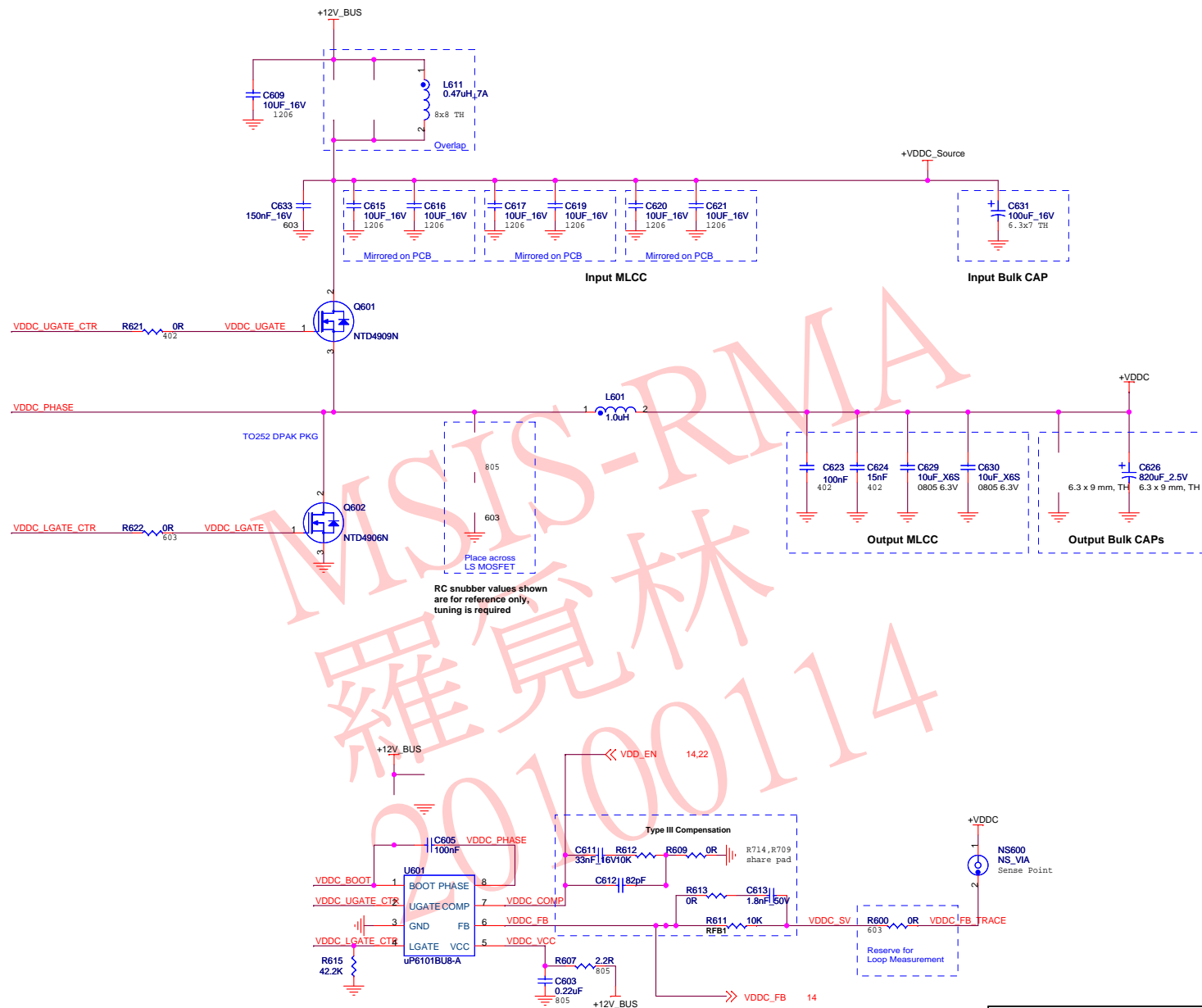
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Title Cedar DDR3 2GB 8pcs 128Mx16 VGAH HDMI/DP dDV7/SVGA105C0276S-00



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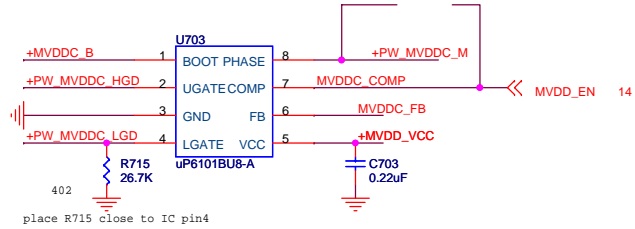


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Title Cedar DDR3 2GB 8pcs 128Mx16 VGAH HDMI/DP dVMSVRA-061705-00

(12) MVDD

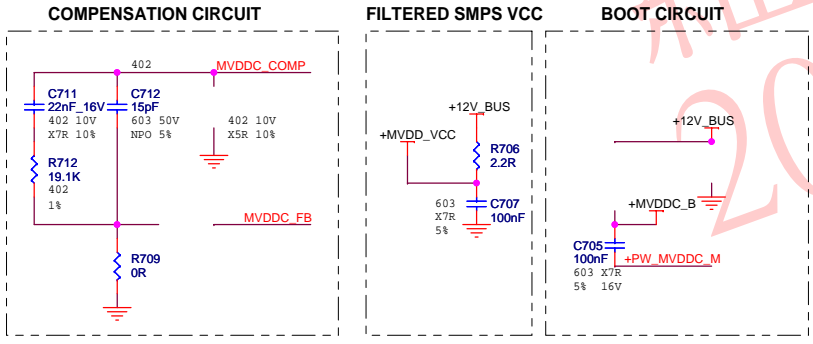
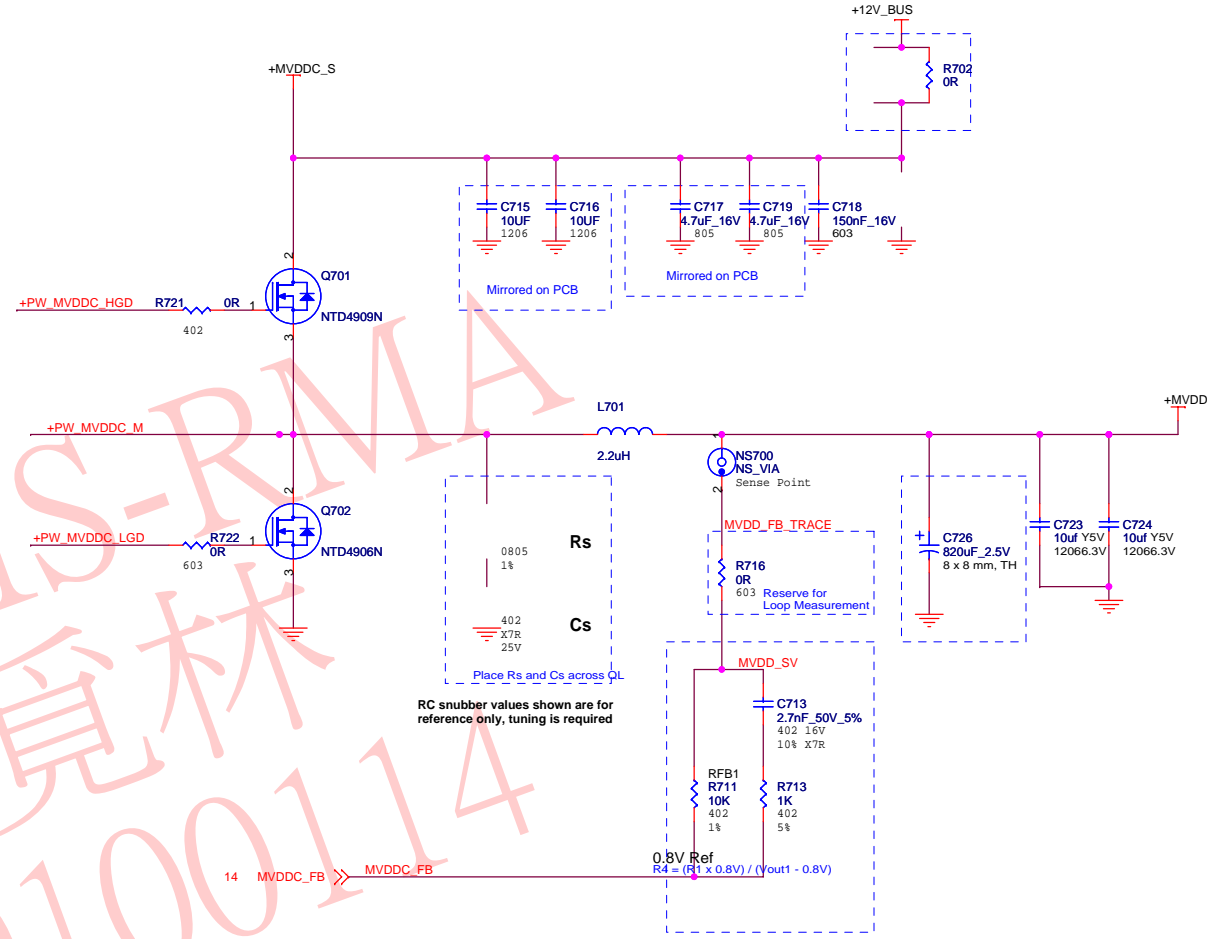


Layout guideline

1-Position the controller (U703) such that LGate(pin4) is the closest to gate of the MOSFETs. You can place the gate resistors R721 and R722 next to the gate of the MOSFETs. Make the gate drive traces(PW MVDDC LGD and PW MVDDC HGD) as short and as wide as possible to reduce the trace inductance.

2-Place the bypass capacitors for Vcc as well as Boost caps as close to the controller as possible. They are as follows:

3-Voltage amplifier compensation network. Place C714 close to the pin 7. Place the rest of the compensation network close to the pins 7 and 6. These are R710, R711, R713, C713 and R712, C711 and C712.



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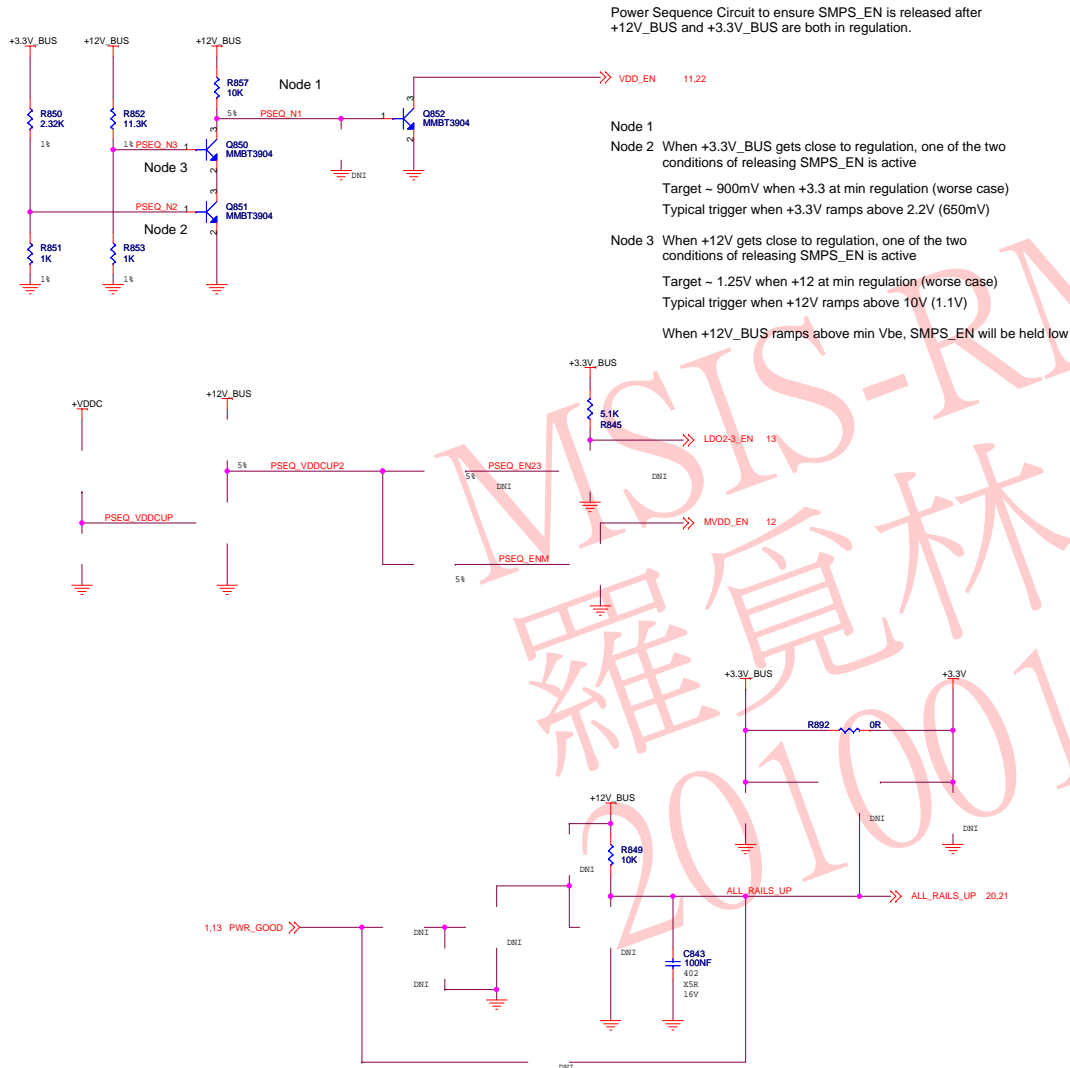
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Title Cedar DDR3 2GB 8pcs 128Mx16 VGAH HDMI/DP dDV7/SVG105C02705-00

(14) Power Management

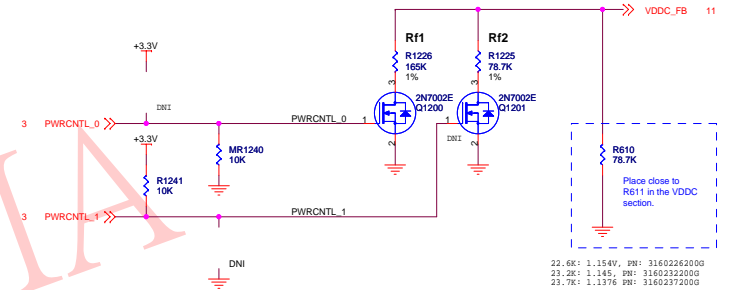
Power up/down Sequencing



Power Play

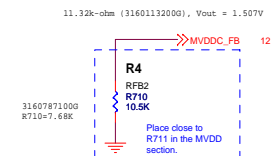
VDDC Voltage Settings Using GPIOs

		Output Voltage (V)			
PWRCTRL_1 OPTO_2D	PWRCTRL_0 OPTO_1S	R1= R2=	R1= R2=	R1= R2=	
0	0				
0	1				
1	0				
1	1	1	0	1	Power-up Default



MVDD Voltage Settings Using GPIOs

		Output Voltage (V)			
PMICNTL_2	Rf1=	Rf1=	Rf1=		
GPIO_6	Rf2=	Rf2=	Rf2=		
0					
1	1 0	1			Power-up Default



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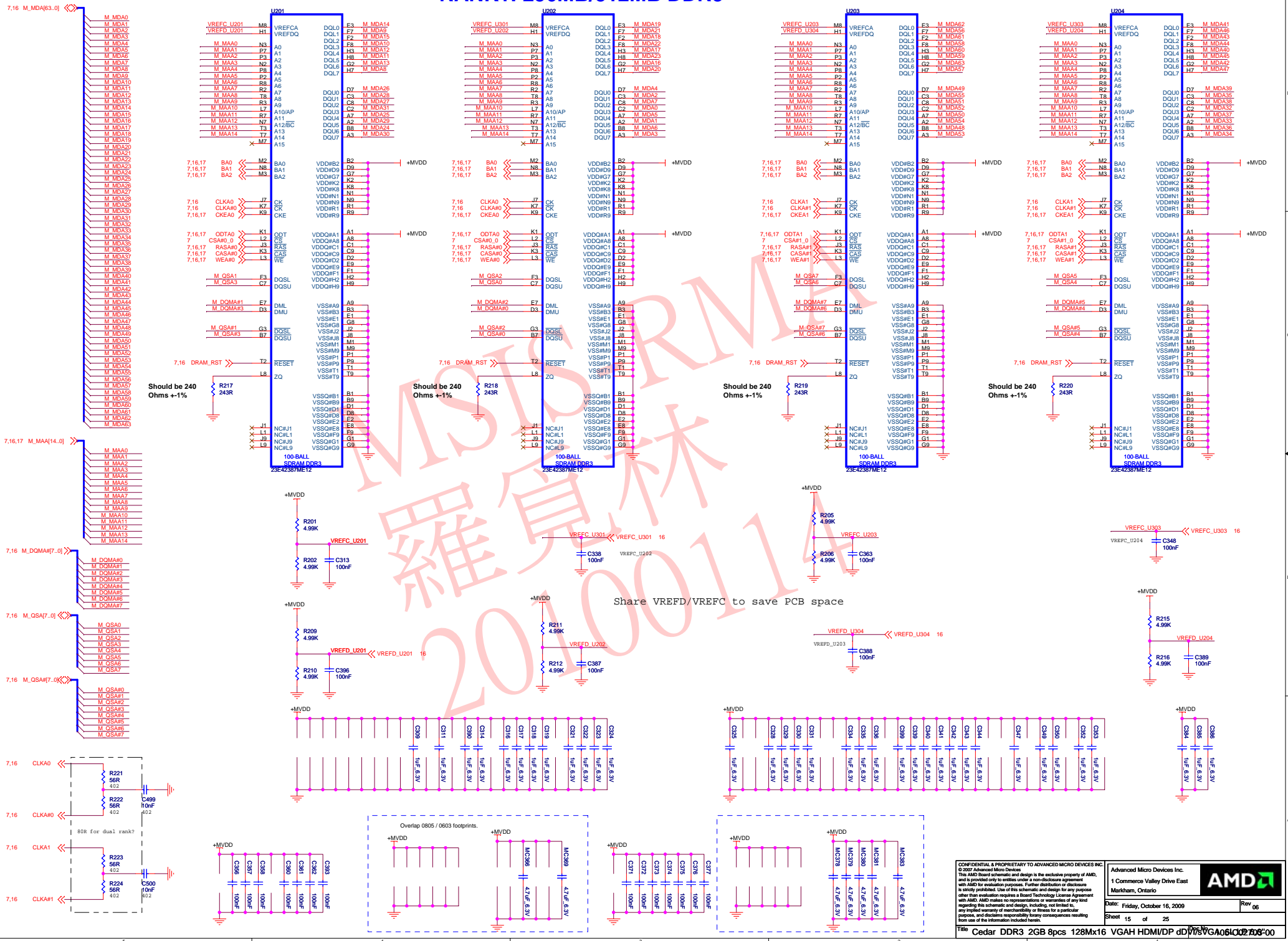
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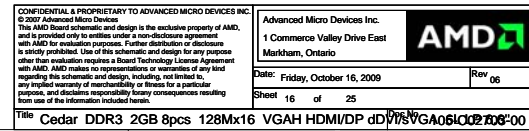
Date: Friday, October 16, 2009	Rev 06
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Title	Cedar DDR3 2GB 8pcs 128Mx16 VGAH HDMI/DP dDVI/sVGA 05L02708-00
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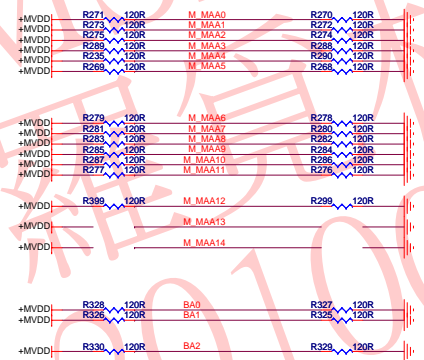
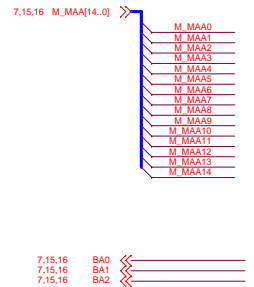
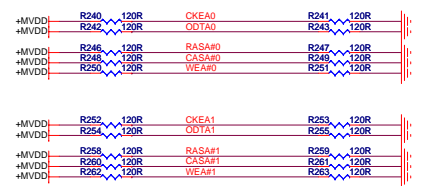
RANK1: 256MB/512MB DDR3



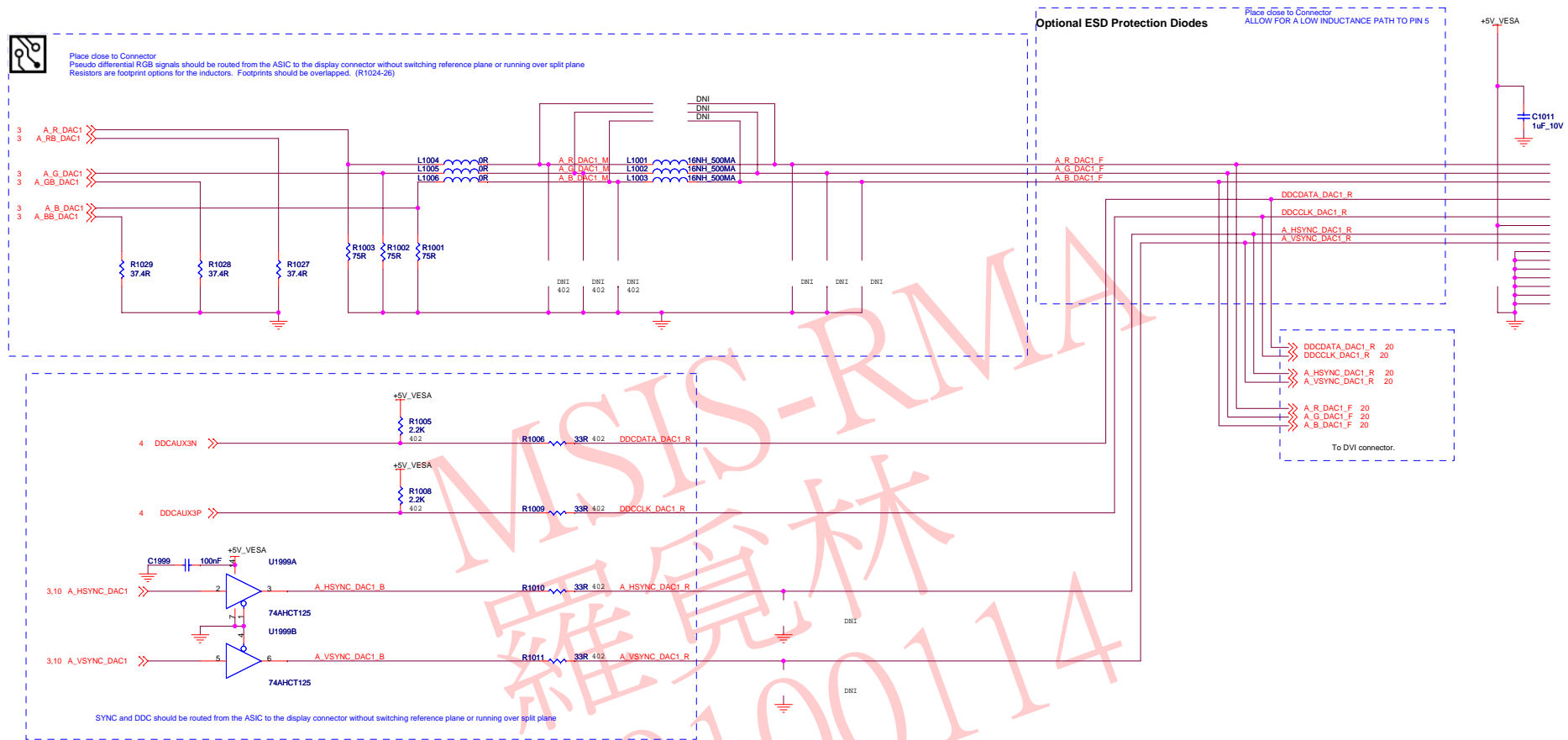
RANK2: 256MB/512MB DDR3



(17) DDR3 Termination



DAC 1 OUTPUT



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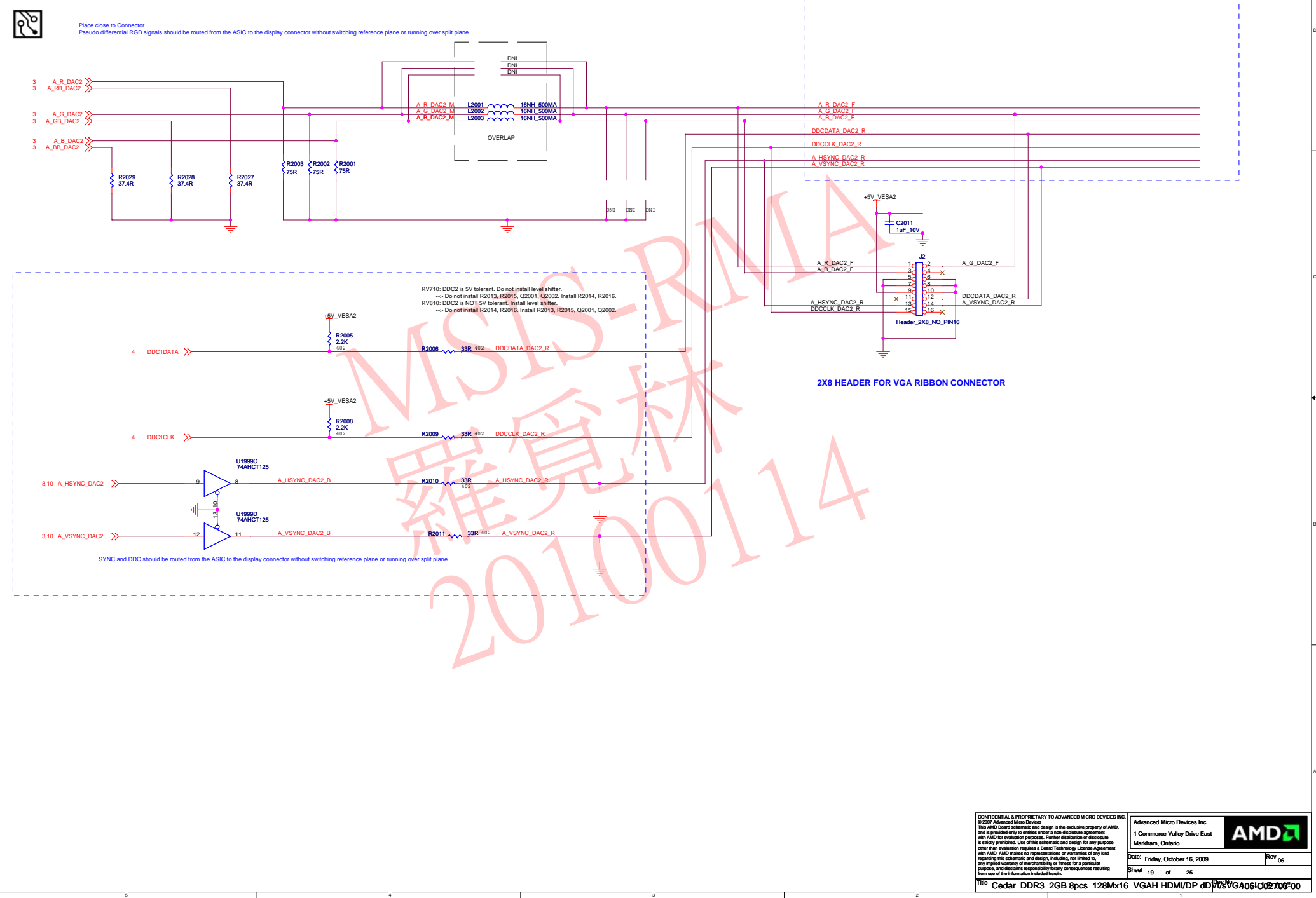


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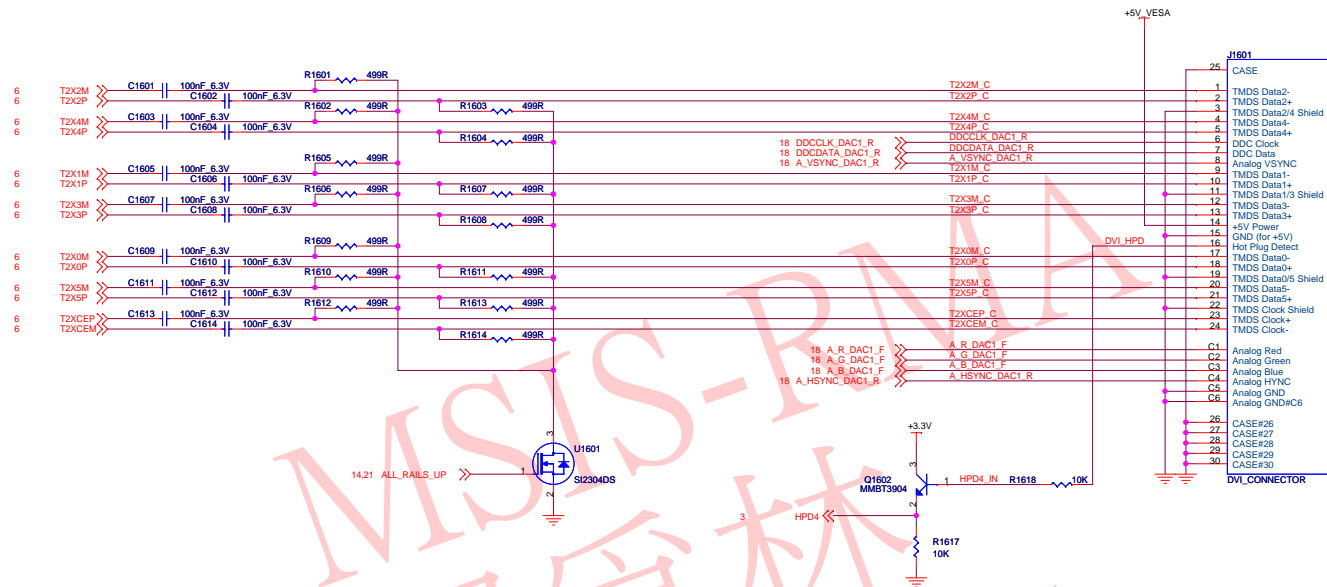
Title Cedar DDR3 2GB 8pcs 128Mx16 VGAH HDMI/DP dVGA061002108-00

(19) DAC2 OUTPUT

DAC 2 OUTPUT



DPE / DPF OUTPUT



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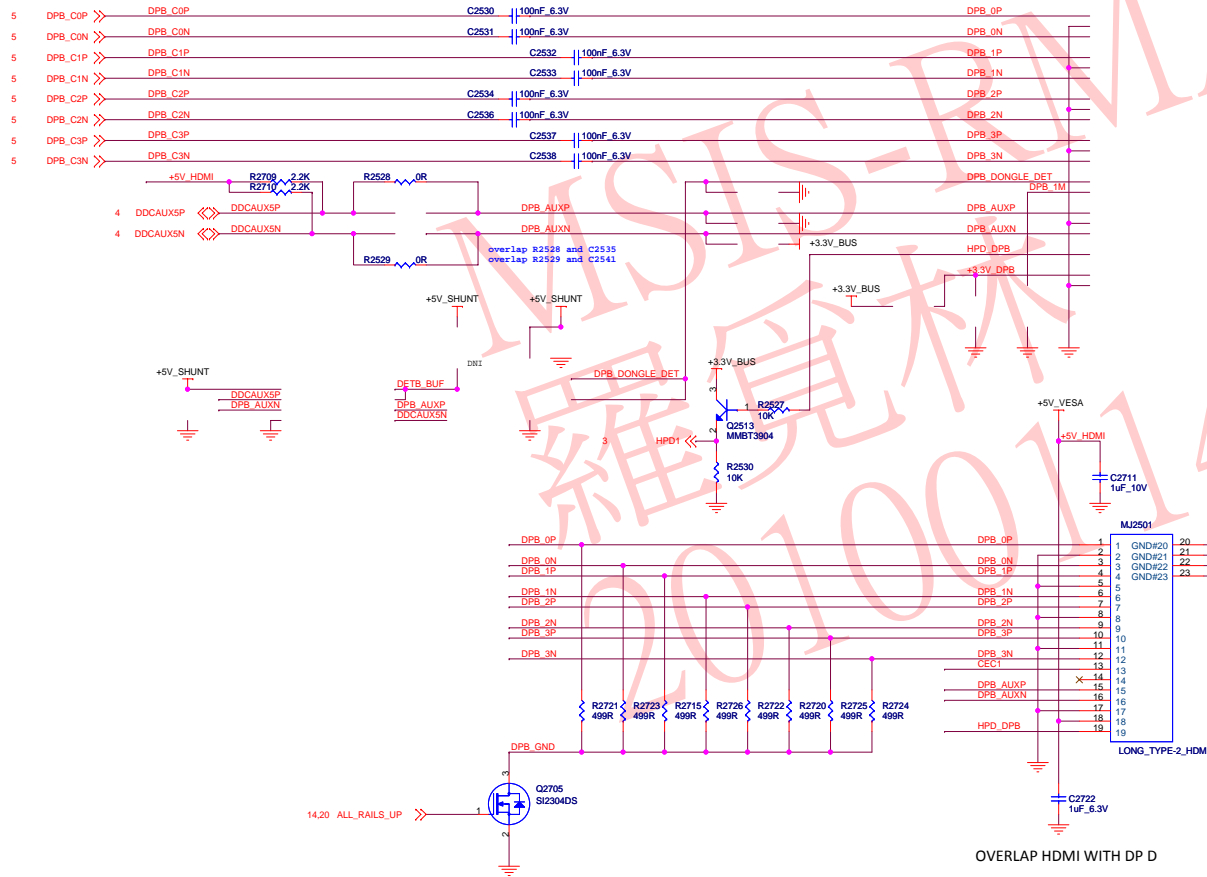
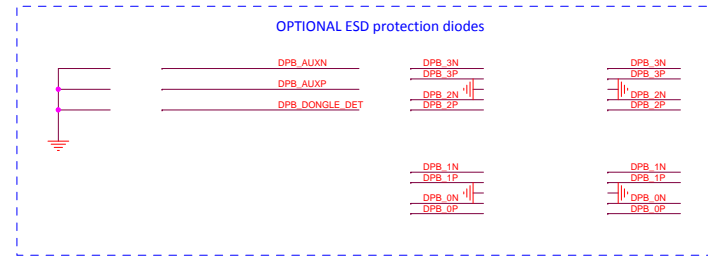
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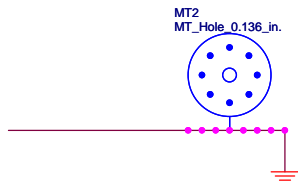
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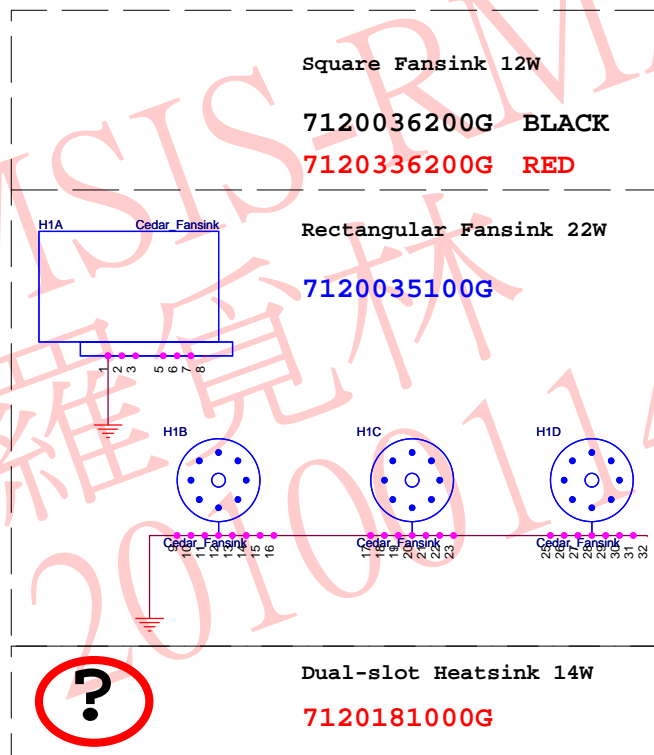
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Title: Cedar DDR3 2GB 8pcs 128Mx16 VGAH HDMI/DP d0V7S VGA061002106-00



SKU	P/N	CONFIGURATION	Form Factor
PERCH	8020051300G	DVI + DP + VGA	FH / SS
BASS	8020051400G	DVI +HDMI+ VGA	FH / SS
PERCH	8020051600G	DVI + DP	LP / SS
BASS	80200516A0G	DVI +HDMI	LP / SS
PERCH	8020051700G	DVI + DP + VGA	FH / DS
BASS	80200517A0G	DVI +HDMI+ VGA	FH / DS



For DVI Connector

ASSY-SCREW200

SCREW
JACKPOST, HEX, 3/16 AF, 4-40 INT/EXT
<3rd part field>

ASSY-SCREW201

SCREW
JACKPOST, HEX, 3/16 AF, 4-40 INT/EXT
<3rd part field>

Bracket Components

ASSY-SCREW202

SCREW
SCREW

7020005200G

ASSY1

BRACKET

8020051400G

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Title Cedar DDR3 2GB 8pcs 128Mx16 VGAH HDMI/DP dDVI/SVG 105C02705-00

<div>AMD</div>			Title		Schematic No.		Date:		
			Cedar DDR3 2GB 8pcs 128Mx16 VGAH HDMI/DP dDVI/sVGA 6L LP 6.6"		105-C02703-00		Friday, October 16, 2009		
			REVISION HISTORY					NOTE: This schematic represents the PCB, it does not represent any specific SKU. For Stuffing options (component values, DNI's, ...) please consult the product specific BOM. Please contact AMD representative to obtain latest BOM closest to the application desired.	
Sch Rev	PCB Rev	Date	Cedar 1GB DDR3 Walleye		REVISION DESCRIPTION				
0	00A	2008.12.30	Sch no change. just modify HDMI connector location on PCB						
1	00B	2009.08.24	Page 1:remove C1,C2,R3,U3,Add Q1,Q2 Page 7:Add C97,C98 Page 10:Add MR43,MR51,MR52,MR56 Page 18:Add L1004,L1005,L1006 Page 19:Remove R2013,R2014,R2015,R2016,Q2001,Q2002						
2	00	2009.09.23	Change HDMI/DP DDC line to DDCAUX3						

MSIS-RMA

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