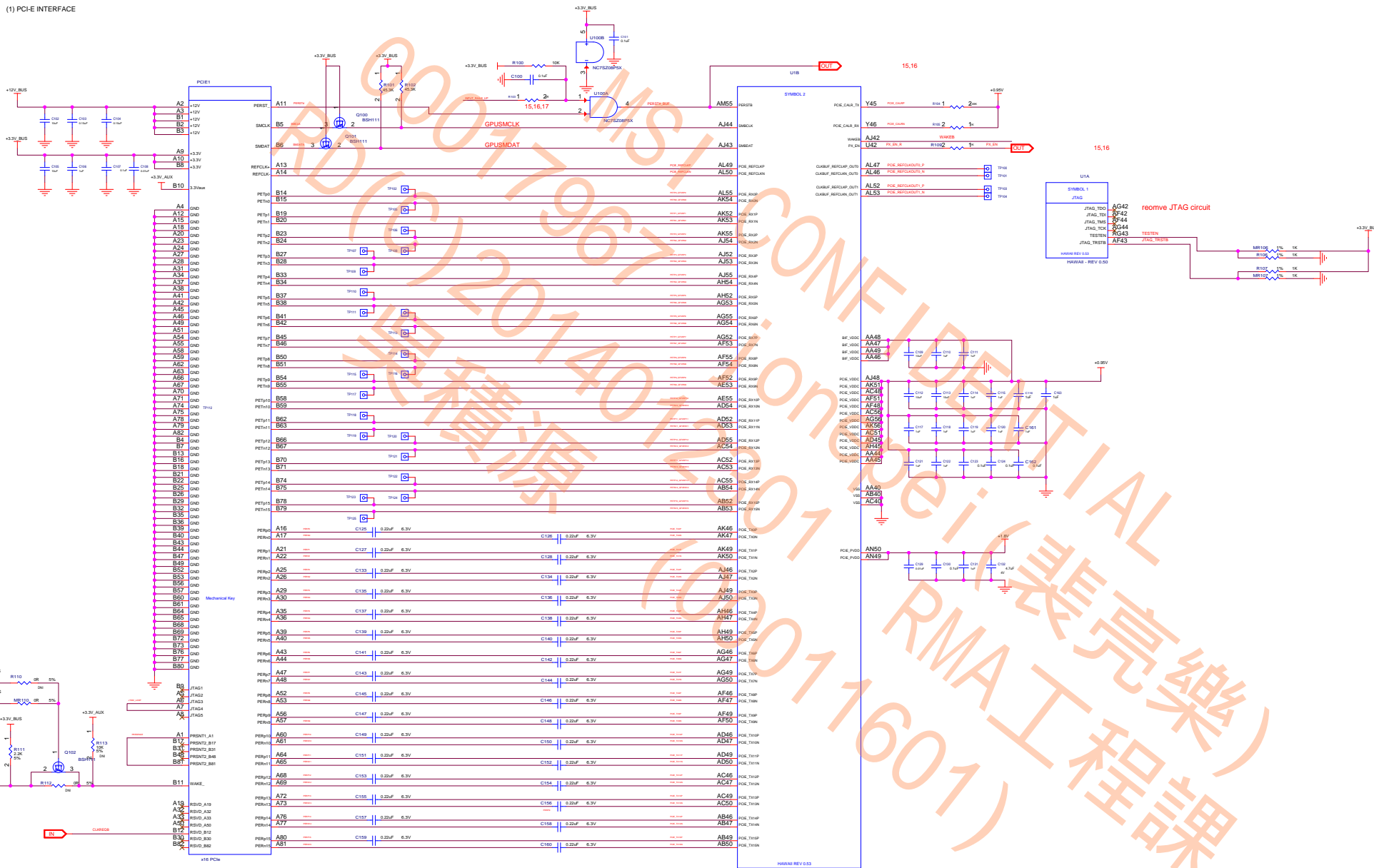
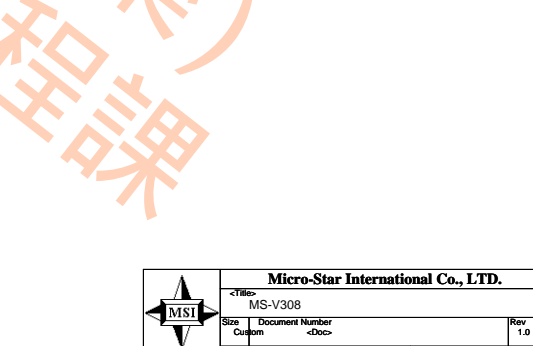
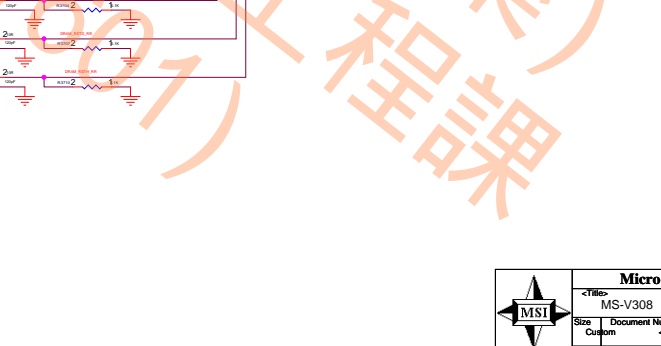
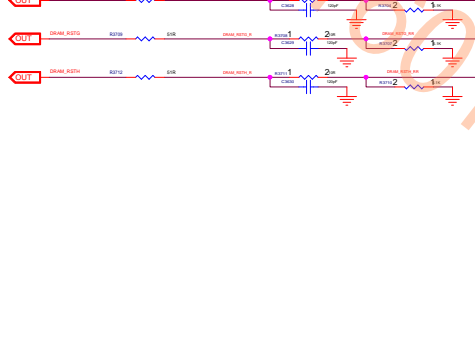
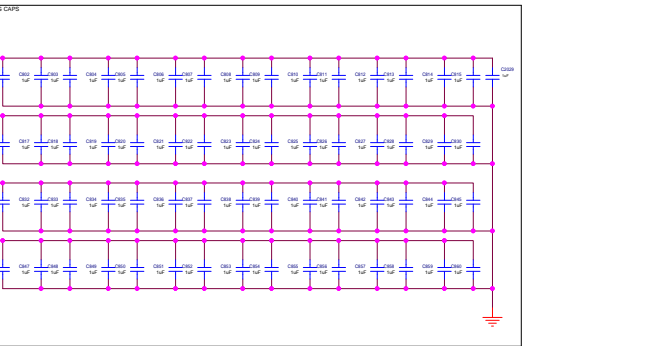
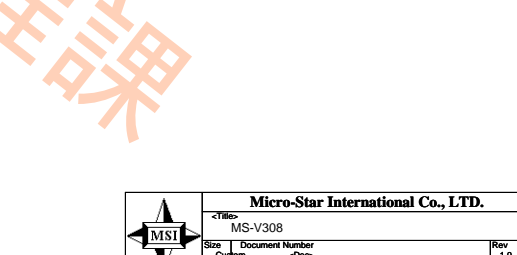
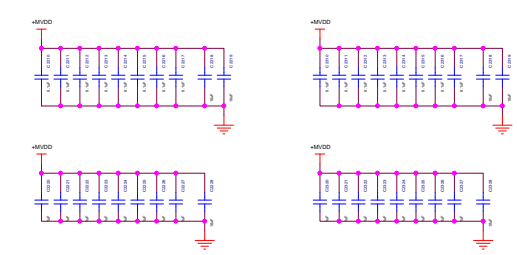
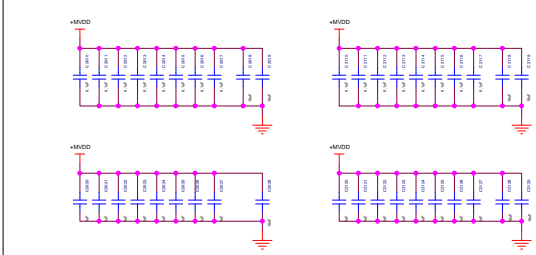
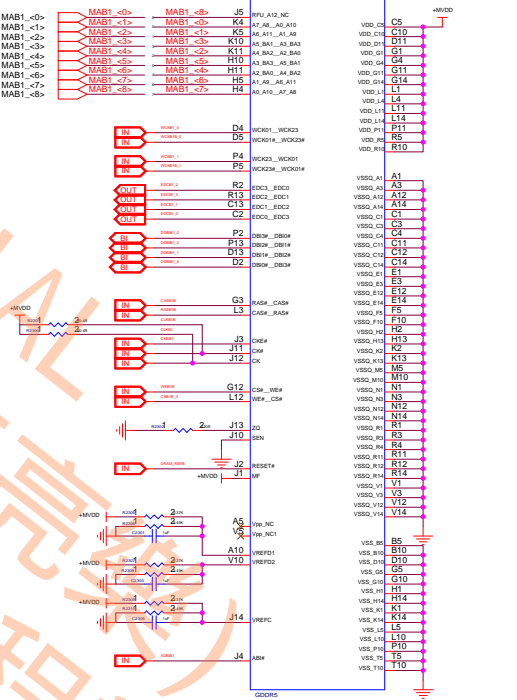
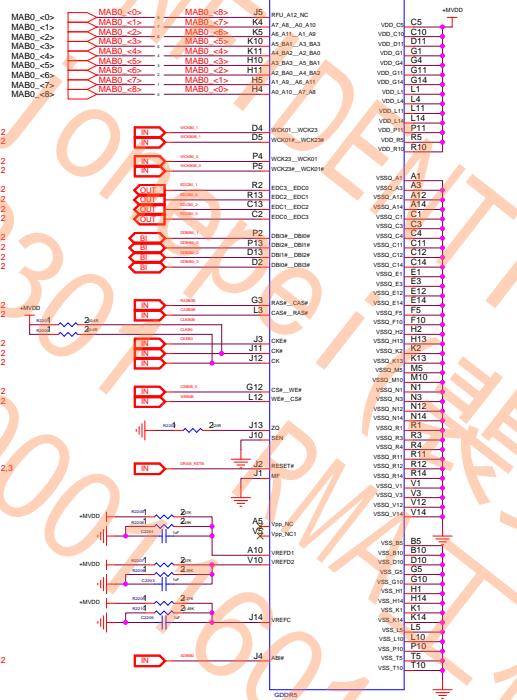
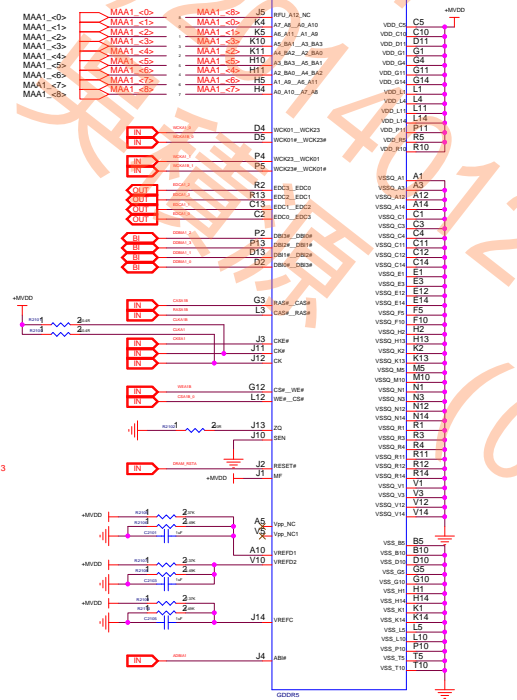
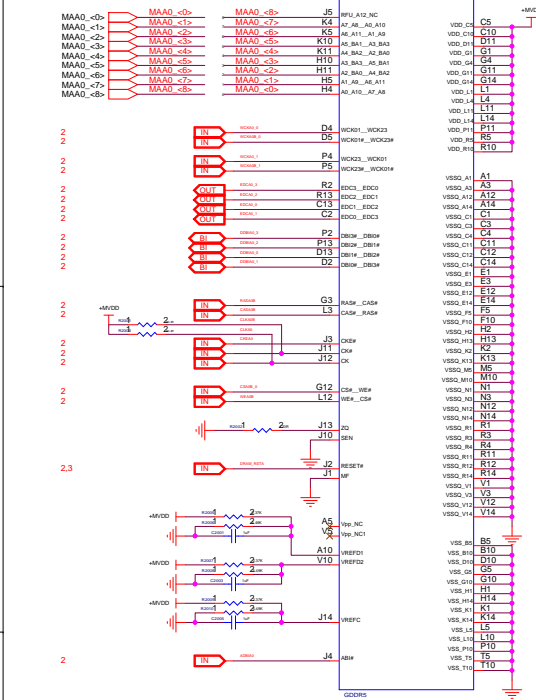
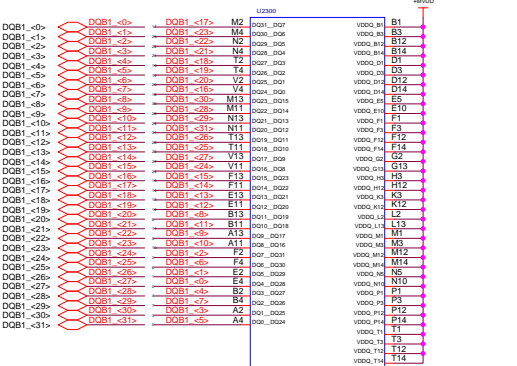
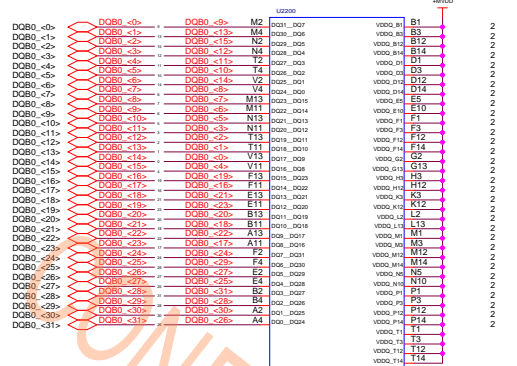
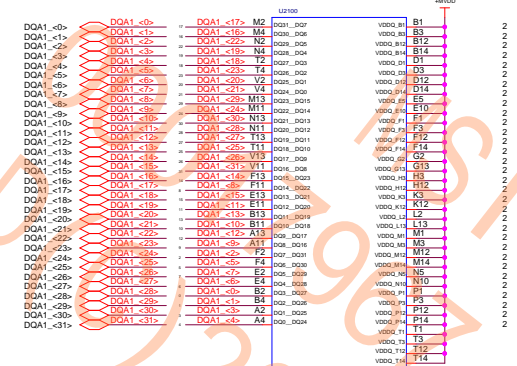
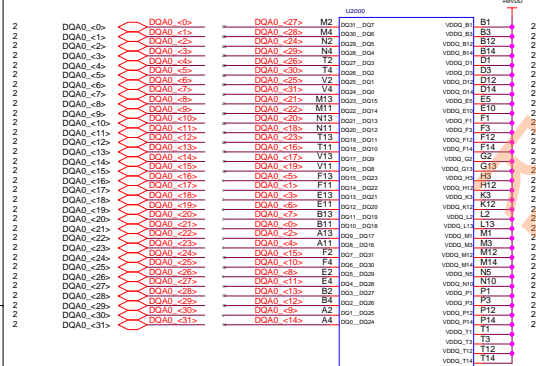
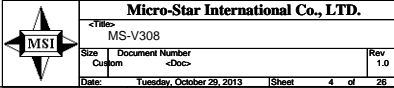


(1) PCIe INTERFACE









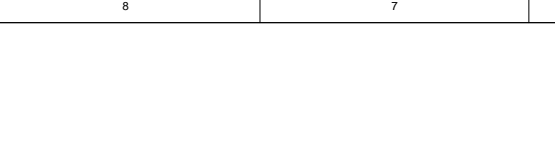
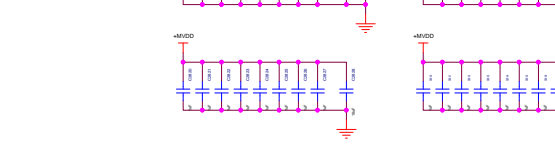
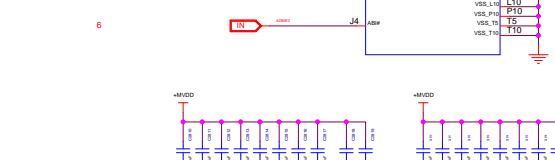
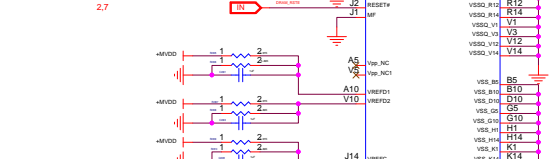
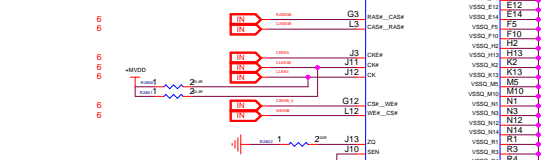
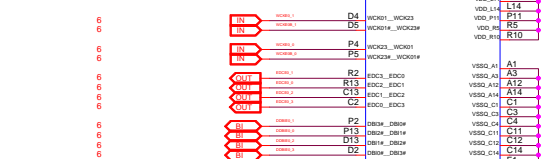
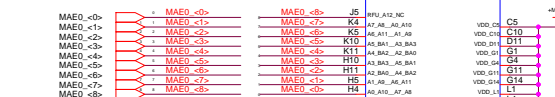
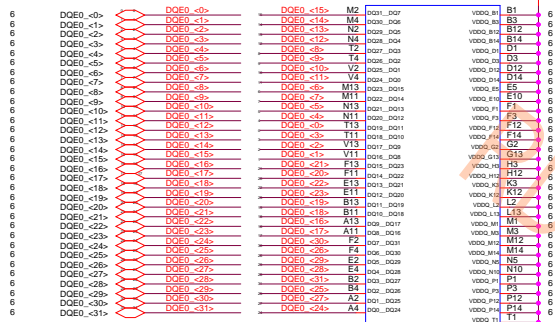




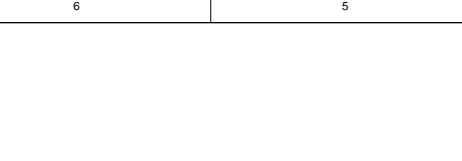
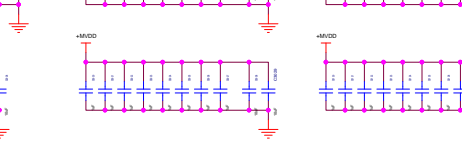
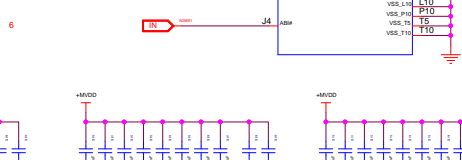
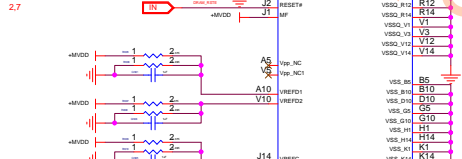
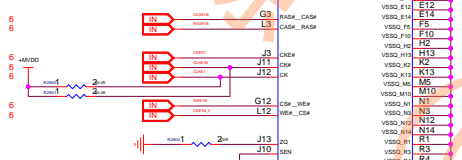
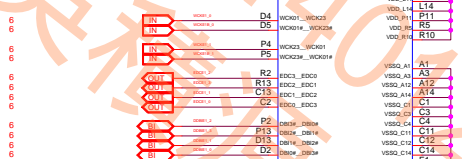
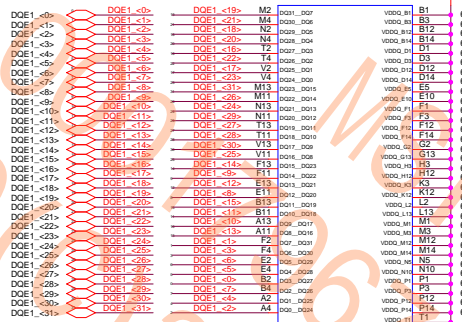
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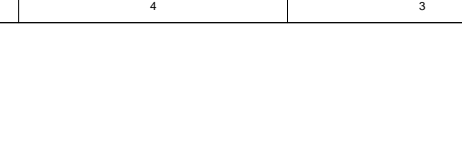
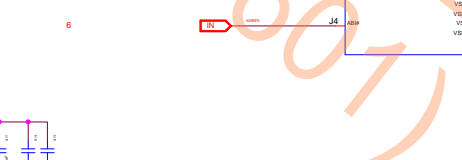
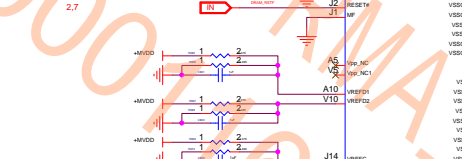
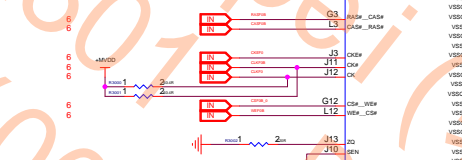
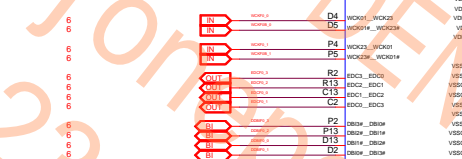
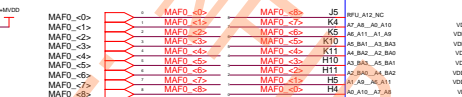
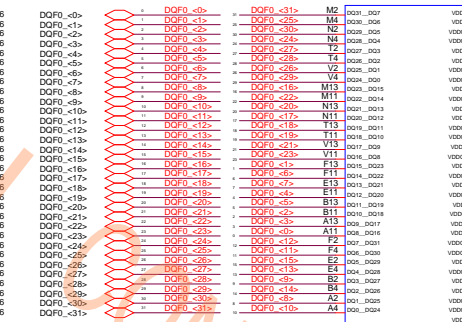
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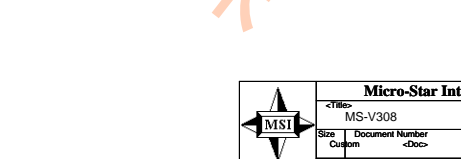
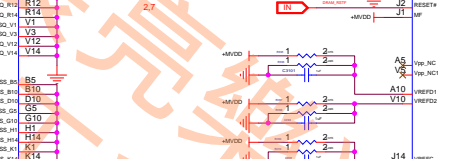
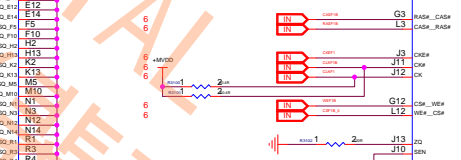
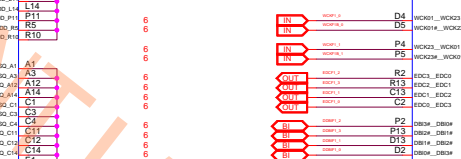
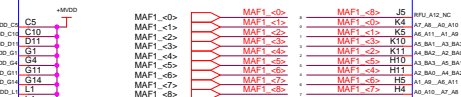
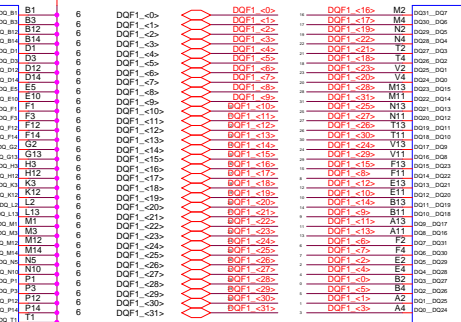
U2900



U3000



U3100



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Document Number

Date

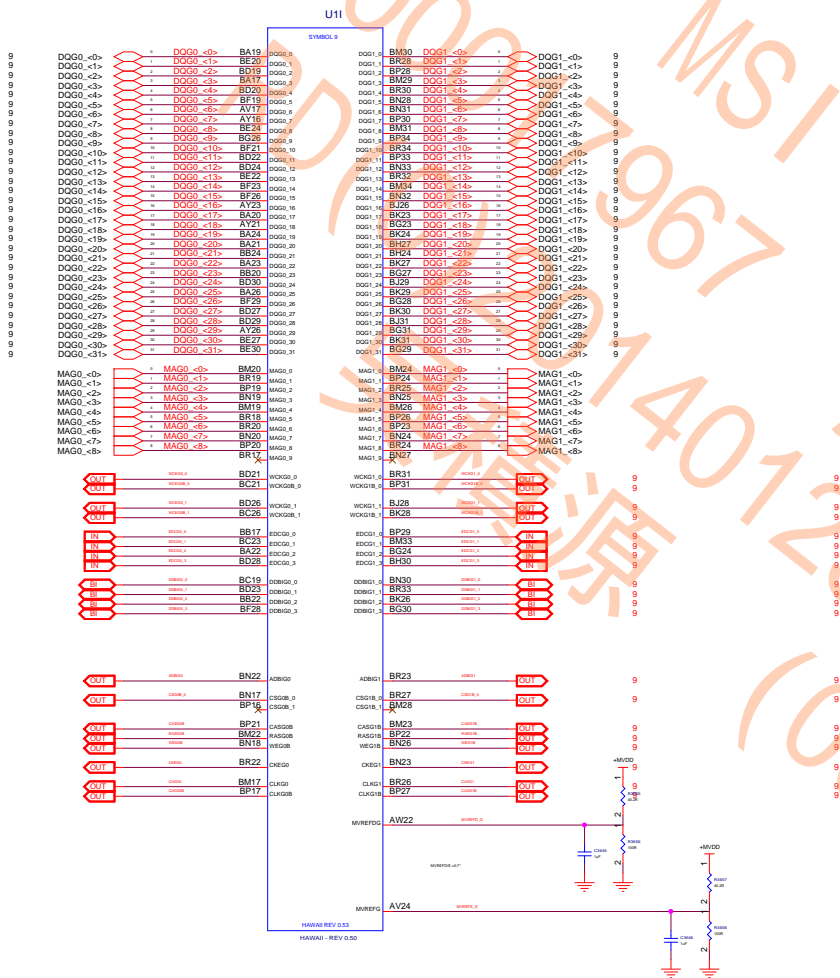
Tuesday, October 29, 2013

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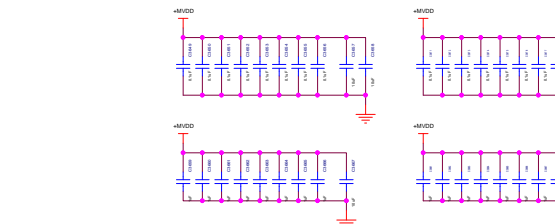
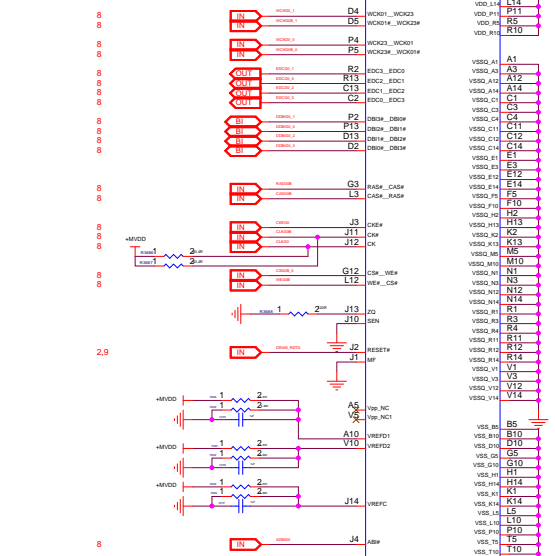
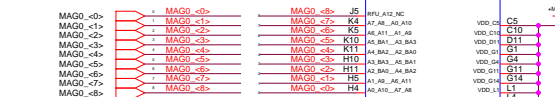
Rev

1.0

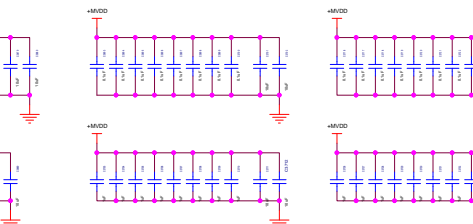
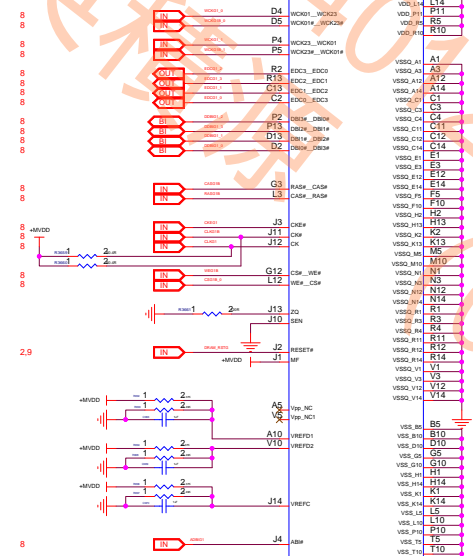
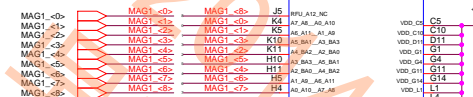




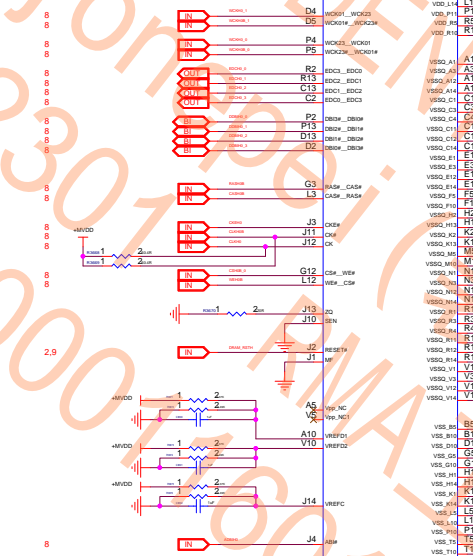
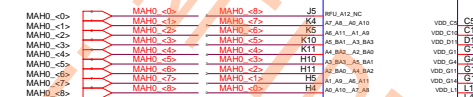
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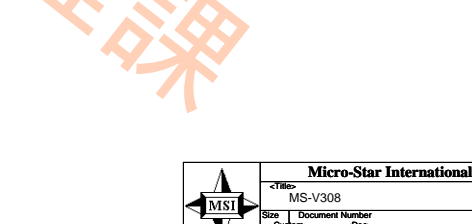
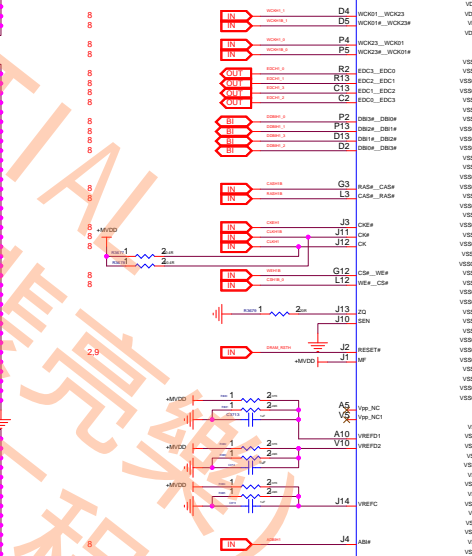
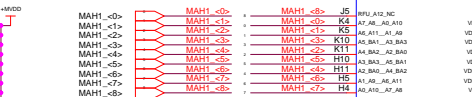
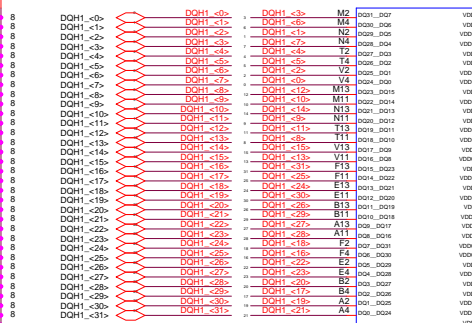
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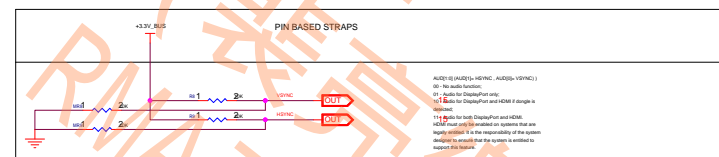


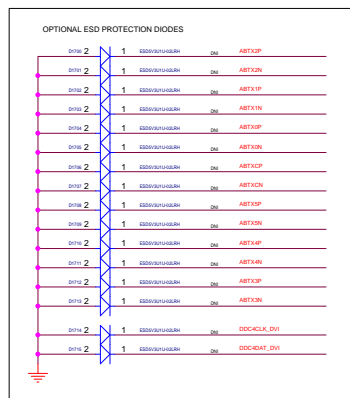
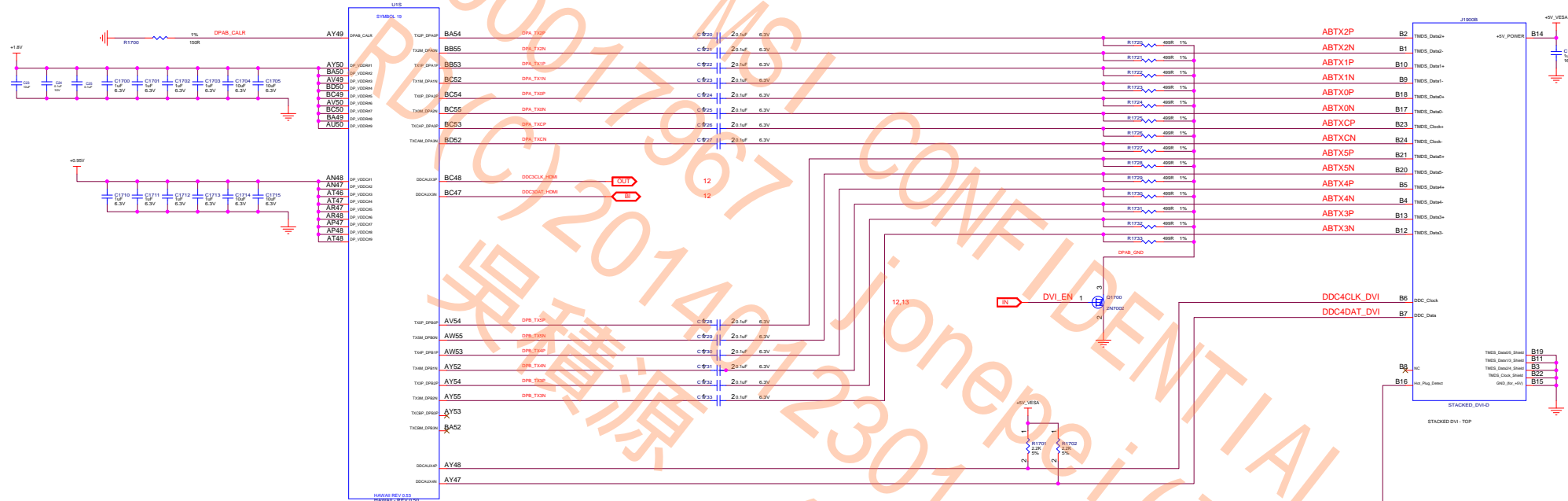
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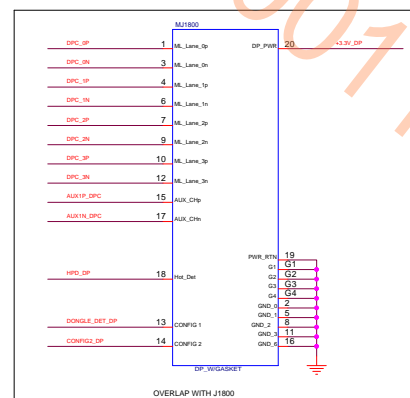
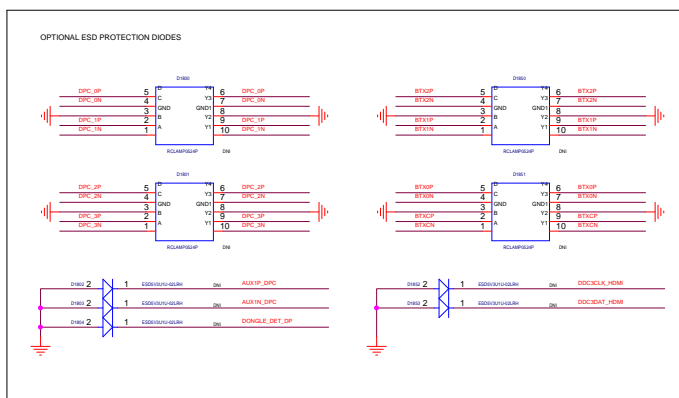
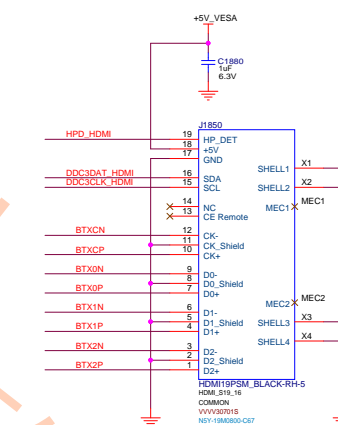
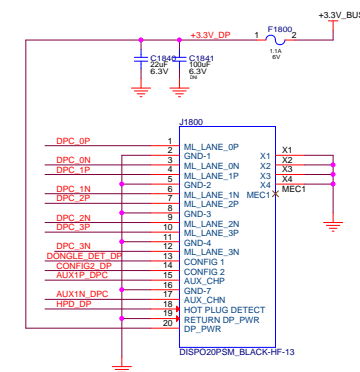
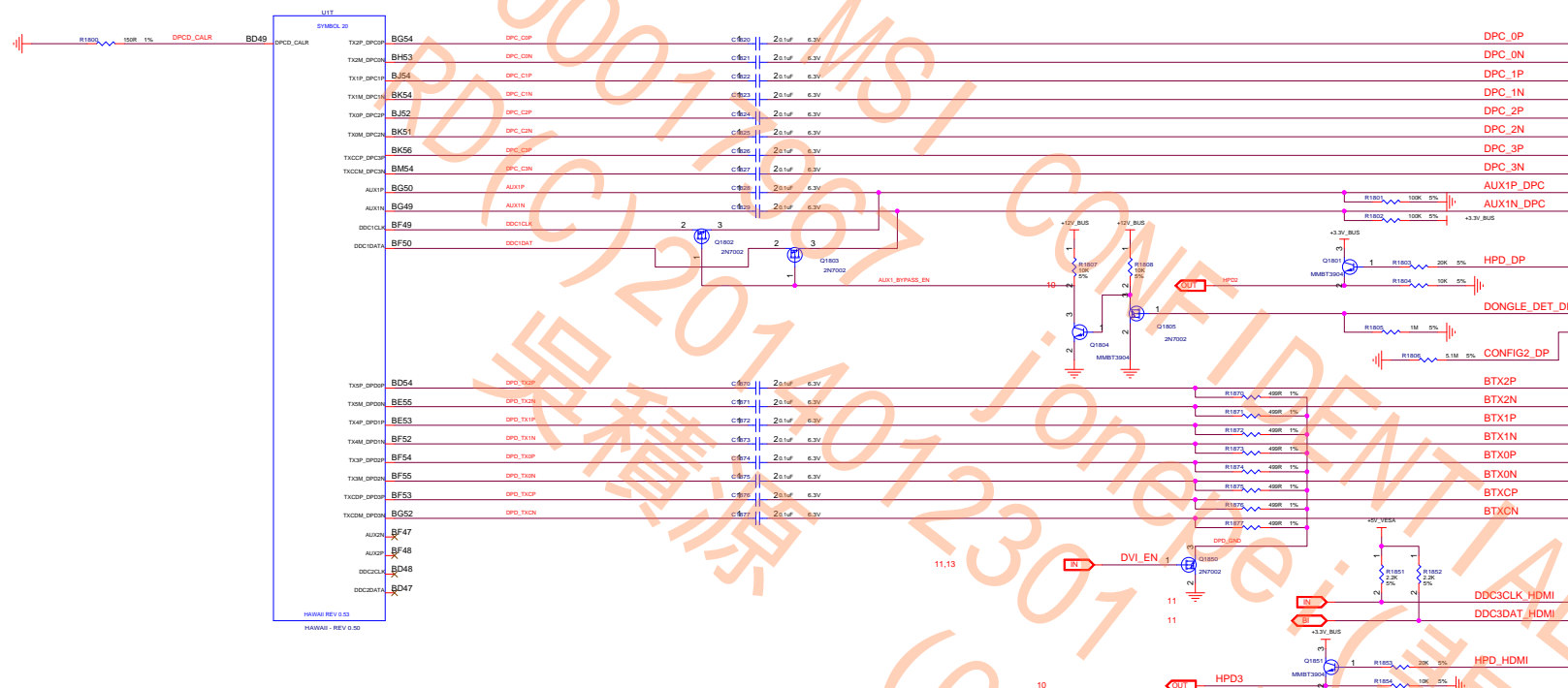


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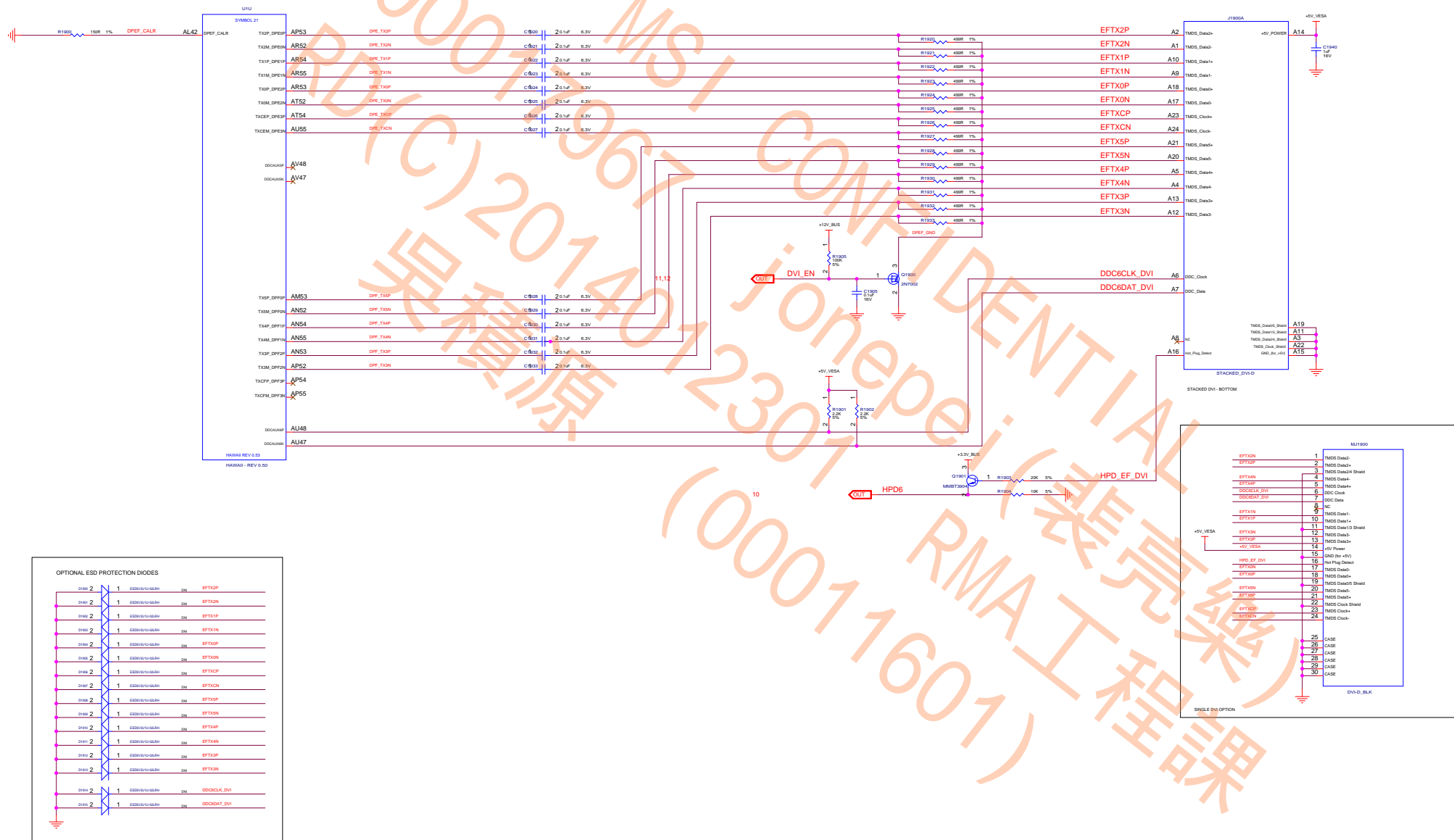


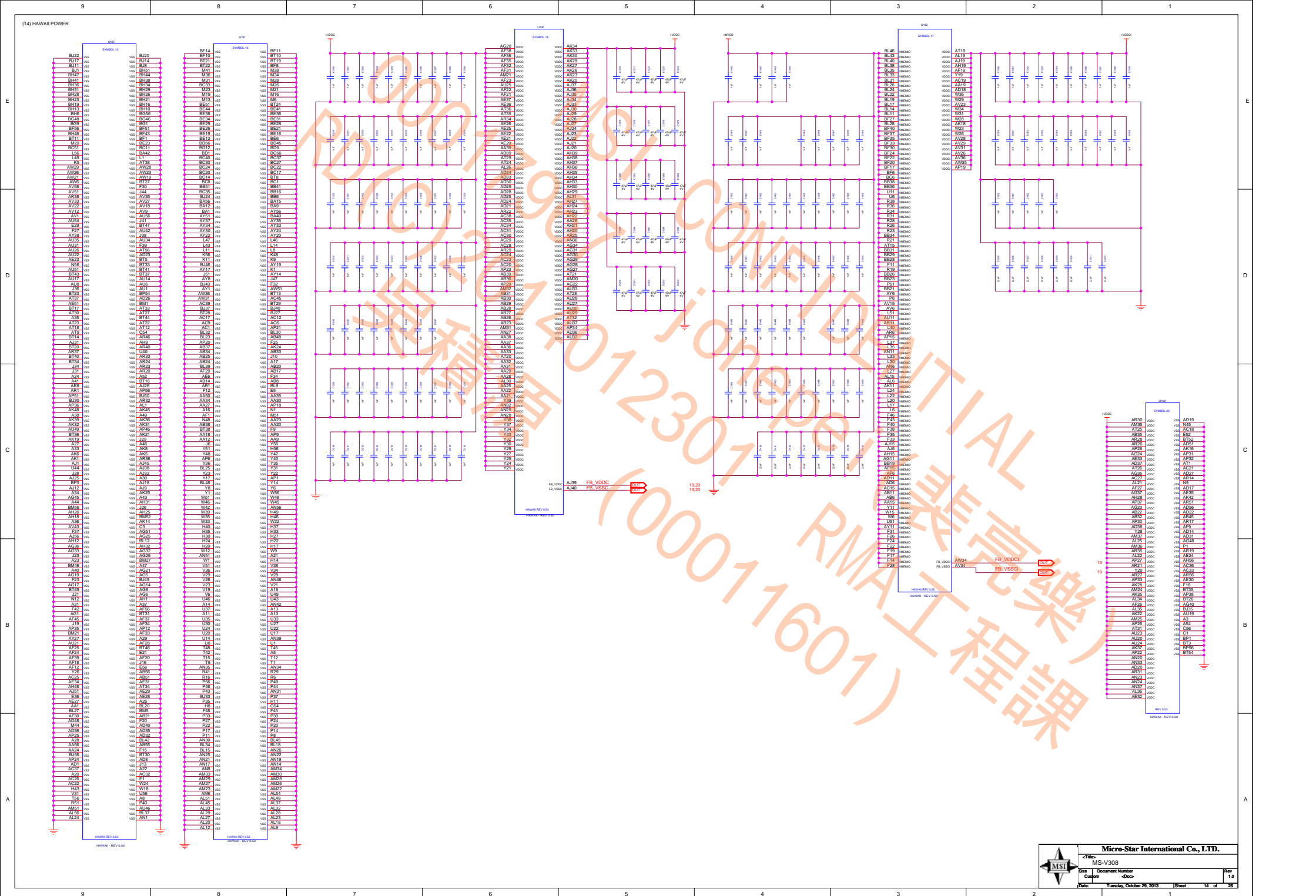


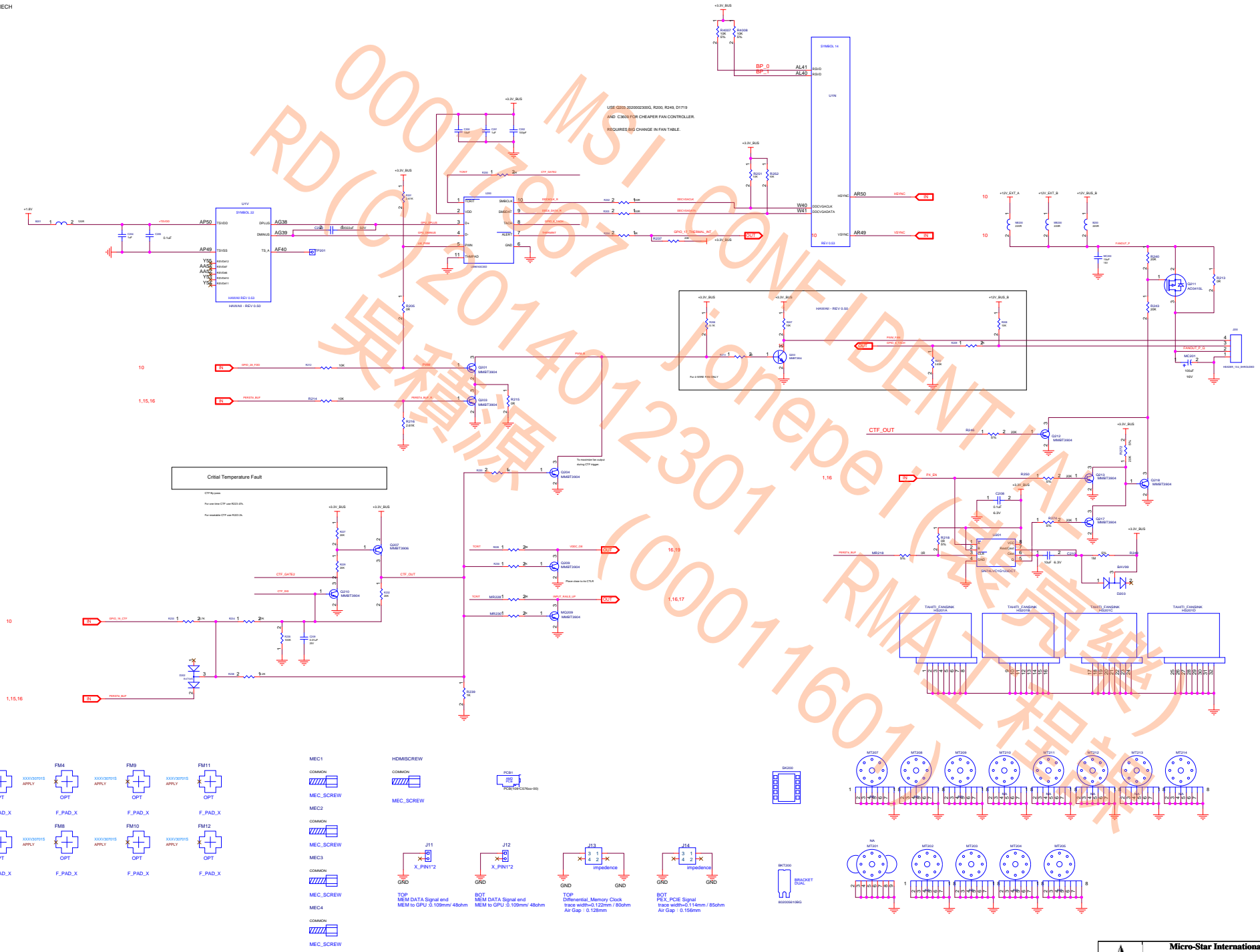


CHANGE HDMI/DP FOOTPRINT



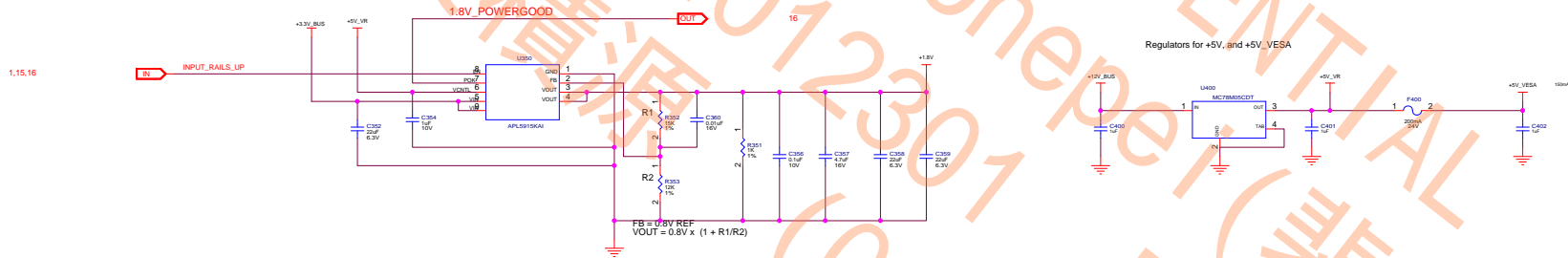












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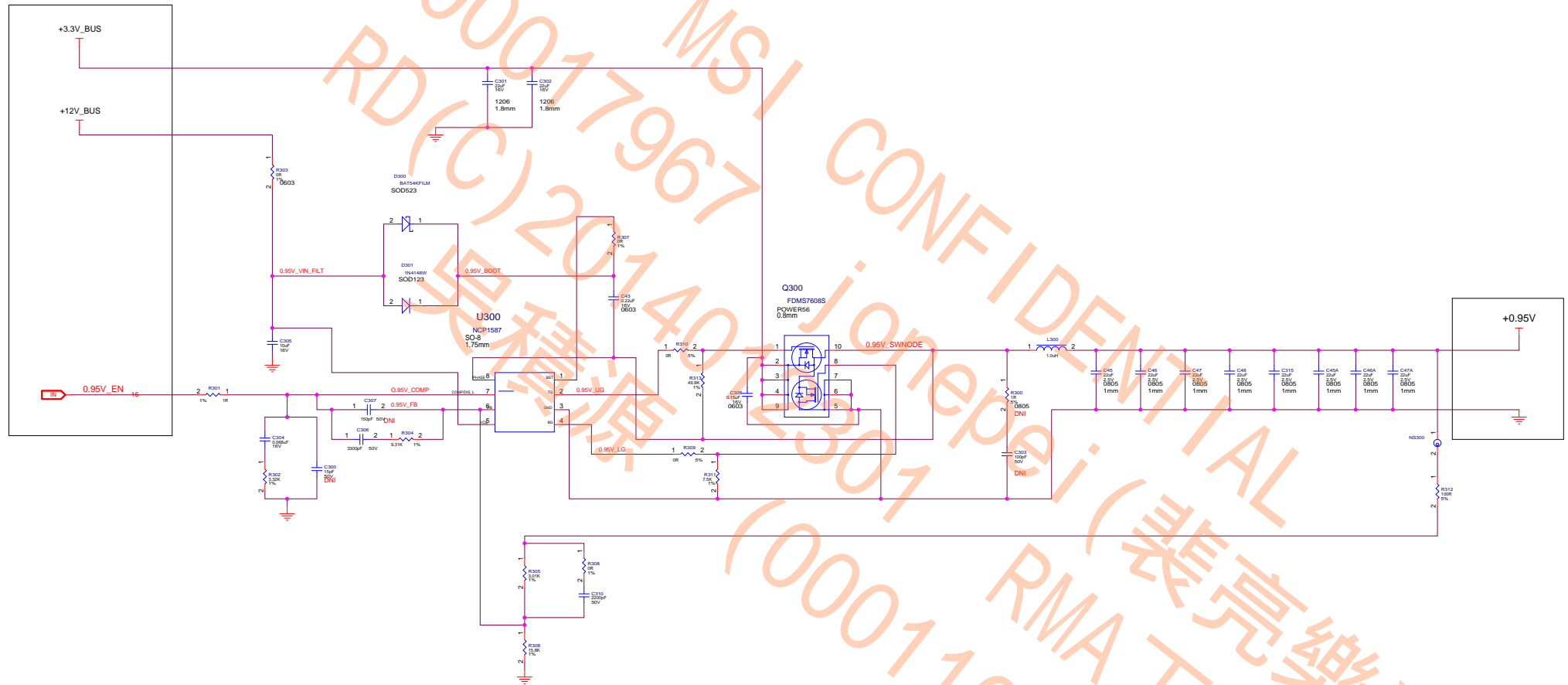
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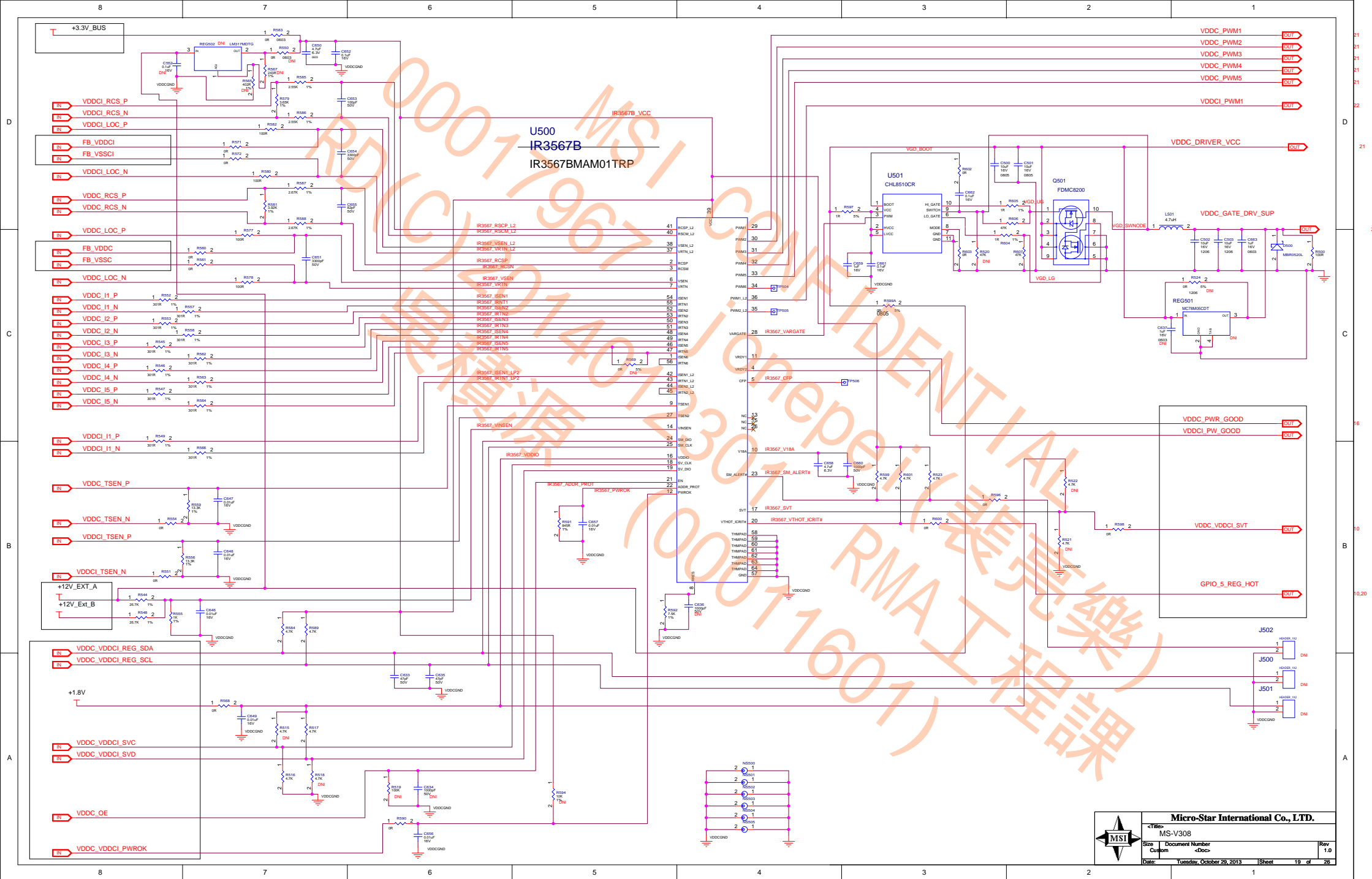
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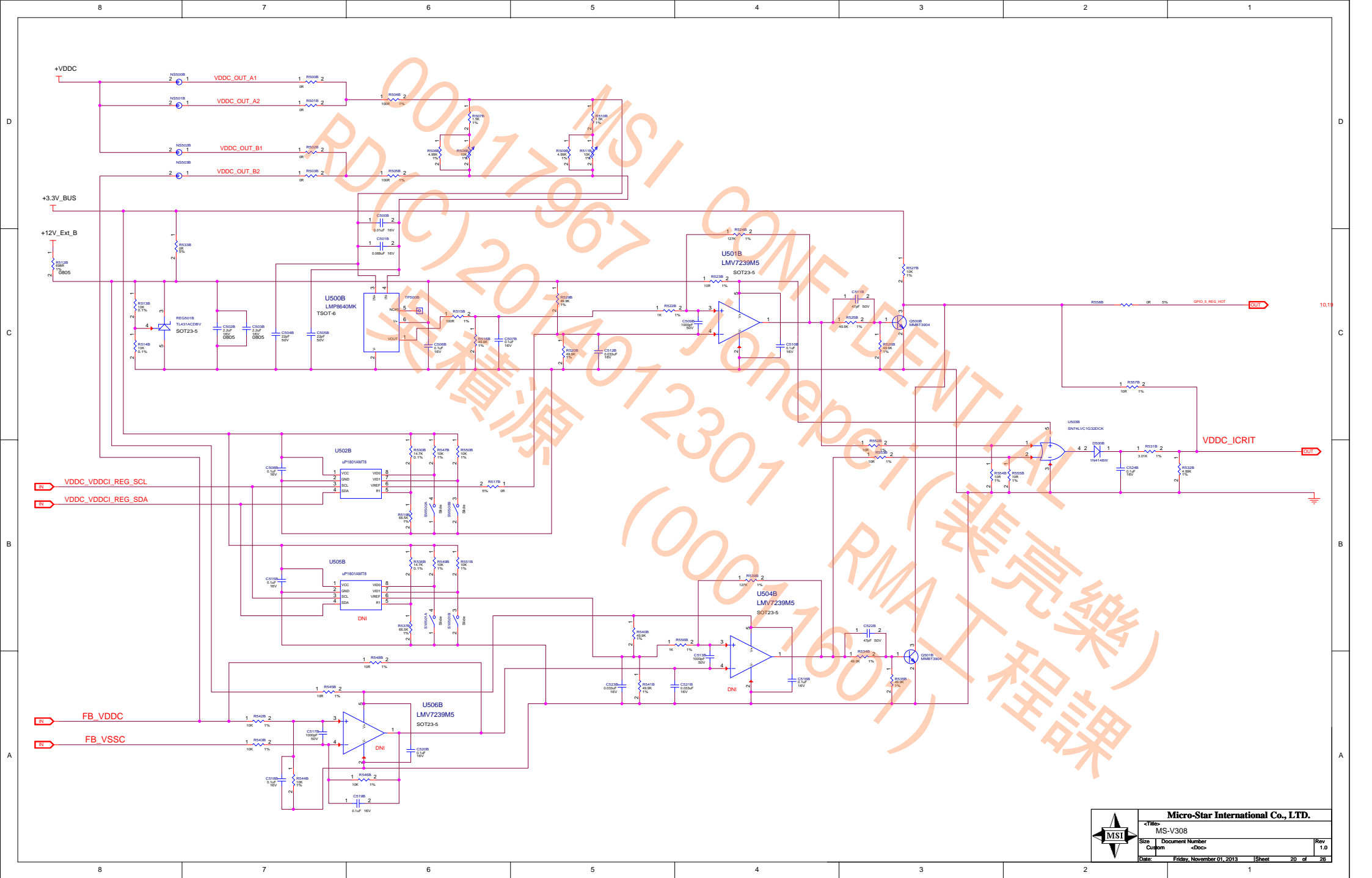
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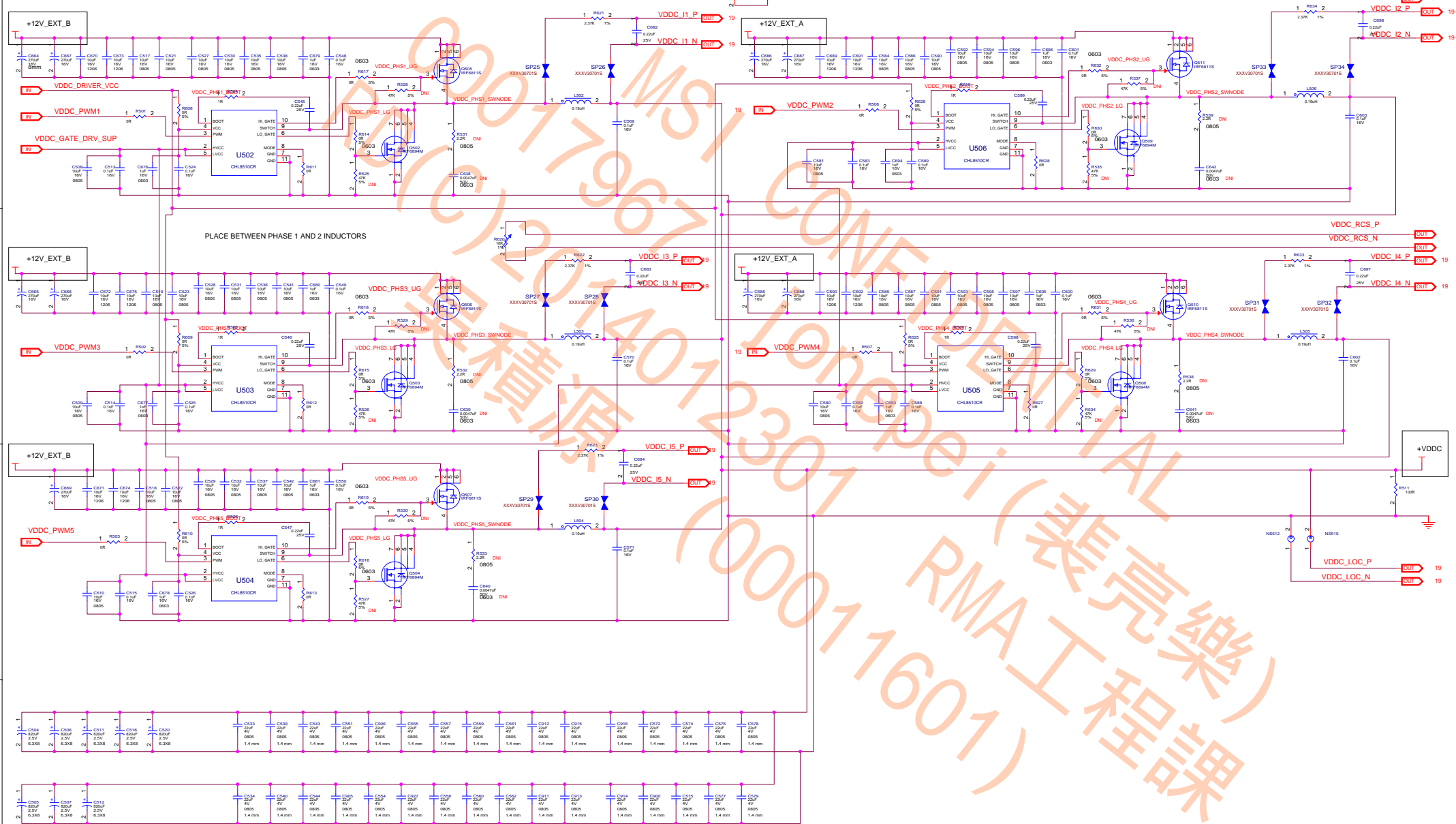
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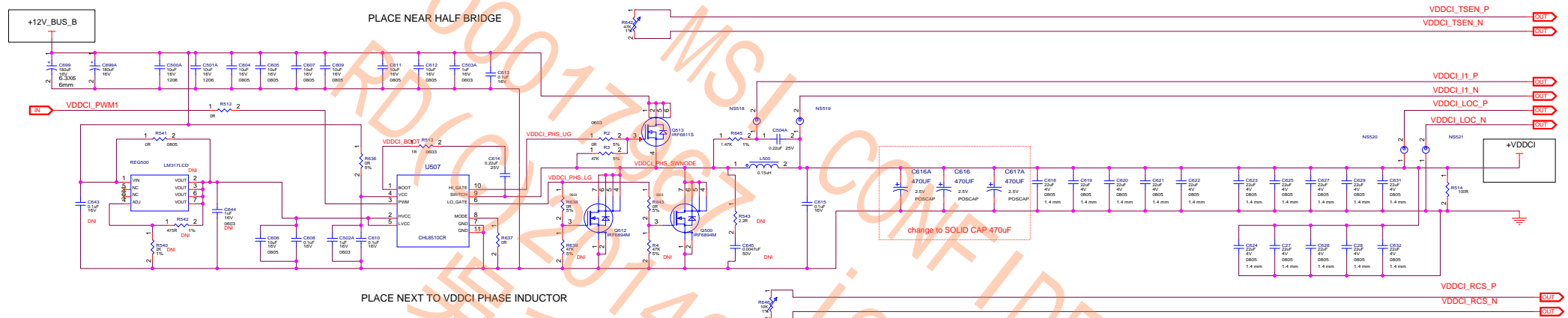




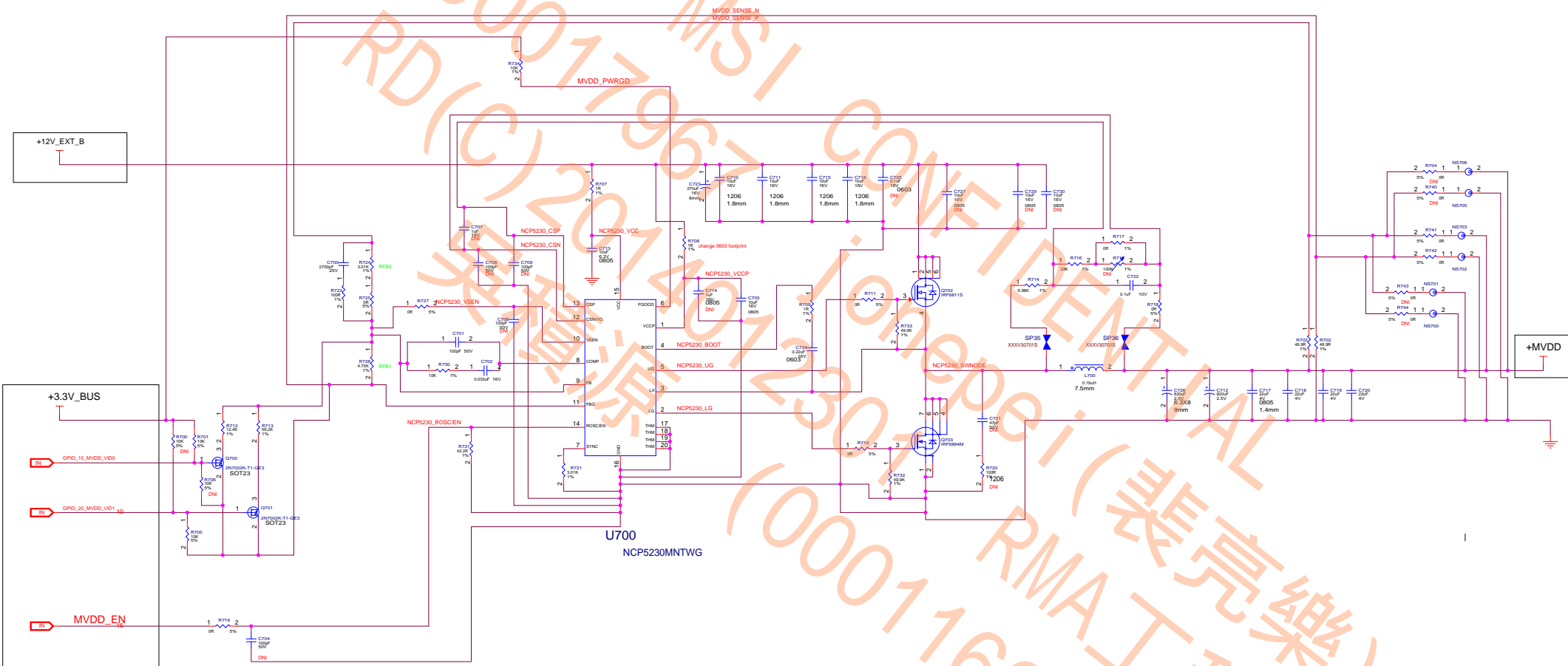
# VDDC 1~5 phase



VDDCI phase



MVDD



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RD(C)2014012301 jonepei (裴亮樂)  
吳積源 RMA工程課  
(00011601)

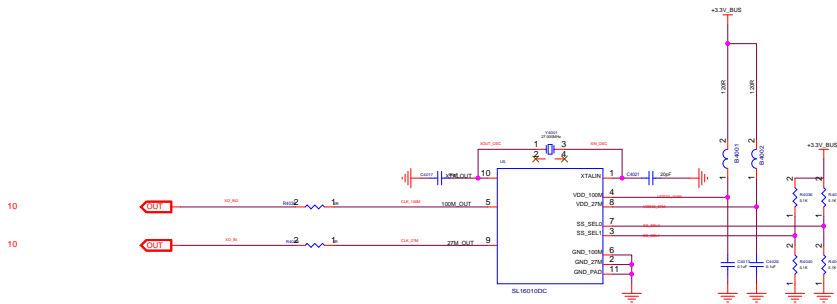
LED GREEN "ON" shows PX\_EN

remove

remove JTAG circuit


LED RED "ON" shows Fault


remove







|                  |            |   |  |  |  |                                   |  |  |  |                               |  |  |  |   |  |
|------------------|------------|---|--|--|--|-----------------------------------|--|--|--|-------------------------------|--|--|--|---|--|
| 8                |            | 7   |  | 6                                      |  | 5                                 |  | 4  |  | 3                             |  | 2  |  | 1 |  |
| AMD              |            |  |  | TITLE:<br>Hawaii GD5 - DVI-DVI+HDMI-DP |  | DOCUMENT NUMBER:<br>105-C671XX-00 |  | DATE:<br>Wed Oct 16 20:41:25 2013  |  | SHEET NUMBER:<br>26 OF 26     |  | REV:<br>1.0  |  |   |  |
| REVISION HISTORY |            |   |  | ENGINEER:<br>XXXX                      |  | NOTES:<br>NOTE                    |  | CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC.<br><small>This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representation or warranty, of any kind regarding this schematic and design, including, not limited to, any implied warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information included herein.</small> |  | C 2013 Advanced Micro Devices |  | AMD - GRAPHICS<br>1 COMMERCE VALLEY<br>MARKHAM, ONTARIO, L3T 7X6 |  |   |  |
| SCH Rev          | PCB Rev    | Date  | REVISION DESCRIPTION   |  |  |                                   |  |  |  |                               |  |  |  |   |  |
| 1                |            |   |  |  |  |                                   |  |  |  |                               |  |  |  |   |  |
| 2                | A          | SEP 6 2013  |  |  |  |                                   |  |  |  |                               |  |  |  |   |  |
|                  | B          | MAR 18 2013   | REMOVED LOCAL ANALOG GROUND FOR U700<br>ADDED SELECTABLE REGULATION POINT AT THE LOAD<br>CONNECTED VSEN (UVP AND OVP DETECT) THROUGH AN RC FILTER R727 C706 (DNI FOR THIS REVISION)<br>MODIFIED PHASE LEAD ZERO POSITION BY CHANGING R723 FROM 590R TO 100R<br>CHANGED C712 FROM DNI TO 820uF 2.5V , Remove C666<br>Added R363 , R364 , Q354<br>PCC CIRCUIT UPDATED WITH NON INVERTOR OPTION , R531B , R532B ADDED |  |  |                                   |  |  |  |                               |  |  |  |   |  |
|                  | C          |   | Power Management (16) , VDD (19) , PCC (20) , +1.8V regulator changed to LDO   |  |  |                                   |  |  |  |                               |  |  |  |   |  |
|                  | D          |   | Fan supply changed from +12_BUS to +12_BUS_B , MC201 added , J201 removed  |  |  |                                   |  |  |  |                               |  |  |  |   |  |
|                  | E          |   | PCC Circuit page 20 updated  |  |  |                                   |  |  |  |                               |  |  |  |   |  |
|                  | -00        |   | CF Connector removed,Replaced C1270-C1294 (25 caps) with 47uF  |  |  |                                   |  |  |  |                               |  |  |  |   |  |
|                  | 2010/10/25 |   | P01. remove JTAG circuit<br>P12. change DP and HDMI footprint<br>P21. VDDC choke change to DIP package<br>input cap change to 270uF 8X8<br>output cap change to 820uF 6.3X8<br>P22. VDDCI output CAP change to 470uF X3<br>P23. MVDD choke change to DIP package<br>input cap change to 270uF 8X8<br>output cap change to 820uF 6.3X8<br>P24. remove debug circuit   |  |  |                                   |  |  |  |                               |  |  |  |   |  |
| 8                |            | 7   |  | 6                                      |  | 5                                 |  | 4  |  | 3                             |  | 2  |  | 1 |  |



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Tuesday, October 29, 2013

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