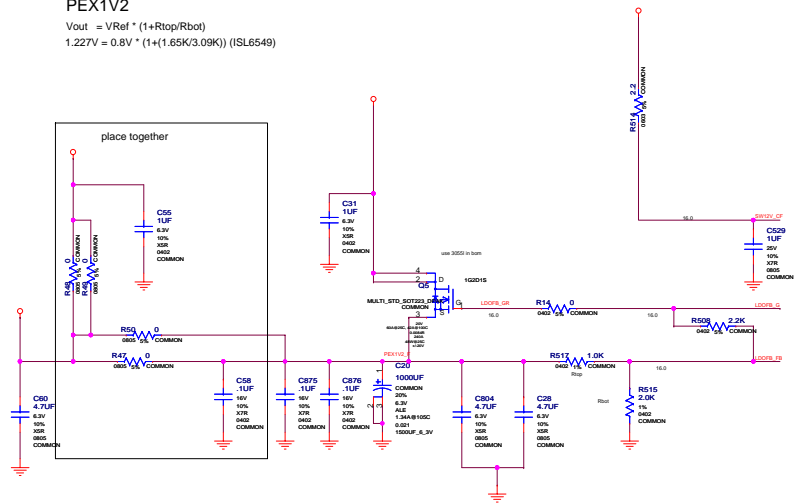


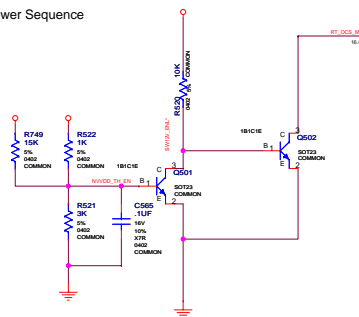
PowerSupplyIII: FBVDDQ, PEX1V2

PEX1V2

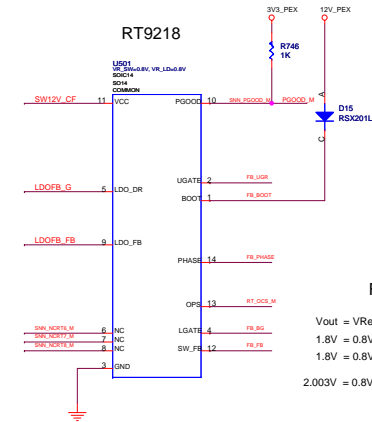
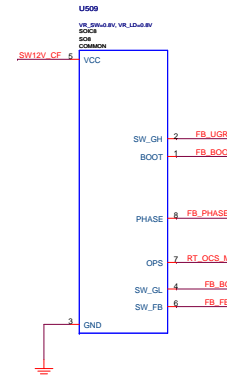
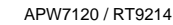
$$V_{out} = V_{ref} * (1 + R_{top}/R_{bot})$$

$$1.227V = 0.8V * (1 + (1.65K/3.09K)) \text{ (ISL6549)}$$


Power Sequence



Power down option
A3V3 Rtop 15K Rbot 10K
NVVDD Rtop 1K Rbot 3K



FBVDDQ:-RT9218

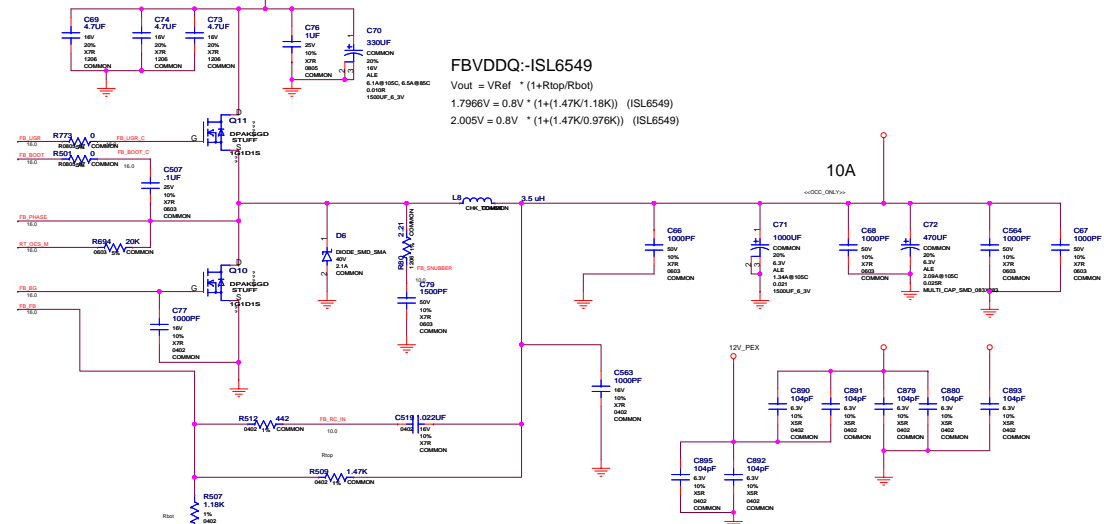
$$V_{out} = V_{Ref} * (1 + R_{top}/R_{bot})$$

$$1.8V = 0.8V * (1 + (5k/4k)) \quad (RT9218)$$

$$1.8V = 0.8V * (1 + (200/160)) \quad (RT9218) \text{ Samsung MEM}$$

$$2.003V = 0.8V * (1 + (200/133)) \quad (RT9218) \text{ Infineon MEM default}$$


FBVDDQ:-ISL6549

$$V_{out} = V_{Ref} * (1 + R_{top}/R_{bot})$$
$$1.7966V = 0.8V * (1 + (1.47K/1.18K)) \quad (ISL6549)$$
$$2.005V = 0.8V * (1 + (1.47K/0.976K)) \quad (ISL6549)$$


ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PowerSupply: FBVDDQ, PLLVDD

NVIDIA CORPORATION

2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA



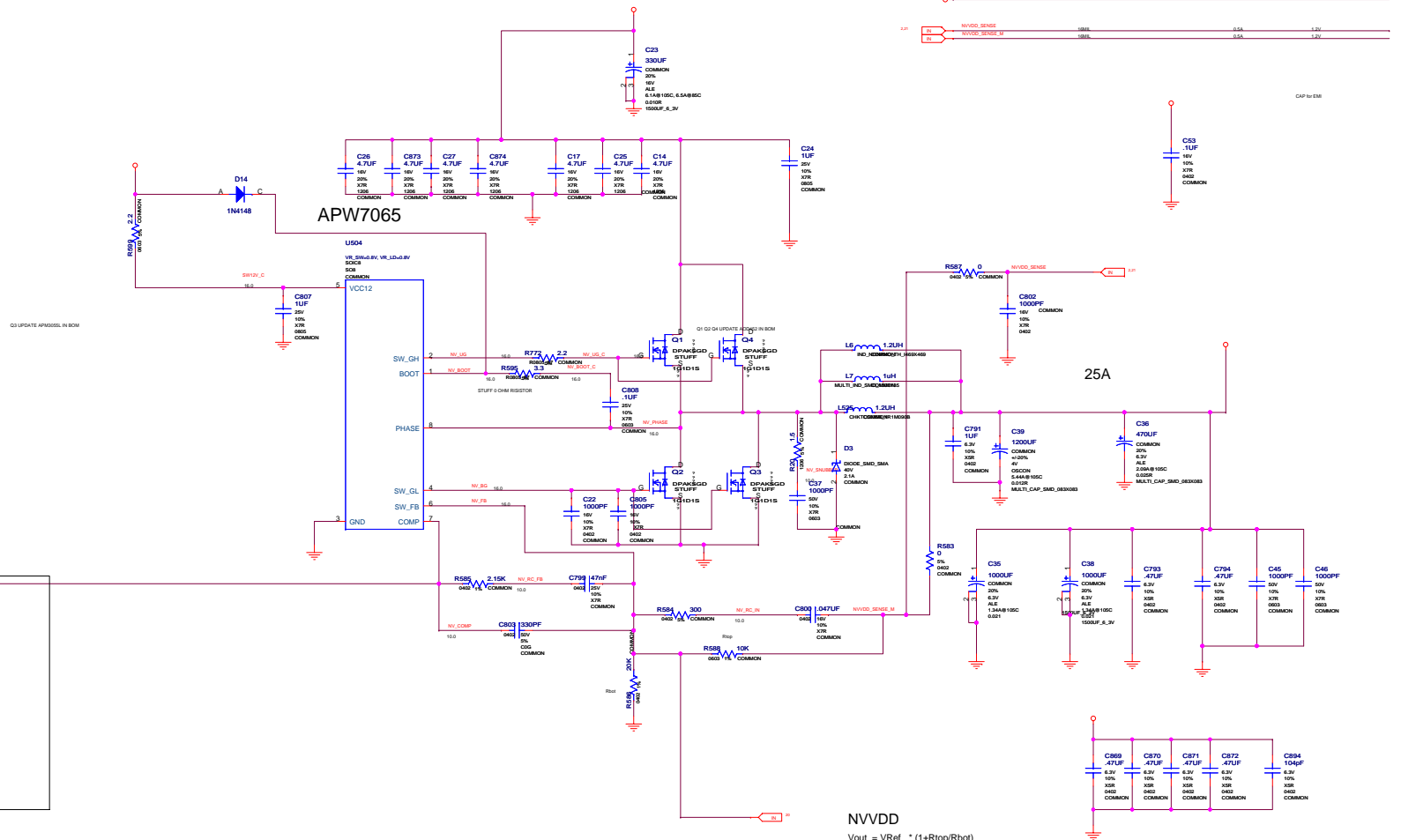
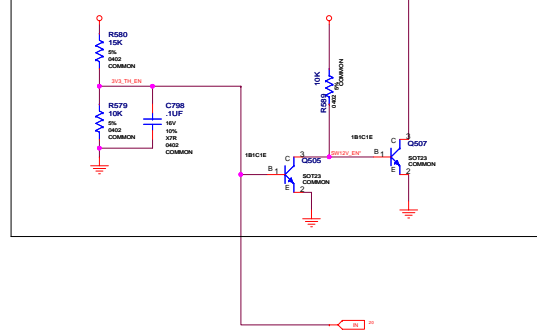
NV_PN	600-10403-0000-200 A
-------	----------------------

ID		PAGE	
NAME		DATE	12-JAN-2007

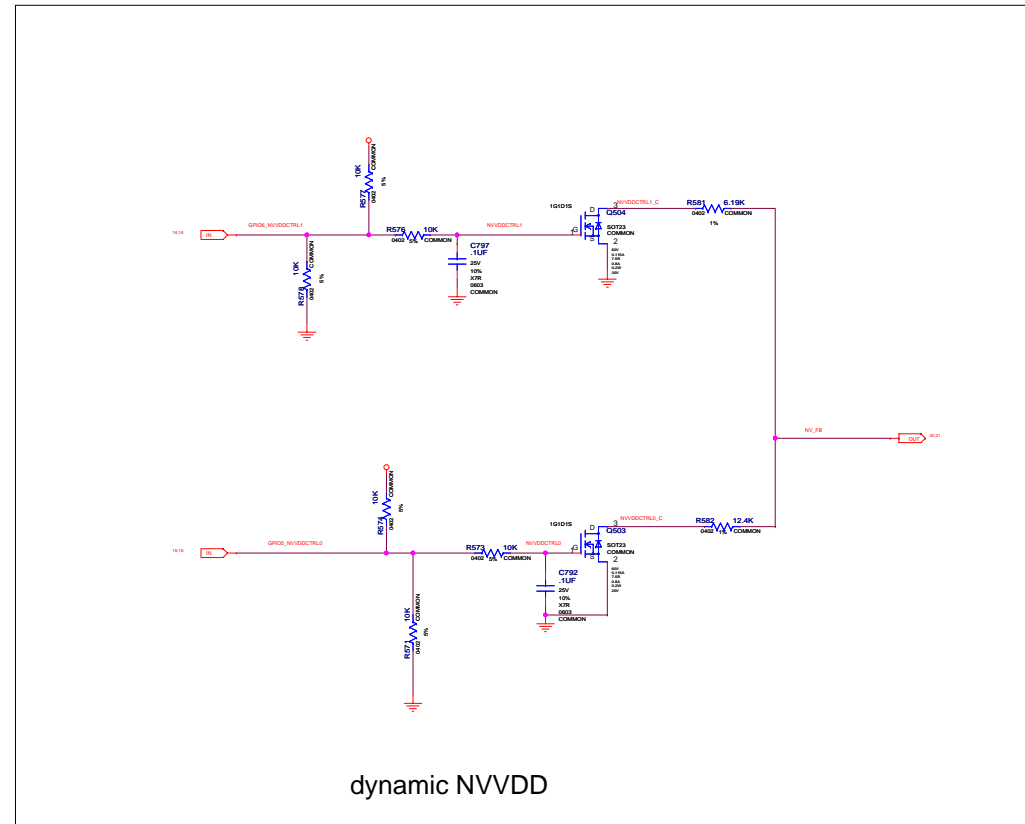
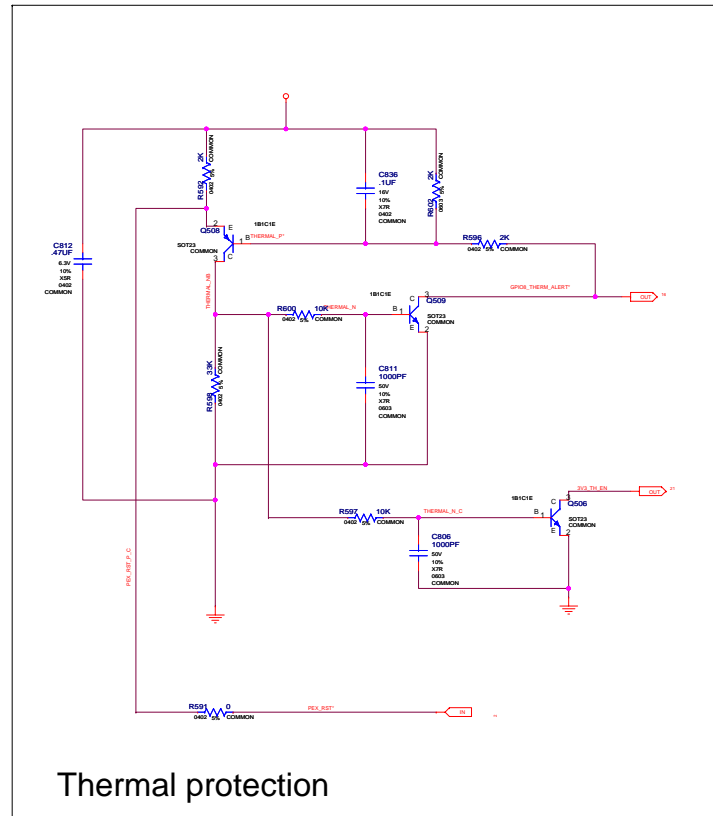
ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, RECOMMENDATIONS, DATA SHEETS, DIAGRAMS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VARIATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

PowerSupplyII: NVVDD

Power Sequence



thermal protection and dynamic NVVDD



ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, RECOMMENDED PRACTICES, STANDARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	thermal protection and dynamic NVDD

NVIDIA CORPORATION

2701 SAN TOMAS EXPRESSWAY

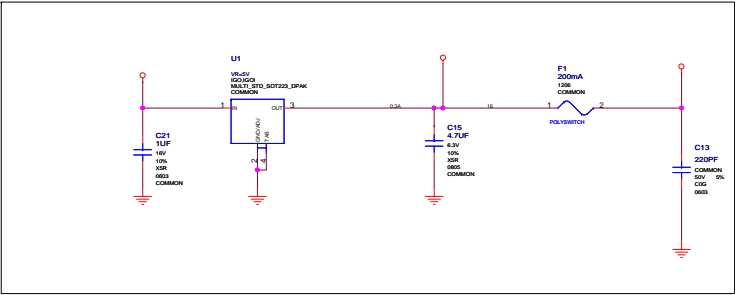


NV_PN	600-10403-0000-200 A
-------	----------------------

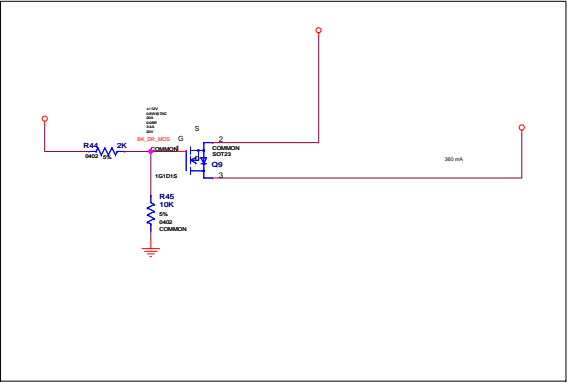
ID	PAGE
NAME	DATE 12-JAN-2007

Power SupplyI:TMDS_IOVDD A3V3 DDC_5V IFP_PLLVDD

DDC 5V



TMDS IO SUPPLY WITH BACKDRIVE PROTECTION

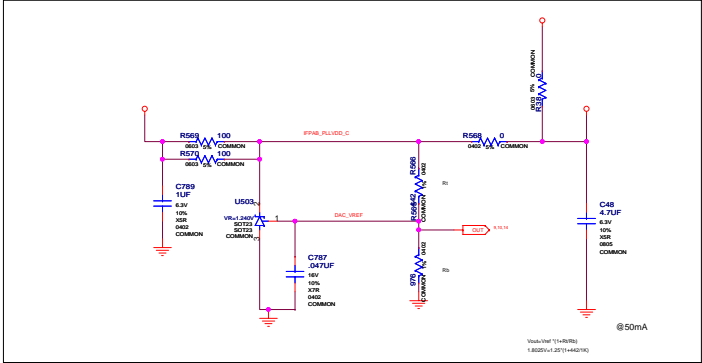


NETNAME	MAX_CURRENT	MIN_LINE_WIDTH	VOLTAGE
DDC_5V	0.1A	12.0	5.00000V
A3V3	1.5A	30.0	3.30000V
IFP4B_OVDD	0.3A	16	3.30000V
GND		30.0	0.00000V
IFP_PLLVDD	0.3A	16	1.80000V

A3V3 Power Supply



IFP_PLL SUPPLY

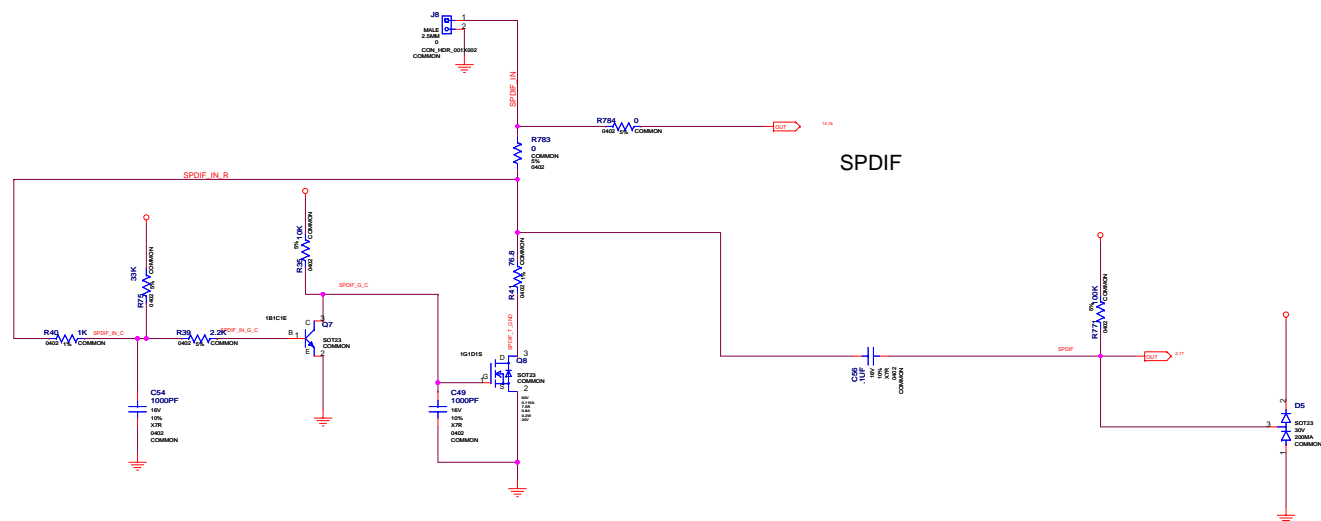


ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, MATERIALS) ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	POWER SUPPLY: TMDS IOVDD 5V A3V3

NVIDIA CORPORATION		3701 SAN TOMAS EXPRESSWAY	
SANTA CLARA, CA 95050, USA		NV_PN	
600-10403-0000-200 A		PAGE	
12		DATE	
12-JAN-2007		NAME	

Signal	Direction	Width	Source
IN	Input	1	SECIM
IN	Input	1	SECIM



place close to GPU

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	SPDIF

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOW AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

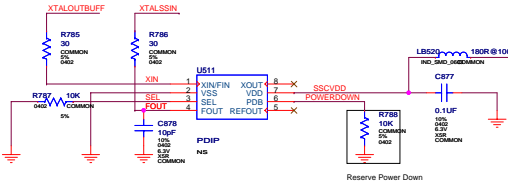
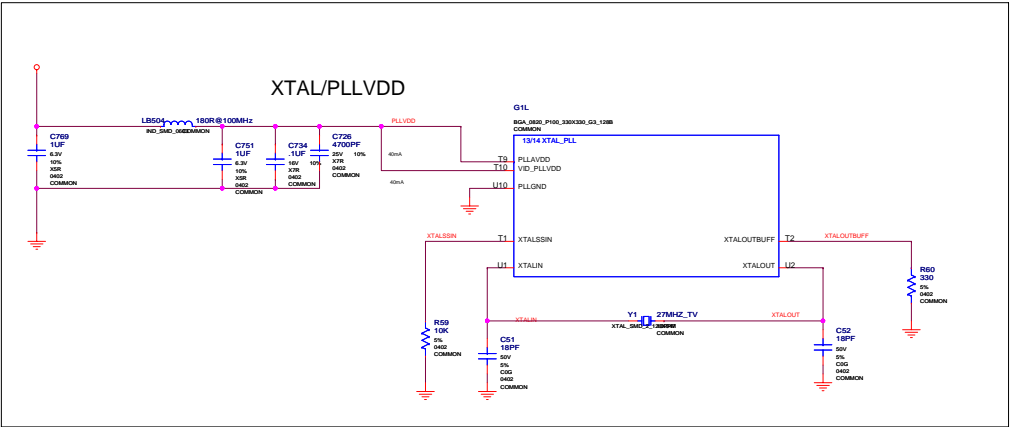
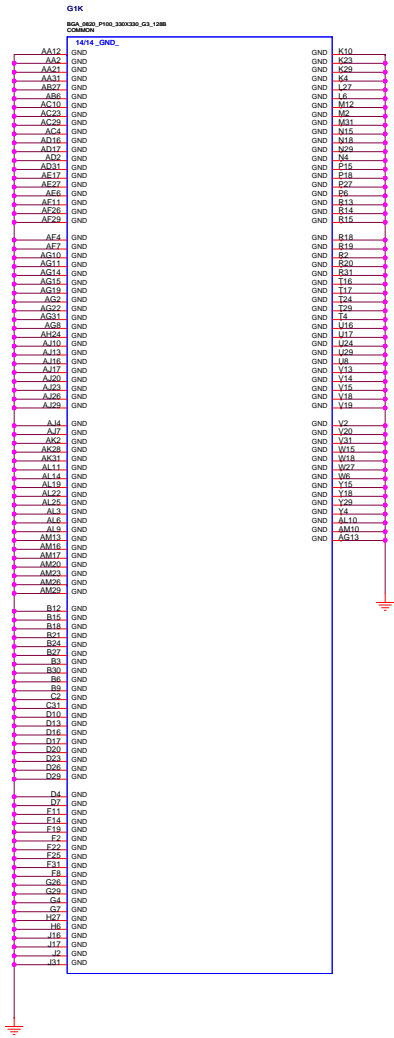


2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA

NV_PN	600-10403-0000-200 A
-------	----------------------

ID		PAGE	
NAME		DATE	12-JAN-2007

GPU GND CONNECTION, XTAL



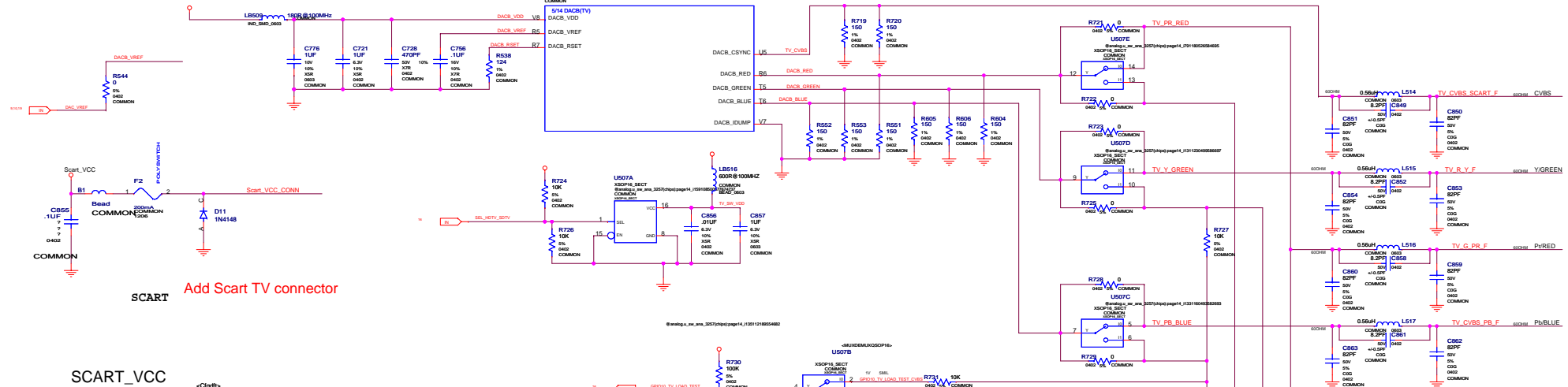
NET	WV_CRITICAL	WV_IMPEDANCE	DIFFPAIR
XTALIN	1	300M	
XTALSSIN	1	300M	
XTALOUT	1	300M	
XTALOUTBUFF	1	300M	
NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
PLLVD	1.2V	0.3A	12MIL

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTIC LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, MATERIALS) ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

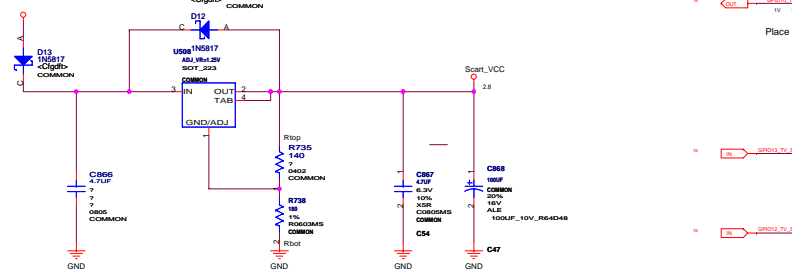
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO. STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	GPU GND CONNECTION, XTAL

NVIDIA CORPORATION	
3701 SAN TOMAS EXPRESSWAY	
SANTA CLARA, CA 95050, USA	
NV_PN	600-10403-0000-200 A
ID	PAGE
NAME	DATE
	12-JAN-2007

DACB .. MiniDIN VIDEO OUT CONNECTOR



SCART_VCC



DACB NET RULES

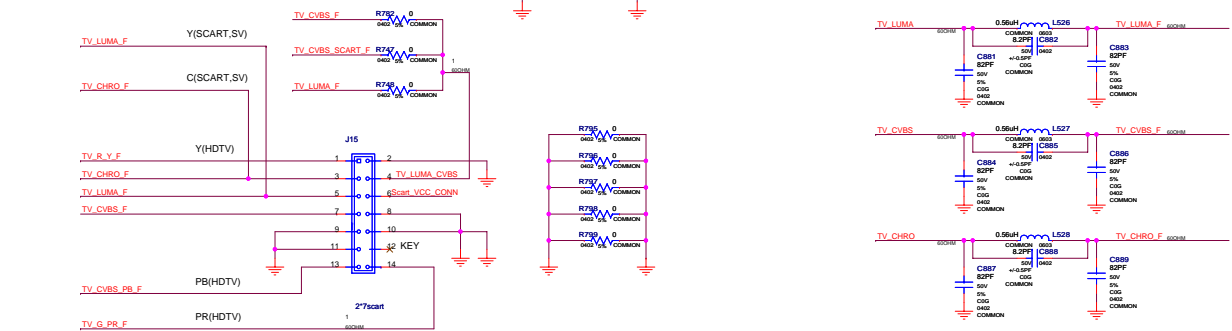
NET	NV_CRITICAL	NV_IMPEDANCE	DIFFPAIR
IN TV_C080_F	1	500M	
IN MDN_C000T_C	1	500M	
IN TV_L000A_F	1	500M	
IN MDN_Y000T_C	1	500M	
IN TV_C085_F	1	500M	
IN MDN_P000T_C	1	500M	

	NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
IN	DACB_VDD	3.30000V	0.07A	16.0
IN	DACB_VREF			16.0
IN	DACB_RESET			16.0

GPO0=0:
GPO1=0: SCART_VID_CNTR = 12V For 4X3

GPO0=0:
GPO1=1: SCART_VID_CNTR = 6V For 16X9

GPO0=1:
GPO1=1: SCART_VID_CNTR = 0V No signal



NVIDIA CORPORATION

2701 SAN TOMAS EXPRESSWAY



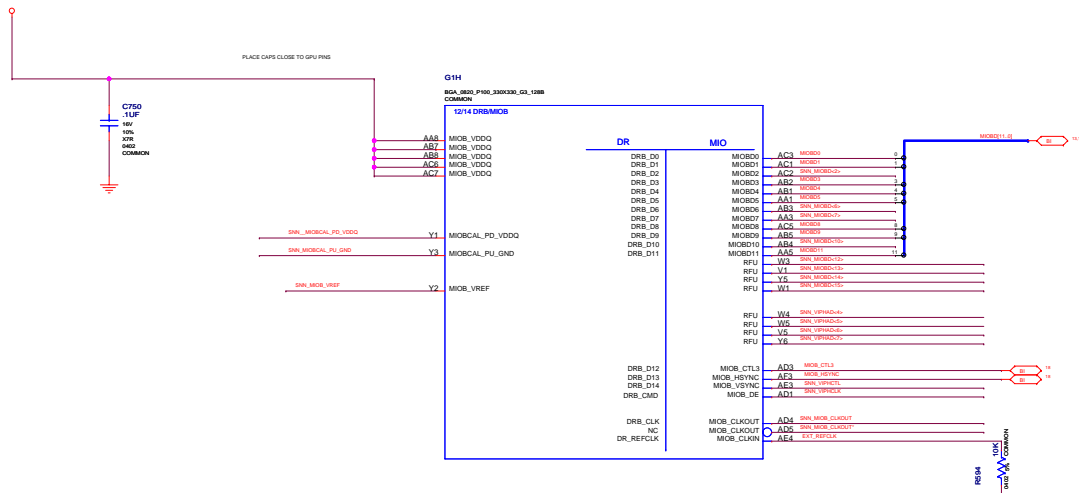
NV_PN	600-10403-0000-200 A
-------	----------------------

ID		PAGE	
NAME		DATE	12-JAN-2007

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE. AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACB FILTERS, SYNC STRIPPER, MINIDIN CONNECTOR NORTH, HDTV HEADER

G3 VIP/MIOB

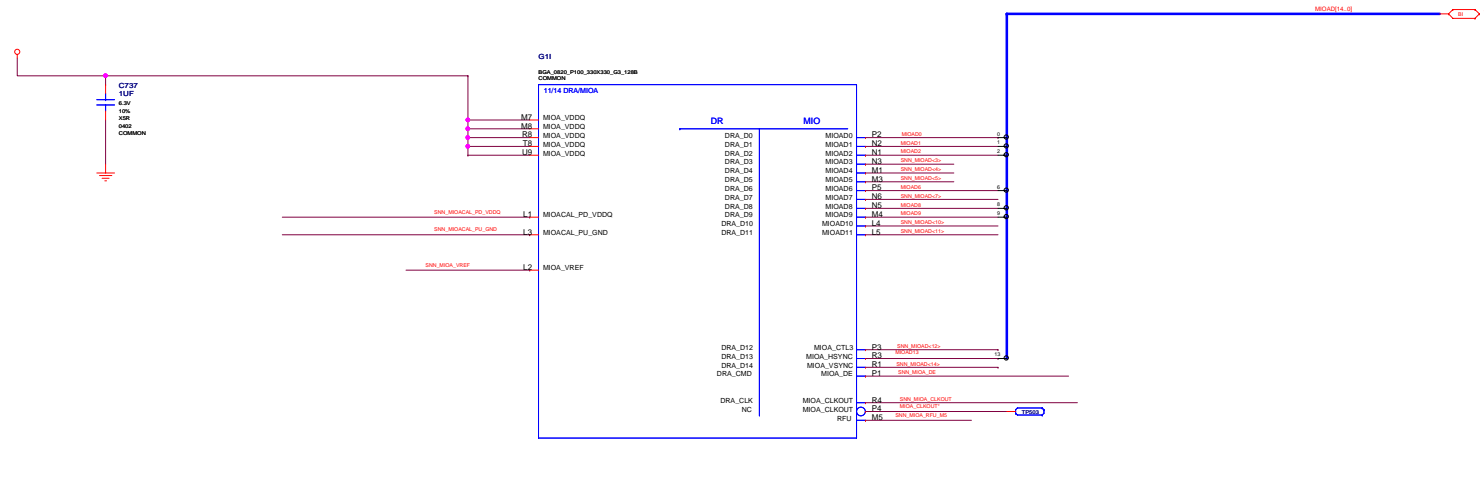


MIO NET RULES

NET	NV_CRITICAL	NV_IMPEDANCE	DIFFPAIR
MIOB11_0	2	50OHM	
EXT_REFCUK	2	50OHM	
MIOB11_0	2	50OHM	
MIOB11_1	2	50OHM	
MIOB11_2	2	50OHM	
MIOB11_3	2	50OHM	
MIOB11_4	2	50OHM	
MIOB11_5	2	50OHM	
MIOB11_6	2	50OHM	
MIOB11_7	2	50OHM	
MIOB11_8	2	50OHM	
MIOB11_9	2	50OHM	
MIOB11_10	2	50OHM	
MIOB11_11	2	50OHM	

NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
MIOB11_0	1.25000V		12.0
MIOB11_1	1.25000V		12.0
MIOB11_2	1.25000V		12.0
MIOB11_3	1.25000V		12.0
MIOB11_4	1.25000V		12.0
MIOB11_5	1.25000V		12.0
MIOB11_6	1.25000V		12.0
MIOB11_7	1.25000V		12.0
MIOB11_8	1.25000V		12.0
MIOB11_9	1.25000V		12.0
MIOB11_10	1.25000V		12.0
MIOB11_11	1.25000V		12.0

G3 MIOA



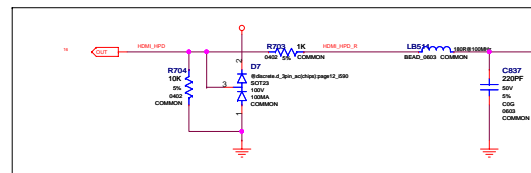
ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, MATERIALS) ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	STATE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	MIOA & MIOB

NVIDIA CORPORATION	
3701 SAN TOMAS EXPRESSWAY	
SANTA CLARA, CA 95050, USA	
NV_PN	600-10403-0000-200 A
ID	
NAME	
PAGE	12-JAN-2007

INTERNAL TMDS .. LINK C

Hotplug Detection

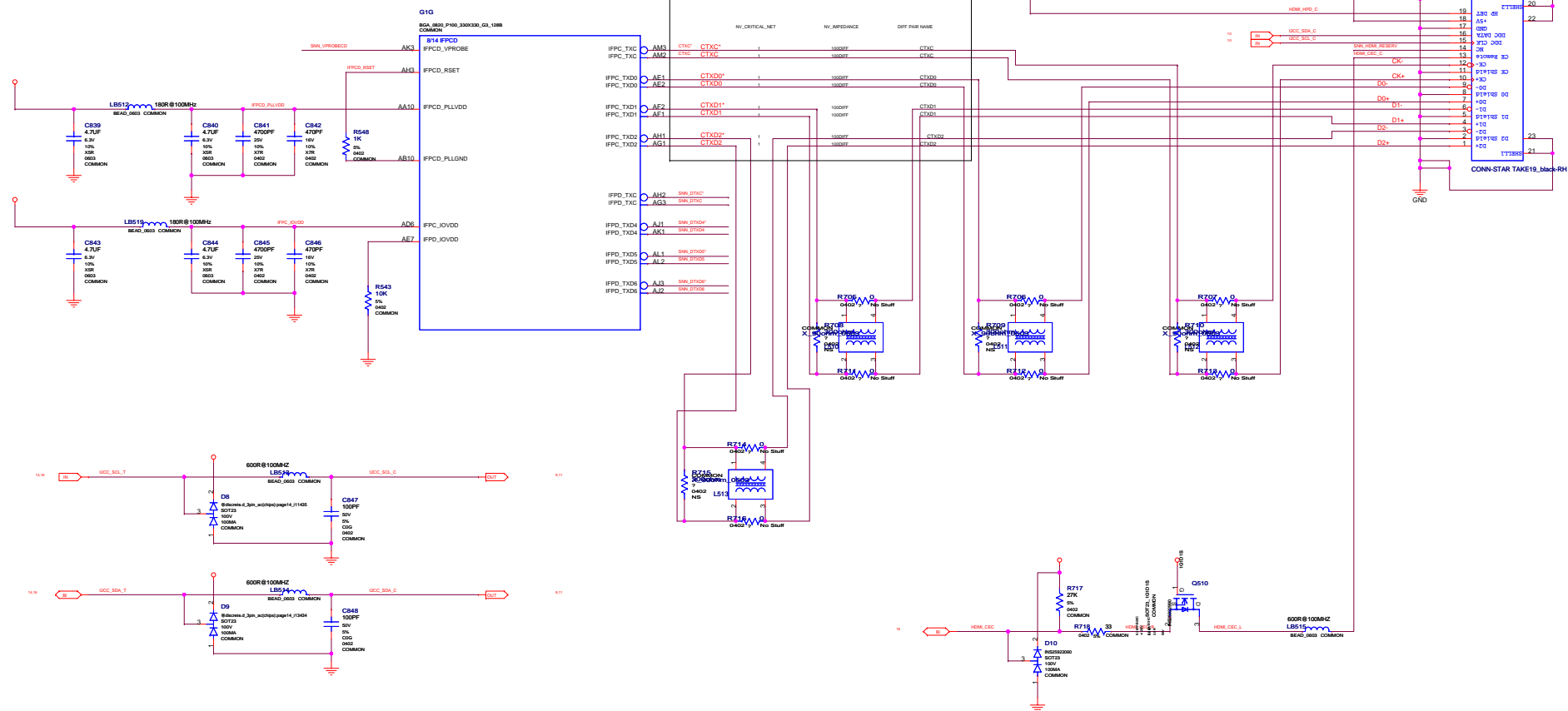


IFPAB NET RULES



NET RULES

	NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
(N)	IFPCD_PLLVDD	3.300000	0.04	16.0
(N)	IFPD_IOVDD	0.000000	0	10.0
(N)	IFPC_IOVDD	3.300000	0.15	16.0



CEC pullup and clamping must be disconnected from HDMI connector when Power down

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	TMD5 LINK C

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

NVIDIA CORPORATION

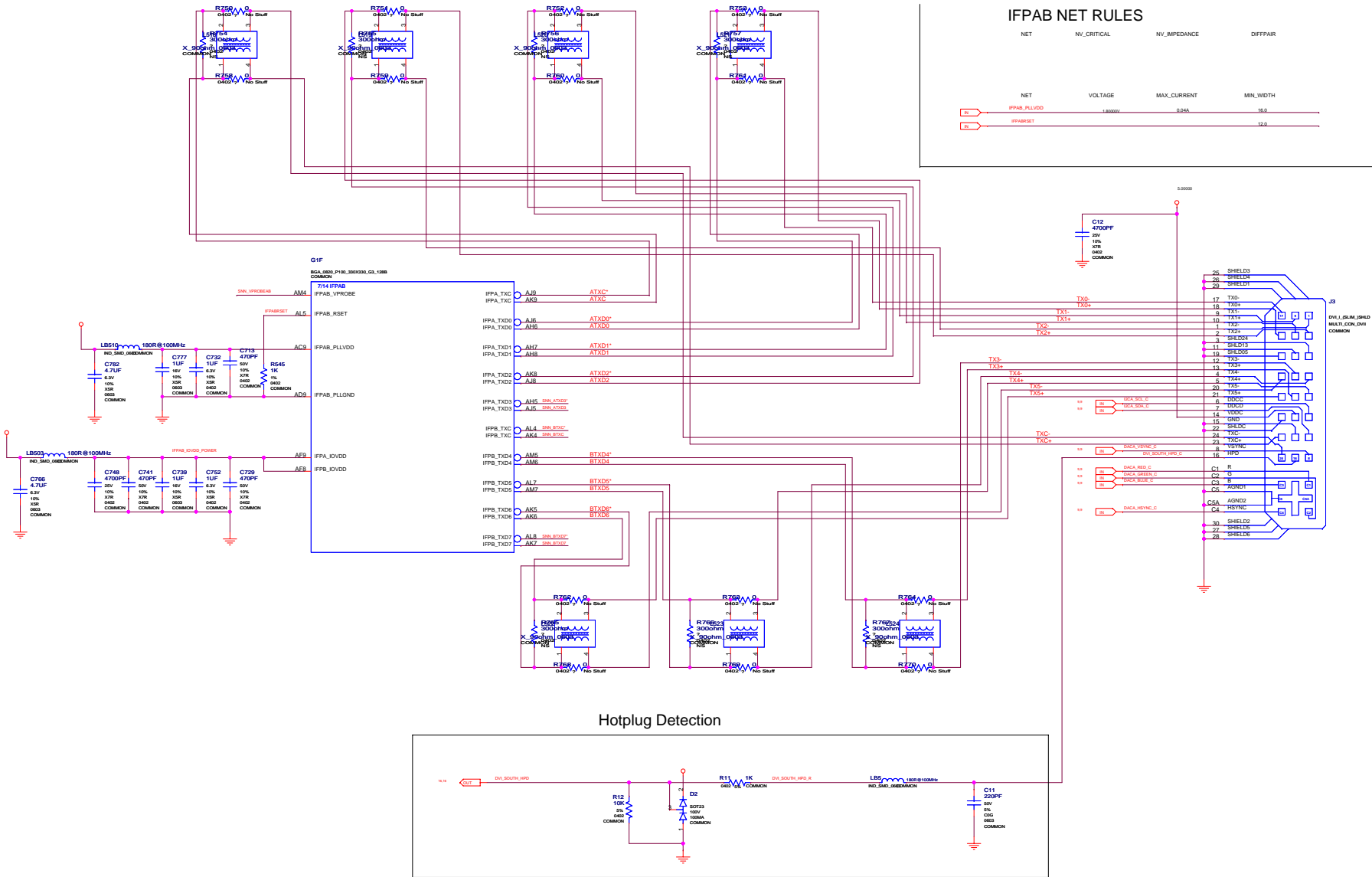


2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA

NV_PN	600-10403-0000-200 A
-------	----------------------

ID	PAGE
NAME	DATE 12-JAN-2007

INTERNAL TMD5 .. LINK A & B



ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, MATERIALS) ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY: STATE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL: TMD5 LINK A & B, DV1 CONNECTOR SOUTH

NVIDIA CORPORATION

3701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA

NV_PN: 600-10403-0000-200 A

ID	PAGE
NAME	DATE: 12-JAN-2007

DACC NET RULES

NET NV_CRITICAL NV_IMPEDANCE DIFFPAIR

DACC_RED	1	50OHM
DACC_GREEN	1	50OHM
DACC_BLUE	1	50OHM
DACC_RED_C	1	50OHM
DACC_GREEN_C	1	50OHM
DACC_BLUE_C	1	50OHM

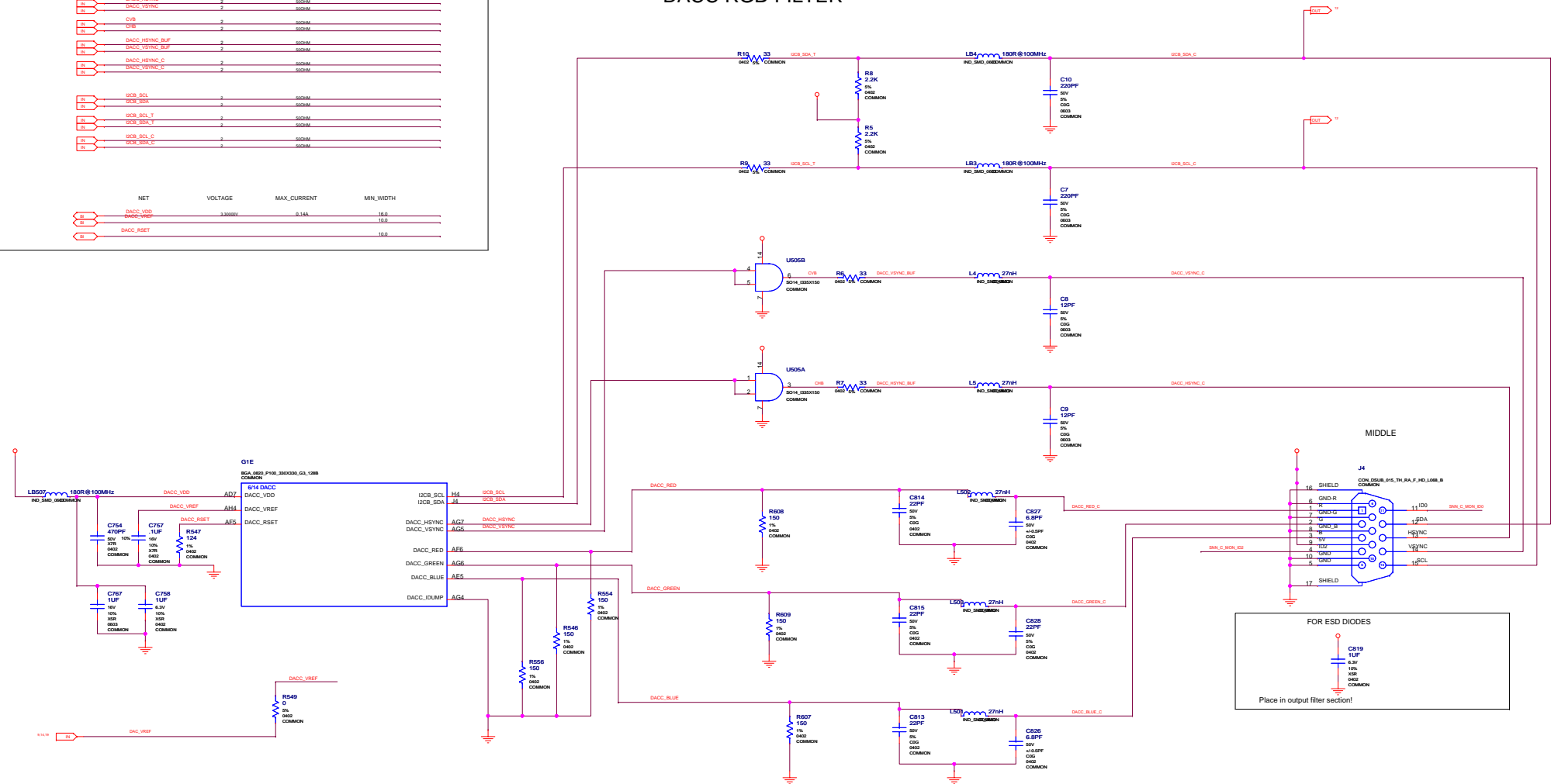
DACC_HSYNC	2	50OHM
DACC_VSYNC	2	50OHM
CVB	2	50OHM
CVB	2	50OHM
DACC_HSYNC_BUF	2	50OHM
DACC_VSYNC_BUF	2	50OHM
DACC_HSYNC_C	2	50OHM
DACC_VSYNC_C	2	50OHM

DCB_SCL	2	50OHM
DCB_SDA	2	50OHM
DCB_SCL_T	2	50OHM
DCB_SDA_T	2	50OHM
DCB_SCL_C	2	50OHM
DCB_SDA_C	2	50OHM

NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
DACC_VDD	3.3000V	0.16A	18.0
DACC_RESET			18.0

Secondary Display (DACC), DB15

DACC RGB-FILTER



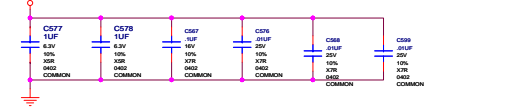
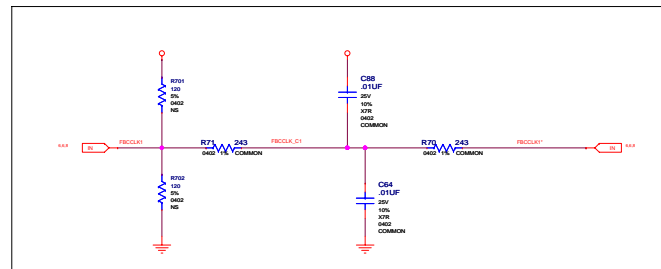
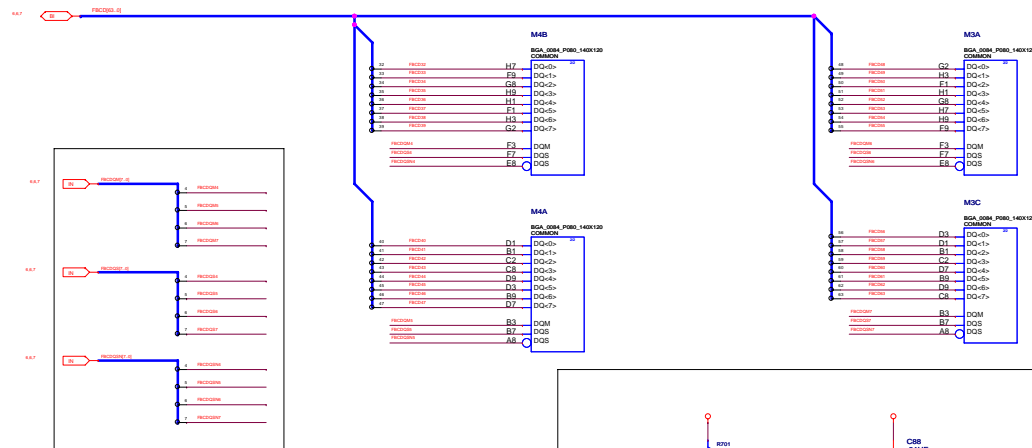
Primary Display (DACA), Slim DB15

DACA RGB-FILTER

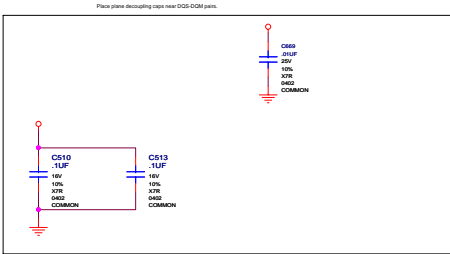
ID		PAGE	
NAME		DATE	12-JAN-2007

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, MATERIALS) ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY



PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY

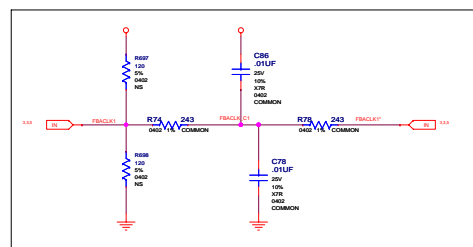
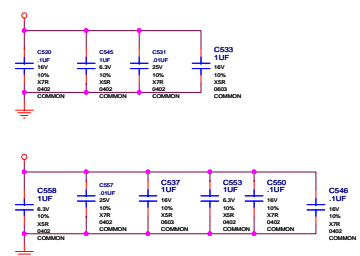
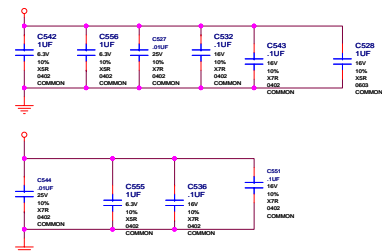



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	FBC 16MX16 DDR2 MEMORIES, 2ND BANK 0..31



ID		PAGE	
NAME		DATE	12-JAN-2007

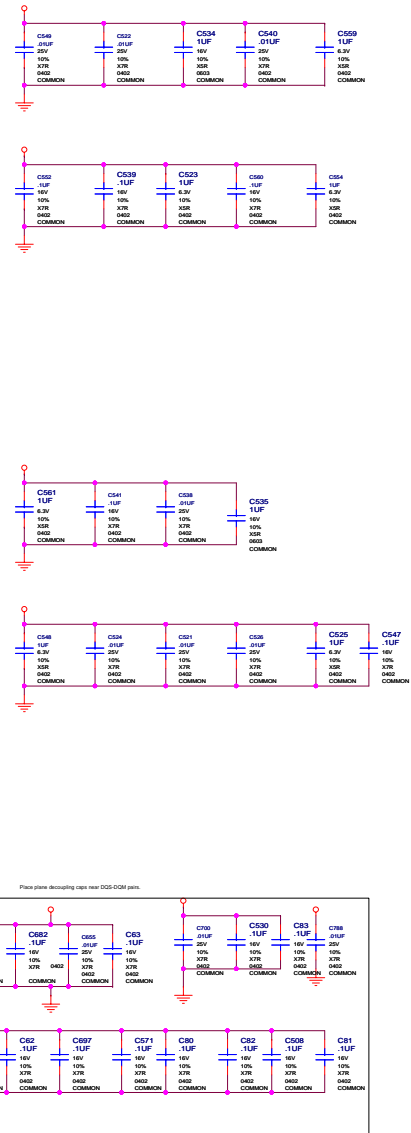
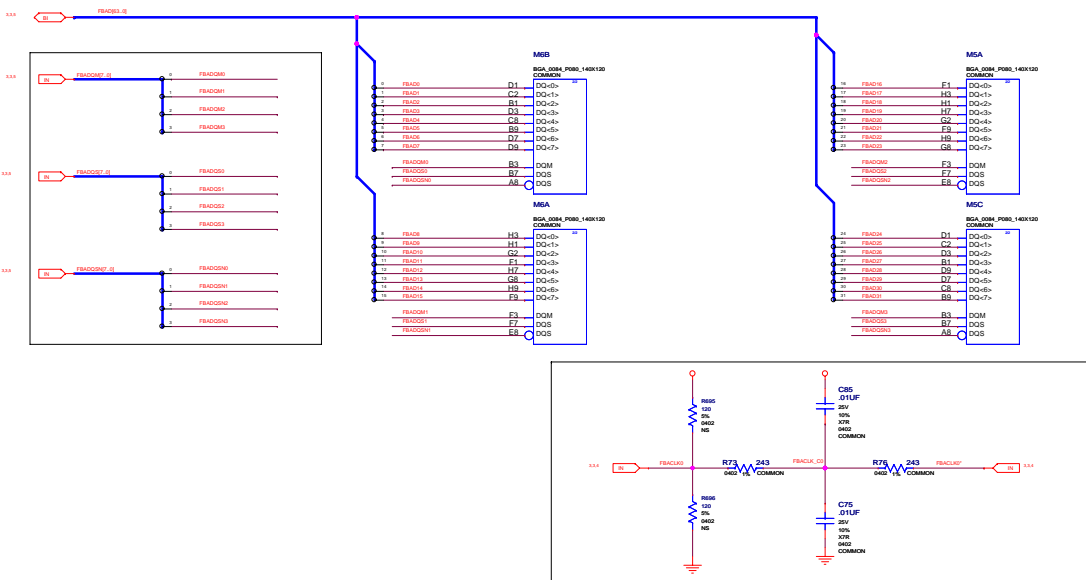
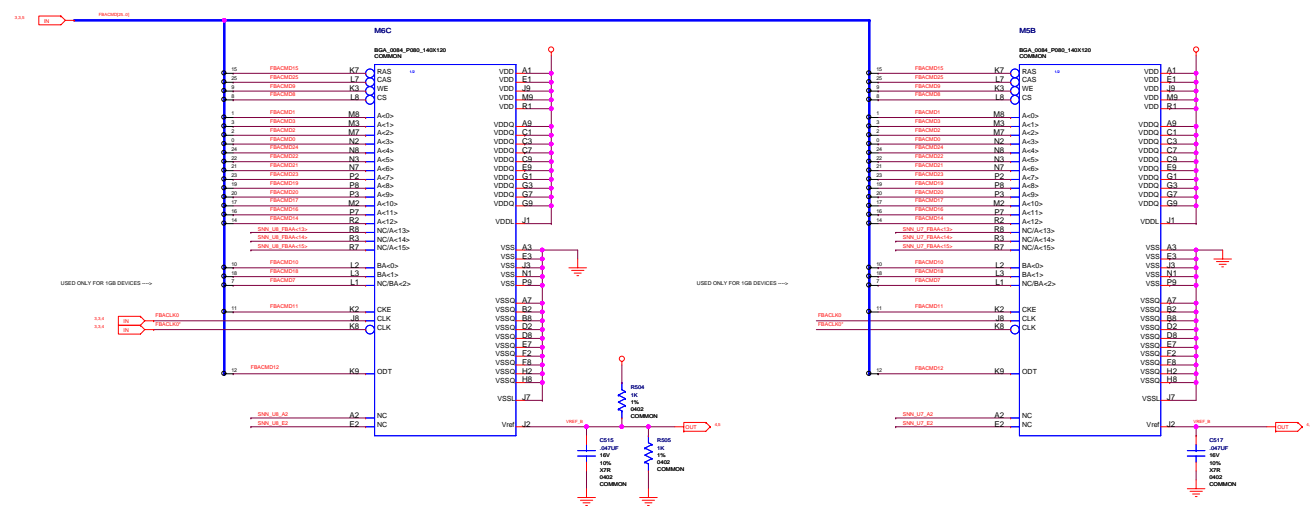
PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY



NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA			
NV_PN	600-10403-0000-200 A		
ID		PAGE	
NAME		DATE	12-JAN-2007

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOW AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY



ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	FBA 16Mx16 DDR2 MEMORIES, BANK 0..31

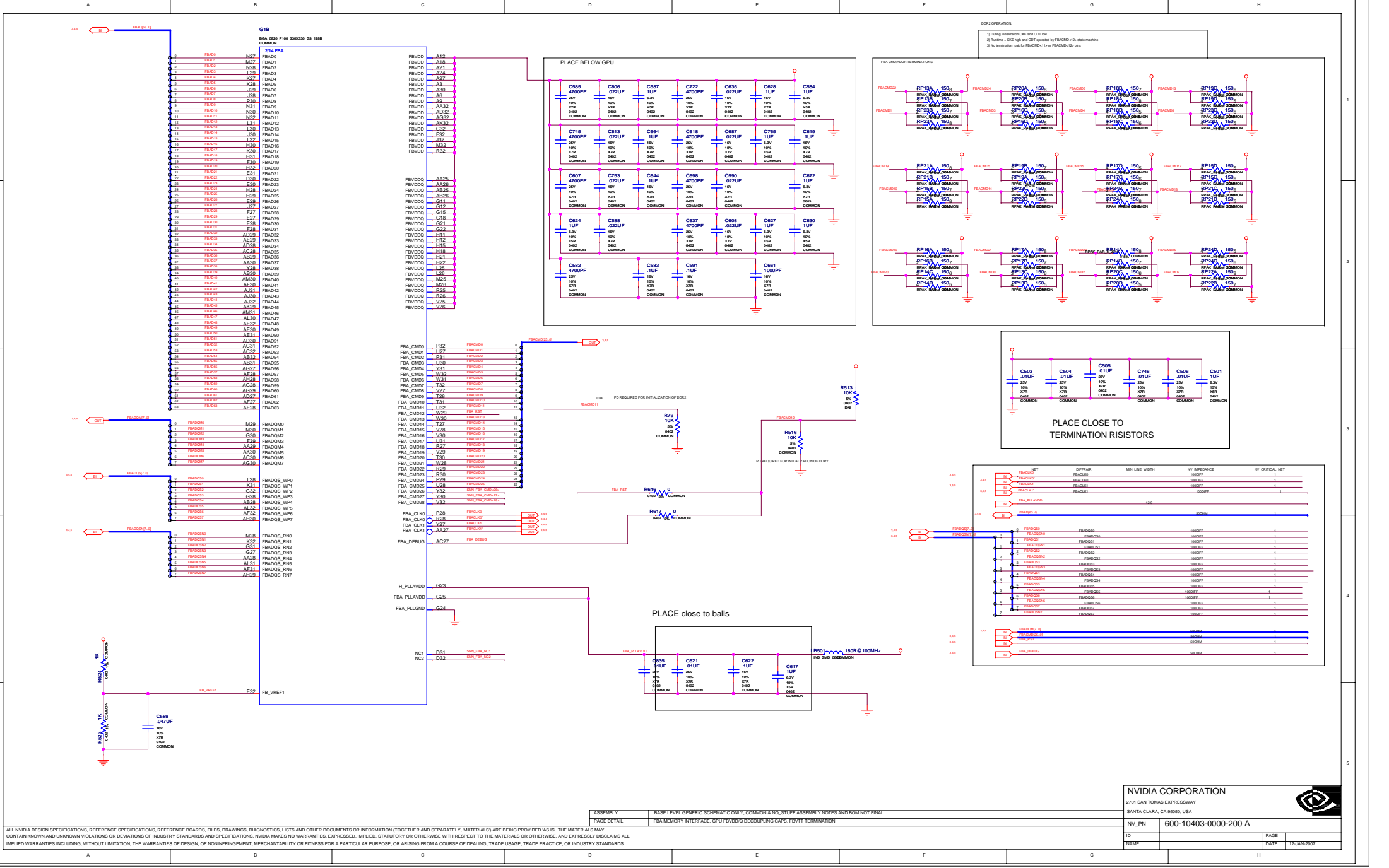
NVIDIA CORPORATION

2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA

NV_PN	600-10403-0000-200 A
-------	----------------------

ID		PAGE	
NAME		DATE	12-JAN-2007

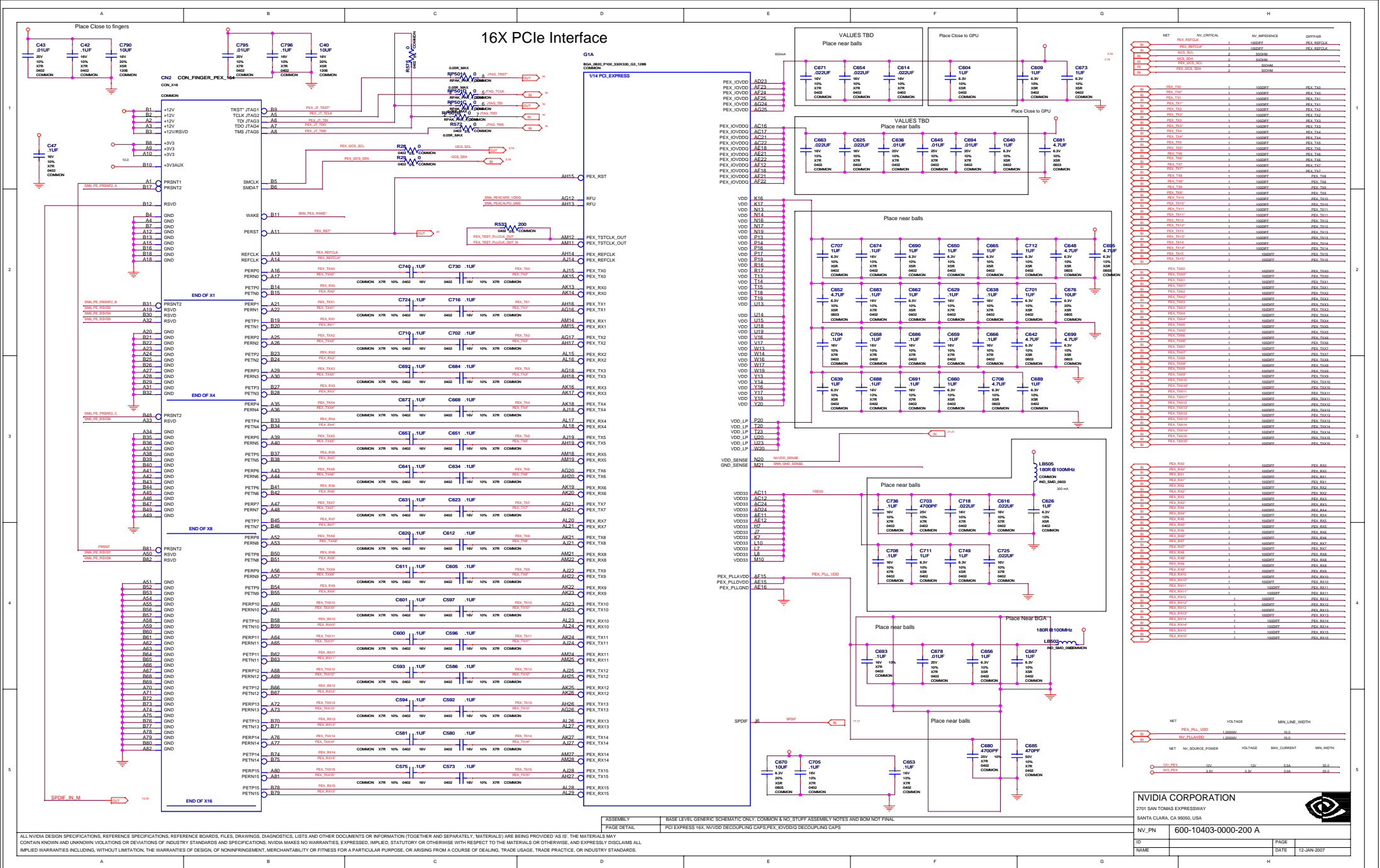




ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTIC LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, MATERIALS) ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY: BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO. 3100FF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL: FBA MEMORY INTERFACE, GPU FBVDD0 DECOUPLING CAPS, FBVTT TERMINATION

NVIDIA CORPORATION	
3701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA	
NV_PN	600-10403-0000-200 A
ID	PAGE
NAME	DATE 12-JAN-2007



P403-B00 DESIGN -- G84-200, 256 MB DDR2,DVI-I, VGA, HDTV

PAGE SUMMARY:

Page 1: TABLE OF CONTENTS & REVISION HISTORY

Page 2: PCI EXPRESS 16X, NVVDD DECOUPLING CAPS,PEX_IOVDD/Q DECOUPLING CAPS

Page 3: FBA MEMORY INTERFACE, GPU FBVDD/Q DECOUPLING CAPS, FBVTT TERMINATION

Page 4: FBA 16Mx16 DDR2 MEMORIES, BANK 0..31

Page 5: FBA 16Mx16 DDR2 MEMORIES, 1ST BANK 32..63

Page 6: FBC MEMORY INTERFACE, GPU FBVTT, FBVDDQ

Page 7: FBC 16MX16 DDR2 MEMORIES, 2ND BANK 0..31

Page 8: FBC 16Mx16 DDR2 MEMORIES, 2ND BANK 32..63

Page 9: DACA FILTERS, DACA SYNC BUFFERS & DB15 SOUTH

Page 10: DACC FILTERS, DACC SYNC BUFFERS & DB15 MID

Page 11: TMDS LINK A & PU's, DVI CONNECTOR SOUTH

Page 12: TMDS LINK C

Page 13: MIOA & MIOB

Page 14: DACB FILTERS, SYNC STRIPPER, MINIDIN CONNECTOR NORTH,HDTV HEADER

Page 15: GPU GND CONNECTION, XTAL

Page 16: JTAG, BIOS ROM, HDCP ROM, FAN CONTROL, GPIO

Page 17: SPDIF

Page 18: BIOS STRAPS & MECHANICALS

Page 19: POWER SUPPLY: TMDS IOVDD,SV,ASV3

Page 20: thermal protection and dynamic NVVDD

Page 21: PowerSupplyII: NVVDD

Page 22: PowerSupplyIII: FBVDDQ, PLLVDD

REV	VARIANT	NVPN	ASSEMBLY
B	BASE	600-10403-0000-200	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU0020	600-10403-0020-200	P403-B00 G84-200 459/400MHz 256MB 16Mx16 BGAB4 GDDR2 DVI-I DL+VGA+HDTV-Out
2	SKU0021	600-10403-0021-200	P403-B00 G84-200 459/400MHz 256MB 16Mx16 BGAB4 GDDR2 DVI-I DL+VGA+HDTV-Out
3	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
4	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
5	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
6	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
7	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
8	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
9	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
10	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
11	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
12	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
13	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
14	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
15	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>


ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS". THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	TABLE OF CONTENTS & REVISION HISTORY

NVIDIA CORPORATION

2701 SAN TOMAS EXPRESSWAY

SANTA CLARA, CA 95050, USA



NV_PN	600-10403-0000-200 A		
ID		PAGE	
NAME		DATE	12-JAN-2007