

MS-V236-2.0 change list

Base on MS-V236-1.1 modified below items,

2010/08/27

Page05 deleted M501 and data net link to M5  
Page06 deleted M502 and data net link to M6  
Page07 deleted M504 and data net link to M3  
Page08 deleted M503 and data net link to M4  
Page09 deleted M506 and data net link to M1  
Page10 deleted M505 and data net link to M2

MS-V236-3.0 change list

1005

P11 删除FBVDDQ DECAP

P17 删除6262 IC

P18 删除SLI

P19 删除JTAG, optional HDCP IC, GPIO12\_VID\_PSI

P22 删除 Hi-c CAP

P24 删除 L3, L4, C35, 0R电阻

P25 FBVDD 删除output CAP C46

P26 Change NVVDD PWM solution to UP6219  
(reference from V242 PCB10) ; chnage R68 to R30

P27 删除GPIO12\_VID\_PSI的电阻

1015

Remove Partition A

P11 Add Hi-C CAP for memory (option)

REV	VARIANT	NVPN	ASSEMBLY
1	BASE	600-11060-base-200	P1060 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
2	SKU001	600-11060-001-200	GE106-275 42 1600MHz/2000MHz 1024MB (Bps) 64Mx16 BGA170 GDDR5 (non gnd), DV+DP+miniHDMI, 140W
3	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
4	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
5	SKU005	600-11060-005-200	GF106-275 42 1600MHz/2000MHz 1024MB (Bps) 64Mx16 BGA170 GDDR5 on FBVFBSC, DV+DP+miniHDMI, 140W
6	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
7	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
8	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
9	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
10	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
11	SKU011	600-11060-011-200	GF106-250 42 1400/2000MHz 1024MB (Bps) 64Mx16 BGA170 GDDR5 (non gnd), DV+DP+miniHDMI, 95W
12	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
13	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
14	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
15	SKU021	600-11060-021-200	GF106-180 3/3 1400/2000MHz 1536MB (12pcs) 64Mx16 BGA170 GDDR5 (non gnd), DV+DP+miniHDMI, 125W

ASSEMBLY	P1060 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
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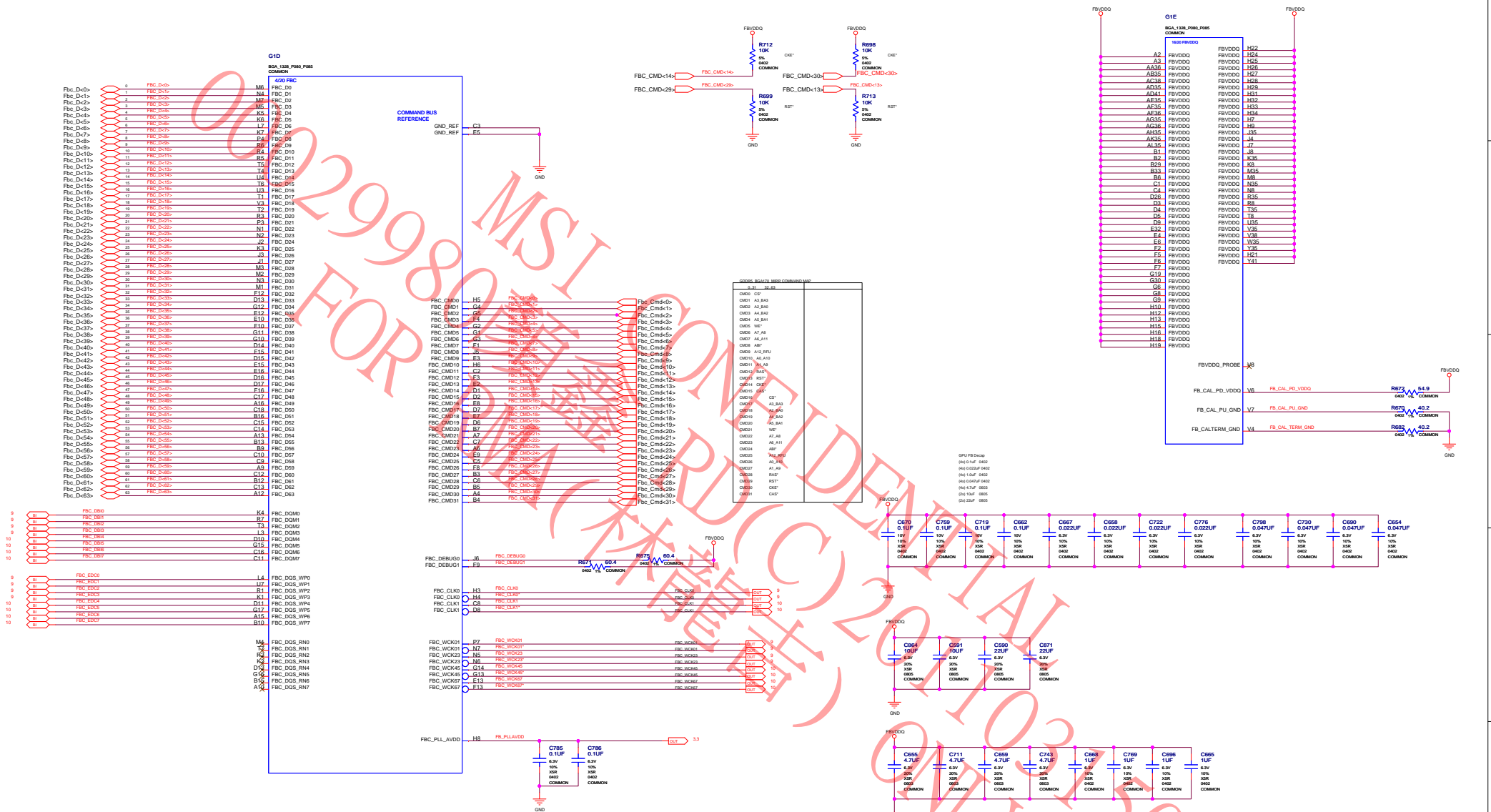


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MEMORY: GPU FB Partition C AND GPU FB Decap



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PAGE DETAIL	GPU FB Partition C and GPU FB Decap

DISABLE PARTITON A


Top-side Memory

Bottom-side memory

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PAGE DETAIL	FBA MEMORY (1 OF 2)

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DISABLE PARTITON A


Top-side Memory

Bottom-side memory

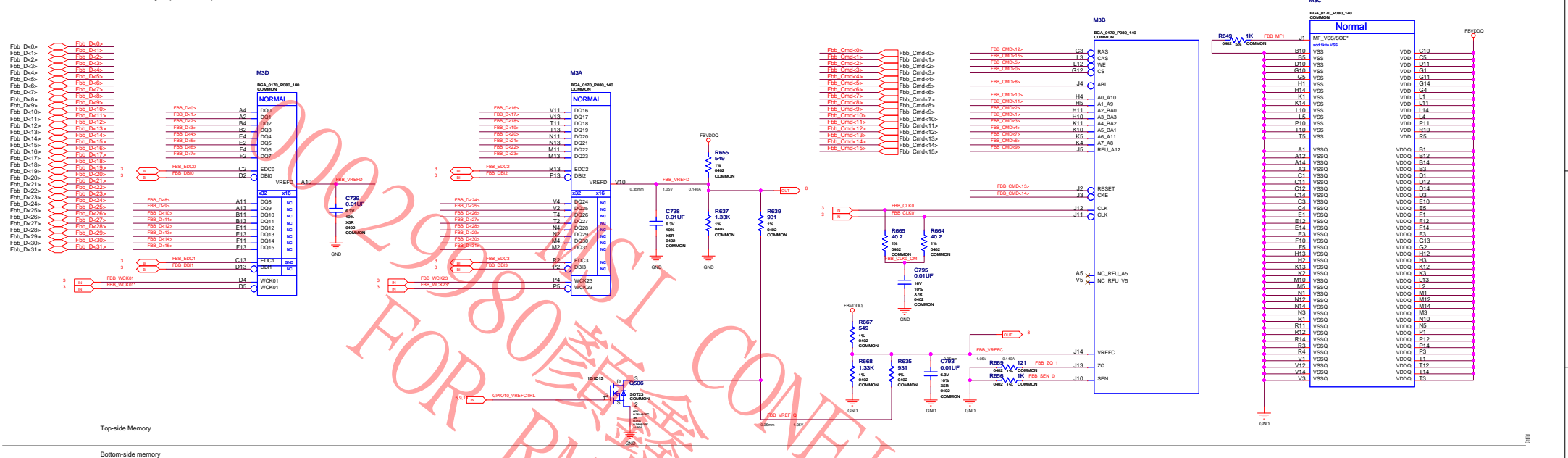
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## FBB Partition Memory (1 of 2)

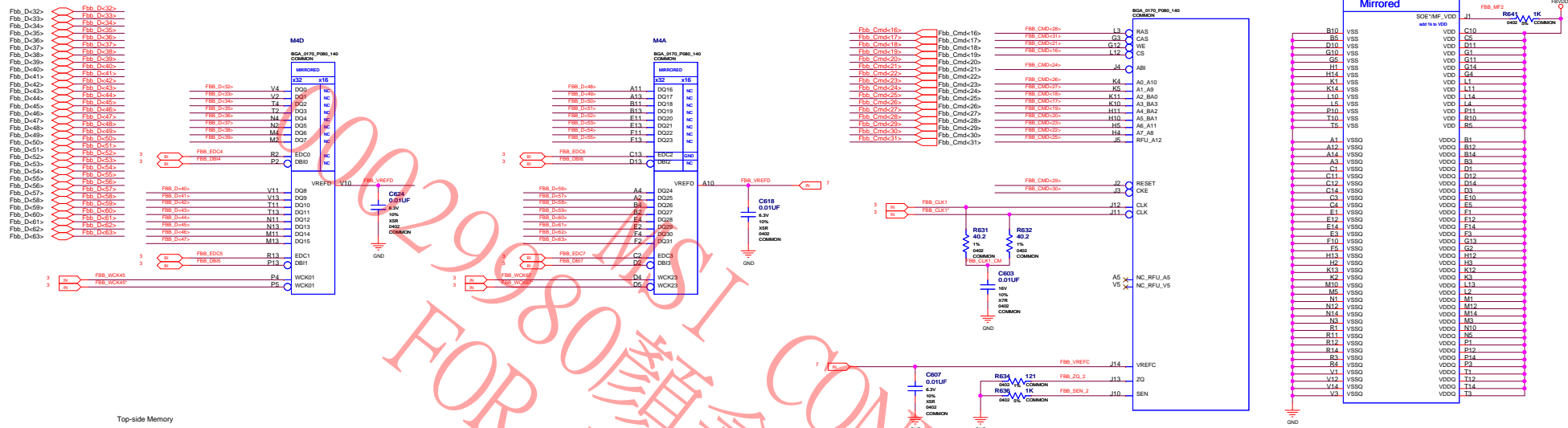


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PAGE DETAIL	FBB MEMORY (1 OF 2)



## FBB Partition Memory (2 of 2)



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PAGE DETAIL FBB MEMORY (2 OF 2)

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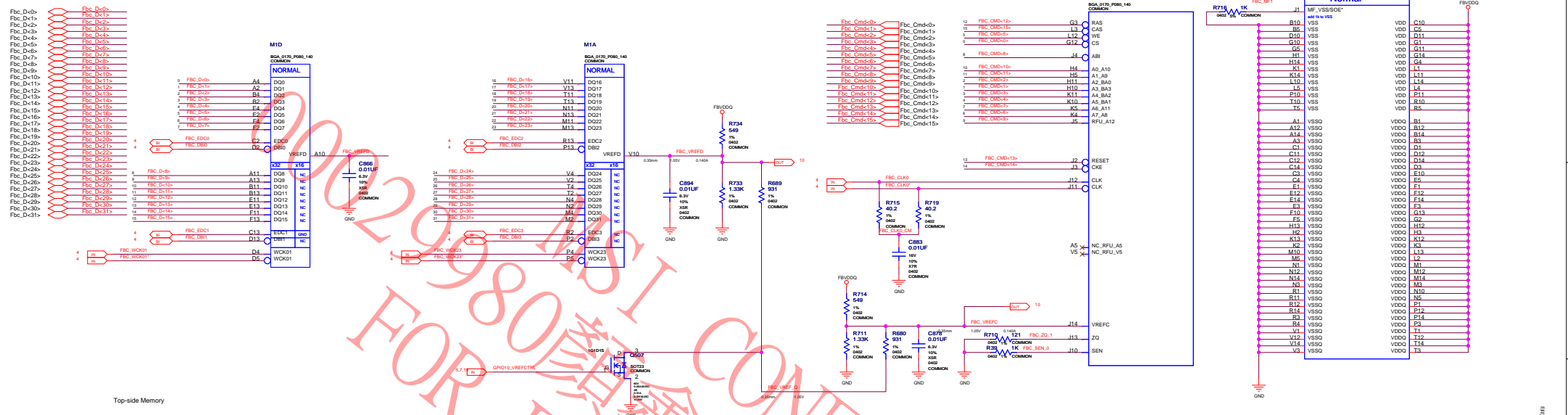
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## FBC Partition Memory (1 of 2)

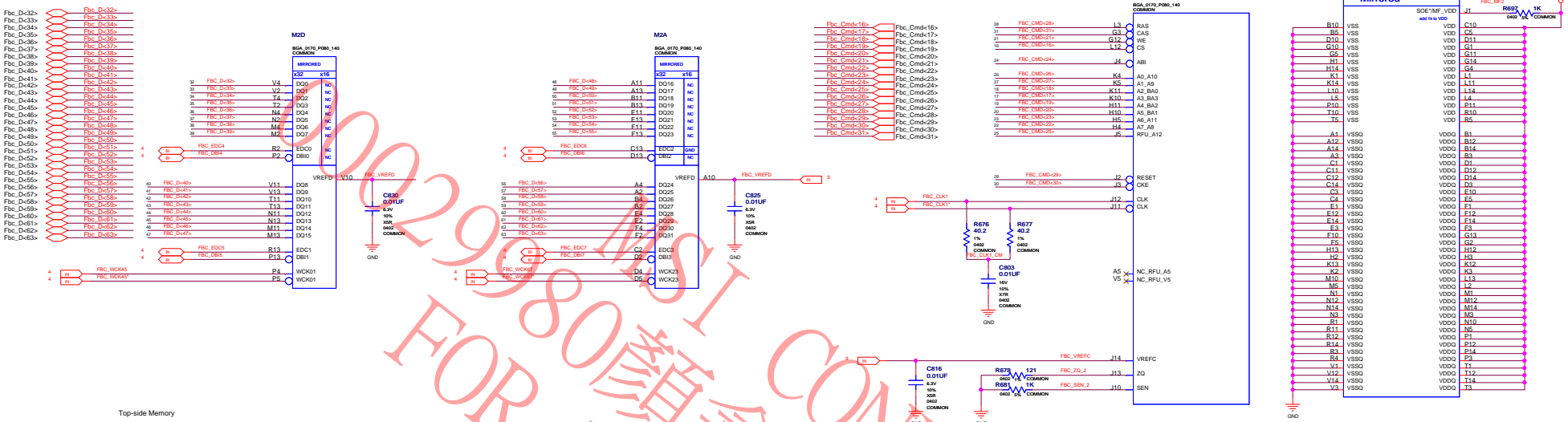


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## FBC Partition (2 of 2)




### Top-side Memory

Bottom-side memory

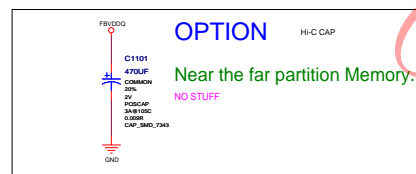
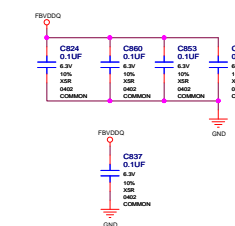
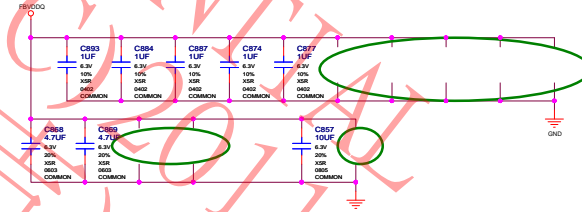
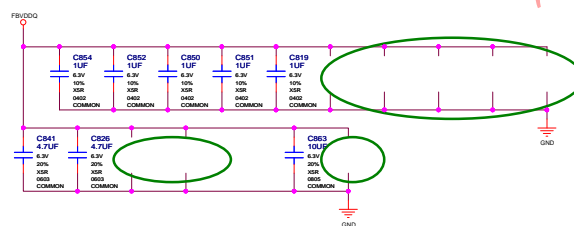
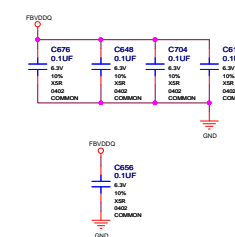
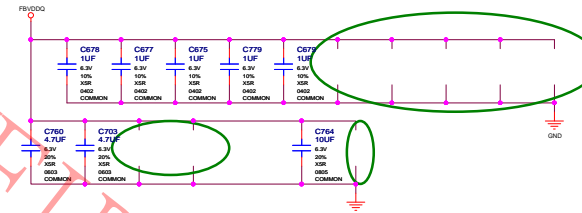
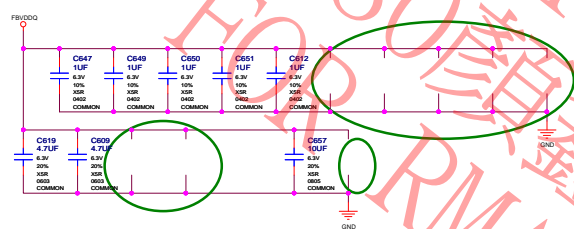
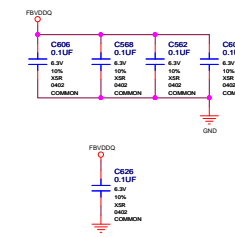
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PAGE DETAIL	FBC MEMORY (2 OF 2)

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## DISABLE PARTITION A



OPTION

Hi-C CAP

Near the far partition Memory.

NO STUFF

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PAGE DETAIL	FB MEMORY DECAP

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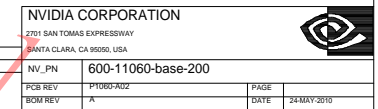
NV_PN	600-11060-base-200
-------	--------------------

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[illegible]

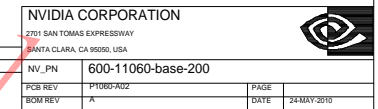
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## DACA RGB-FILTER



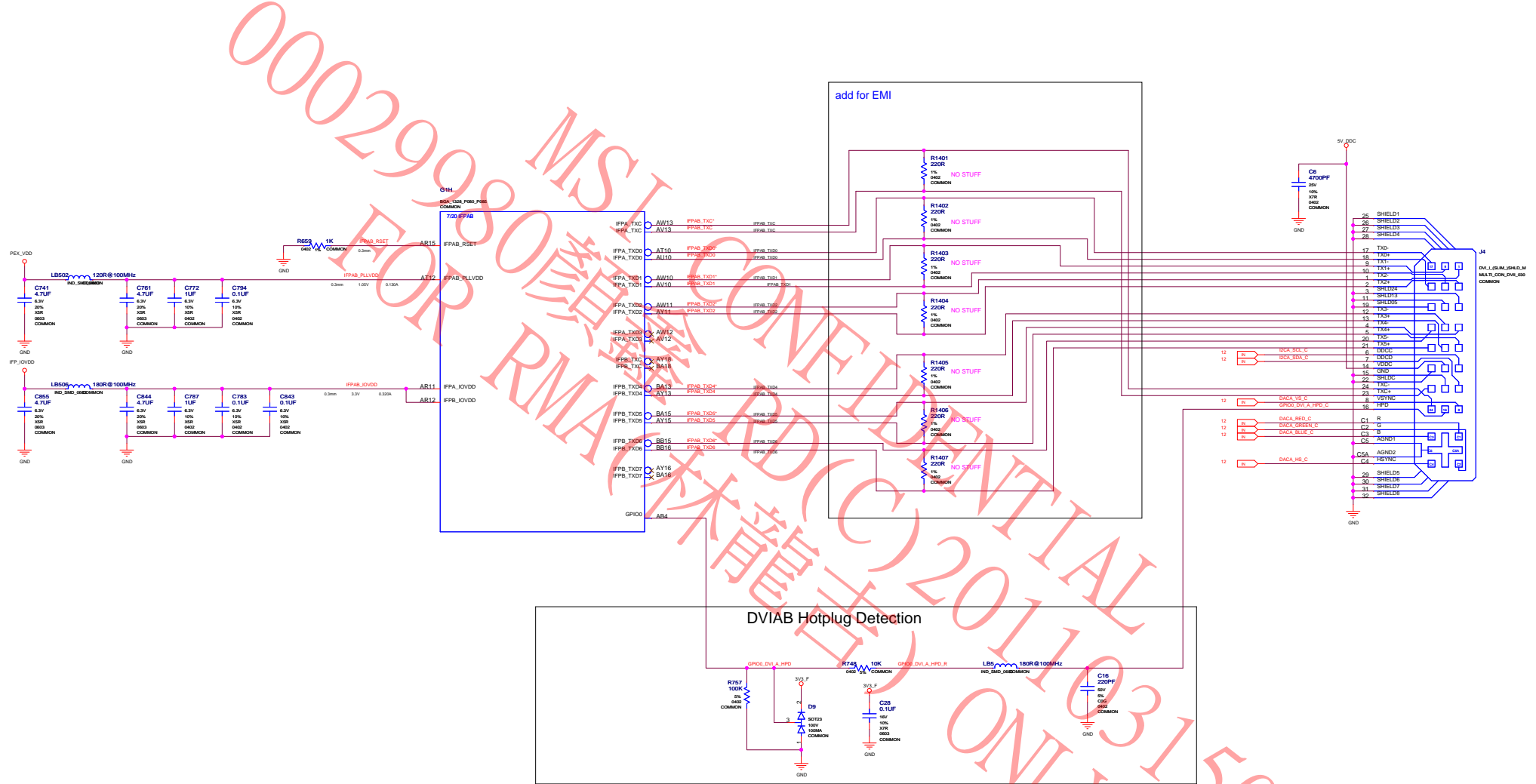
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## DACB RGB-FILTER



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# IFP A/B Interface -- DVI Connector, South

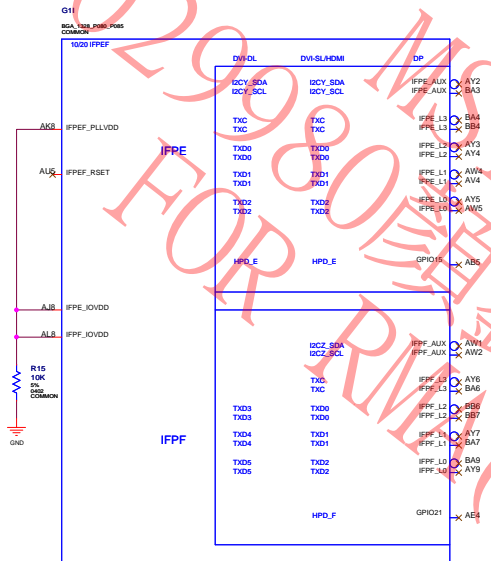


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PAGE/DETAIL	IFP A/B Interface -- DVI Connector, South


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## IFP E/F Interface -- DVI Connector, Mid



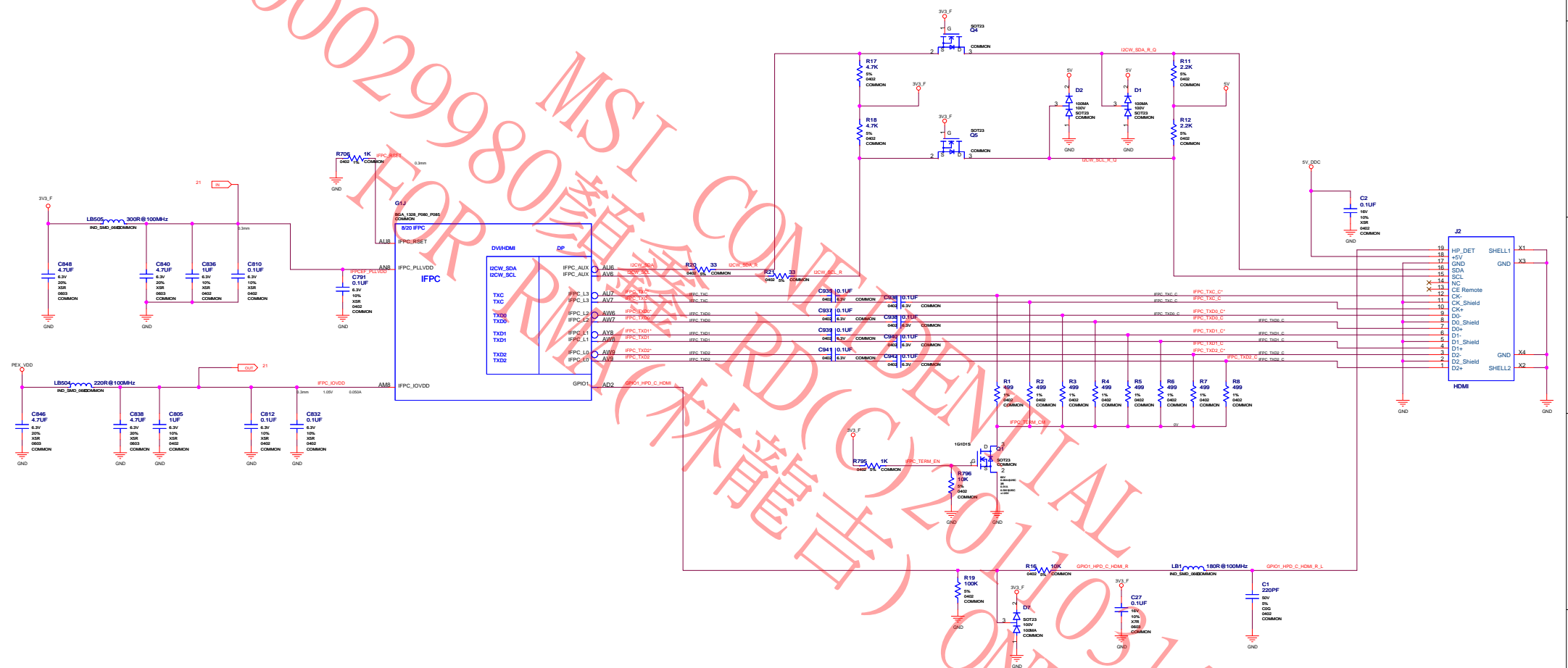
ASSEMBLY	P1060 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	IFP E/F Interface == DVI Connector, Mid

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## IFP C Interface -- HDMI, North



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PAGE DETAIL	IFPC Interface -- HDMI

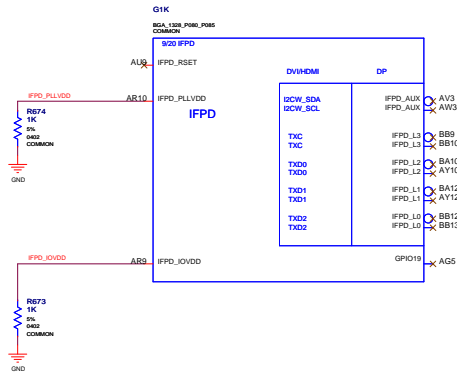


## IFP D INTERFACE (UNUSED), DP from IFPE, Mid

Three cases to be considered:

1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through
3. DP AUX to DVI connector: AUX level shifted to DDC

DEL 6262 NVVDD ADJ VOLTAGE IC



ASSEMBLY	P1060 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DP FROM IFPE, and IFPD (UNUSED)

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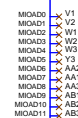
1


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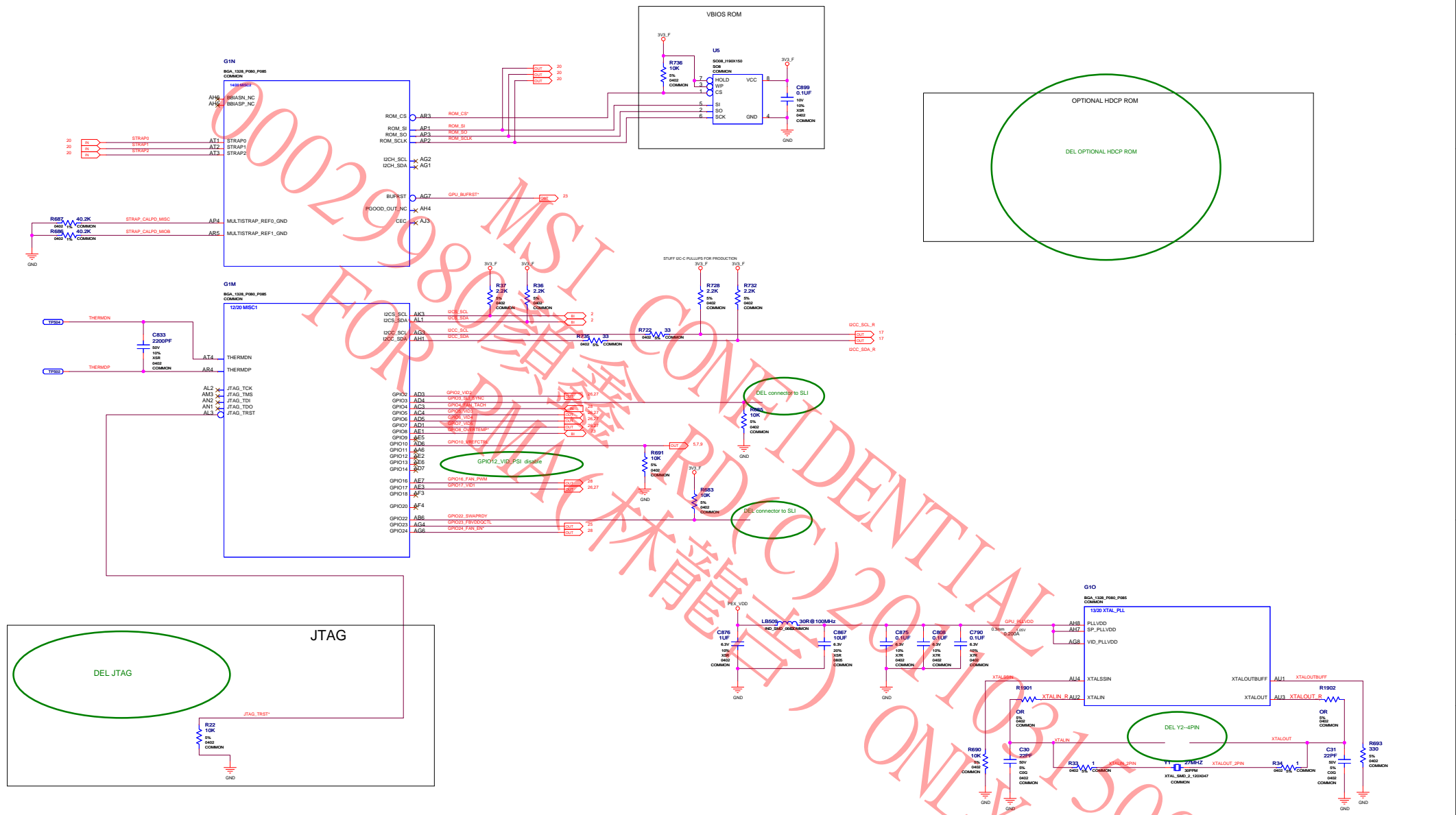
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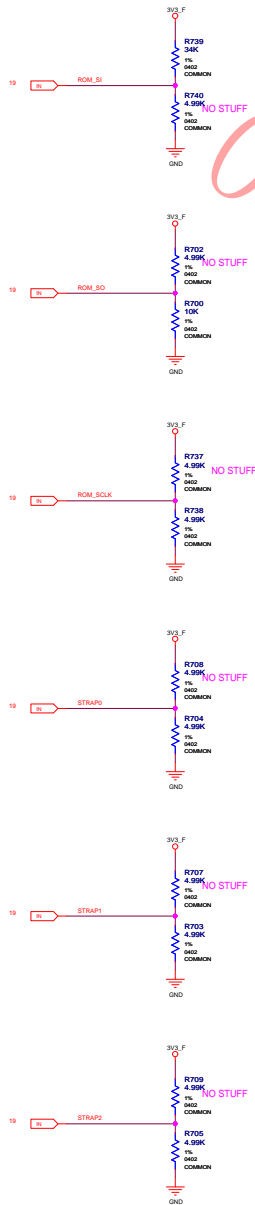
## MISC GPIO I2C ROM HDCP and XTAL



ASSEMBLY	P1060 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	MISC: GPIO, I2C, ROM, HDCP, and XTAL

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Strap Configuration



ROM_SO	VGA_DEVICE	LSB	1: VGA DEVICE	PU	PD
	PHM_A17_A20		0: 0x00000000		
	PLD_0_SCLK_S0		0: 0x00000000		
	WGLA_A17	MSB	0: 0x77777777		
ROM_SI	RAM_C0C0C0		0x0: 0x00000000 Samsung	45K	
			0x1: 0x00000000 Samsung	35K	45K
			0x2: 0x00000000 Samsung	35K	35K
ROM_SCLK	PEX_A17_A18_TERM10	LSB	0: 0x00000000 (P0)		35K
	PLUT_0_SCLK_S0		1: 0x0: 0x00000000 COMMON RESET		
	PLUT_0_SCLK_S0		1: 0x1: 0x00000000 COMMON RESET		
	PLUT_0_SCLK_S0		1: 0x2: 0x00000000 COMMON RESET		
STRAP0	STRAP_0_SCLK_S0		0x0: 0x00000000	5K	
STRAP1	STRAP_1_SCLK_S0		0x0: 0x00000000	5K	
STRAP2	STRAP_2_SCLK_S0		0x0: 0x00000000	10K	

Strap	PU	PD
0	PD 15	
1	PD 10	
2	PD 15	
3	PD 20	
4	PD 25	
5	PD 30	
6	PD 35	
7	PD 40	
8	PU 5	
9	PU 10	
10	PU 15	
11	PU 20	
12	PU 25	
13	PU 30	
14	PU 35	
15	PU 40	

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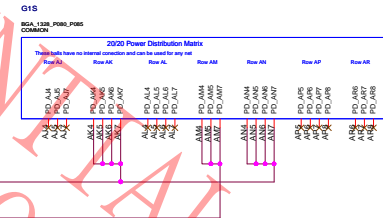
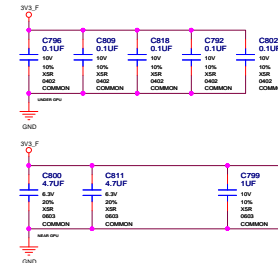
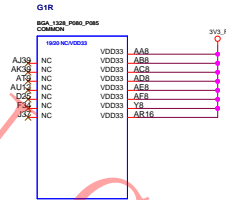
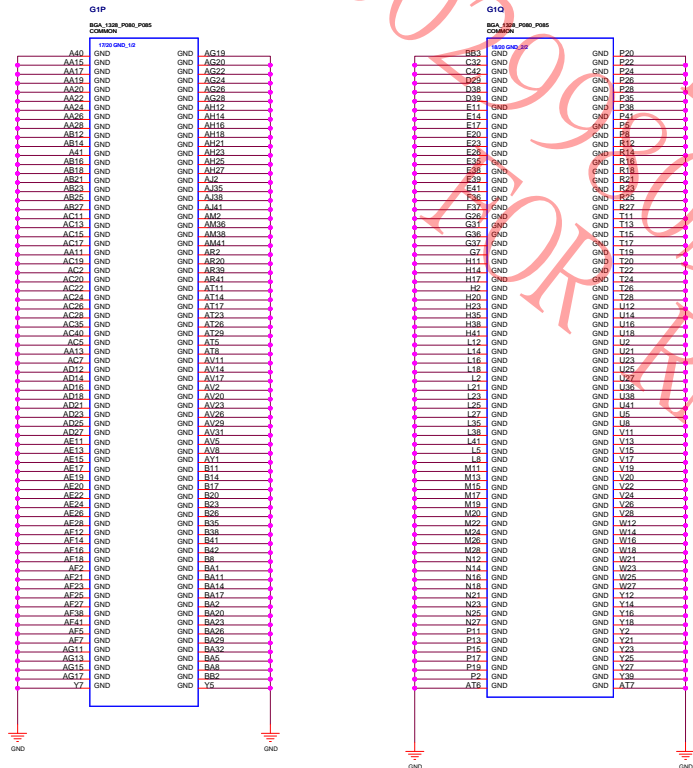
ASSEMBLY	P1060 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE/DETAIL	Strap Configuration

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NV_PN	600-11060-base-200		
PCB REV	P1060-A02	PAGE	
BOM REV	A	DATE	24 MAY 2010

## PWR and GND Signals



ASSEMBLY	P1060 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PWR and GND Signals

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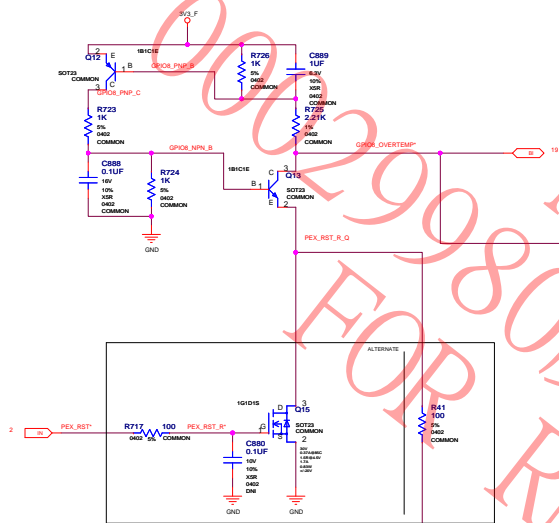
<b>NVIDIA CORPORATION</b> 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA			
<b>NV_PN</b>	<b>600-11060-base-200</b>		
PCB REV	P1060-A02	PAGE	
BOM REV	A	DATE	24-MAY-2010

# NVDD Decoupling

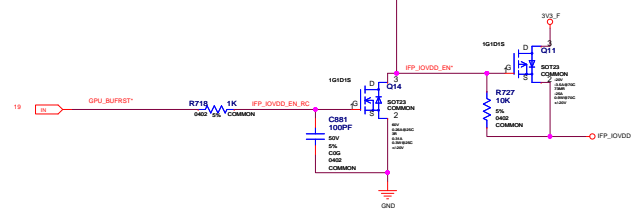
G1T		BGA_106B_P060_P065		COMMON	
NVDD	AA12	VDD	AAW38	VDD	AAW38
	AA14	VDD	AAW37	VDD	AAW37
	AA16	VDD	AAW36	VDD	AAW36
	AA18	VDD	AAW35	VDD	AAW35
	AA21	VDD	AAW40	VDD	AAW40
	AA23	VDD	AAW41	VDD	AAW41
	AA25	VDD	AAW42	VDD	AAW42
	AA27	VDD	AAW43	VDD	AAW43
	AB11	VDD	AY35	VDD	AY35
	AB13	VDD	AY36	VDD	AY36
	AB15	VDD	AY37	VDD	AY37
	AB17	VDD	AY38	VDD	AY38
	AB19	VDD	AY39	VDD	AY39
	AB20	VDD	AY40	VDD	AY40
	AB22	VDD	AY41	VDD	AY41
	AB24	VDD	AY42	VDD	AY42
	AB26	VDD	BA35	VDD	BA35
	AB28	VDD	BA36	VDD	BA36
	AC12	VDD	BA37	VDD	BA37
	AC14	VDD	BA38	VDD	BA38
	AC16	VDD	BA39	VDD	BA39
	AC18	VDD	BA40	VDD	BA40
	AC21	VDD	BA41	VDD	BA41
	AC23	VDD	BA42	VDD	BA42
	AC25	VDD	BB36	VDD	BB36
	AC27	VDD	BB37	VDD	BB37
	AD11	VDD	BB38	VDD	BB38
	AD13	VDD	BB40	VDD	BB40
	AD15	VDD	BB41	VDD	BB41
	AD17	VDD	BB42	VDD	BB42
	AD19	VDD	BB43	VDD	BB43
	AD20	VDD	BB44	VDD	BB44
	AD22	VDD	BB45	VDD	BB45
	AD24	VDD	BB46	VDD	BB46
	AD26	VDD	BB47	VDD	BB47
	AD28	VDD	BB48	VDD	BB48
	AE12	VDD	BB49	VDD	BB49
	AE14	VDD	BB50	VDD	BB50
	AE16	VDD	BB51	VDD	BB51
	AE18	VDD	BB52	VDD	BB52
	AE21	VDD	BB53	VDD	BB53
	AE23	VDD	BB54	VDD	BB54
	AE25	VDD	BB55	VDD	BB55
	AE27	VDD	BB56	VDD	BB56
	AE31	VDD	BB57	VDD	BB57
	AE33	VDD	BB58	VDD	BB58
	AE35	VDD	BB59	VDD	BB59
	AE37	VDD	BB60	VDD	BB60
	AE39	VDD	BB61	VDD	BB61
	AE40	VDD	BB62	VDD	BB62
	AE42	VDD	BB63	VDD	BB63
	AE44	VDD	BB64	VDD	BB64
	AE46	VDD	BB65	VDD	BB65
	AE48	VDD	BB66	VDD	BB66
	AE50	VDD	BB67	VDD	BB67
	AE52	VDD	BB68	VDD	BB68
	AE54	VDD	BB69	VDD	BB69
	AE56	VDD	BB70	VDD	BB70
	AE58	VDD	BB71	VDD	BB71
	AE60	VDD	BB72	VDD	BB72
	AE62	VDD	BB73	VDD	BB73
	AE64	VDD	BB74	VDD	BB74
	AE66	VDD	BB75	VDD	BB75
	AE68	VDD	BB76	VDD	BB76
	AE70	VDD	BB77	VDD	BB77
	AE72	VDD	BB78	VDD	BB78
	AE74	VDD	BB79	VDD	BB79
	AE76	VDD	BB80	VDD	BB80
	AE78	VDD	BB81	VDD	BB81
	AE80	VDD	BB82	VDD	BB82
	AE82	VDD	BB83	VDD	BB83
	AE84	VDD	BB84	VDD	BB84
	AE86	VDD	BB85	VDD	BB85
	AE88	VDD	BB86	VDD	BB86
	AE90	VDD	BB87	VDD	BB87
	AE92	VDD	BB88	VDD	BB88
	AE94	VDD	BB89	VDD	BB89
	AE96	VDD	BB90	VDD	BB90
	AE98	VDD	BB91	VDD	BB91
	AE100	VDD	BB92	VDD	BB92
	AE102	VDD	BB93	VDD	BB93
	AE104	VDD	BB94	VDD	BB94
	AE106	VDD	BB95	VDD	BB95
	AE108	VDD	BB96	VDD	BB96
	AE110	VDD	BB97	VDD	BB97
	AE112	VDD	BB98	VDD	BB98
	AE114	VDD	BB99	VDD	BB99
	AE116	VDD	BB100	VDD	BB100
	AE118	VDD	BB101	VDD	BB101
	AE120	VDD	BB102	VDD	BB102
	AE122	VDD	BB103	VDD	BB103
	AE124	VDD	BB104	VDD	BB104
	AE126	VDD	BB105	VDD	BB105
	AE128	VDD	BB106	VDD	BB106
	AE130	VDD	BB107	VDD	BB107
	AE132	VDD	BB108	VDD	BB108
	AE134	VDD	BB109	VDD	BB109
	AE136	VDD	BB110	VDD	BB110
	AE138	VDD	BB111	VDD	BB111
	AE140	VDD	BB112	VDD	BB112
	AE142	VDD	BB113	VDD	BB113
	AE144	VDD	BB114	VDD	BB114
	AE146	VDD	BB115	VDD	BB115
	AE148	VDD	BB116	VDD	BB116
	AE150	VDD	BB117	VDD	BB117
	AE152	VDD	BB118	VDD	BB118
	AE154	VDD	BB119	VDD	BB119
	AE156	VDD	BB120	VDD	BB120
	AE158	VDD	BB121	VDD	BB121
	AE160	VDD	BB122	VDD	BB122
	AE162	VDD	BB123	VDD	BB123
	AE164	VDD	BB124	VDD	BB124
	AE166	VDD	BB125	VDD	BB125
	AE168	VDD	BB126	VDD	BB126
	AE170	VDD	BB127	VDD	BB127
	AE172	VDD	BB128	VDD	BB128
	AE174	VDD	BB129	VDD	BB129
	AE176	VDD	BB130	VDD	BB130
	AE178	VDD	BB131	VDD	BB131
	AE180	VDD	BB132	VDD	BB132
	AE182	VDD	BB133	VDD	BB133
	AE184	VDD	BB134	VDD	BB134
	AE186	VDD	BB135	VDD	BB135
	AE188	VDD	BB136	VDD	BB136
	AE190	VDD	BB137	VDD	BB137
	AE192	VDD	BB138	VDD	BB138
	AE194	VDD	BB139	VDD	BB139
	AE196	VDD	BB140	VDD	BB140
	AE198	VDD	BB141	VDD	BB141
	AE200	VDD	BB142	VDD	BB142
	AE202	VDD	BB143	VDD	BB143
	AE204	VDD	BB144	VDD	BB144
	AE206	VDD	BB145	VDD	BB145
	AE208	VDD	BB146	VDD	BB146
	AE210	VDD	BB147	VDD	BB147
	AE212	VDD	BB148	VDD	BB148
	AE214	VDD	BB149	VDD	BB149
	AE216	VDD	BB150	VDD	BB150
	AE218	VDD	BB151	VDD	BB151
	AE220	VDD	BB152	VDD	BB152
	AE222	VDD	BB153	VDD	BB153
	AE224	VDD	BB154	VDD	BB154
	AE226	VDD	BB155	VDD	BB155
	AE228	VDD	BB156	VDD	BB156
	AE230	VDD	BB157	VDD	BB157
	AE232	VDD	BB158	VDD	BB158
	AE234	VDD	BB159	VDD	BB159
	AE236	VDD	BB160	VDD	BB160
	AE238	VDD	BB161	VDD	BB161
	AE240	VDD	BB162	VDD	BB162
	AE242	VDD	BB163	VDD	BB163
	AE244	VDD	BB164	VDD	BB164
	AE246	VDD	BB165	VDD	BB165
	AE248	VDD	BB166	VDD	BB166
	AE250	VDD	BB167	VDD	BB167
	AE252	VDD	BB168	VDD	BB168
	AE254	VDD	BB169	VDD	BB169
	AE256	VDD	BB170	VDD	BB170
	AE258	VDD	BB171	VDD	BB171
	AE260	VDD	BB172	VDD	BB172
	AE262	VDD	BB173	VDD	BB173
	AE264	VDD	BB174	VDD	BB174
	AE266	VDD	BB175	VDD	BB175
	AE268	VDD	BB176	VDD	BB176
	AE270	VDD	BB177	VDD	BB177
	AE272	VDD	BB178	VDD	BB178
	AE274	VDD	BB179	VDD	BB179
	AE276	VDD	BB180	VDD	BB180
	AE278	VDD	BB181	VDD	BB181
	AE280	VDD	BB182	VDD	BB182
	AE282	VDD	BB183	VDD	BB183
	AE284	VDD	BB184	VDD	BB184
	AE286	VDD	BB185	VDD	BB185
	AE288	VDD	BB186	VDD	BB186
	AE290	VDD	BB187	VDD	BB187
	AE292	VDD	BB188	VDD	BB188
	AE294	VDD	BB189	VDD	BB189
	AE296	VDD	BB190	VDD	BB190
	AE298	VDD	BB191	VDD	BB191
	AE300	VDD	BB192	VDD	BB192
	AE302	VDD	BB193	VDD	BB193
	AE304	VDD	BB194	VDD	BB194
	AE306	VDD	BB195	VDD	BB195
	AE308	VDD	BB196	VDD	BB196
	AE310	VDD	BB197	VDD	BB197
	AE312	VDD	BB198	VDD	BB198
	AE314	VDD	BB199	VDD	BB199
	AE316	VDD	BB200	VDD	BB200
	AE318	VDD	BB201	VDD	BB201
	AE320	VDD	BB202	VDD	BB202
	AE322	VDD	BB203	VDD	BB203
	AE324	VDD	BB204	VDD	BB204
	AE326	VDD	BB205	VDD	BB205
	AE328	VDD	BB206	VDD	BB206
	AE330	VDD	BB207	VDD	BB207
	AE332	VDD	BB208	VDD	BB208
	AE334	VDD	BB209	VDD	BB209
	AE336	VDD	BB210	VDD	BB210
	AE338	VDD	BB211	VDD	BB211
	AE340	VDD	BB212	VDD	BB212
	AE342	VDD	BB213	VDD	BB213
	AE344	VDD	BB214	VDD	BB214
	AE346	VDD	BB215	VDD	BB215
	AE348	VDD	BB216	VDD	BB216
	AE350	VDD	BB217	VDD	BB217
	AE352	VDD	BB218	VDD	BB218
	AE354	VDD	BB219	VDD	BB219
	AE356	VDD	BB220	VDD	BB220
	AE358	VDD	BB221	VDD	BB221
	AE360	VDD	BB222	VDD	BB222
	AE362	VDD	BB223	VDD	BB223
	AE364	VDD	BB224	VDD	BB224
	AE366	VDD	BB225	VDD	BB225
	AE368	VDD	BB226	VDD	BB226
	AE370	VDD	BB227	VDD	BB227
	AE372	VDD	BB228	VDD	BB228
	AE374	VDD	BB229	VDD	BB229
	AE376	VDD	BB230	VDD	BB230
	AE378	VDD	BB231	VDD	BB231
	AE380	VDD	BB232	VDD	BB232
	AE382	VDD	BB233	VDD	BB233
	AE384	VDD	BB234	VDD	BB234
	AE386	VDD	BB235	VDD	BB235
	AE388	VDD	BB236	VDD	BB236
	AE390	VDD	BB237	VDD	BB237
	AE392	VDD	BB238	VDD	BB238
	AE394	VDD	BB239	VDD	BB239
	AE396	VDD	BB240	VDD	BB240
	AE398	VDD	BB241	VDD	BB241
	AE400	VDD	BB242	VDD	BB242
	AE402	VDD	BB243	VDD	BB243
	AE404	VDD	BB244	VDD	BB244
	AE406	VDD	BB245	VDD	BB245
	AE408	VDD	BB246	VDD	BB246
	AE410	VDD	BB247	VDD	BB247
	AE412	VDD	BB248	VDD	BB248
	AE414	VDD	BB249	VDD	BB249
	AE416	VDD	BB250	VDD	BB250
	AE418	VDD	BB251	VDD	BB251
	AE420	VDD	BB252	VDD	BB252
	AE422	VDD	BB253	VDD	BB253
	AE424	VDD	BB254	VDD	BB254
	AE426	VDD	BB255	VDD	BB255
	AE428	VDD	BB256	VDD	BB256
	AE430	VDD	BB257	VDD	BB257
	AE432	VDD	BB258	VDD	BB258
	AE434	VDD	BB259	VDD	BB259
	AE436	VDD	BB260	VDD	BB260
	AE438	VDD	BB261	VDD	BB261
	AE440	VDD	BB262	VDD	BB262
	AE442	VDD	BB263	VDD	BB263
	AE444	VDD	BB264	VDD	BB264
	AE446	VDD	BB265	VDD	BB265
	AE448	VDD	BB266	VDD	BB266

## Power Sequencing, Thermal Protection, and IFP\_IOVDD Power Supply

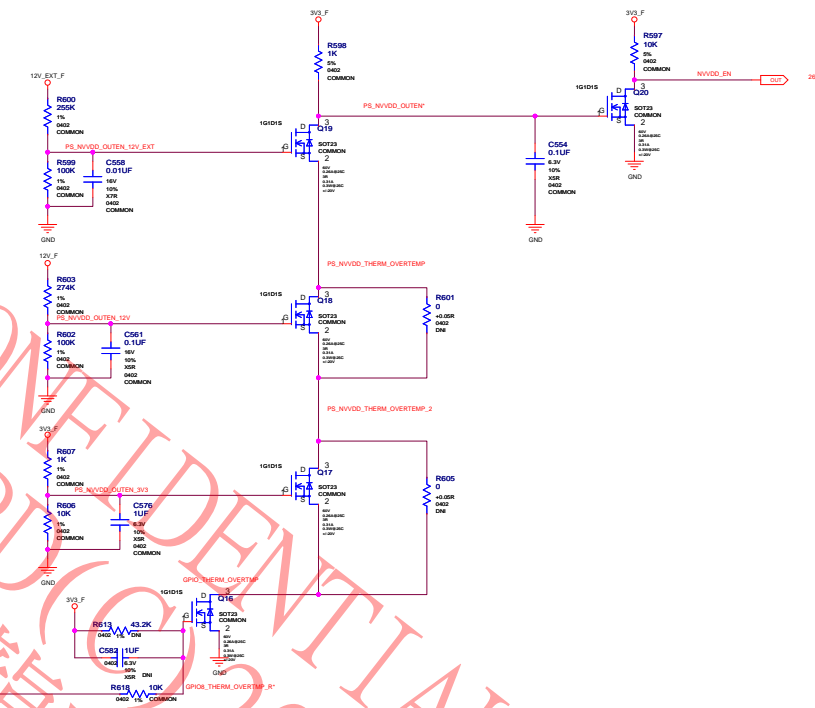
Thermal Protection (latch)



## IFP\_IOVDD Backdrive Prevention



### NVVDD Sequence



ASSEMBLY	P1060 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Backdrive and Thermal Protection, Power Sequencing

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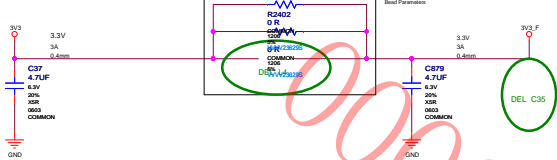
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PCB REV	P1060-A02	PAGE	
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# PS I: 3V3, 12V, and 12V\_EXT Power Supply Filter, 5V, DDC VOLTAGE

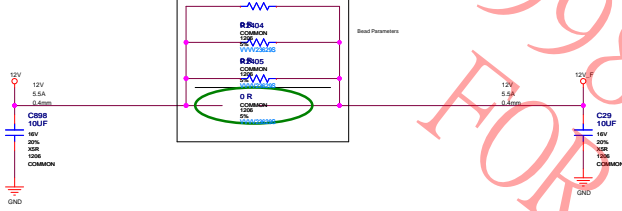
## 3V3 Power Supply Filter

3V3\_F = 3.3V @ 2.4A

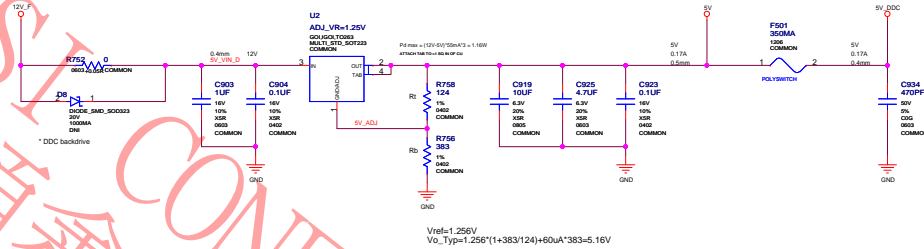


## 12V Power Supply Filter

12V\_F = 12V @ 5.5A



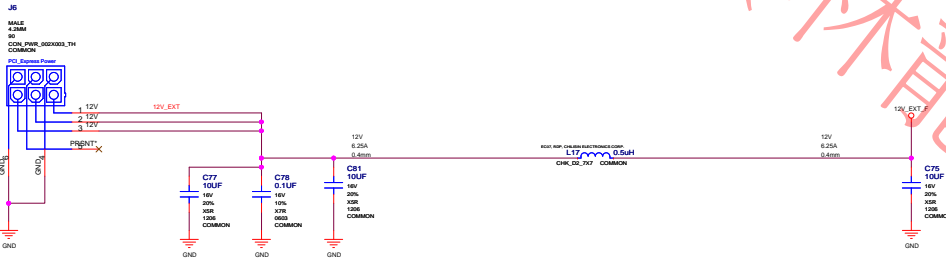
## 5V POWER SUPPLY, DDC POWER



## 12V\_EXT Power Connector

## 12V\_EXT Power Supply Filter

12V\_EXT\_F = 12V @ 6A



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PAGE DETAIL	PS I: 12V Power Supply Filter, 5V AND DDC VOLTAGE

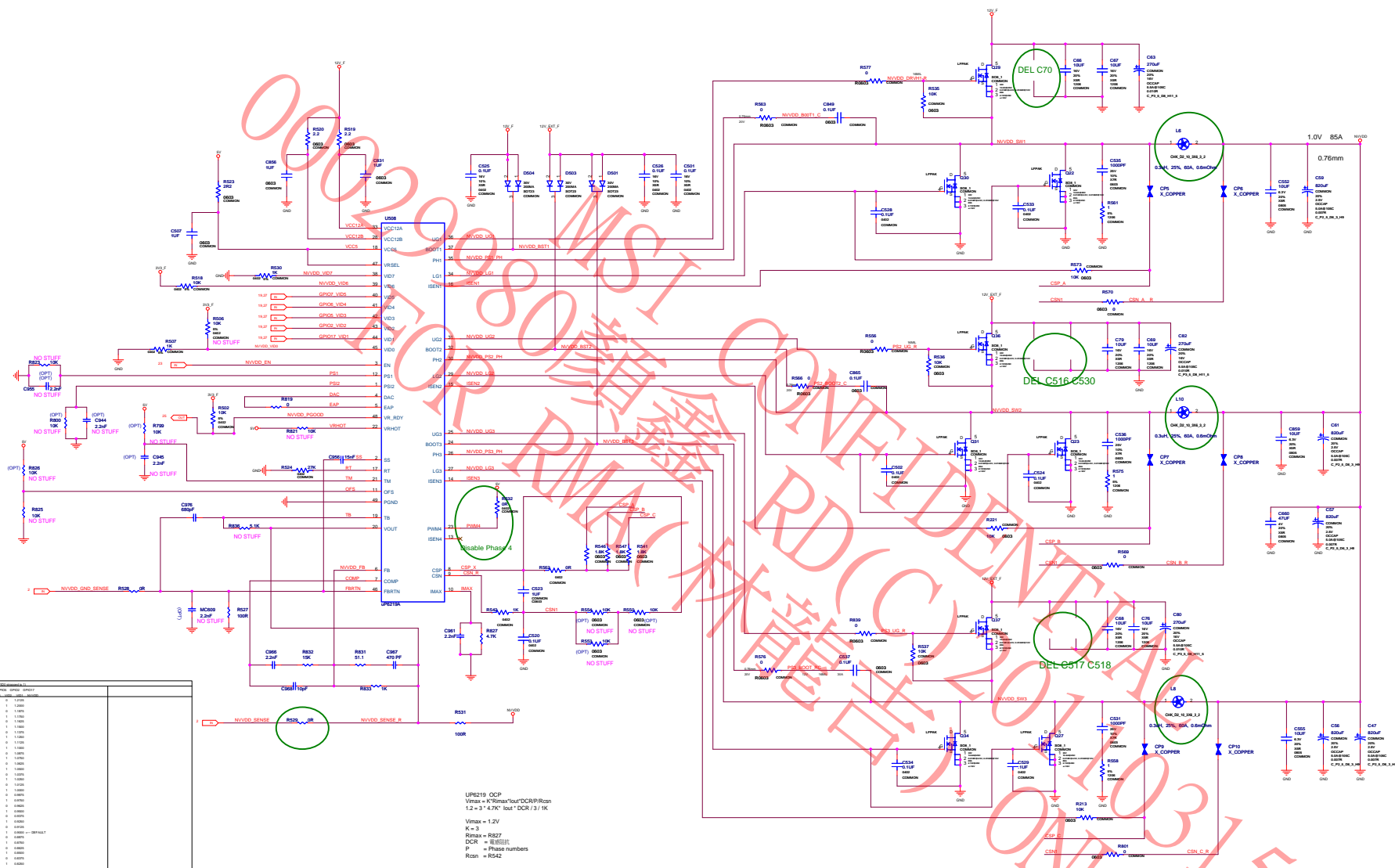
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NV_PN	600-11060-base-200		
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BOM REV	A	DATE	24-MAY-2010

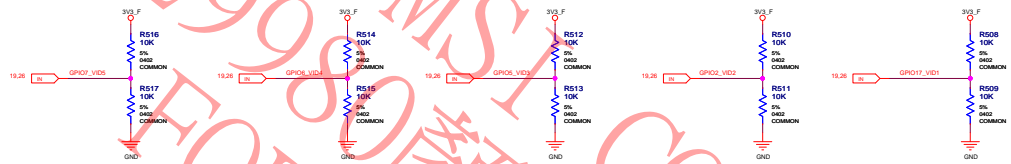




ASSEMBLY	P1060 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PS II: PEX_VDD, FBVDDQ

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DEL GPIO12\_VID\_PSI  
It is for power saving function

VS is single-phase clock-simulation mode  
when PDI is asserted low

NVAPI VID pin control is 0									
GPIO1_VID	GPIO2_VID	GPIO3_VID	GPIO2_VID2	GPIO17_VID1	VID	VID4	VID5	VID6	VID7
0	0	0	0	0	1.2125				
0	0	0	1	0	1.2025				
0	0	0	1	0	1.1925				
0	0	0	1	1	1.1750				
0	0	1	0	0	1.1625				
0	0	1	0	1	1.1500				
0	0	1	1	0	1.1375				
0	0	1	1	1	1.1250				
0	1	0	0	0	1.1125				
0	1	0	0	1	1.1000				
0	1	0	1	0	1.0875				
0	1	0	1	1	1.0750				
0	1	1	0	0	1.0625				
0	1	1	0	1	1.0500				
0	1	1	1	0	1.0375				
0	1	1	1	1	1.0250				
1	0	0	0	0	1.0125				
1	0	0	0	1	1.0000				
1	0	0	1	0	0.9875				
1	0	0	1	1	0.9750				
1	0	1	0	0	0.9625				
1	0	1	0	1	0.9500				
1	0	1	1	0	0.9375				
1	0	1	1	1	0.9250				
1	1	0	0	0	0.9125				
1	1	0	0	1	0.9000				
1	1	0	1	0	0.8875				
1	1	0	1	1	0.8750				
1	1	1	0	0	0.8625				
1	1	1	0	1	0.8500				
1	1	1	1	0	0.8375				
1	1	1	1	1	0.8250				

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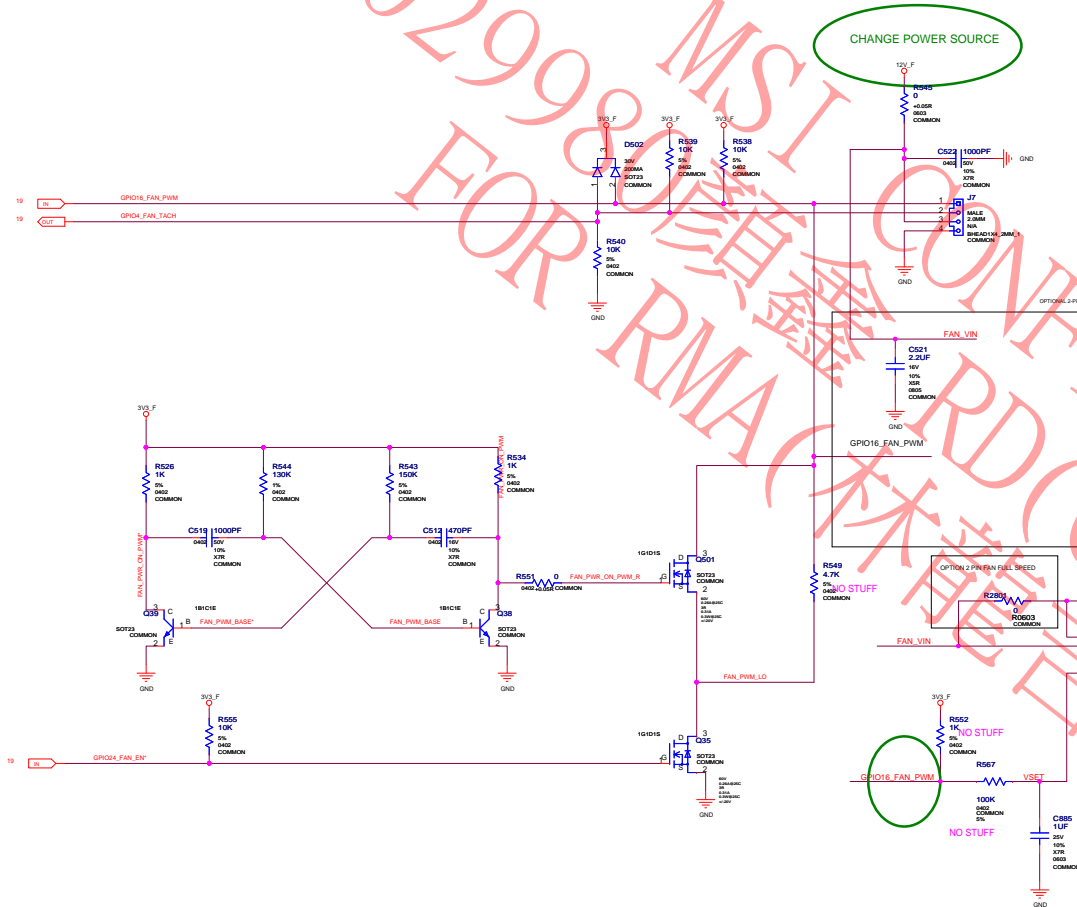
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PAGE/DETAIL	PS IV: NVAPI VID

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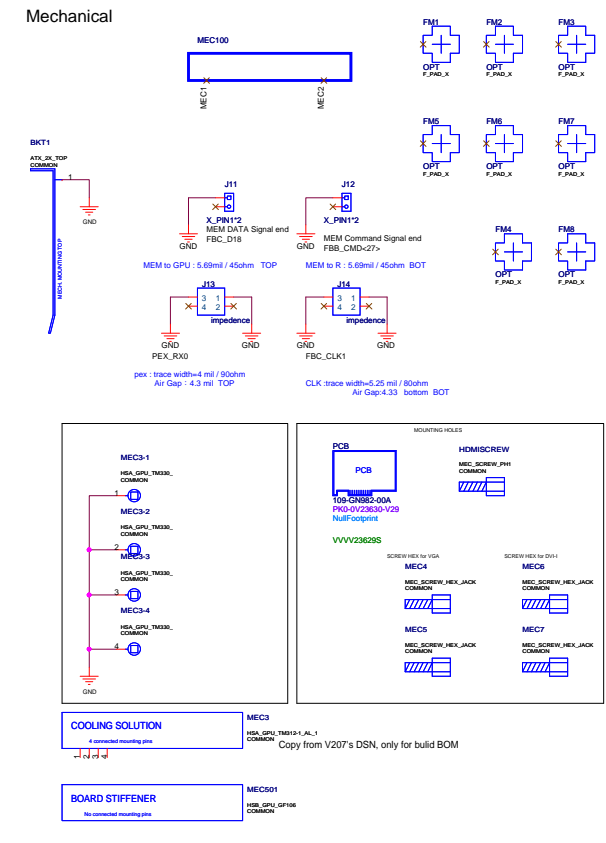
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Thermal, Mechanical, and Bracket



Mechanical



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