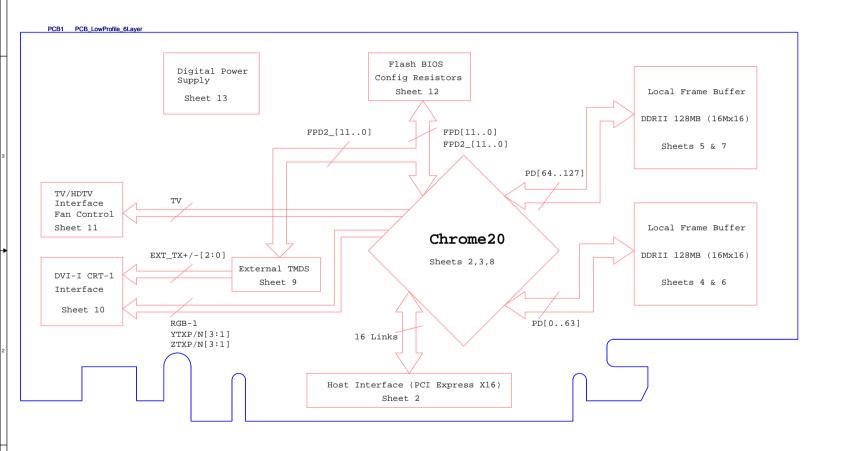
Chrome20 "LOW PROFILE MONTEREY" DESKTOP

PCB No: VT6330A



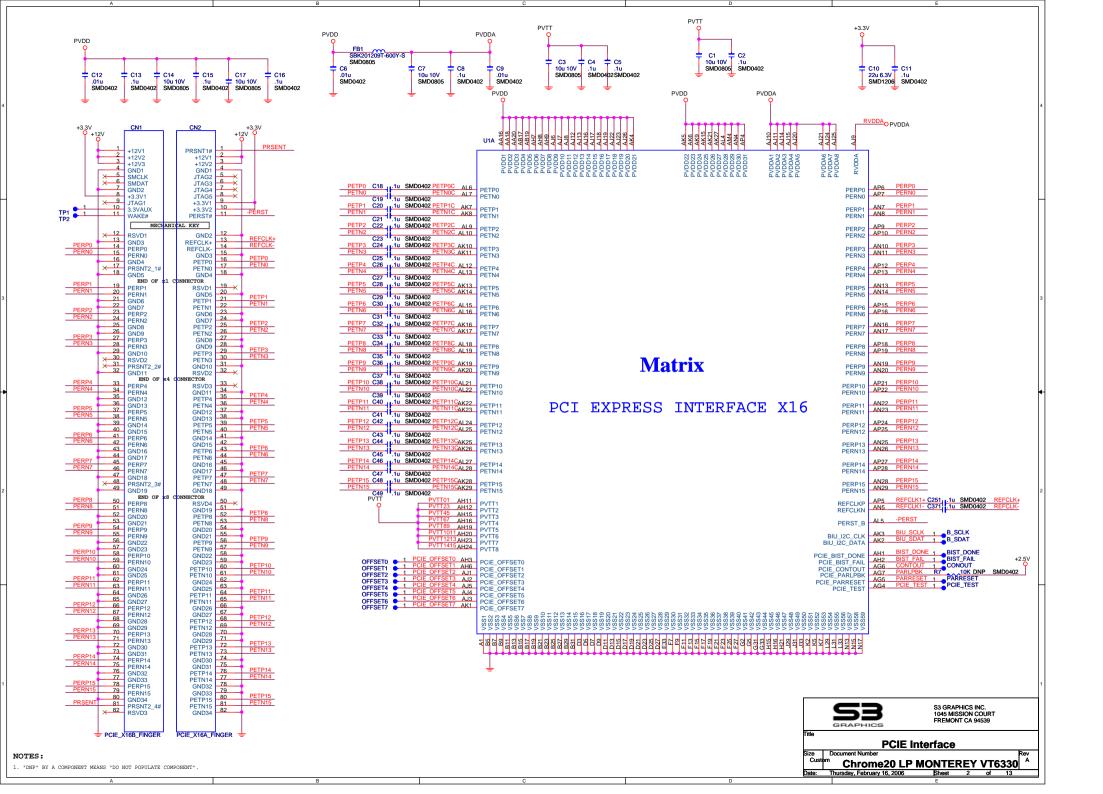
Size B Document Number Chrome20 LP MONTEREY VT6330

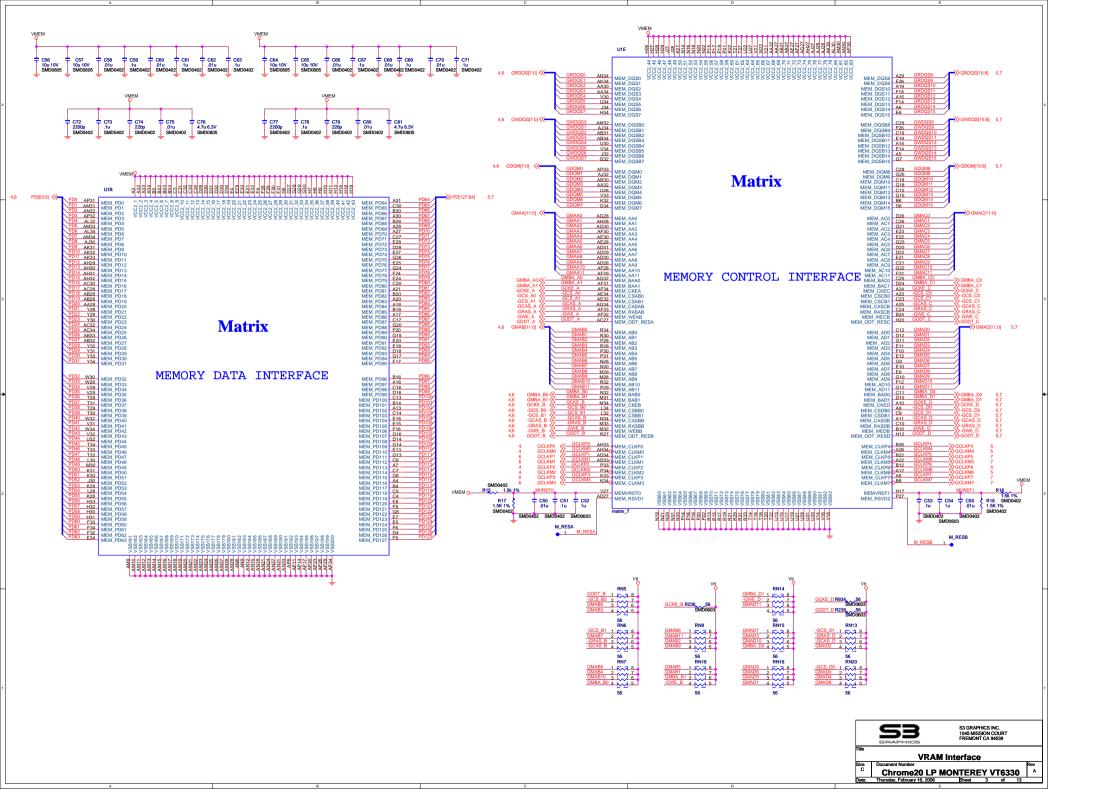
Thursday, February 16, 2006

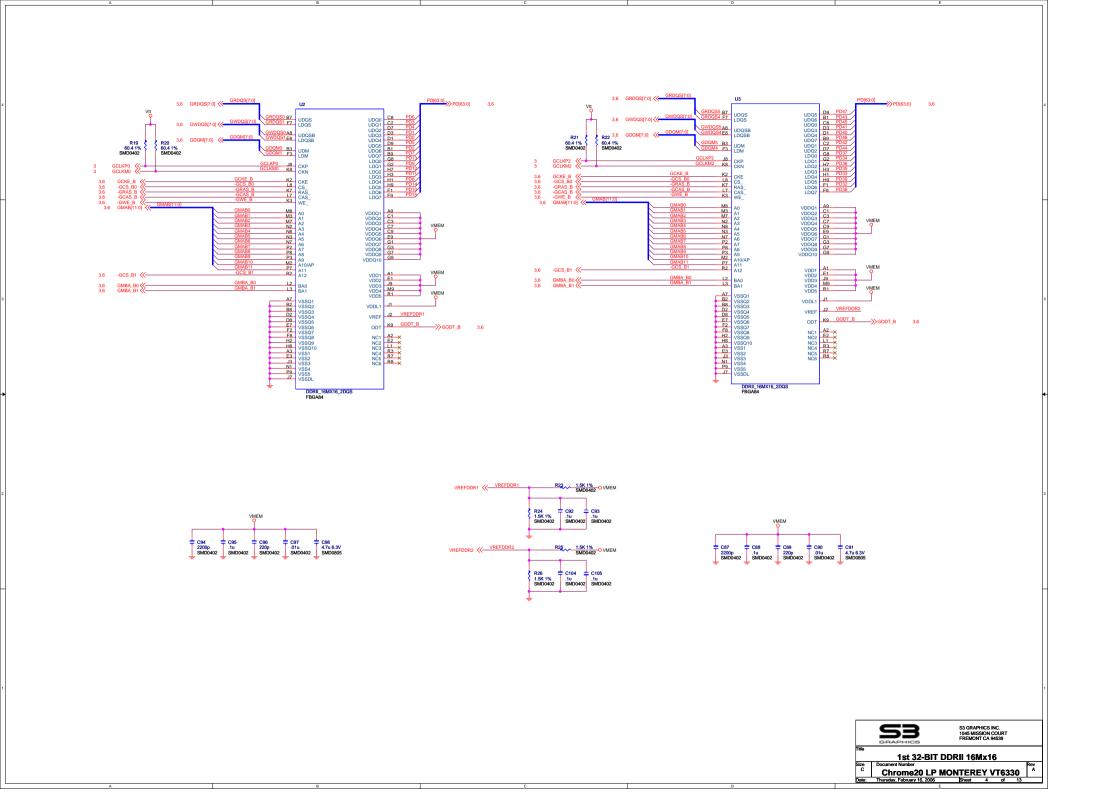
01/16/06

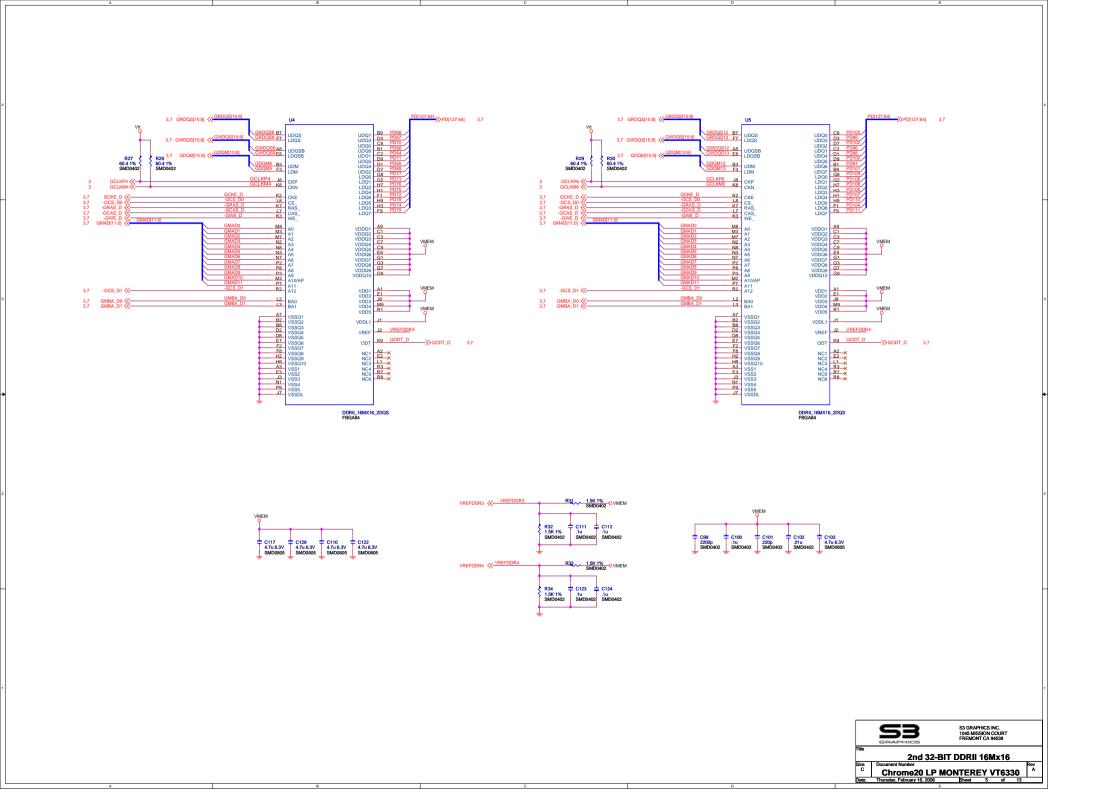
Schematic created

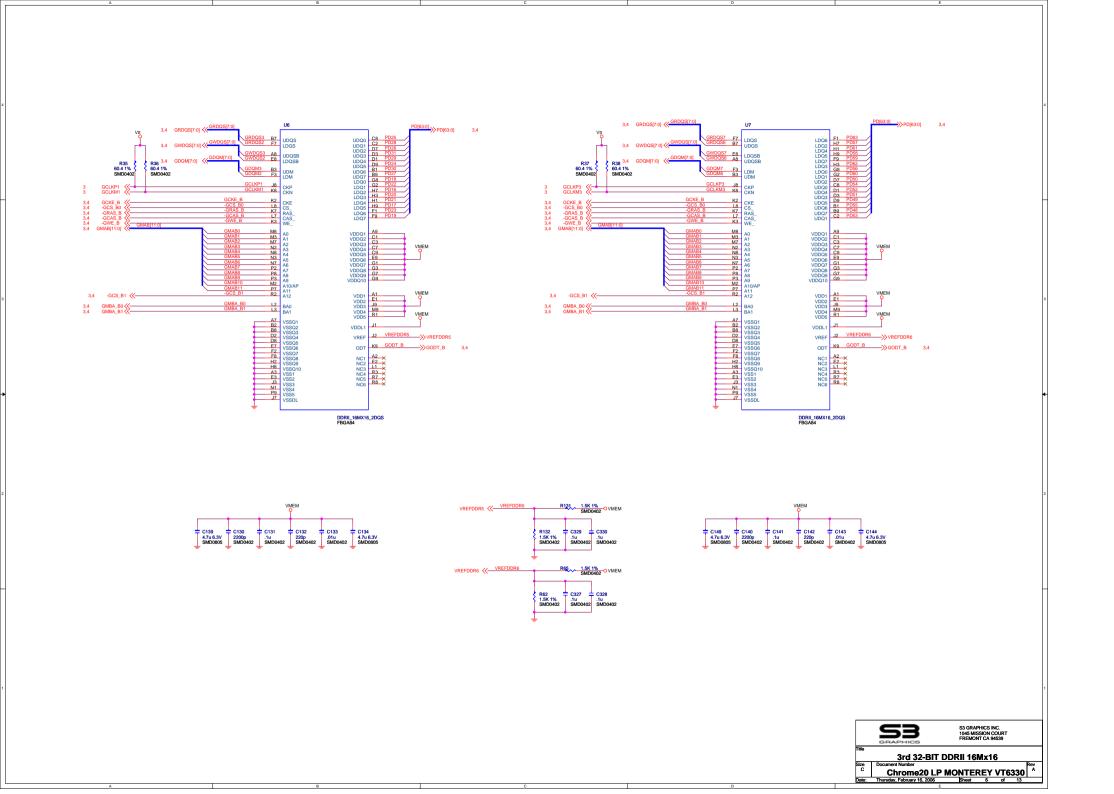
THE INFORMATION CONTAINED IN THIS DOCUMENT IS PRELIMINARY AND SUBJECT TO CHANGE. 33 GRAPHICS BEARS NO RESPONSIBILITY FOR ANY ERRORS IN THESE DRAWLINGS

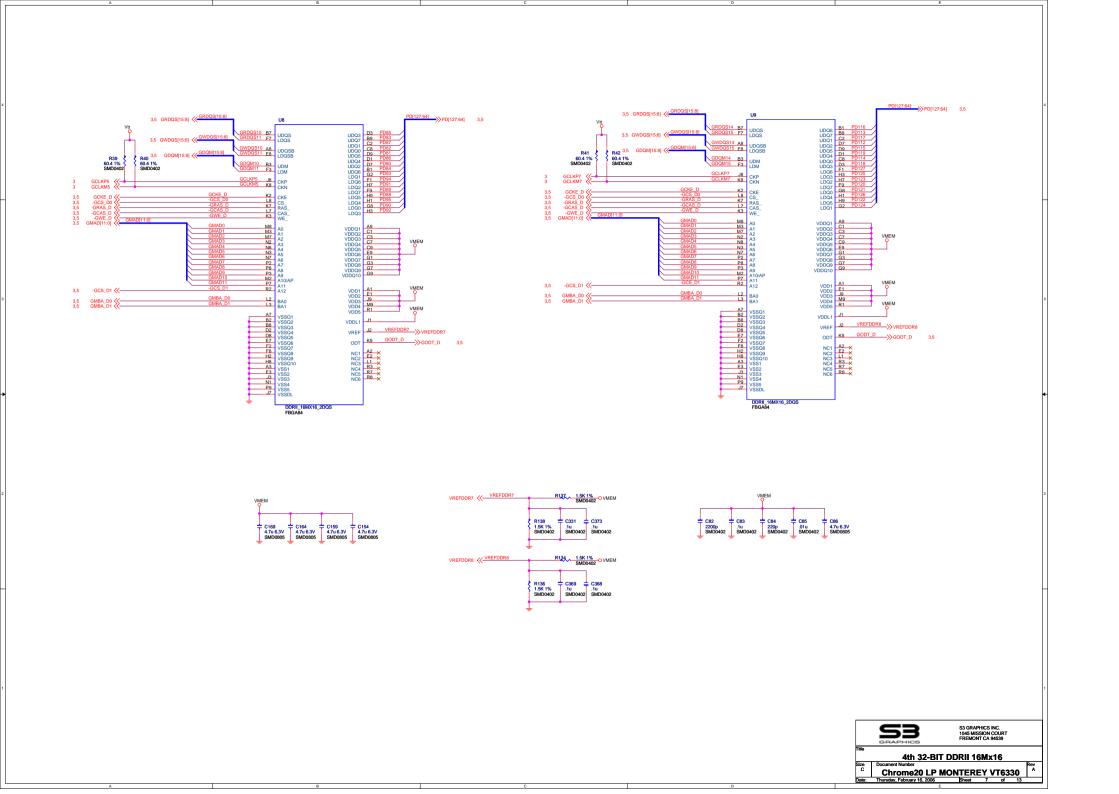


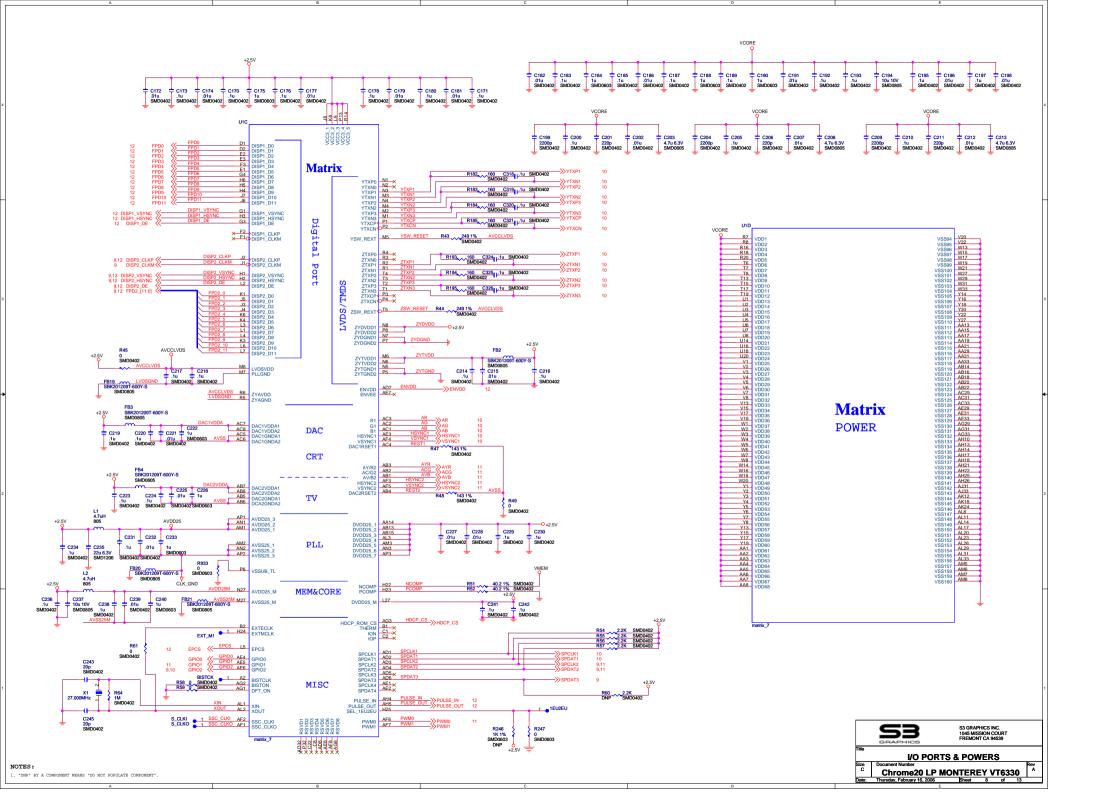


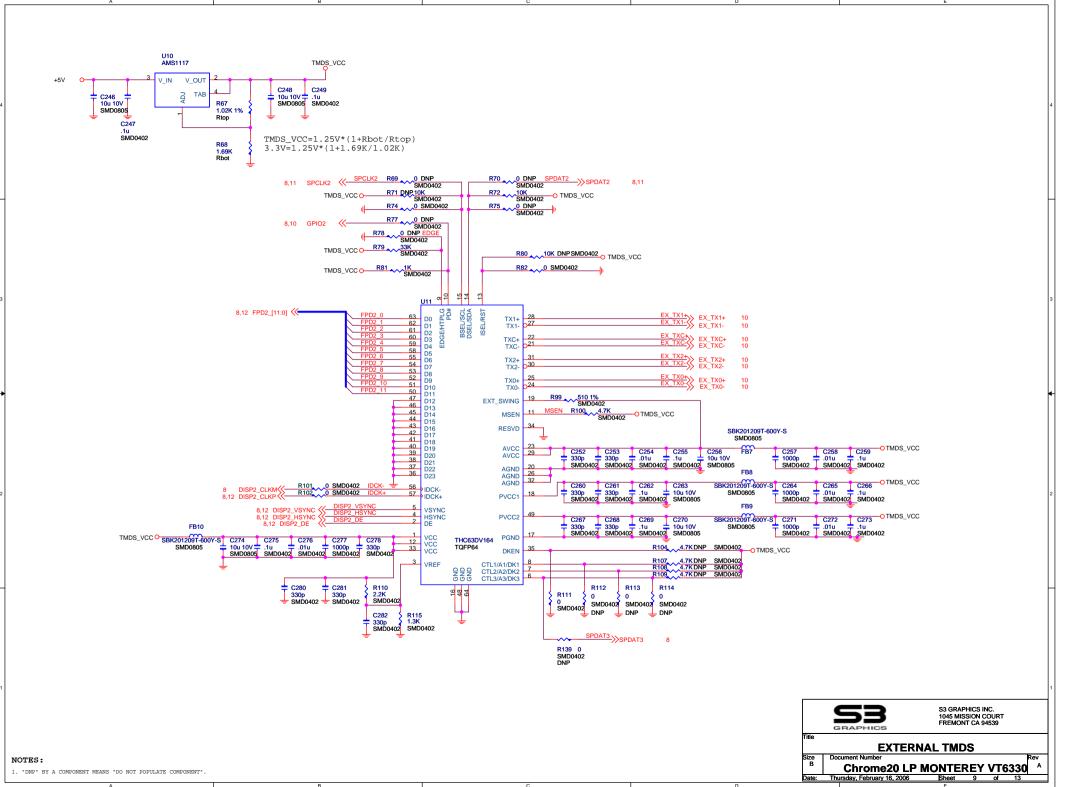


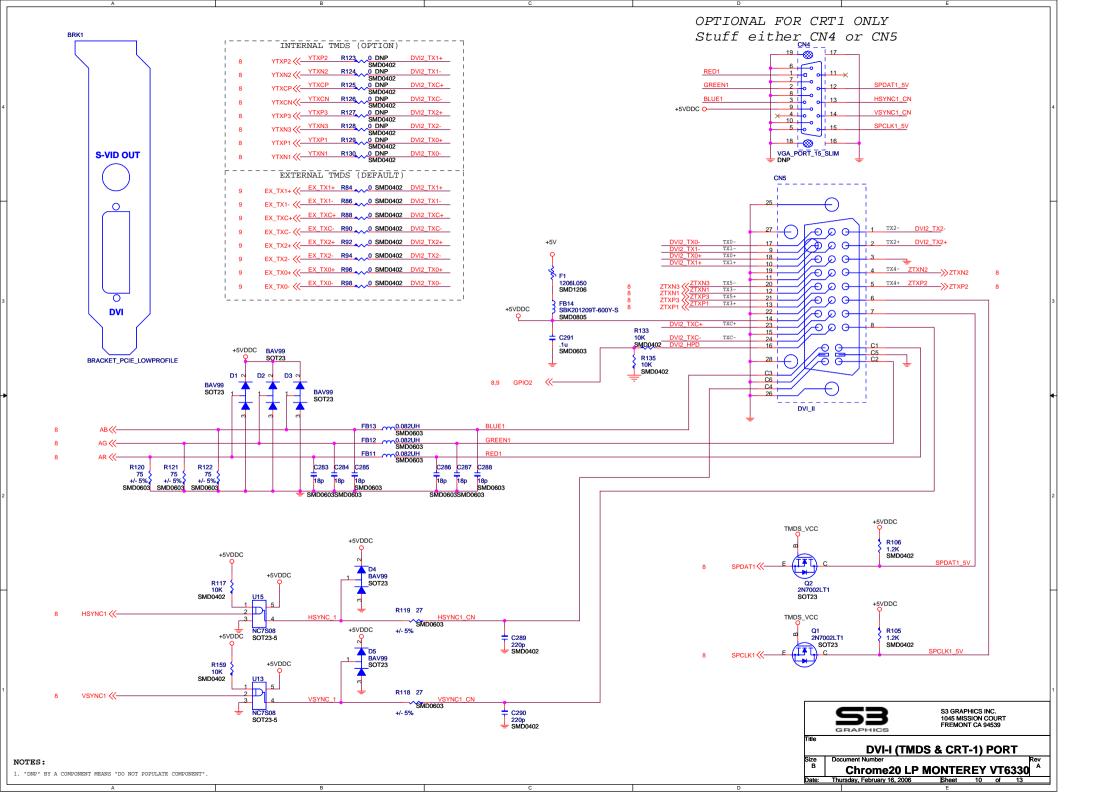


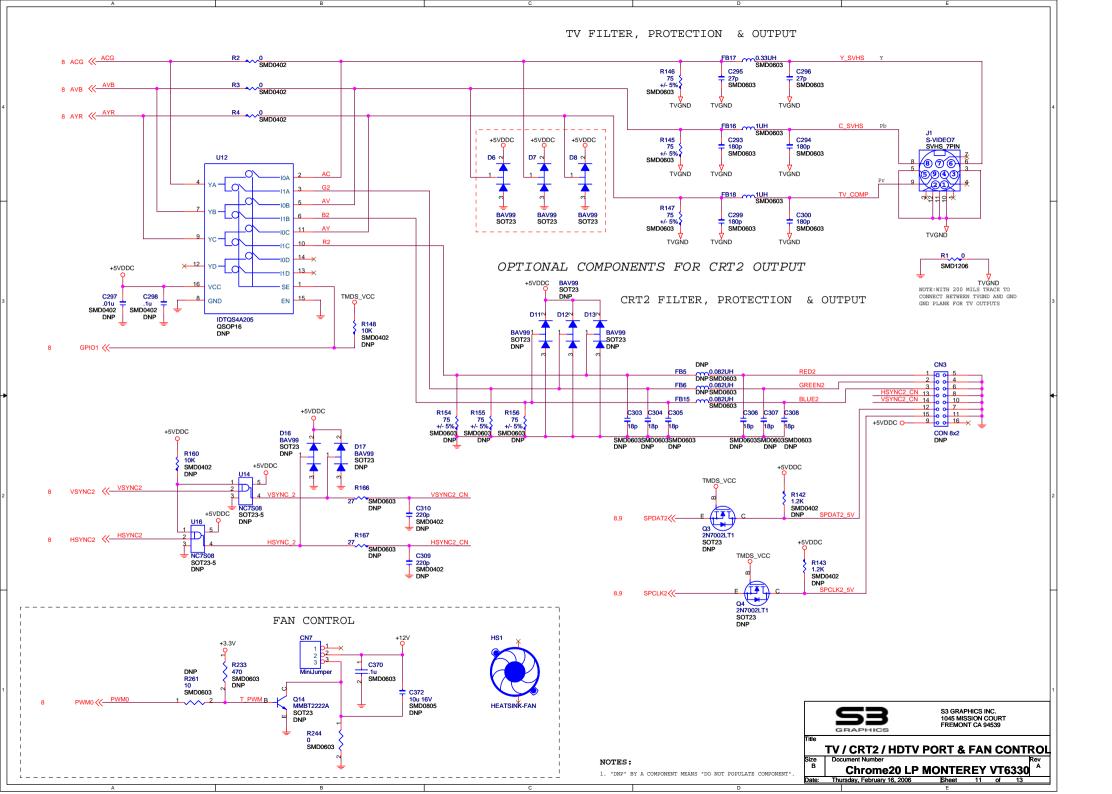












		А	В			С	D E
			•				
	Q 1						
		<u>ping pi</u>					
	REGISTER	SIGNAL	FUNCTION POLE LO NOT GUIDDODEED	VALUE	STUFF	RESISTOR	l I
	CR36 0	FPD0	PCIE LO NOT SUPPORTED PCIE LO SUPPORTED (DEFAULT)	"1"	YES	R168	8 FPD0 R168 10K SMD0402 0+2.5V FPD1 R169 10K SMD0402 1 0+2.5V
			PCIE L1 NOT SUPPORT	"0"	YES		8 FPD1 FPD2 R170 10K SMD0402
ا،	CR36_1	FPD1	PCIE L1 SUPPORTED (DEFAULT)	"1"		R169	8 FPD2 FPD3 R171 10K SMD0402 8 FPD3 R171
1	CR36_2	FPD2	PCIE SCRAMBLE (DEFAULT) PCIE SCRAMBLE DISABLE	"0" "1"	YES	R170	8 FPD4 R172 10K SMD0402 FPD5 R173 10K SMD0402 FPD5 R173 10K SMD0402
	CK30_Z	r FDZ	PCIE MSI_EN DISABLE (DEFAULT)	"0"	YES	K170	8 FPD5 S FPD6 R174 10K SMD0402
	CR36_3	FPD3	PCIE MSI_EN ENABLE	"1"		R171	8 FPD6 FPD7 R197 10K DNP SMD0402
	CR36_4	FPD4	PCIE NOT COMMON CLOCK PCIE COMMON CLOCK (DEFAULT)	"0" "1"	YES	R172	O EDDO // FFDO K190 TOK DIVE SIVIDU402
	CR30_4	FPD4	USE PCIE INTERNAL PCLK (DEFAULT)	"0"	YES	K1/Z	8 FPD9 S FPD10 R200 10K DNP SMD0402
	CR36_5	FPD5	USE PCIE EXTERNAL PCLK	"1"	-	R173	8 FPD10 FPD11 R245 10K SMD0402
┪	anac c	777.C	PCIE 32-BIT ADDRESS MODE	"0"	YES	D104	8 FPD11 SDISP1_DE R179 10K SMD0402 B DISP1_DE R179 10K SMD0402 DISP1_DE R179 10K SMD0402
	CR36_6	FPD6	PCIE 64-BIT ADDRESS MODE 256K byte ROM size	"1"		R174	8 DISP1_HSYNC \\ DISP1_VSYNC_R180 \\ 10K_DNP_SMD0402
	CR36_7	FPD7	64K byte ROM size	"1"	NO	R197	8 DISP1_VSYNC (DISTI_VSTNG KIST
			ROM TYPE BIT-30] CR37[30]	"0"			8,9 FPD2_0 (FPD2_0 R264 10K DNP SMD0402 PD544 P
	CR37_0	FPD8	000 SST SST39VF512 (PARALLEL 512kBIT) 001 LPC ROM, SST49LF080A	"1"	NO	R198	8,9 FPD2_1 R254 10K SMD0402 FPD2_2 R255 10K DNP SMD0402
			100 ATMEL 25F1024 (SERIAL 1MBIT)	"0"			8,9 FPD2_2 FPD2_3 R263 10K DNP SMD0402
	CR37_1	FPD9	101 ST MICRO 25P10 (SERIAL 1MBIT)	"1"	NO	R199	8,9 FPD2_3 FPD2_4 R248 10K DNP SMD0402
3			110 ST MICRO 25P05 (SERIAL 512kBIT) 111 SST SST25VF512 (SERIAL 512kBIT)	"0"		KIJJ	3
	CR37_2	FPD10	OTHERS RESERVED	"1"	NO	R200	FPD2 7 R186 10K DNP SMD0402
	CR37_3	FPD11	ROM CLK is PCLK/32	"0"	YES		8,9 FPD2_/ \$\ \text{FPD2.8 R187 10K SMD0402}
	0107_0		ROM CLK is PCLK/16	"1"		R245	8,9 FPD2_8 FPD2_9 R188 10K SMD0402 8,9 FPD2_9 R188 10K SMD0402
	CR37_4	DISP1_DE	X16 PCI EXPRESS LINK WIDTH (DEFAULT) X8 PCI EXPRESS LINK WIDTH	"0" "1"	YES	R179	8,9 FPD2_10 R189 10K SMD0402 I FPD2_11 R190 10K SMD0402 I
	CR37_4	DISPI_DE	MEMORY SPEED BITS[20] CR68[3]CR37[65]	"0"	YES	KIII	8,9 FPD2_11 DISP2 DE R101 10K SMD0402
	CR37_5	DISP1_HSYNC	000 RESERVED 100 RESERVED	"1"		R180	8,9 DISP2_DE DISP2_VSYNC R192 10K DNP SMD0402
1	CR37 6	DIGD1 VOVNO	001 RESERVED 101 400MHz 010 300MHz 110 500MHz	"0" "1"	NO	R181	TANKED DOOR AND OND OND
	CR37_6	DISP1_VSYNC	010 300MHz 110 300MHz 011 350MHz 111 RESERVED	"0"	NO	KIOI	8 ENVDD R201 10K DNP SMD0402 8 PULSE_IN R202 10K DNP SMD0402 PULSE_IN R202 10K DNP SMD0402
	CR68_3	FPD2_4		"1"	NO	R248	8 PULSE_IN PULSE_OUT R205 10K DNP SMD0402
	and I		HYNIX	"0"	170	D001	0 F0L3E_001(\(\sigma\)
	CR6F_7	ENVDD	SAMSUNG USE EXTERNAL MCLK/MCLK90 ON EXTMCLK/SPDAT2	"1"	NO	R201	
	CR37_7	FPD2_0	USE INTERNAL MCLK/MCLK90 (DEFAULT)	"1"	NO	R264	
,	anco o	TDD 0 1	USE EXTERNAL ECLK ON EXTECLK	"0"	YES	2054	DISP1_DE 5 SDI SDO 2 >>> DISP2_CLKP 8,9 2
٦	CR68_0	FPD2_1	USE INTERNAL ECLK (DEFAULT) USE EXTERNAL DCLK1 ON XIN	"1"		R254	TMDC VCC
	CR68_1	FPD2_2	USE INTERNAL DCLK1 (DEFAULT)	"1"	NO	R255	8,9 DISP2_HSYNC (
			USE EXTERNAL DCLK2 ON SPDAT4	"0"			TMDS_VCC
	CR68_2	FPD2_3	USE INTERNAL DCLK2 (DEFAULT) CRYSTAL CONNECTED TO XIN, XOUT (DEFAULT)	"1"	NO YES	R263	TMDS VCC 0 R196 4.7K 7 HOLD
	CR68_7	FPD2_8	OSCILLATOR CONNECTED TO XIN	"1"	150	R187	EDCS WP L SMD0403
		_	1 RANK POPULATED	"0"			
\dashv	CR68_6	FPD2_7	2 RANKS	"1"	NO	R186	SST25VF512_0_SOIC-8
	CR6F_0	FPD2_9	POPULATED 000 DDR2 16Mx16	"1"	YES	R188	
			001 DDR3 8Mx32 101 DDR1 8Mx32	"0"	YES	R189	1
	CR6F_1	FPD2_10	010 DDR2 32Mx16	"1"			
	CR6F_2	FPD2_11	011 DDR3 16Mx32 111 DDR1 4Mx32	"0" "1"	YES	R190	
		1122_11	00 0 FB	"0"	YES	11170	S3 GRAPHICS INC.
	CR6F_3	DISP2_DE	01 32-BIT MEMORY BUS WIDTH 10 128-BIT MEMORY BUS WIDTH	"1"		R191	1045 MISSION COURT
1	CR6F_5	DISP2 VSYNC	11 64-BIT MEMORY BUS WIDTH(DEFAULT)	"0" "1"	NO	R192	GRAPHICS FREMONT CA 94539
	CKOF_5	DIBEZ_ABINC	FB MEMORY SIZE = 256M (DEFAULT)	"0"	NO	NT22	Title
	CRCF1_6	PULSE_IN	FB MEMORY SIZE = 512M	"1"		R202	VGA BIOS EEPROM & Config Setting
	CDCE1 7	DIII CE OTTE	BIGENDIAN DISABLE (DEFAULT)	"0" "1"	NO	DONE	Size Document Number Rev
	NOTES:	PULSE_OUT	BIGENDIAN ENABLE	" 1 "		R205	<u> </u>
							Chromezu LP MONTERET V16330
L	1. "DNP" BY 2		ANS "DO NOT POPULATE COMPONENT".				Date: Thursday, February 16, 2006 Sheet 12 of 13
		Α	В			С	D E

