

P2002

2GB GDDR5, 256b, 64Mx32

Stacked DVI-I/DVI-D + HDMI + DP

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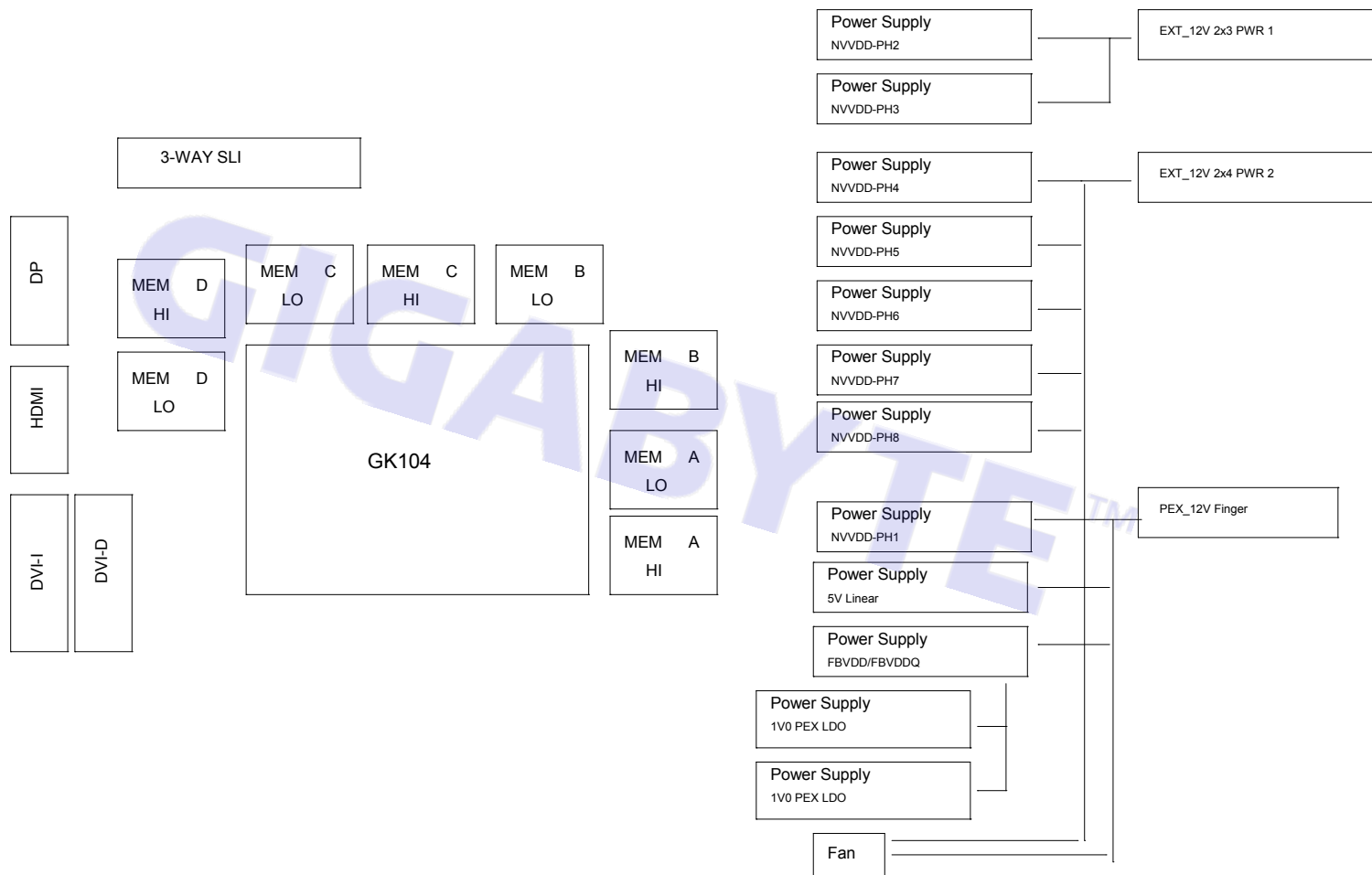
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Page	Description
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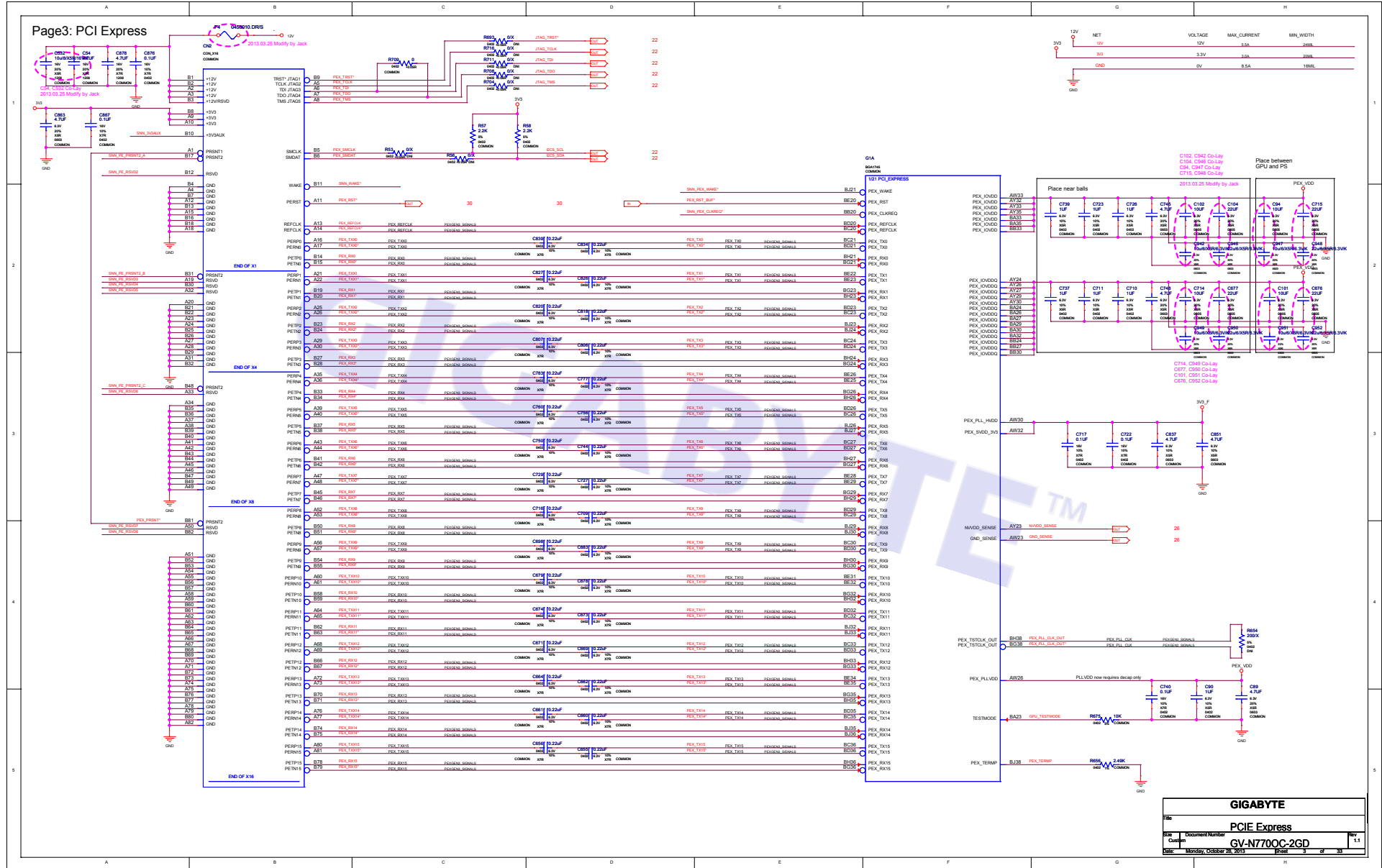
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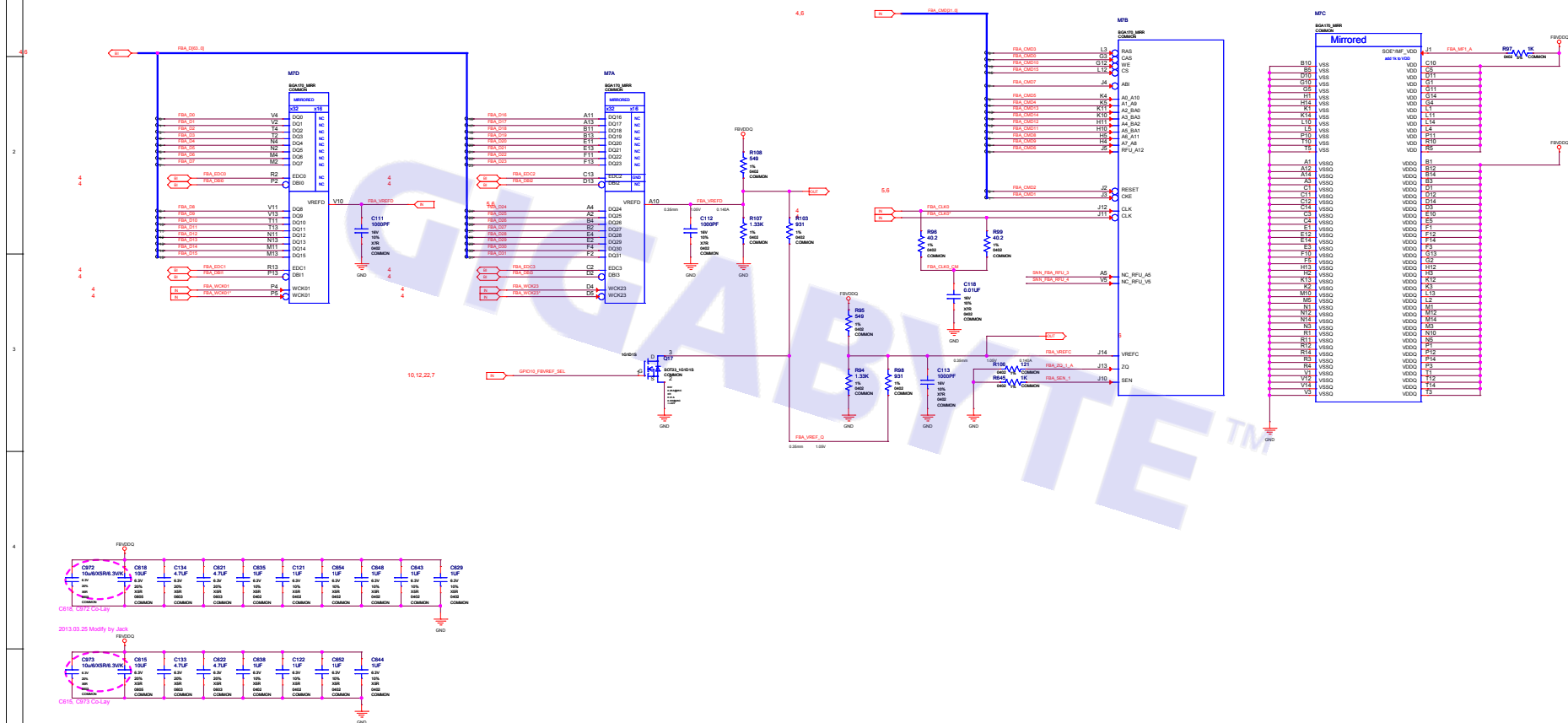
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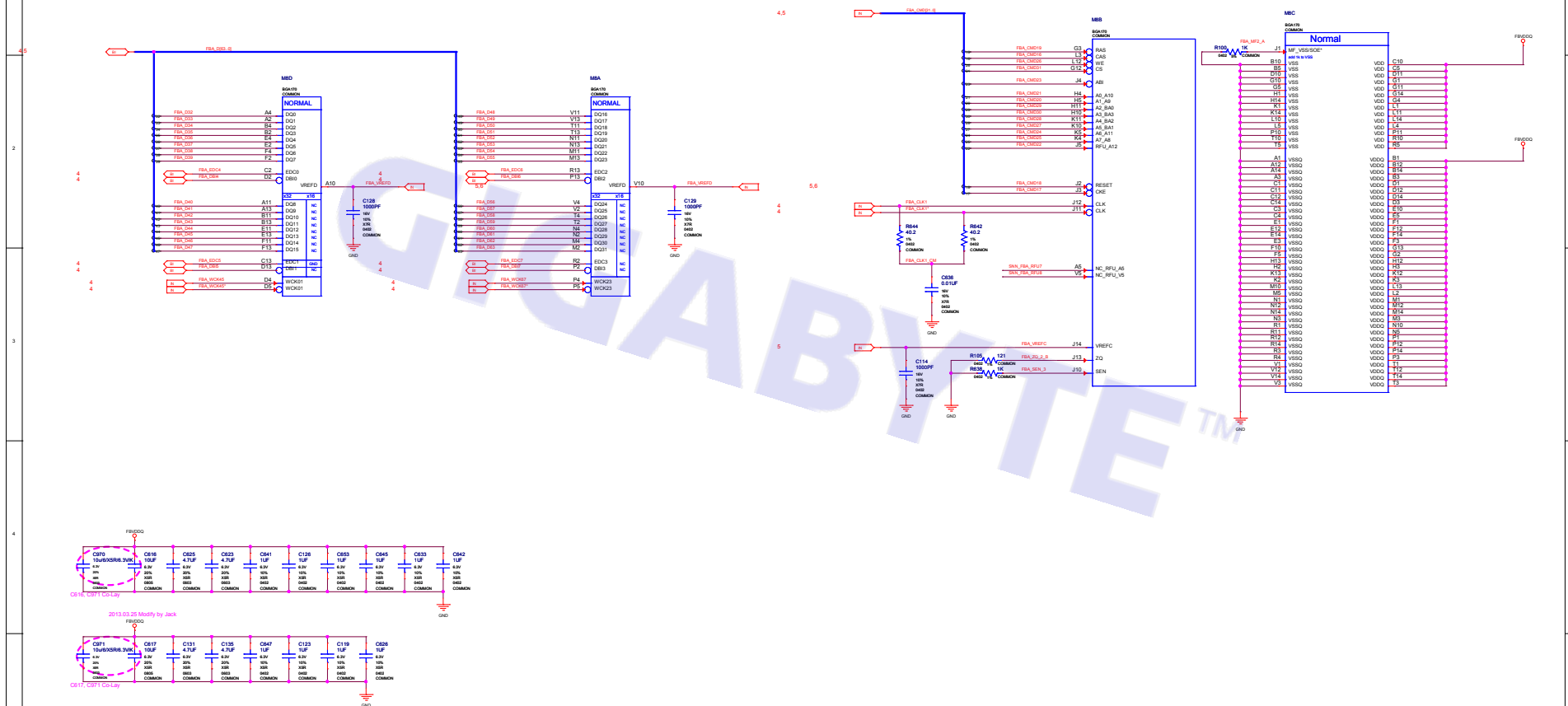
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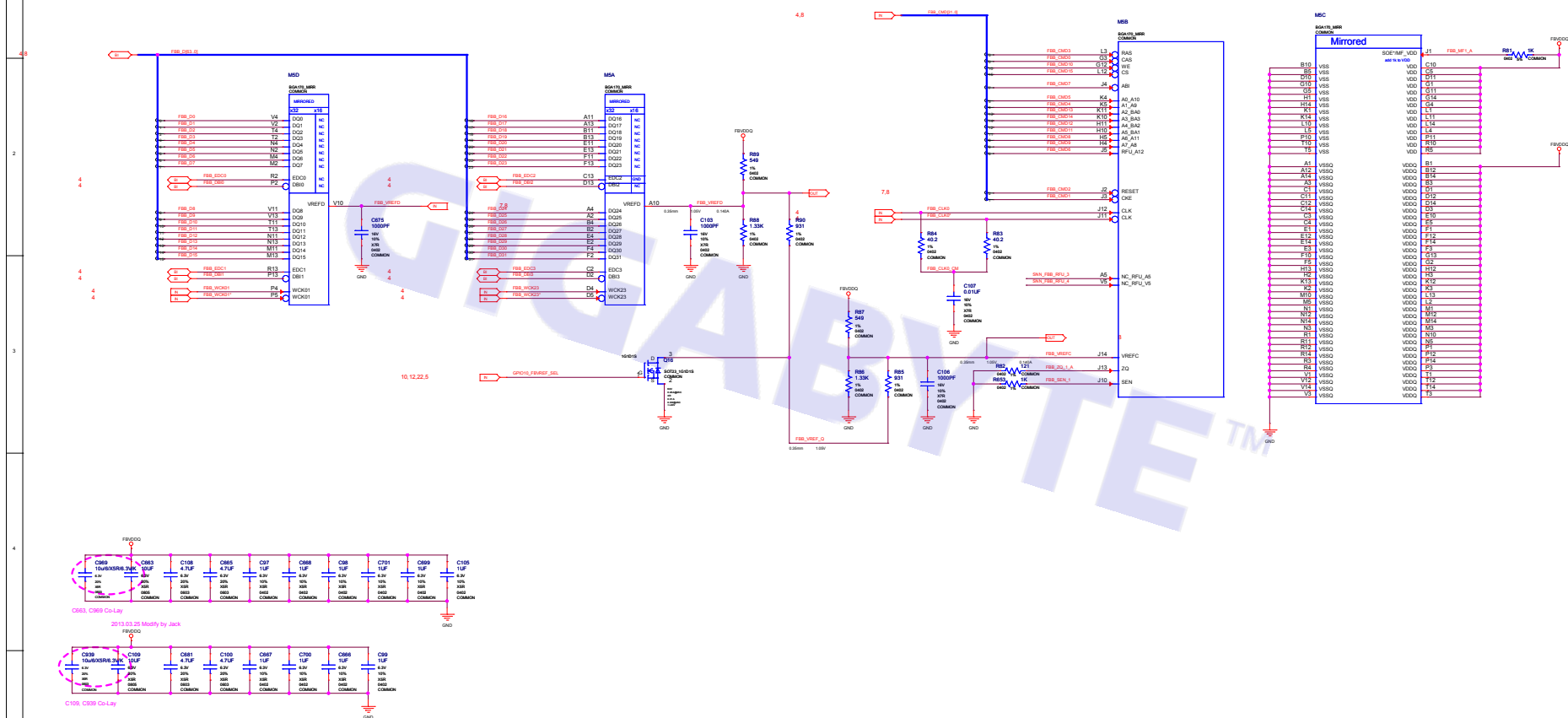


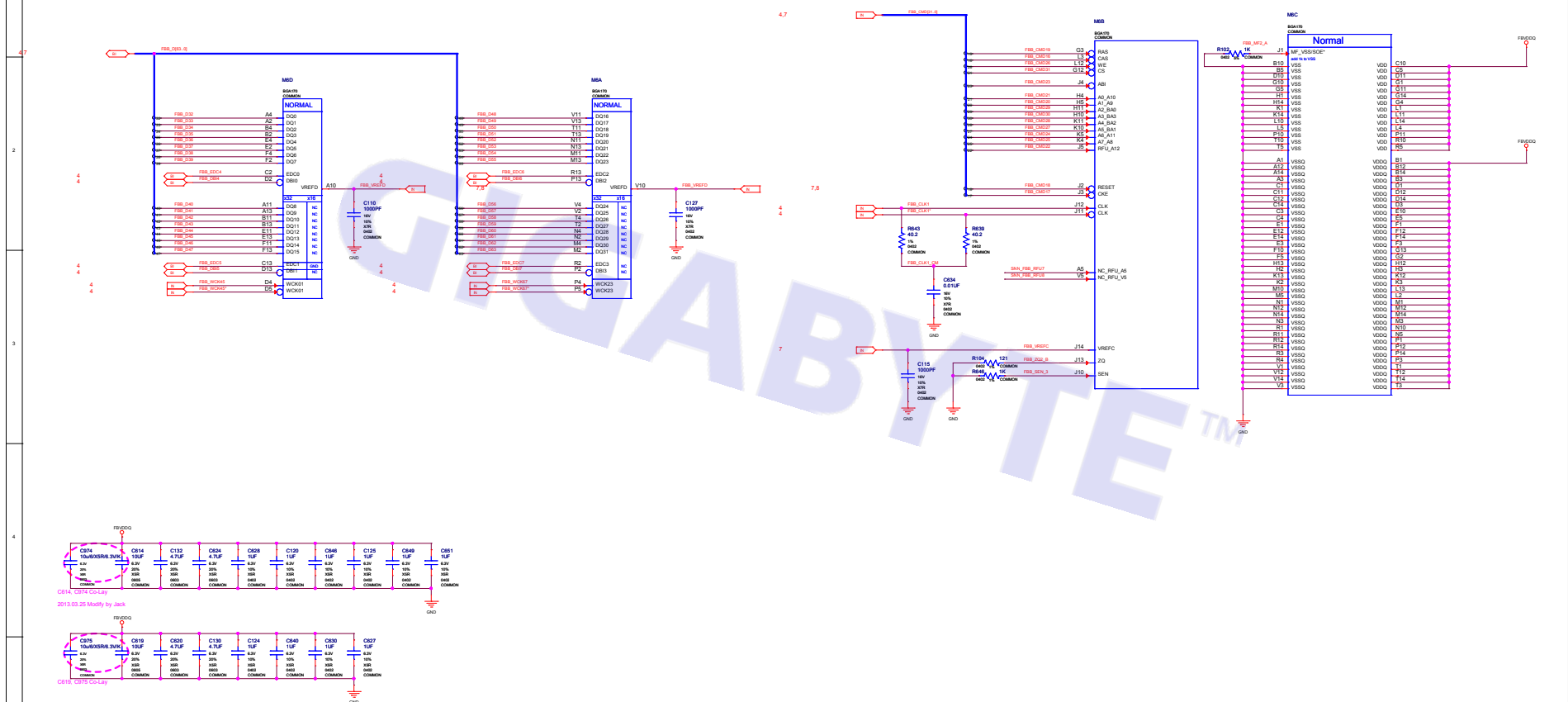
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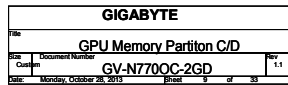


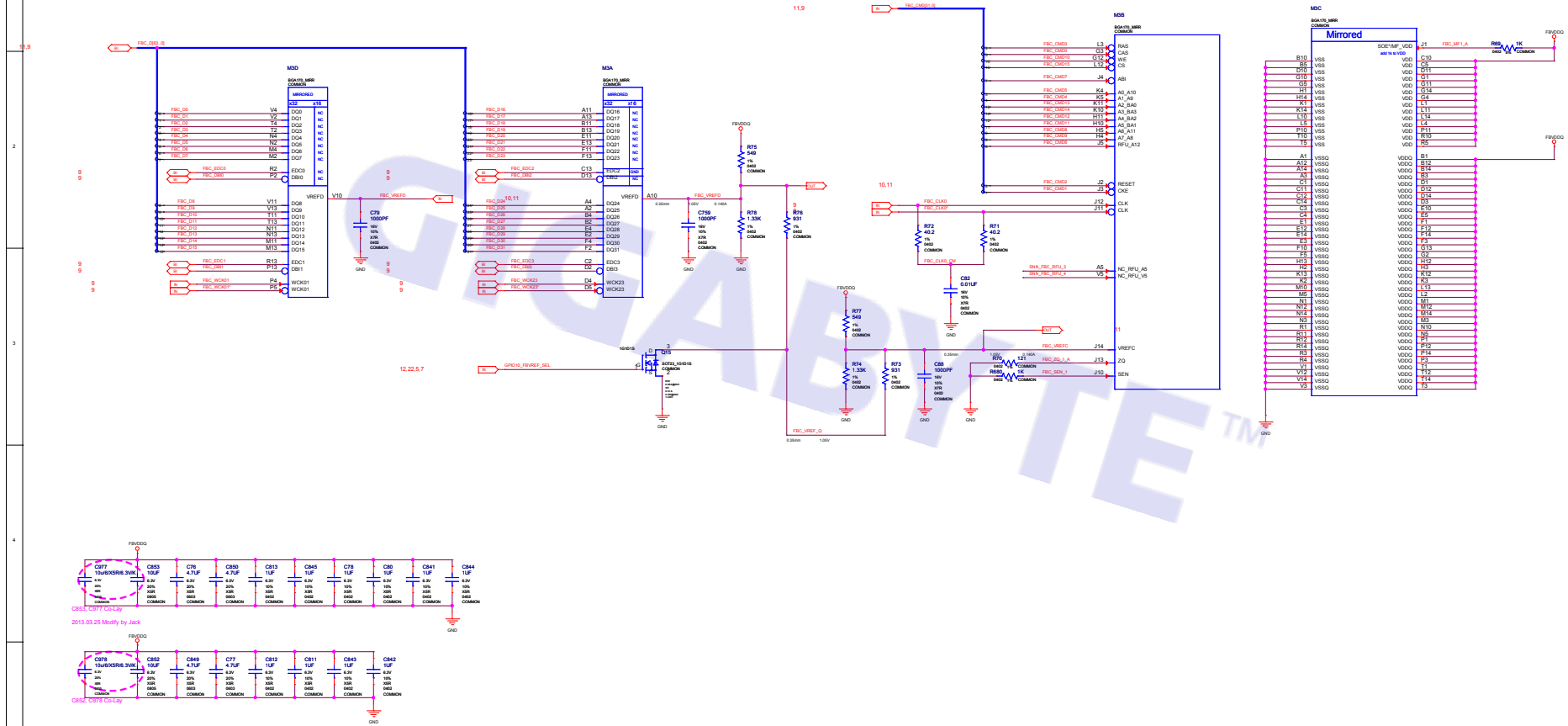










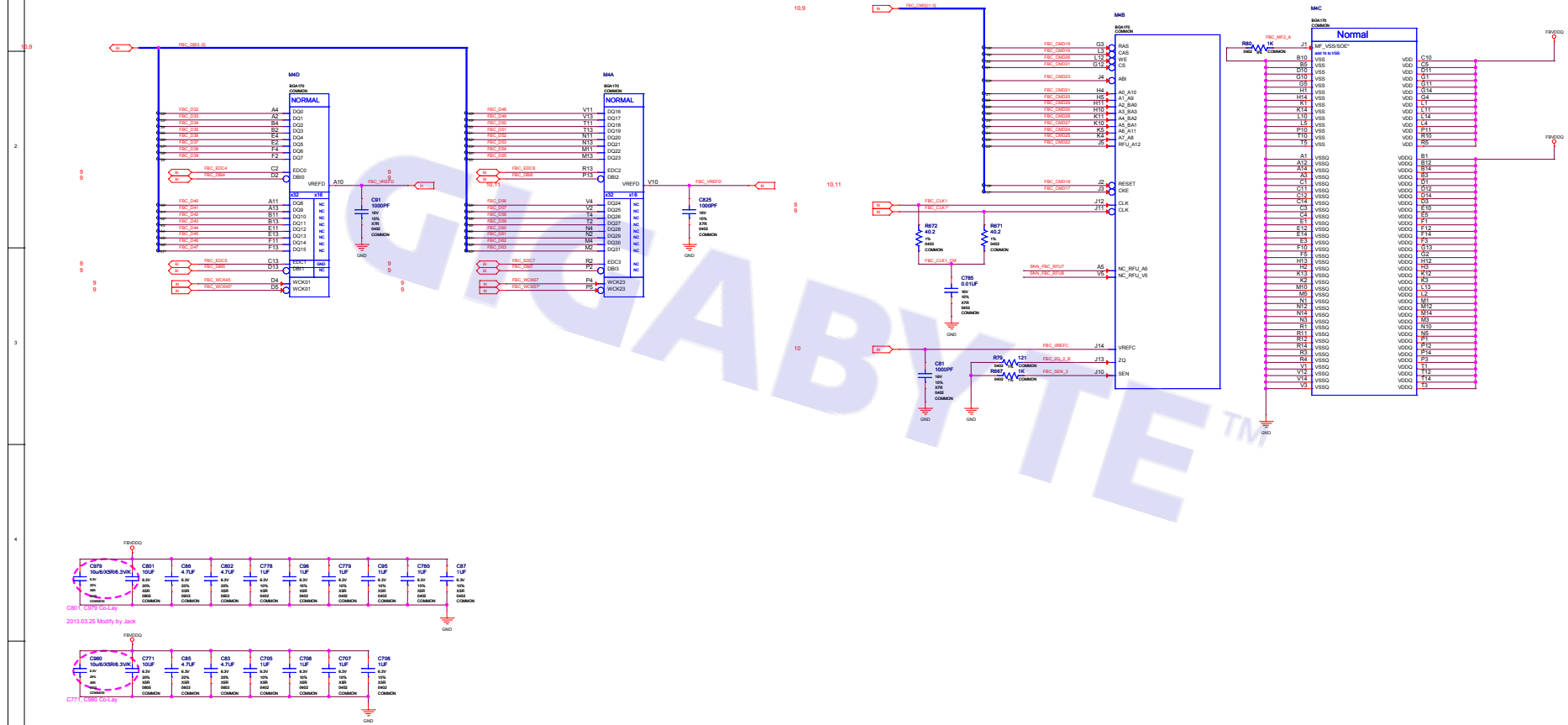


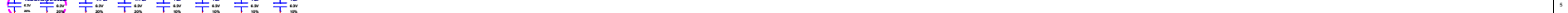
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ASSEMBLY
PAGE DETAIL

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MEMORY: FBC(31..0)

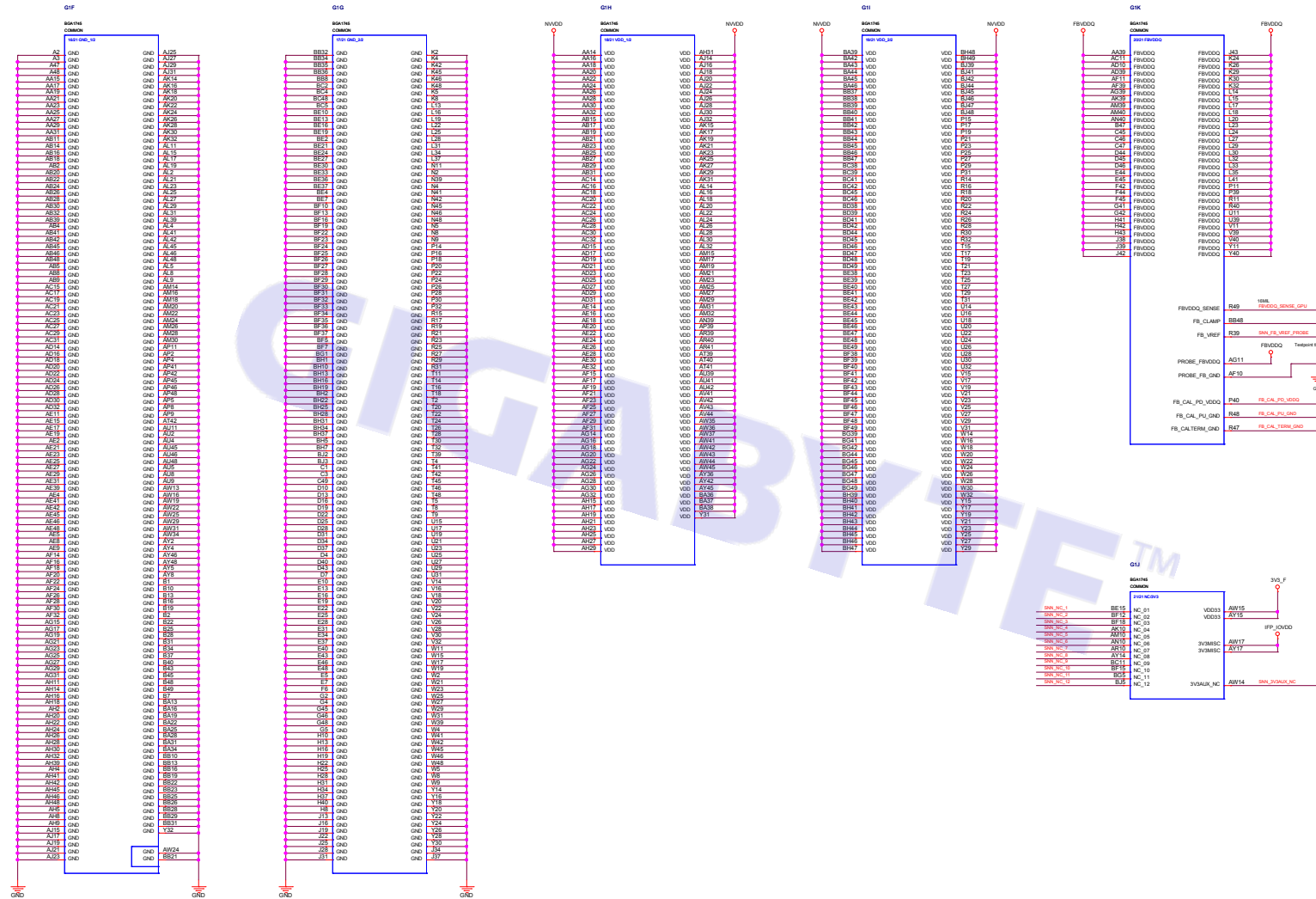
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Part Number	Memory FBC(0..31)		
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A	B	C	D
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Page14: GPU PWR and GND



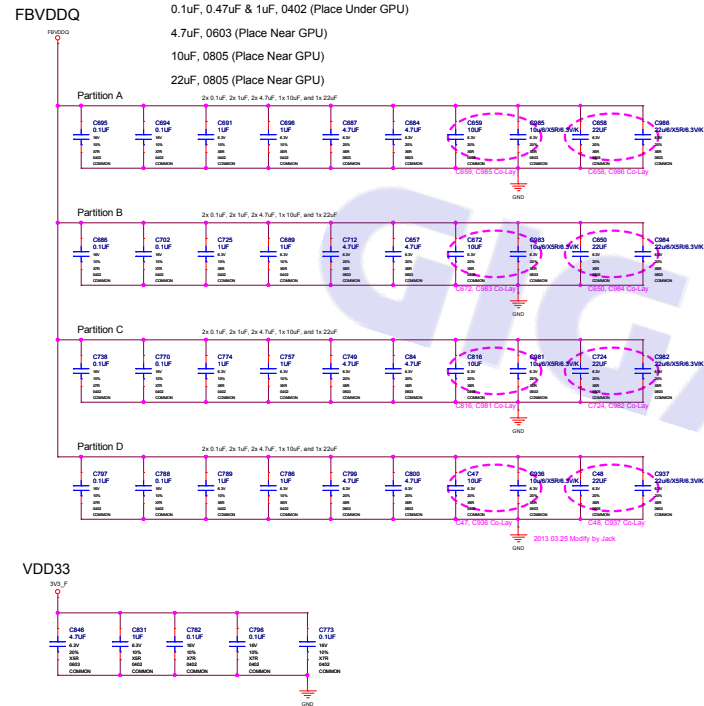
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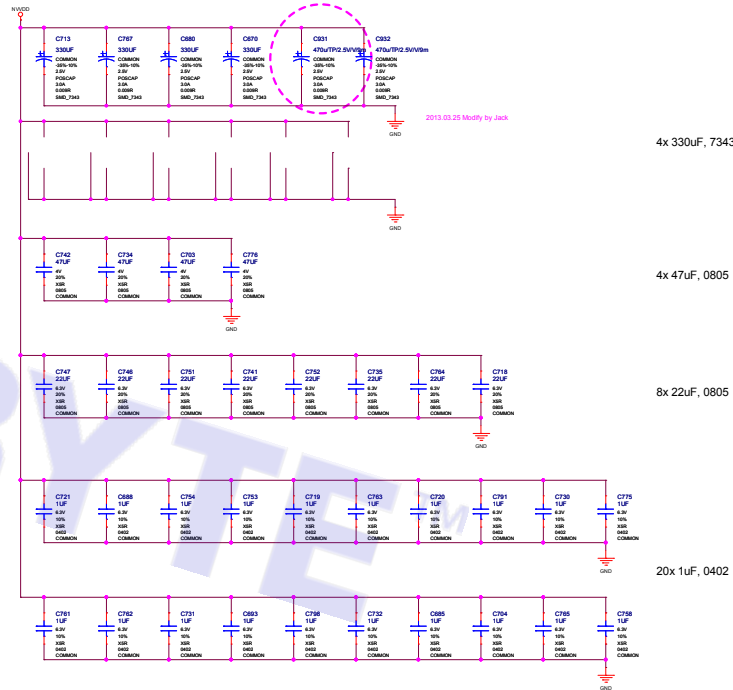
BASE LEVEL GENERIC SCHEMATIC ONLY
GPU PWR and GND

GIGABYTE	
GPU PWR and GND	
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Based on GB2-X GDDR5 FBVDDQ Decap Guideline



NVVDG Decoupling caps. Place under GPU.
 NVVDG



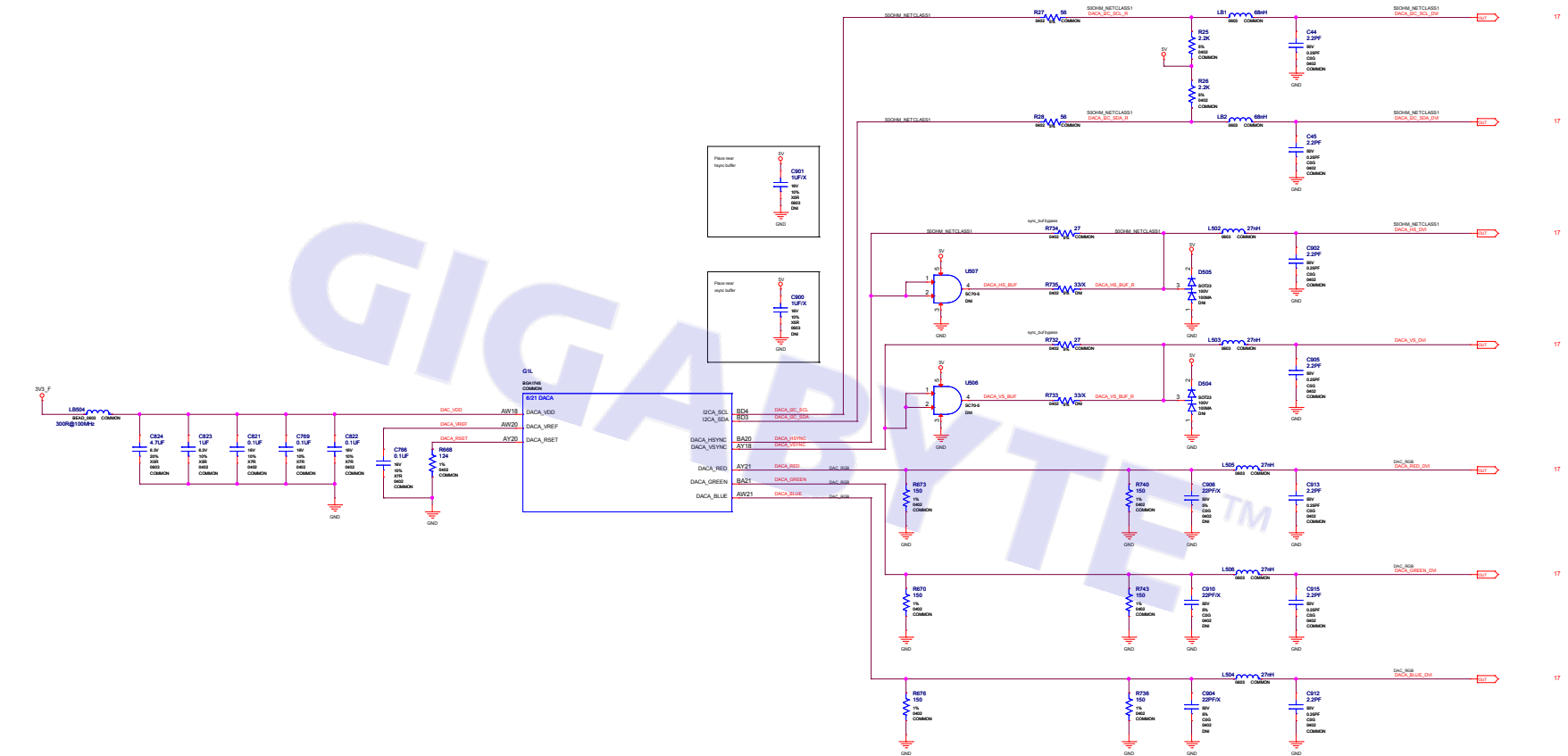
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ASSEMBLY
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GPU Decoupling

GIGABYTE			
File	GPU Decoupling		
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Page16: DACA Interface

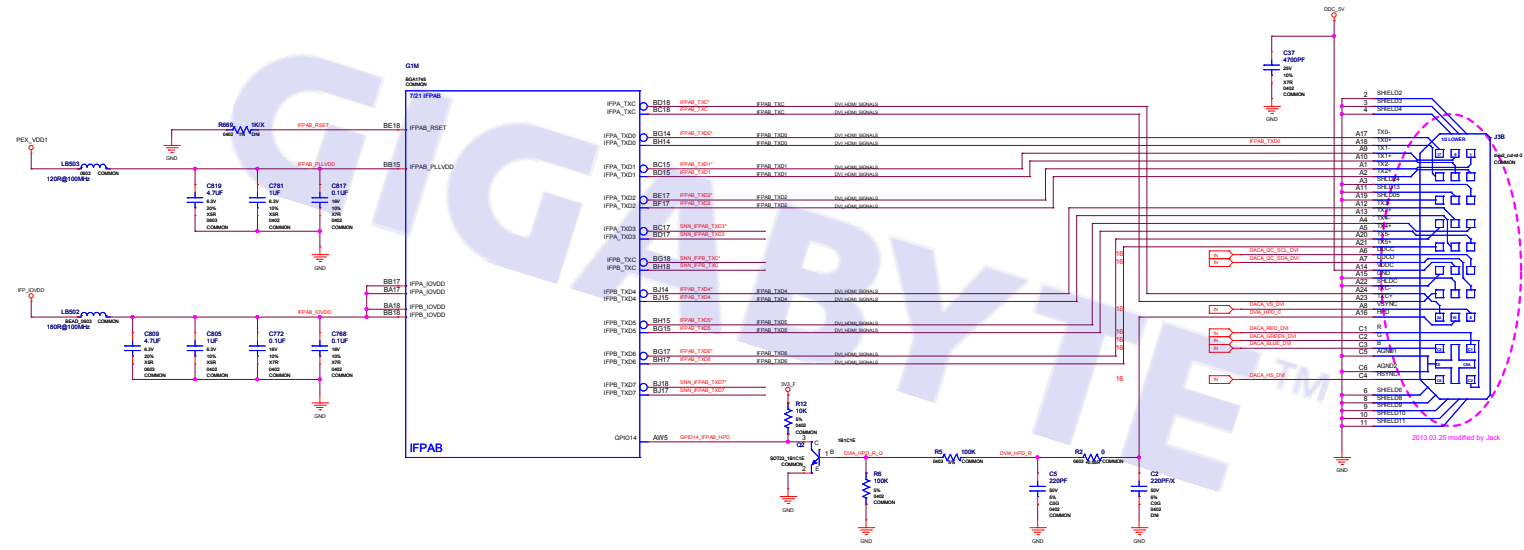


NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
DACA_VDD	3.3V	0.25A	0.15mm

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ASSEMBLY PAGE DETAIL
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DACA Interface

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ASSEMBLY
PAGE DETAIL

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IFPAB DVI-I-DL

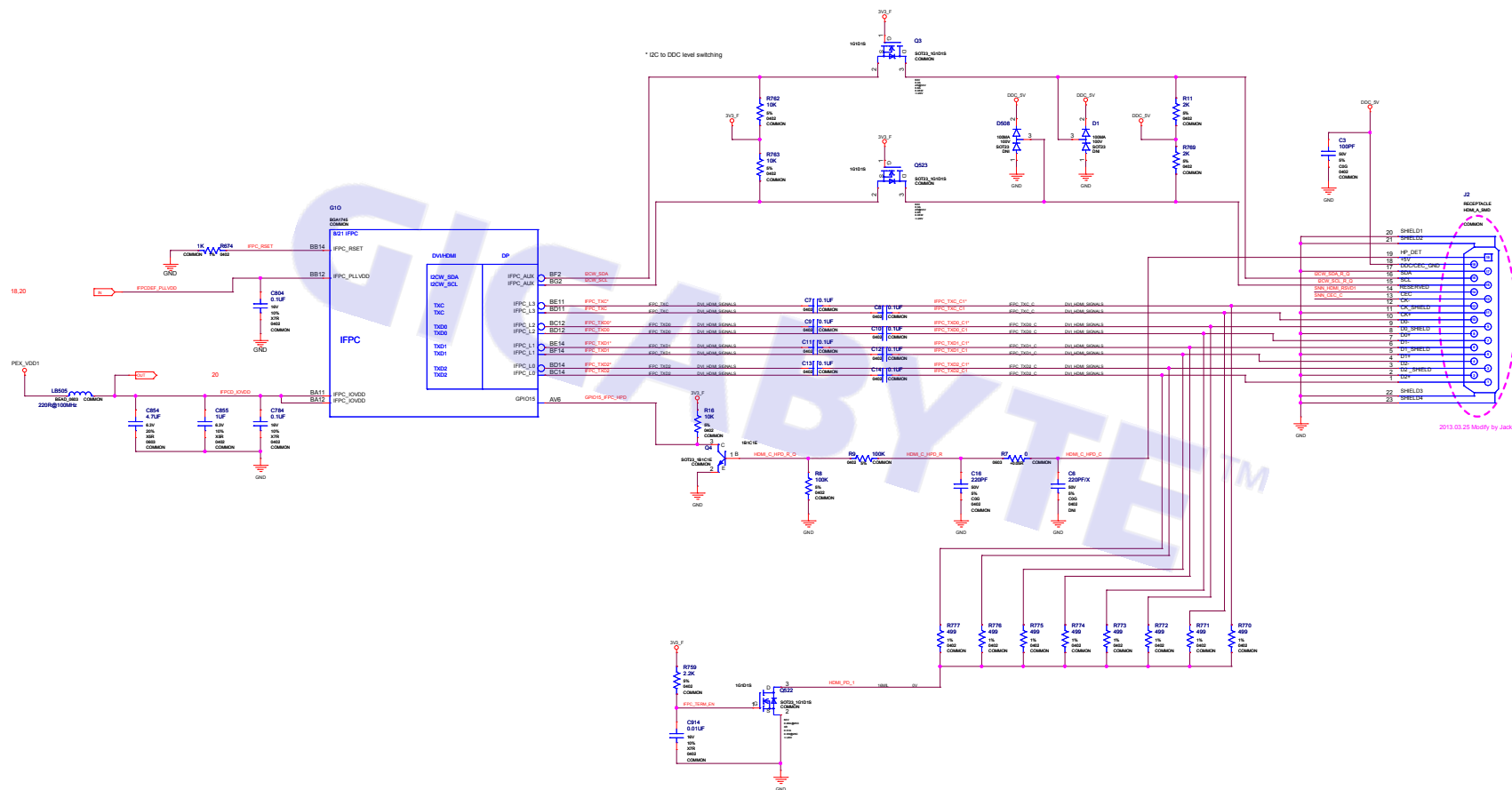
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ASSEMBLY
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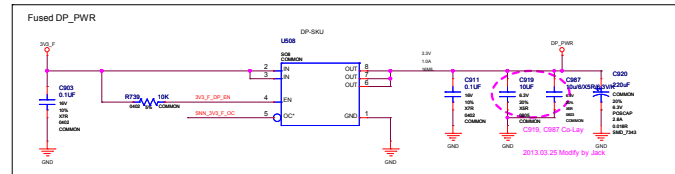
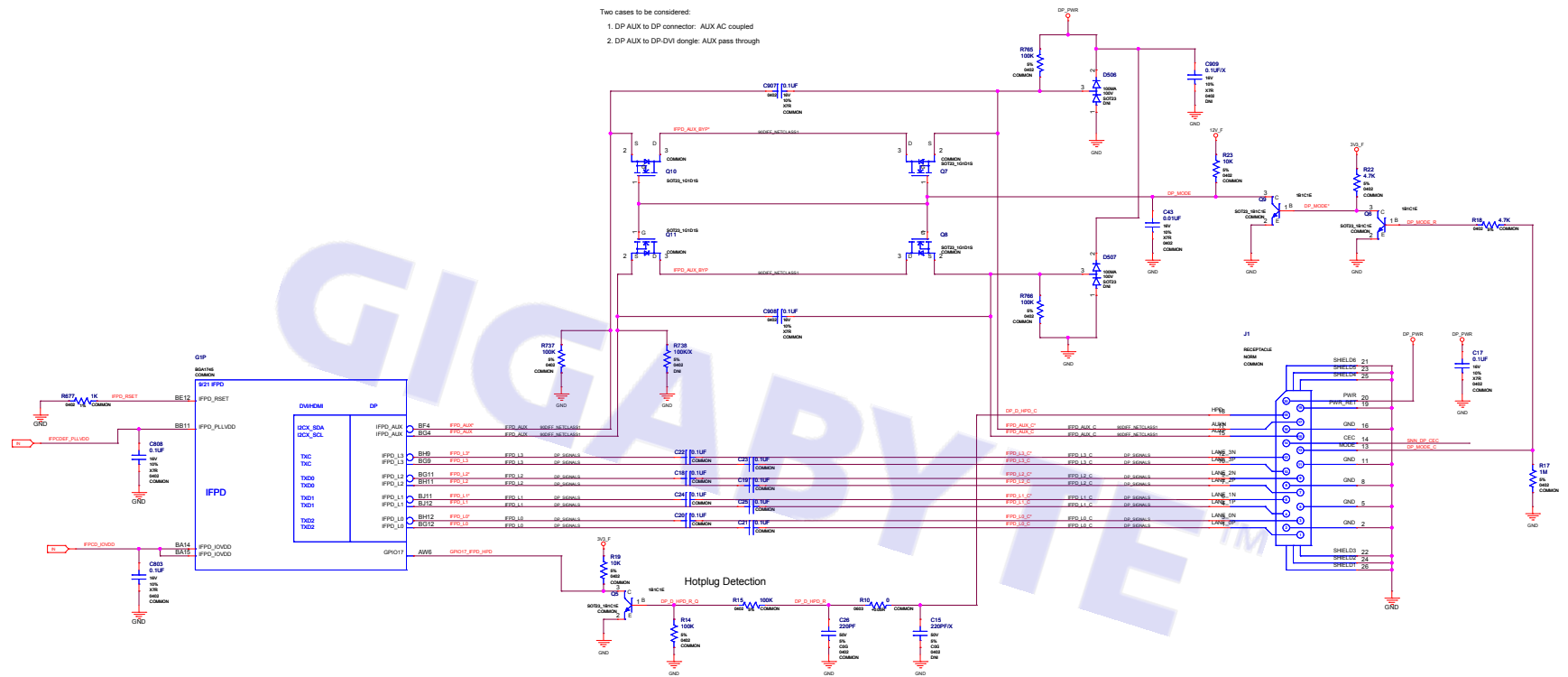
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IFPC HDMI

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IFPC HDMI			
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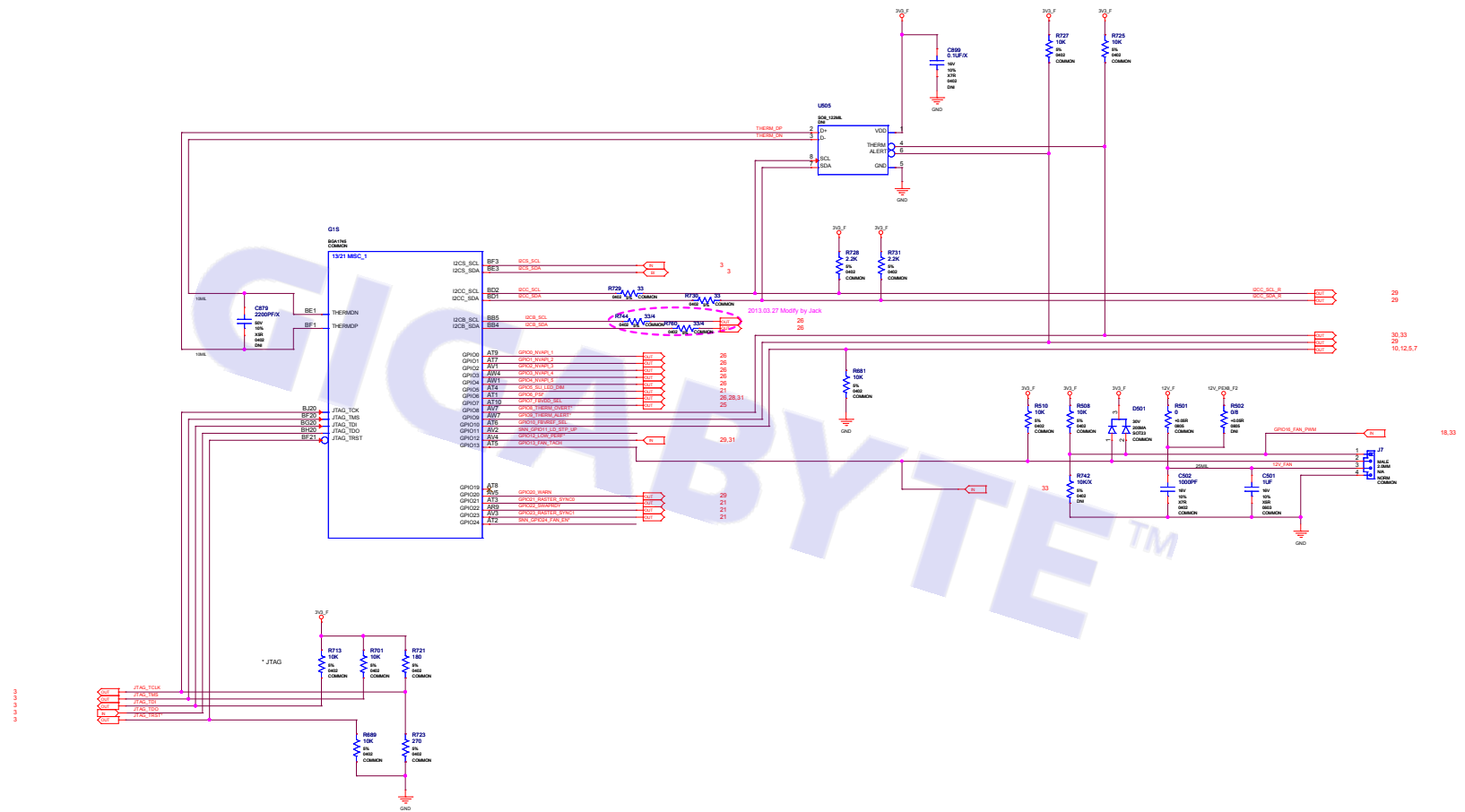
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1. DP AUX to DP connector: AUX AC coupled
 2. DP AUX to DP-DVI dongle: AUX pass through



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File	IFPD DP		
Doc	Document Number	GV-N770OC-2GD	
Rev	Revision	1.1	1.1
Date	Monday, October 10, 2011	Printed	20 02 30

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IFPD DP



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ASSEMBLY
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BASE LEVEL GENERIC SCHEMATIC ONLY
MISC1: Fan, Thermal, JTAG, GPIO

GIGABYTE			
Title			
FAN Thermal JTAG			
Size	Document Number	Rev	
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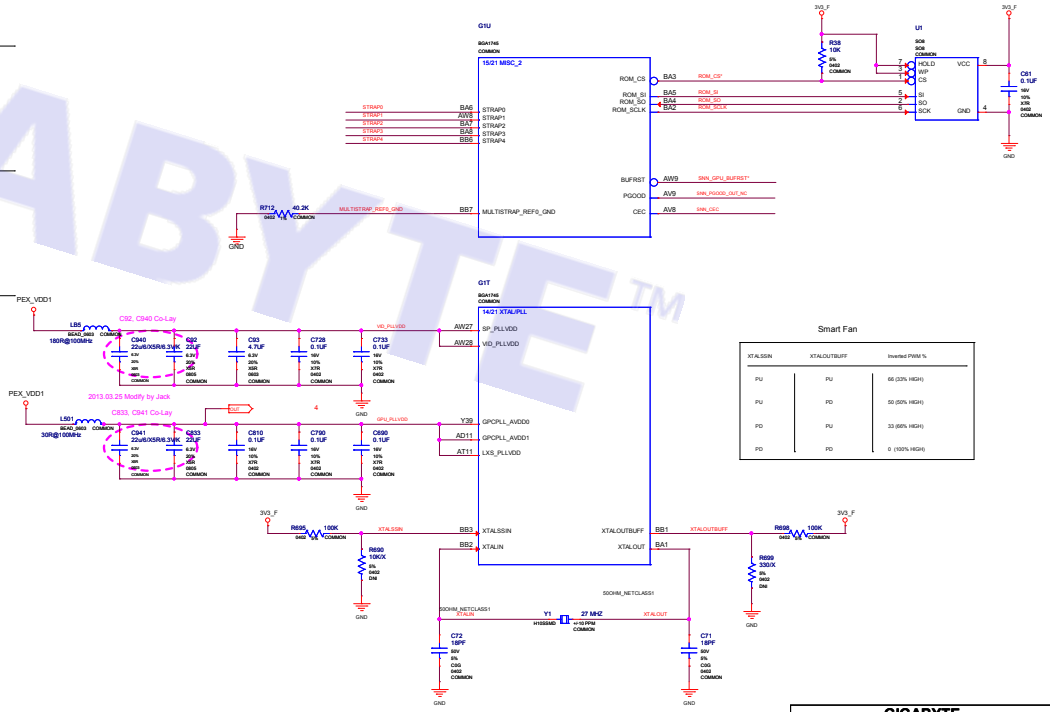
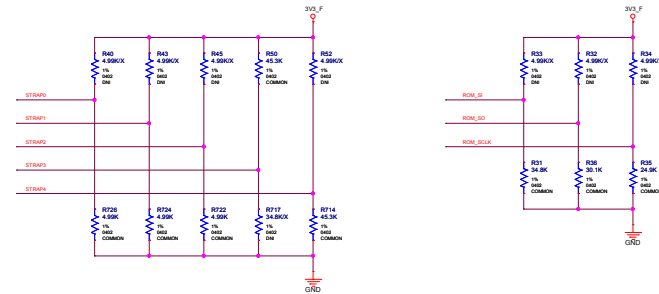
Page23: MISC2: ROM, XTAL, Straps

STRAP0	USER_BIT [3:0]*	0000*	5K PD*
STRAP1	3GIO_PADCFG_LUT_ADR*	0000*	5K PD Desktop*
STRAP2	PCI_DEVID [3:0]*	0000 - (0x1180)*	5K PD 400 GPU*
STRAP3	SOR_EXPOSED [3:0]*	1111*	45K PU*
STRAP4	DP_PLL_VDD_33V*	1* FOR 3_3V*	
	PEX_MAX_SPEED*	1* FOR GEN2/3*	45K PD*
	PEX_SPD_CHANGE_GEN3*	1* ENABLED*	
	*		
ROM_SI	RAMCFG[0]*	0*	
	RAMCFG[1]*	1*	35K PD*
	RAMCFG[2]*	1*	
	RAMCFG[3]*	0*	
ROM_SO	VGA_DEVICE*	1*	
	SMB_ALT_ADDR*	0*	10k PU*
	FB[0]_APERTURE_SIZE*	0* For 256MB*	
	FB[1]_APERTURE_SIZE*	1* For 256MB*	
ROM_SCLK	PEX_PLL_EN_TERM100*	0* DISABLED*	
	PCI_DEVID_EXT[5]*	0* For 0x1180*	25K PD*
	SUB_VENDOR*	1* Dedicated BIOS*	
	PCI_DEVID_EXT[4]*	0* For 0x1180*	

	GND	3V3
5k	0000	1000
10k	0001	1001
15k	0010	1010
20k	0011	1011
25k	0100	1100
30k	0101	1101
35k	0110	1110
45k	0111	1111

CFG[3:0]	Config	Vendor
0000	Reserved	
0001	32Mx32 256-bit Elpsida	
0010	32Mx32 256-bit Hynix	
0011	32Mx32 256-bit Samsung	
0100	Reserved	
0101	64Mx32 256-bit Elpsida	
0110	64Mx32 256-bit Hynix	
0111	64Mx32 256-bit Samsung	
1000	Reserved	
1001	32Mx32 192-bit Elpsida	
1010	32Mx32 192-bit Hynix	
1011	32Mx32 192-bit Samsung	
1100	Reserved	
1101	64Mx32 192-bit Elpsida	
1110	64Mx32 192-bit Hynix	
1111	64Mx32 192-bit Samsung	

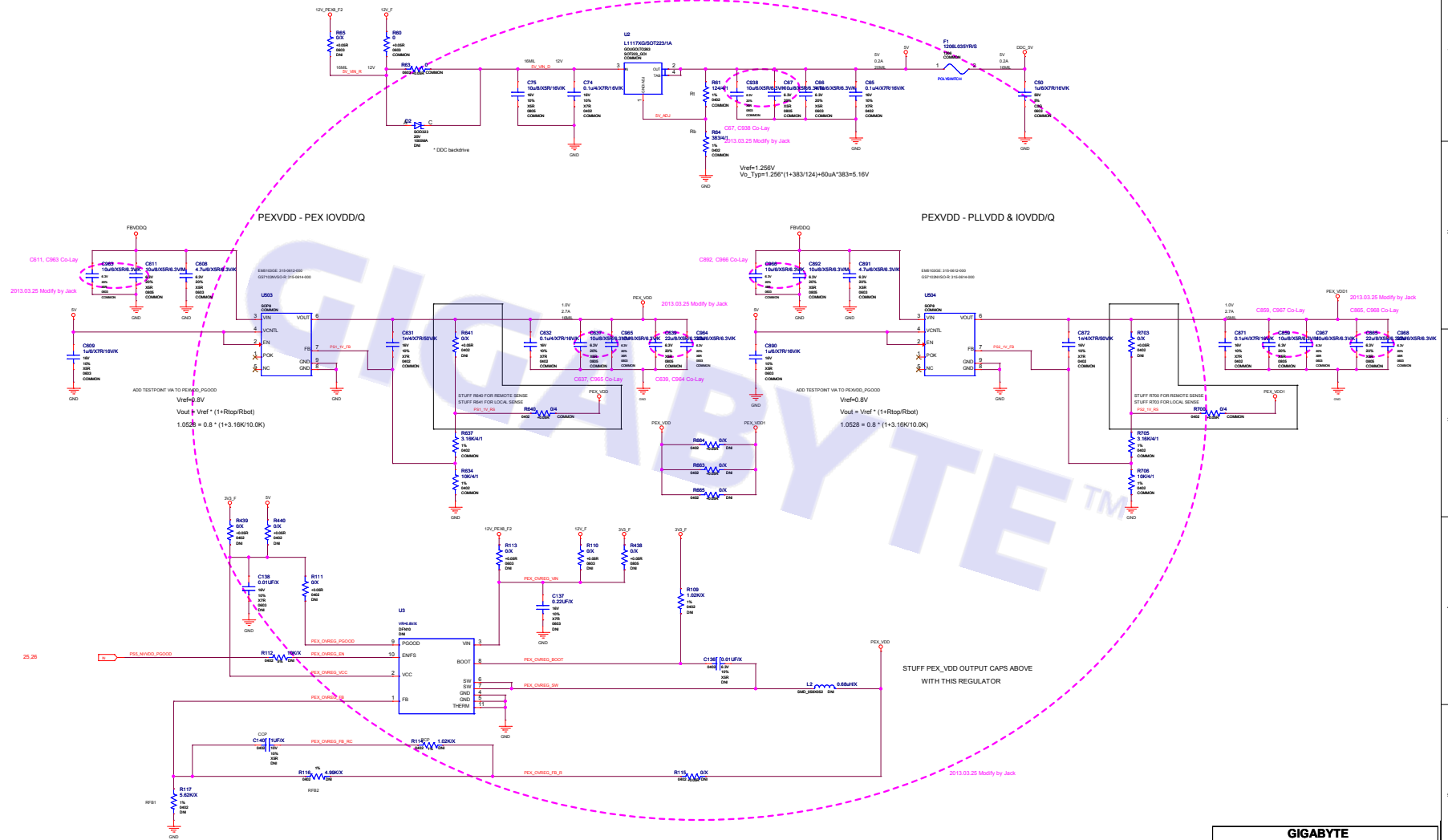
MULTI_STRAP_REFS_GND	
BINARY PRODUCTION	NC
BINARY BRINGUP	NC
MULTI-LEVEL	40.2K 1% TO 0.250



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ASSEMBLY PAGE DETAIL
BASE LEVEL GENERIC SCHEMATIC ONLY
MISC2 ROM, XTAL, Straps

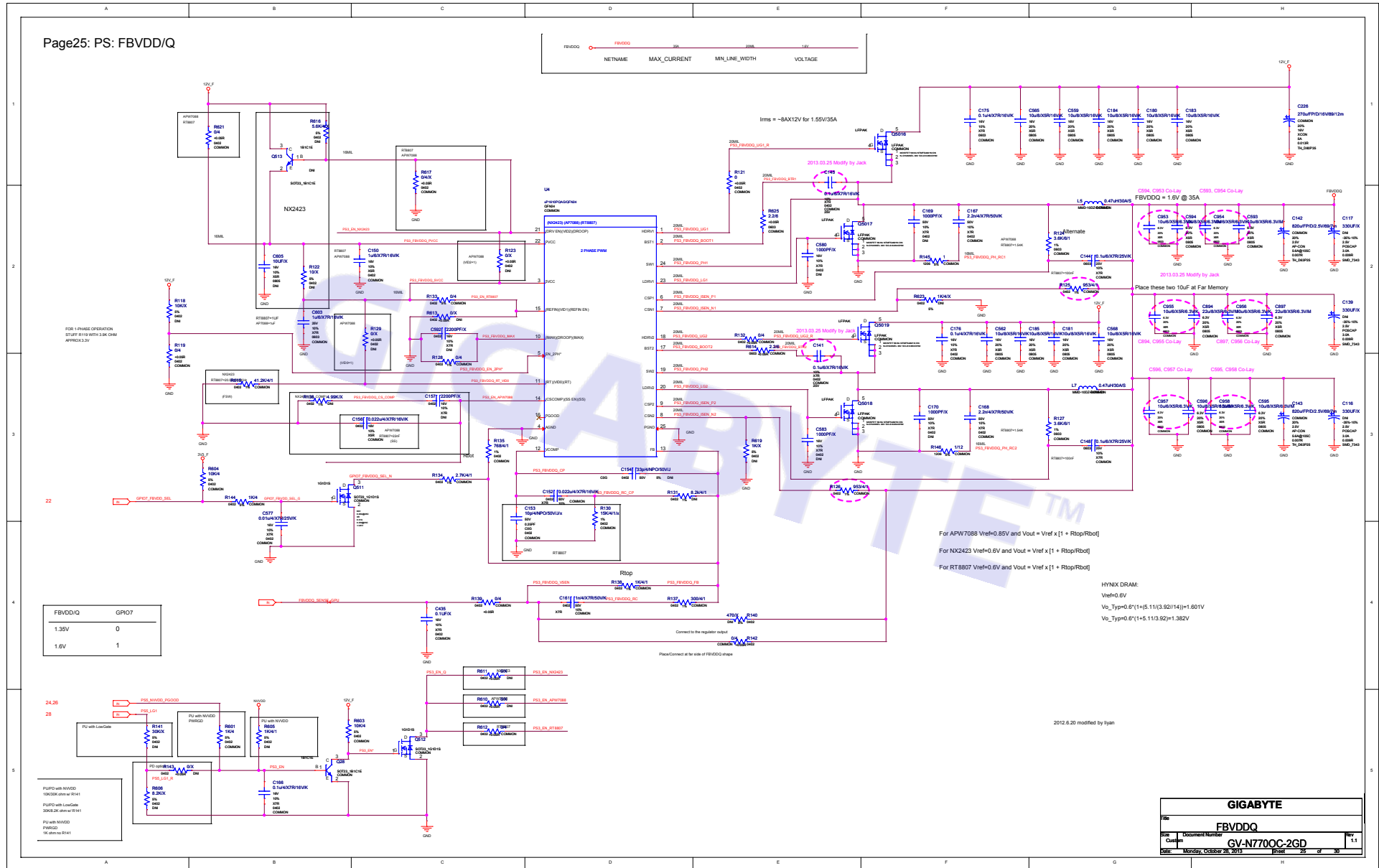
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File	ROM Xtal Straps		
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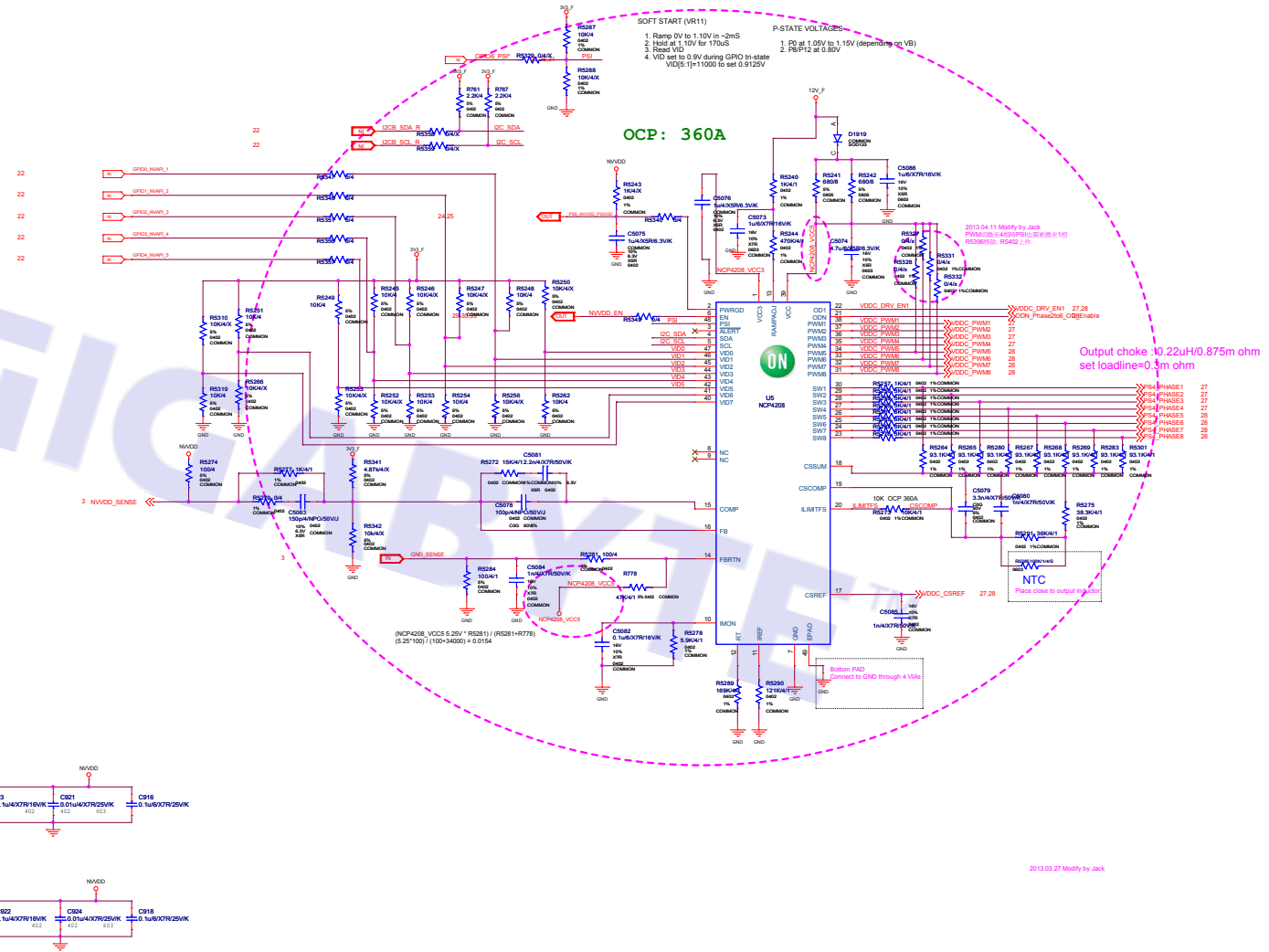


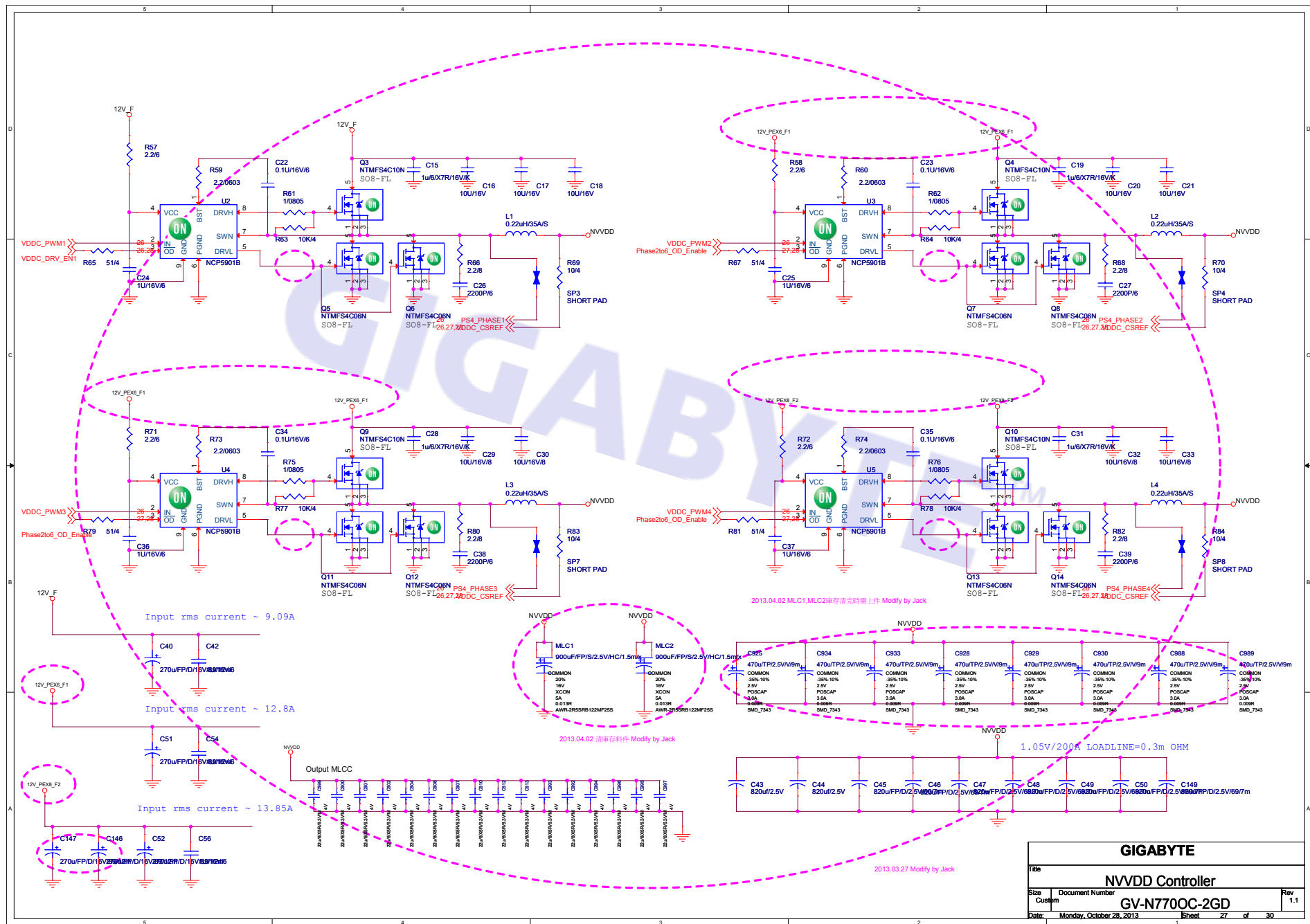
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ASSEMBLY PAGE DETAIL
BASE LEVEL GENERIC SCHEMATIC ONLY
PS: 5V, PEX_VDD

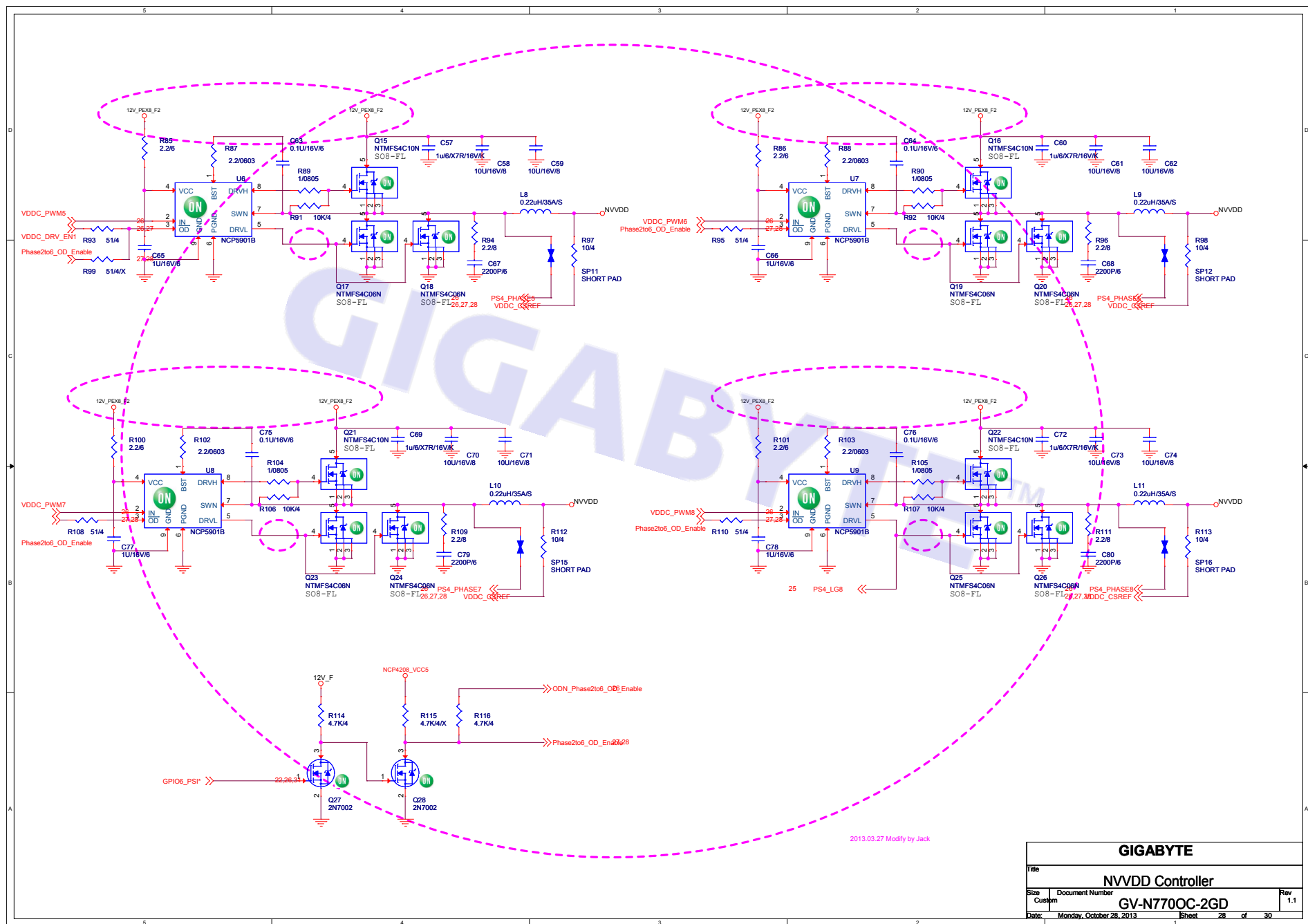
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Rev	Document Number	Rev	1.1
1.0	5V PEX_VDD	1.0	1.1
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1.0	Monday, October 24, 2011	1.0	1.1

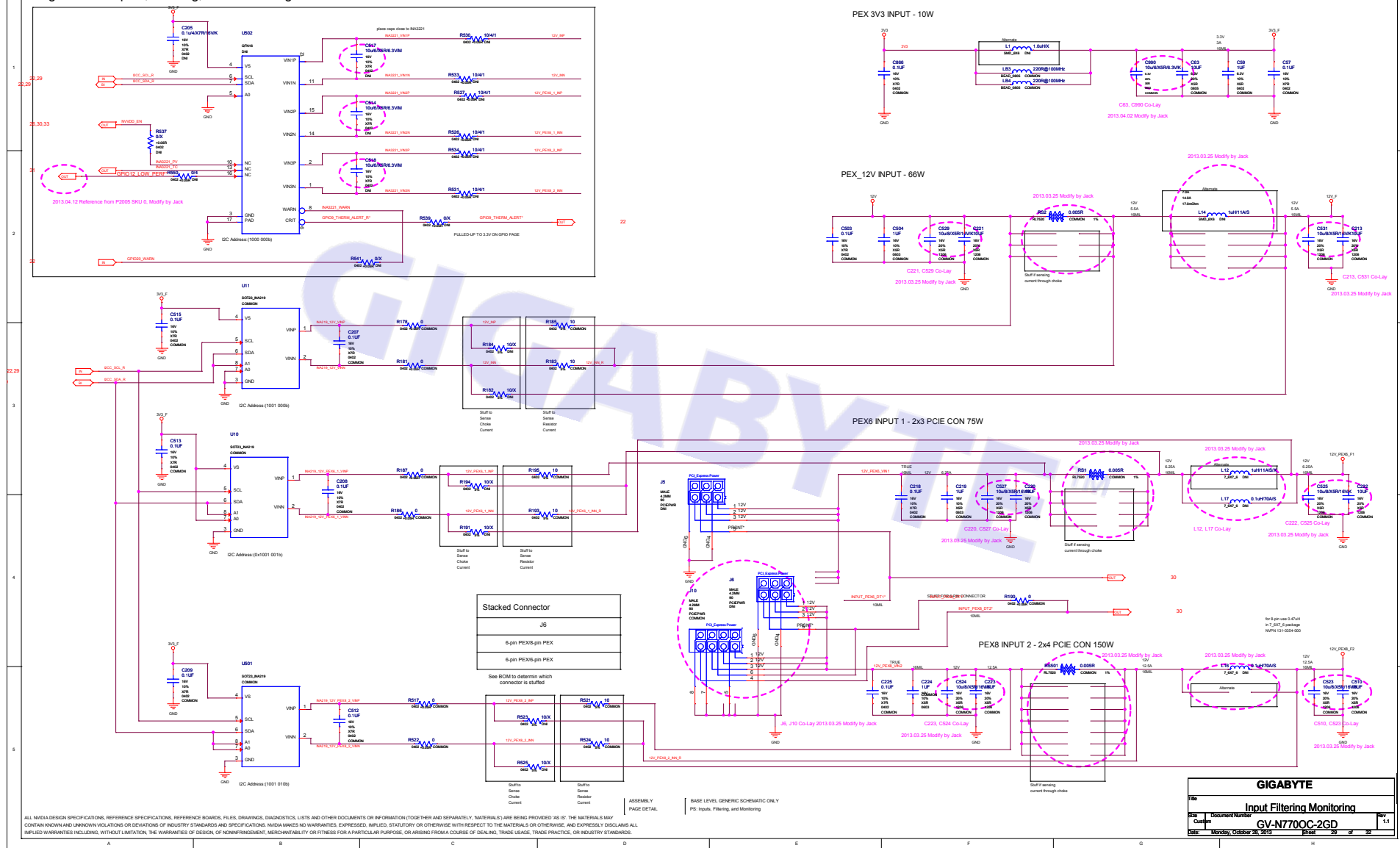






GIGABYTE			
Title			
NVDD Controller			
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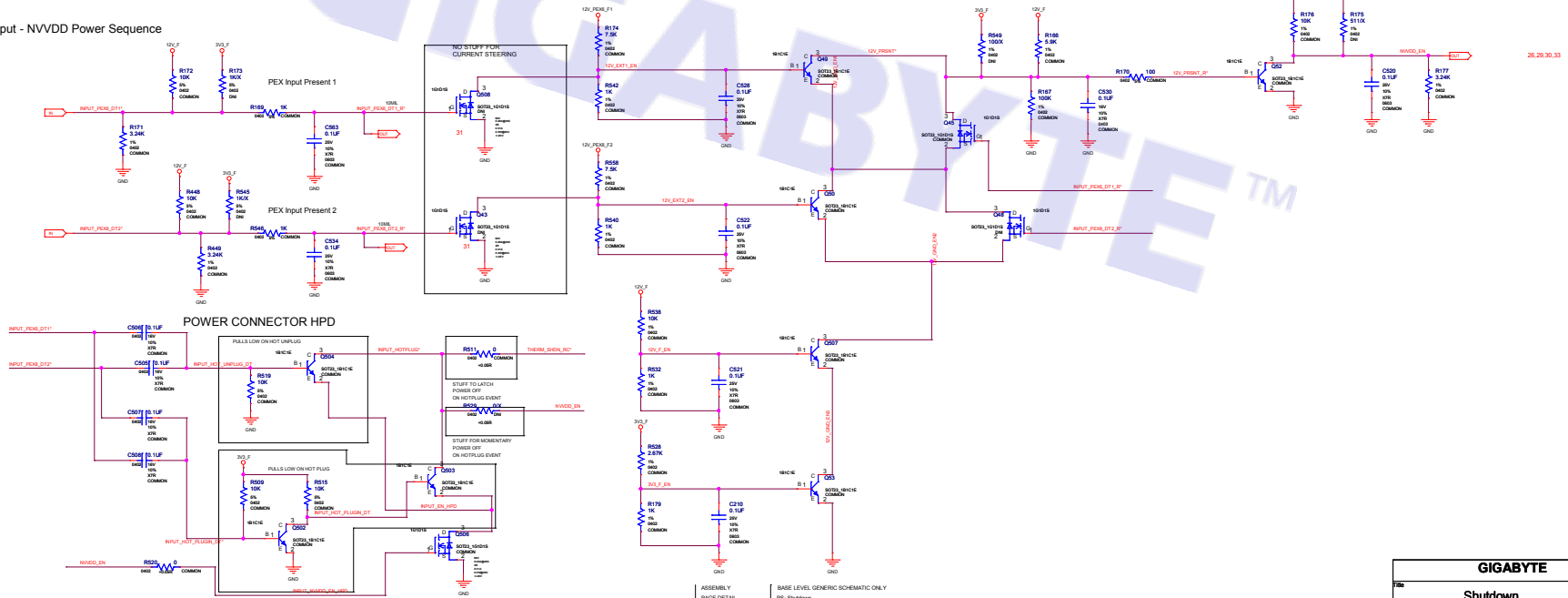


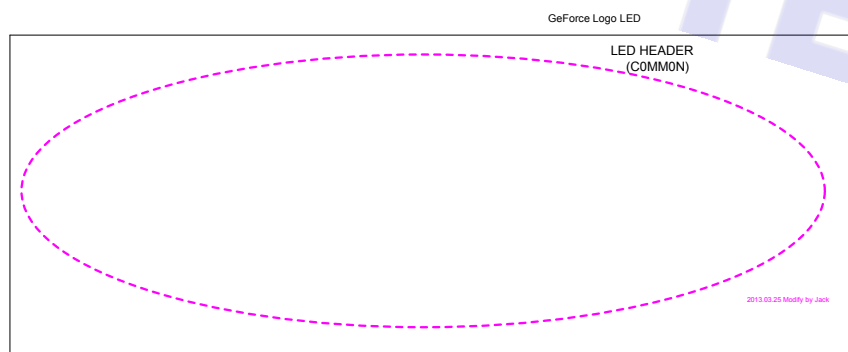
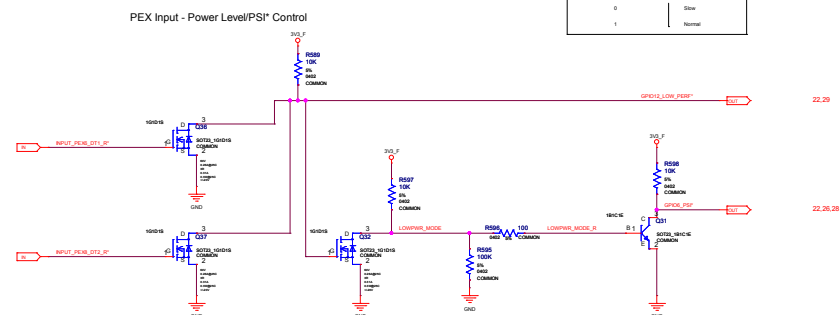
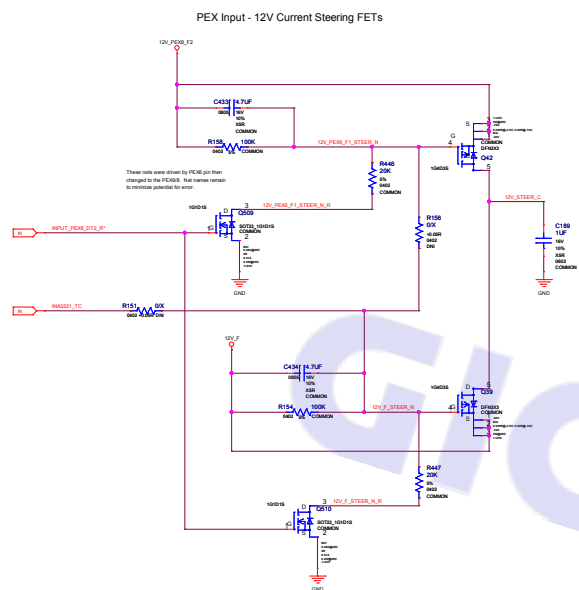


THERM OVERT SHUTDOWN LATCH

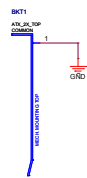
IFP_IOVDD (backdrive prevention)

PEX Input - NVVDD Power Sequence





Brackets:



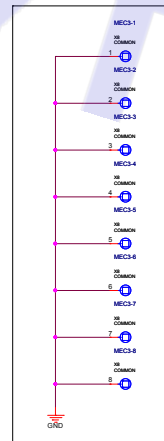
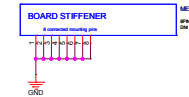
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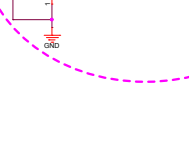
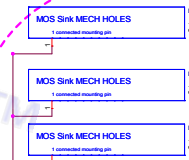
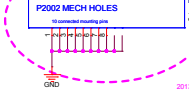
Hockey Stick



GPU Stiffener



Mechanical Holes Symbol



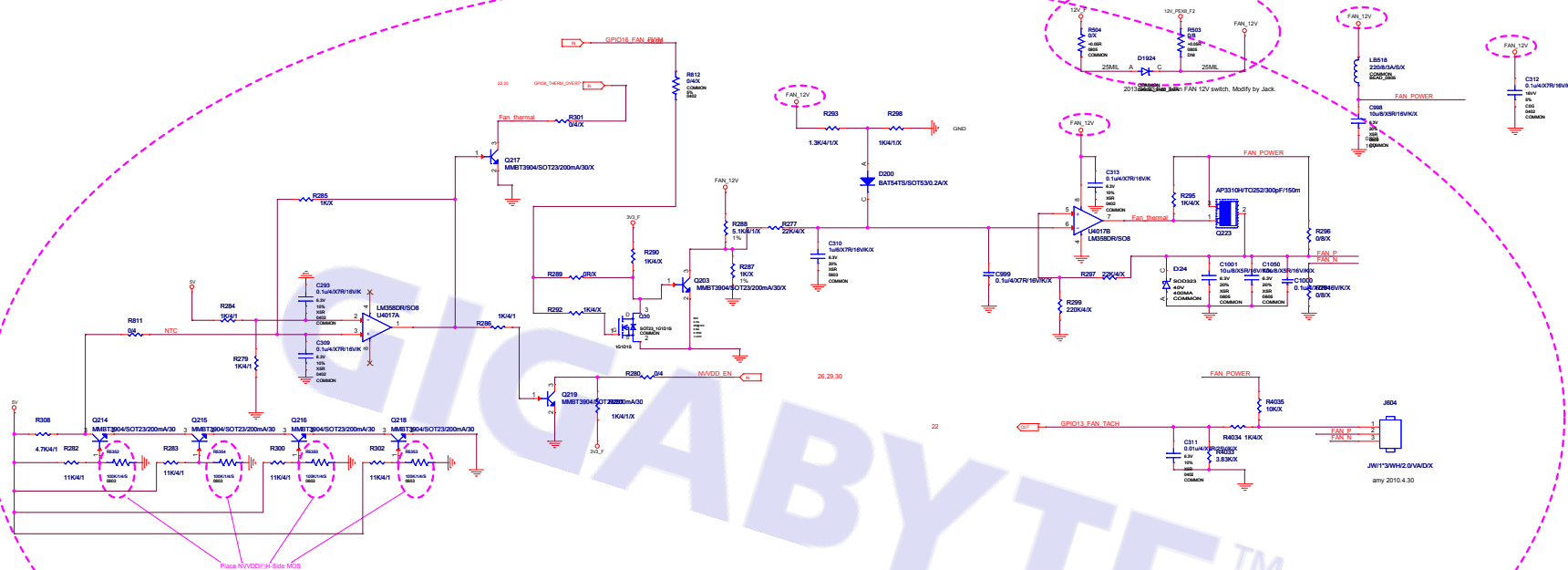
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MECH Bracket/Thermal

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ASSEMBLY
PAGE DETAIL

BASE LEVEL GENERIC SCHEMATIC ONLY
Straps, Thermal, Mechanical

GIGABYTE

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