

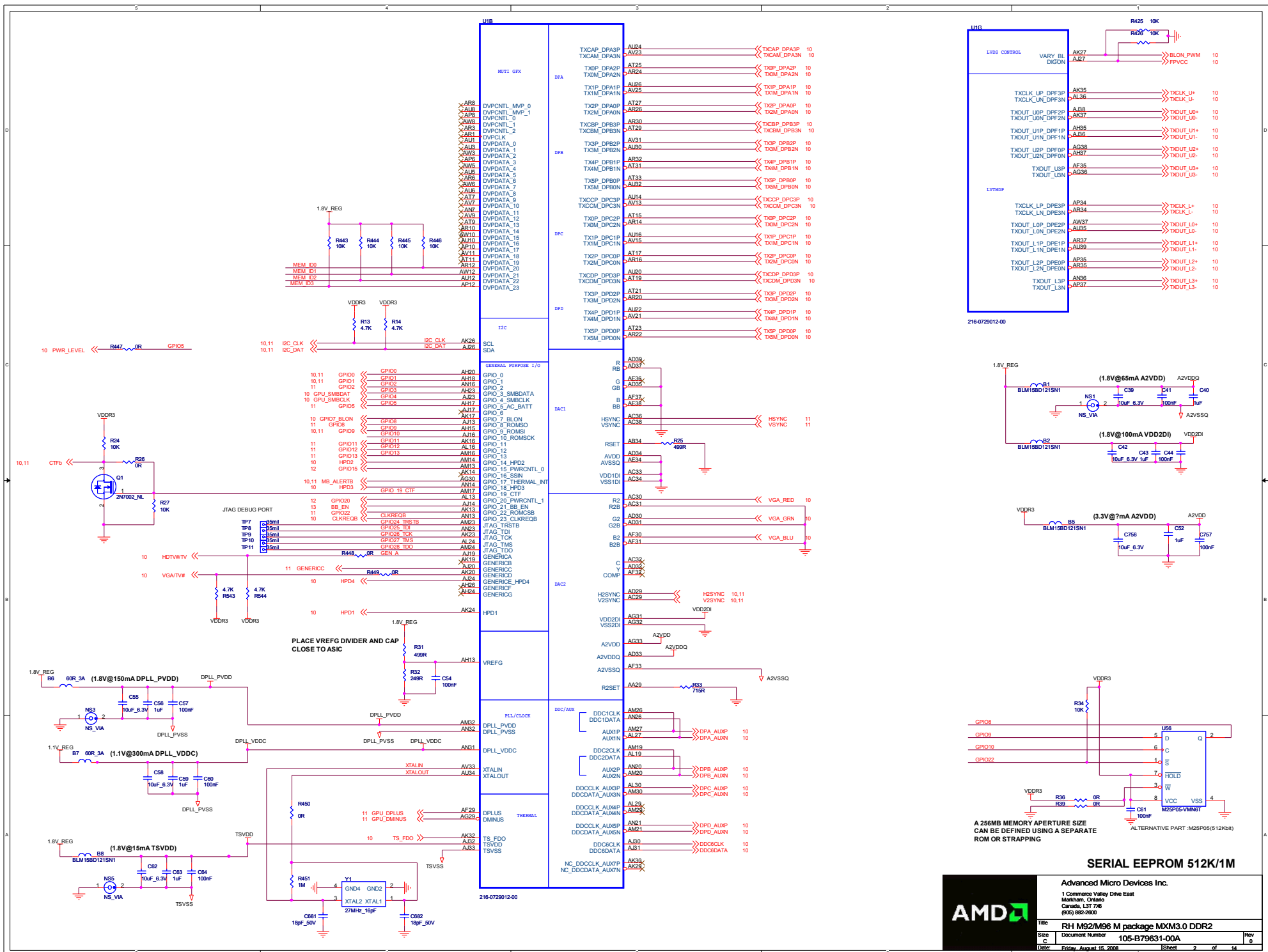
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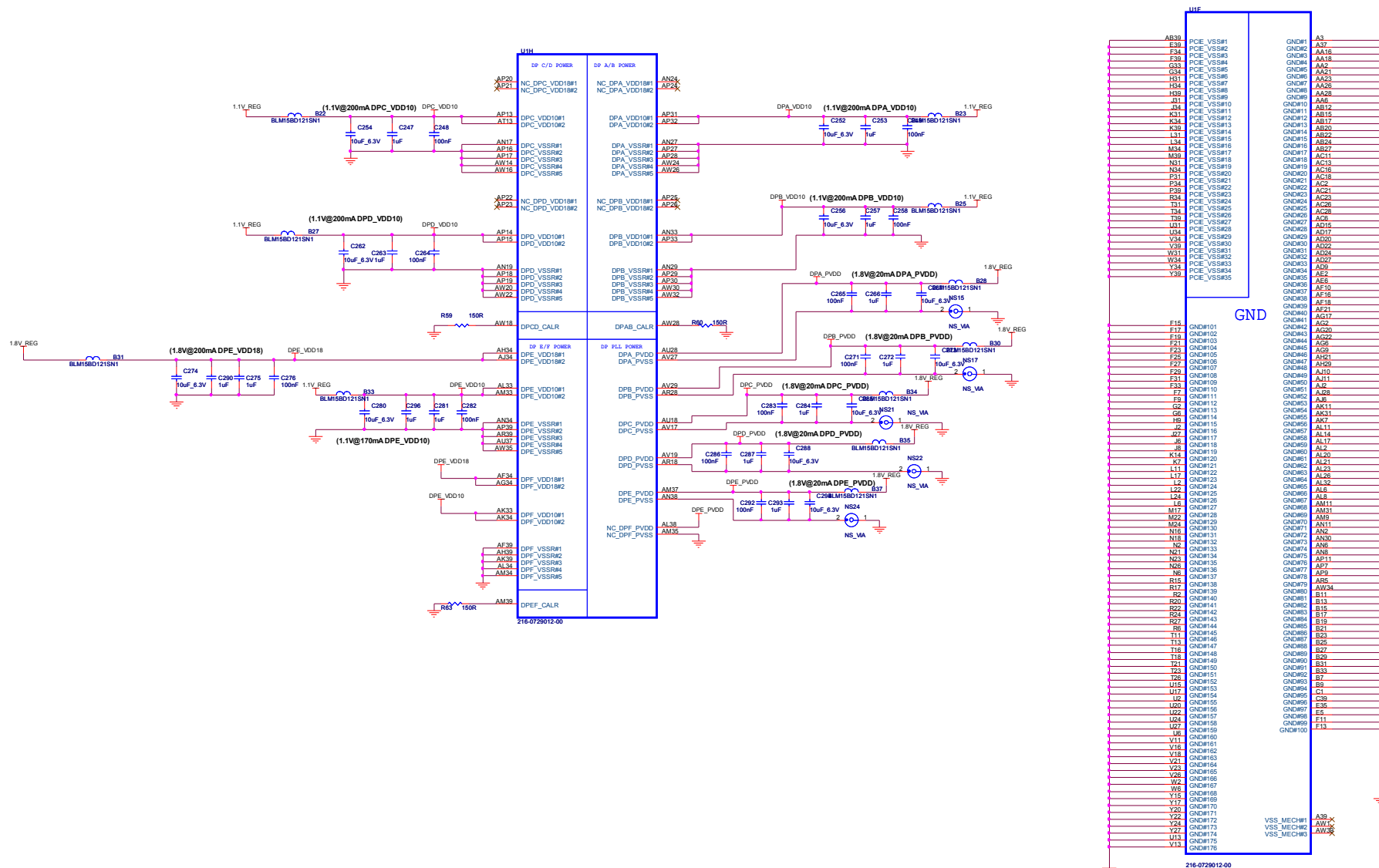
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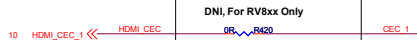


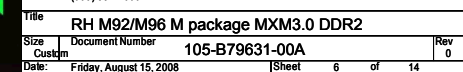
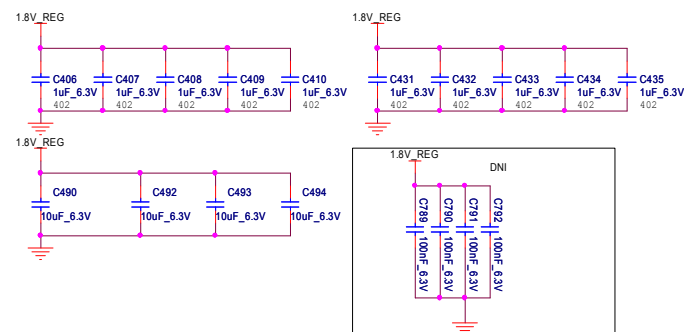
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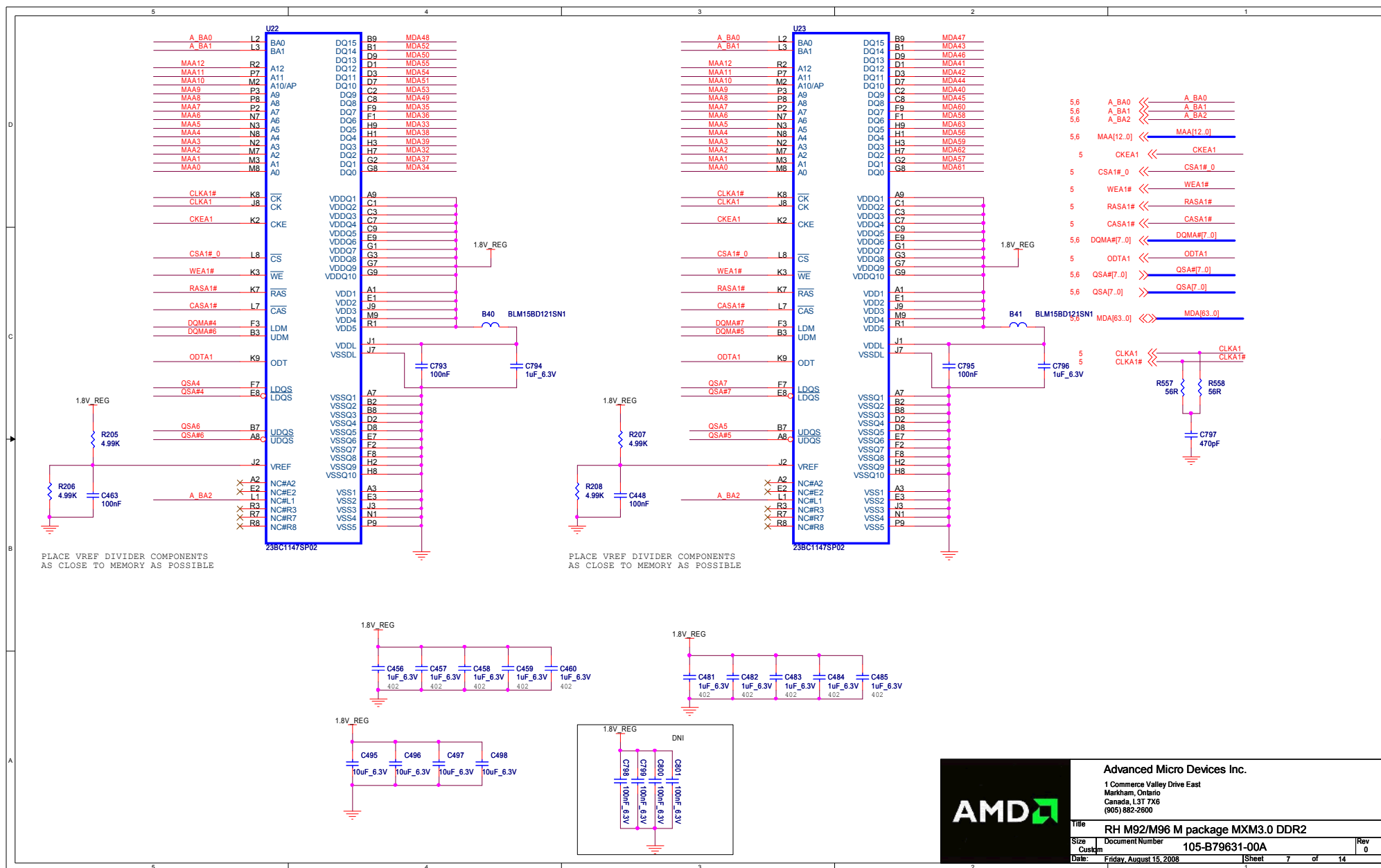
Title: RH M92/M96 M package MXM3.0 DDR2
Doc No. 105-B79631-00A

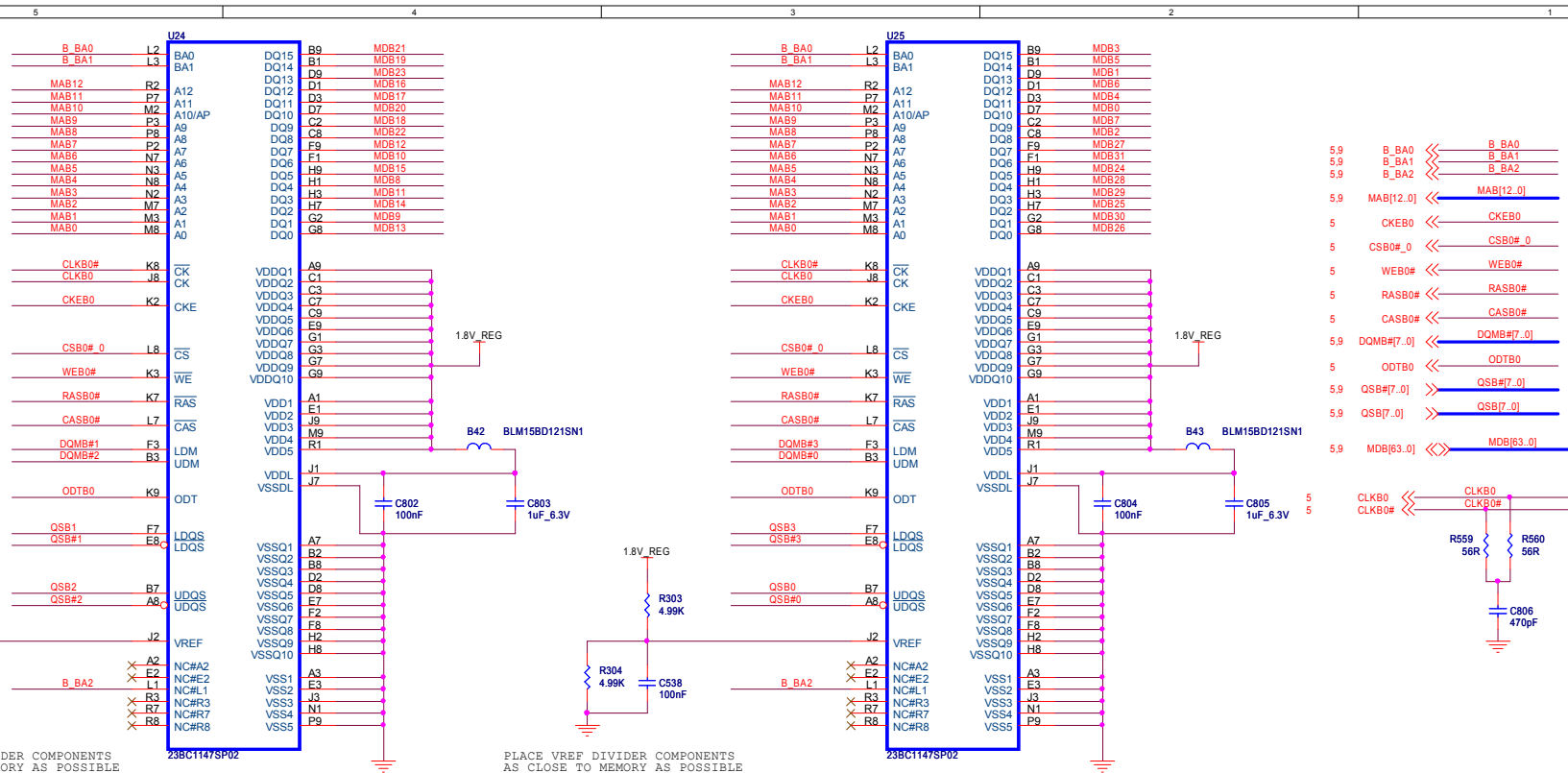






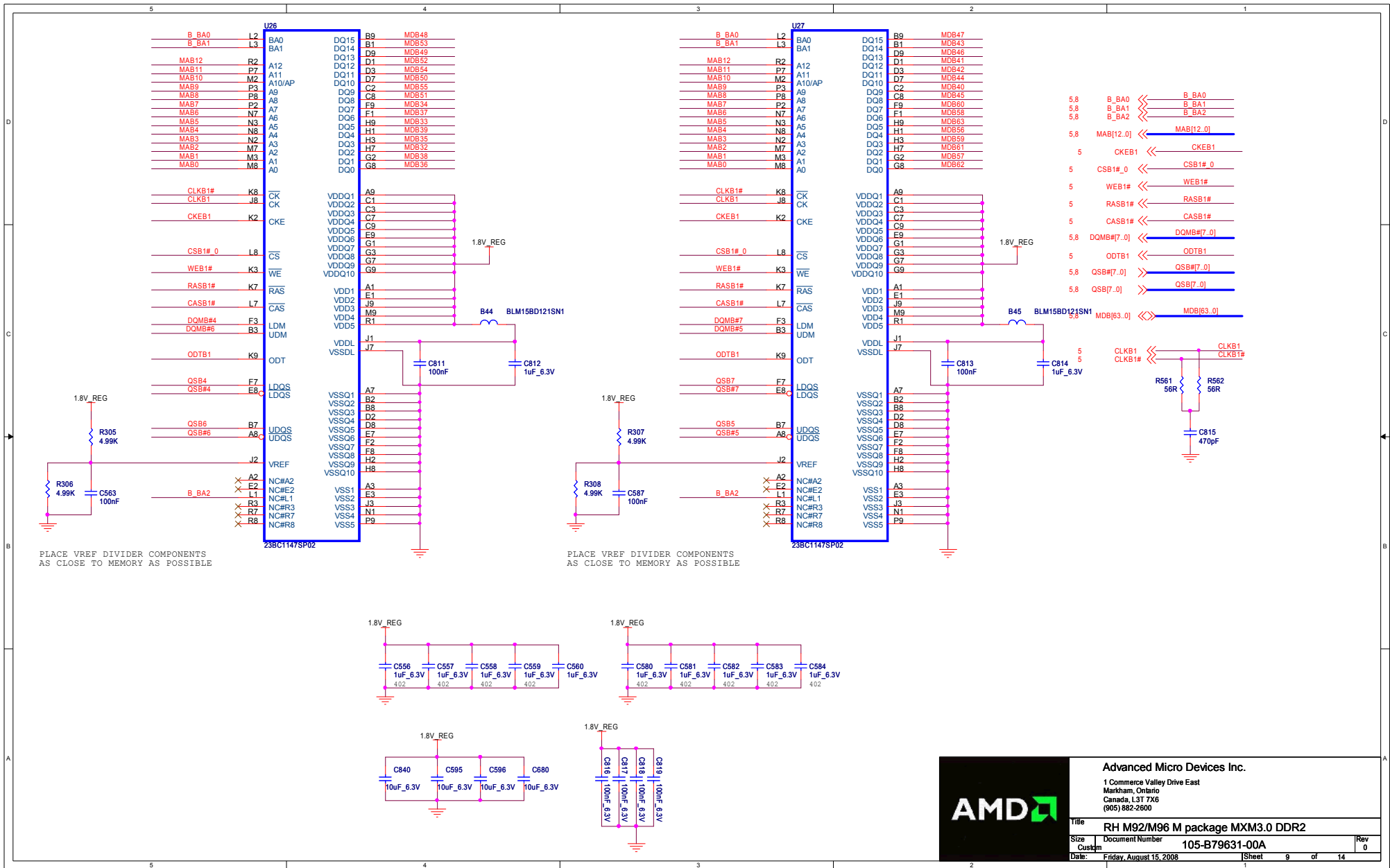






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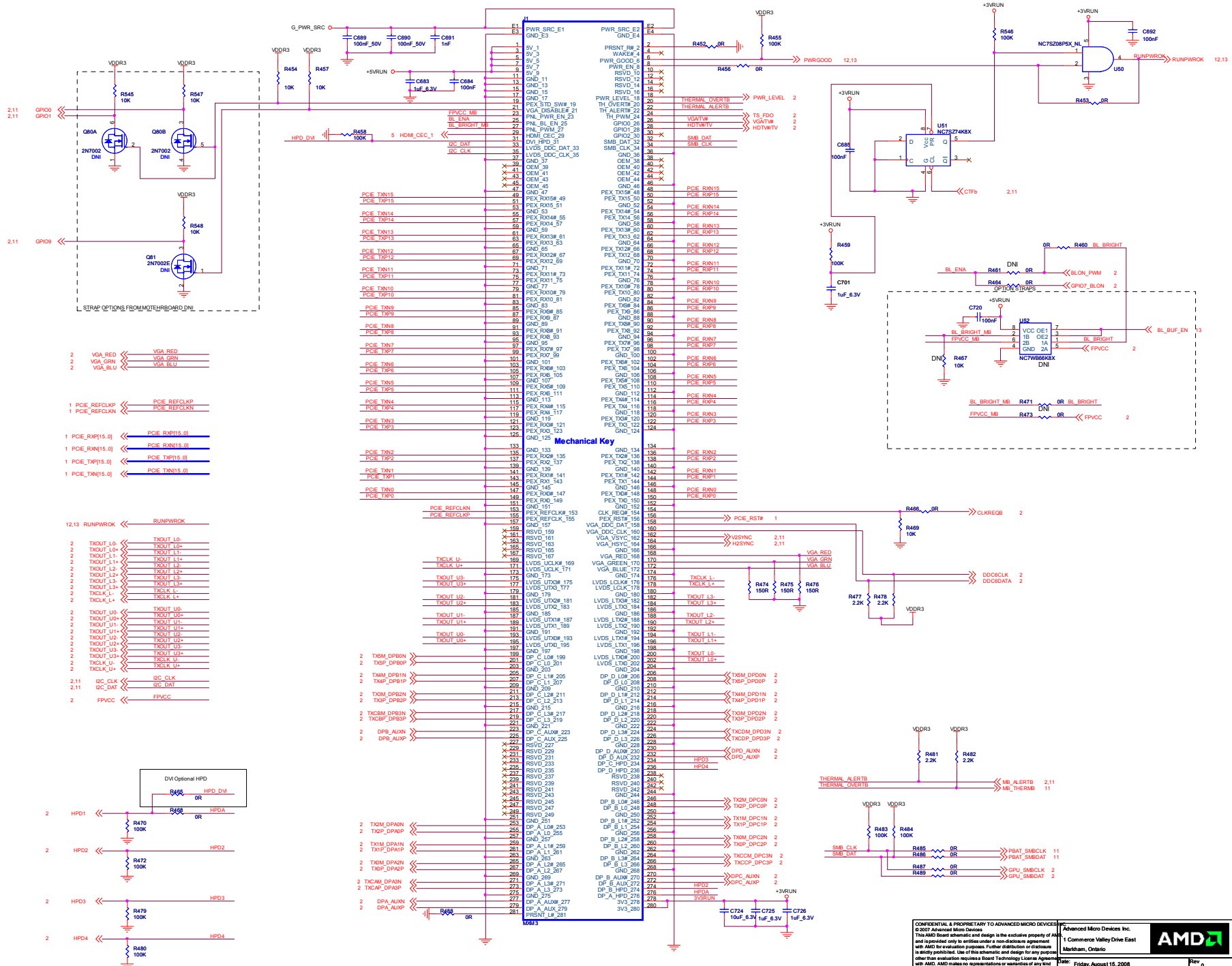
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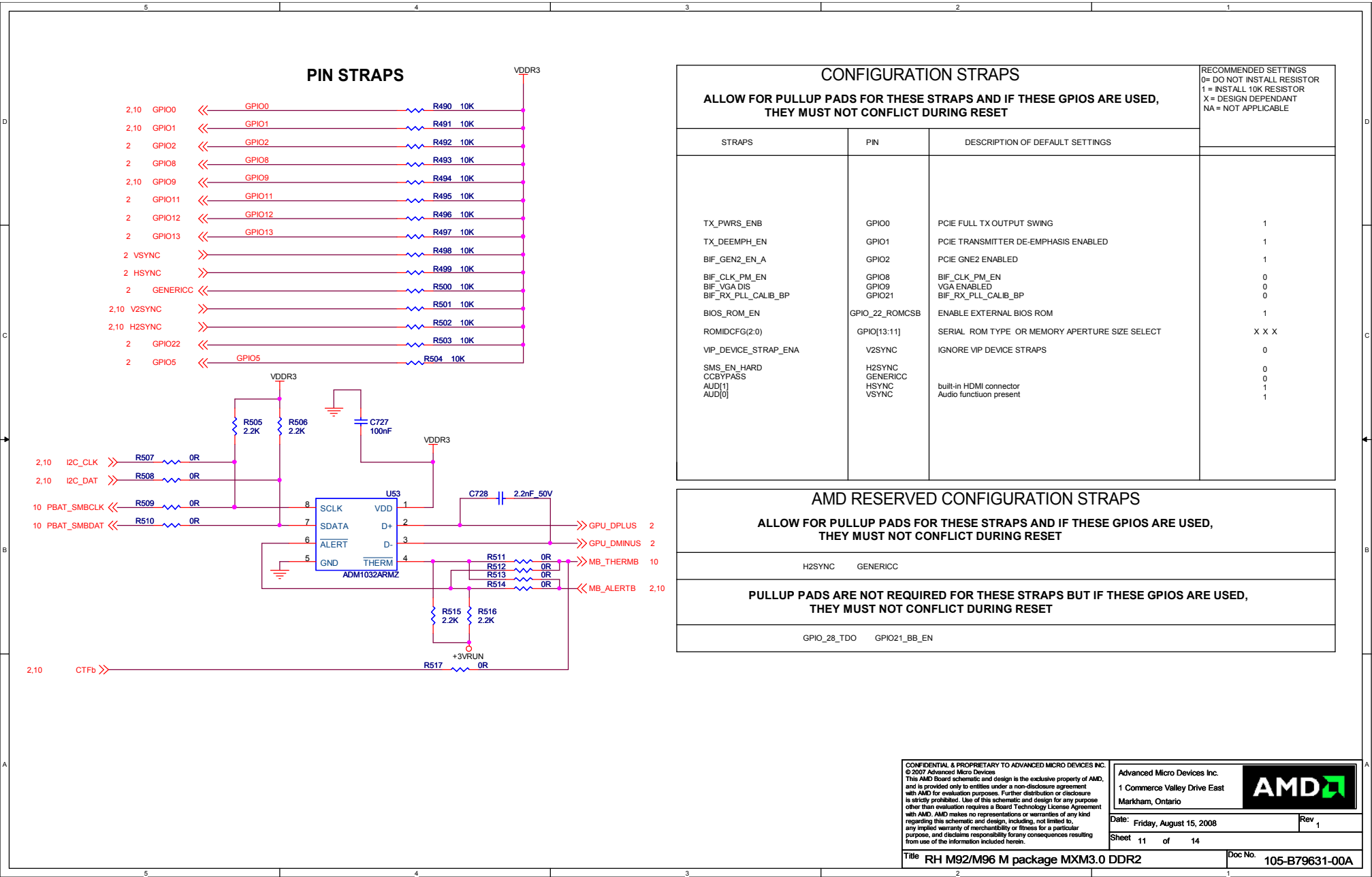
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PLACE CAPS ON THIS PAGE AS CLOSE TO CONNECTOR AS POSSIBLE

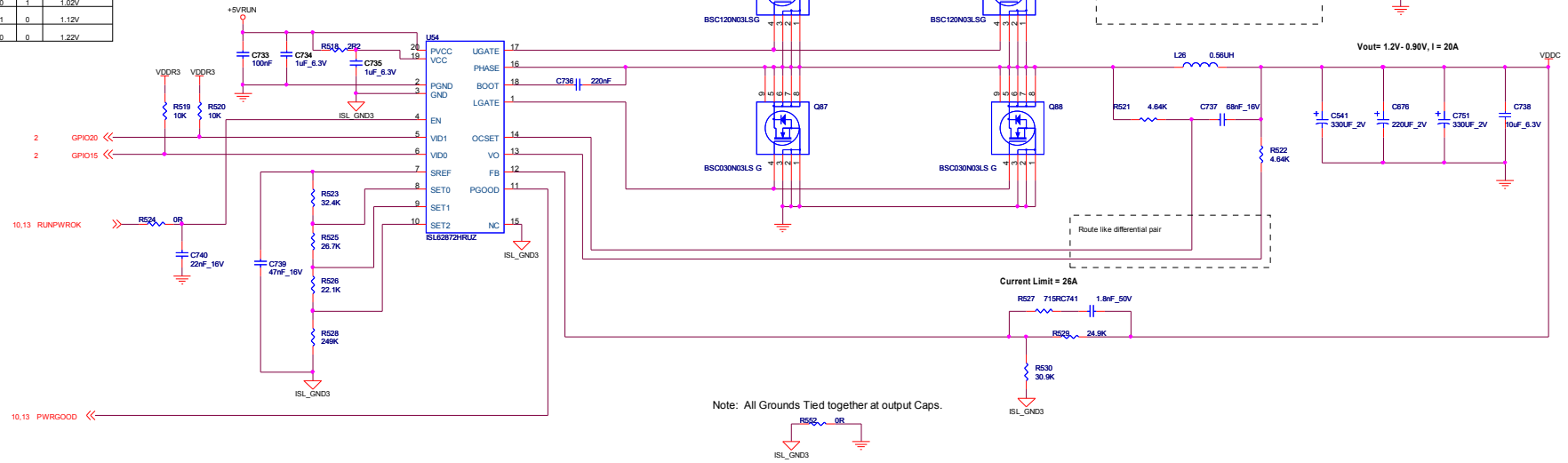


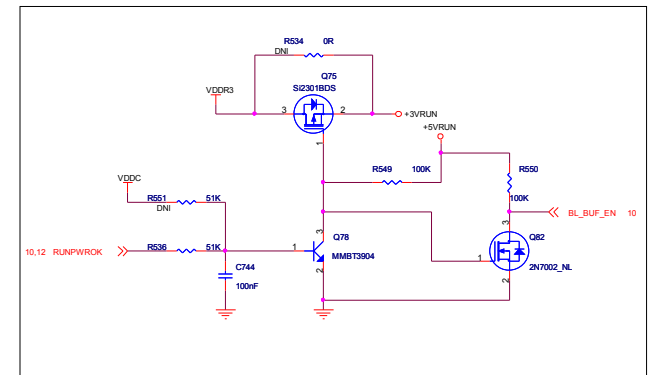
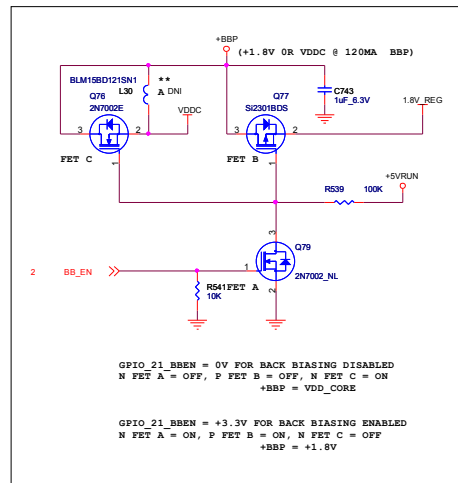
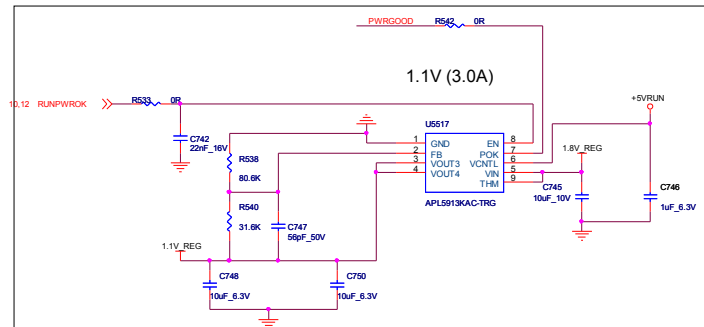
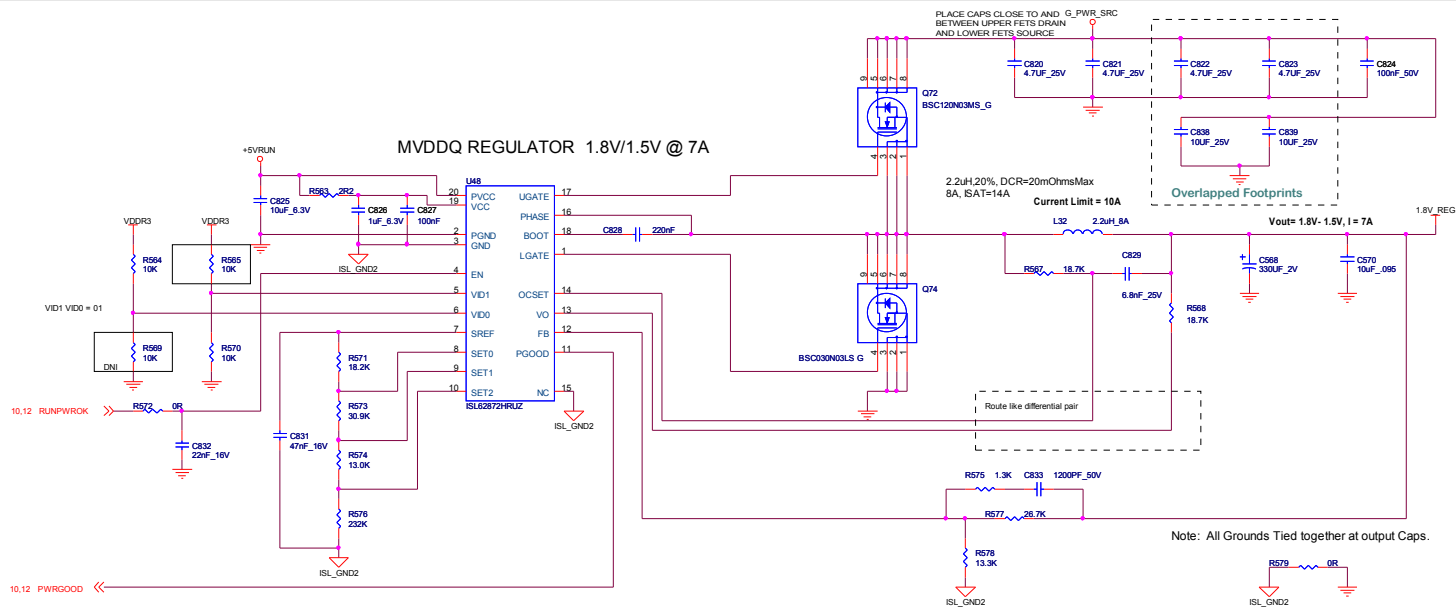
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VDDC REGULATOR 1.2V-0.9V @ 20A

VID1	VID0	VDDC
1	1	0.92V
0	1	1.02V
1	0	1.12V
0	0	1.22V





<div>AMD</div>			Title			Schematic No.		Date:	
			RH M92/M96 M package MXM3.0 DDR2			105-B79631-00A		Friday, August 15, 2008	
			REVISION HISTORY			NOTE: This schematic represents the PCB, it does not represent any specific SKU. For Stuffing options (component values, DNI's, ...) please consult the product specific BOM. Please contact AMD representative to obtain latest BOM closest to the application desired.			Rev 1
Sch Rev	PCB Rev	Date	REVISION DESCRIPTION						
0	00A	22/07/08	On base B735						