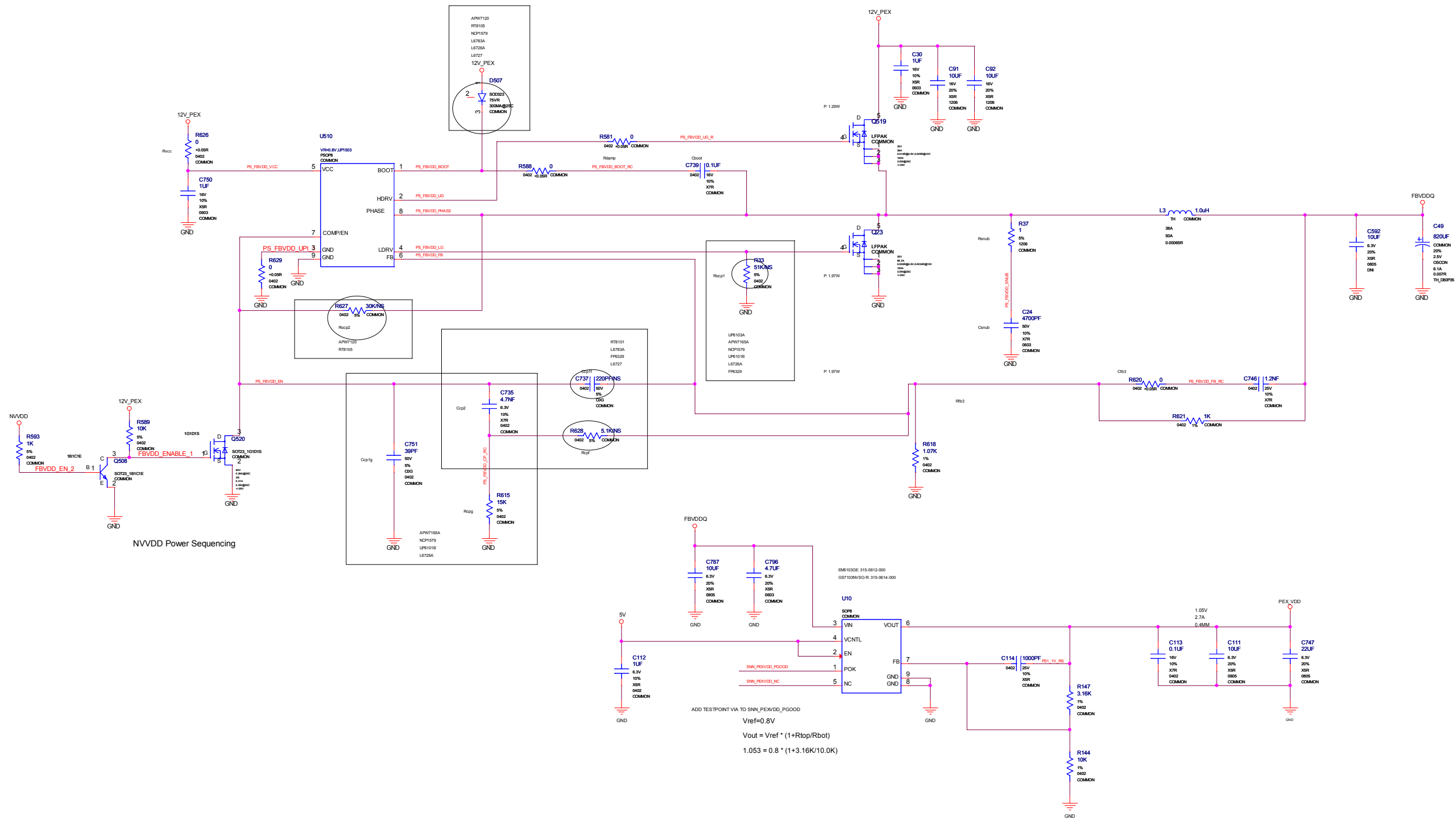
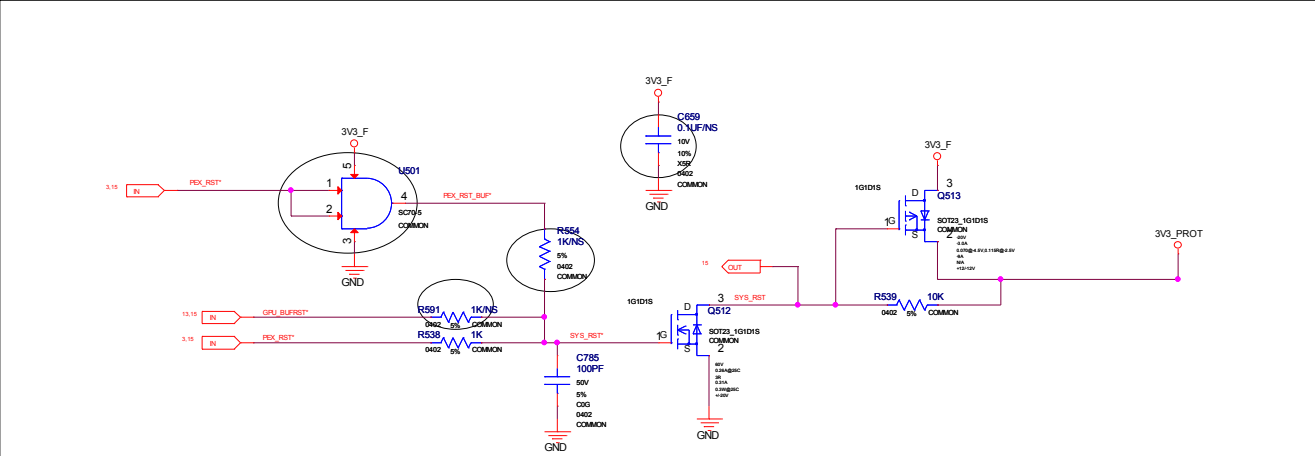
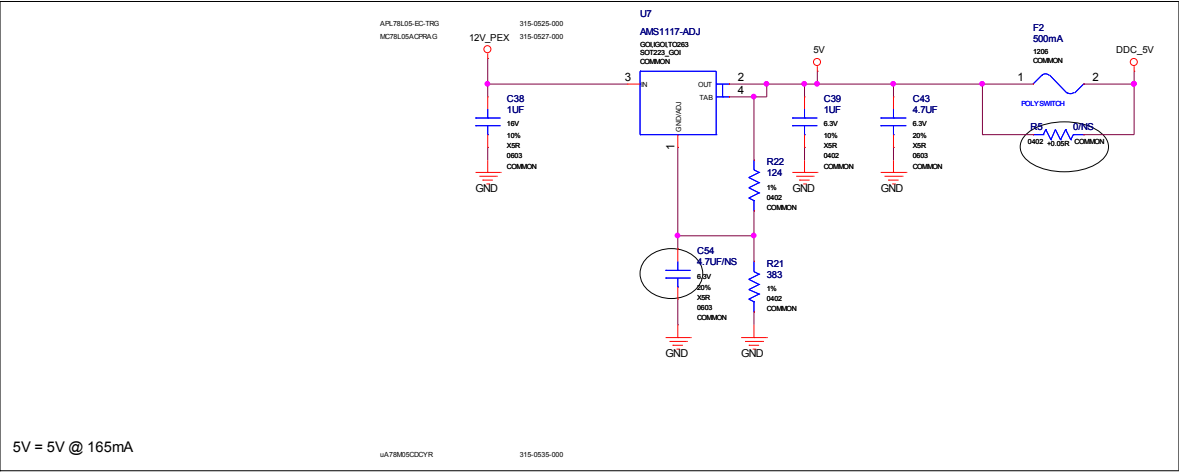
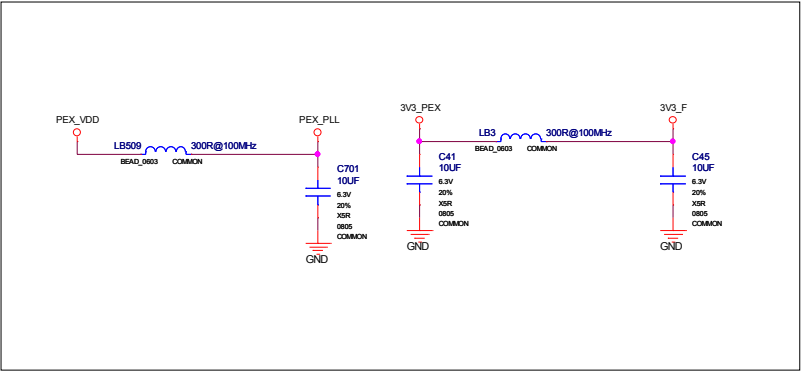
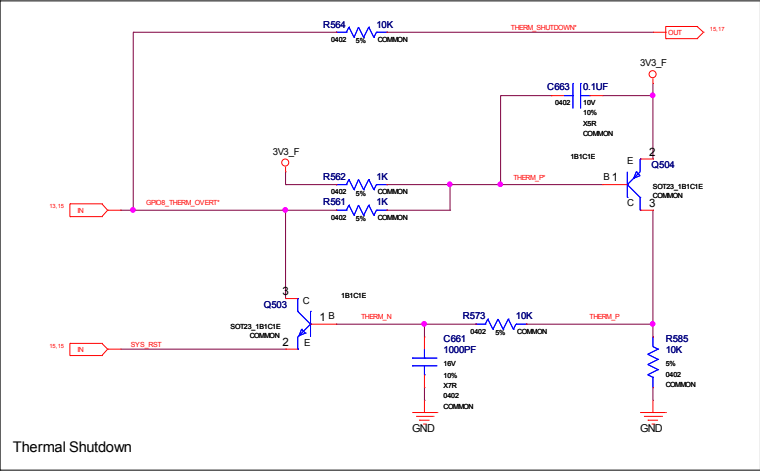


GPOQ[7:5]	N/VCO
0000	0.870V
0001	0.885V
0010	0.900V
0011	0.915V
0100	0.930V
0101	0.945V
0110	0.960V
0111	0.975V
1000	0.990V
1001	1.005V
1010	1.020V
1011	1.035V
1100	1.050V
1101	1.065V
1110	1.080V
1111	1.095V



Power Supply I: Misc Power, Thermal Shutdown



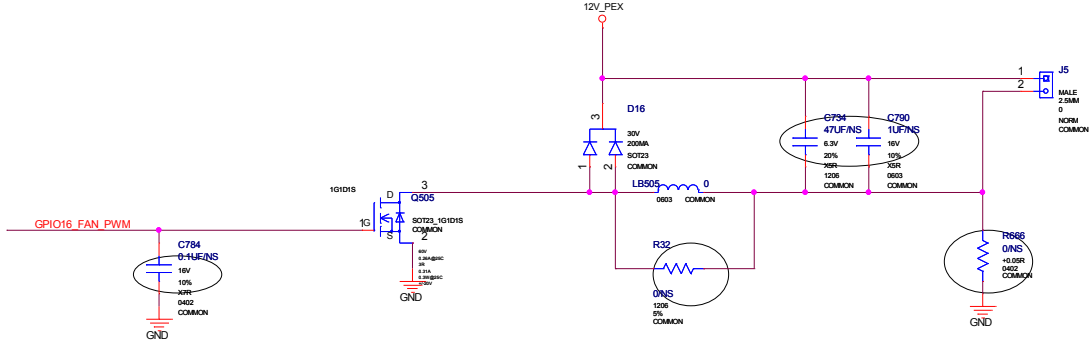
Net Name		MIN_WIDTH	VOLTAGE	MAX_CURRENT
15.17	OUT			
13.15	GPDR_THERM_OVERT			
	THERM_P			
	THERM_P			
	THERM_N			
15.15	SYS_RST			
	SYS_RST			
3.15	PEX_RST			
13.15	GPU_BUFRES1			
	PEX_RST_BLP			
	PS_EV_BACKDRIVE	120ML		
	PS_EV_PROT	120ML		
	PS_RESERVED1	120ML		
	PS_RESERVED2	120ML		
	PEX_PLL	120ML	1.00V	0.350A
	3V3_F	120ML	3.3V	3A
	3V3_PROT	120ML	3.3V	0.300A
	5V	120ML	5V	0.165A
	DDC_5V	120ML	5V	0.165A
	PEX_VDD	150ML	1.00V	1.7A

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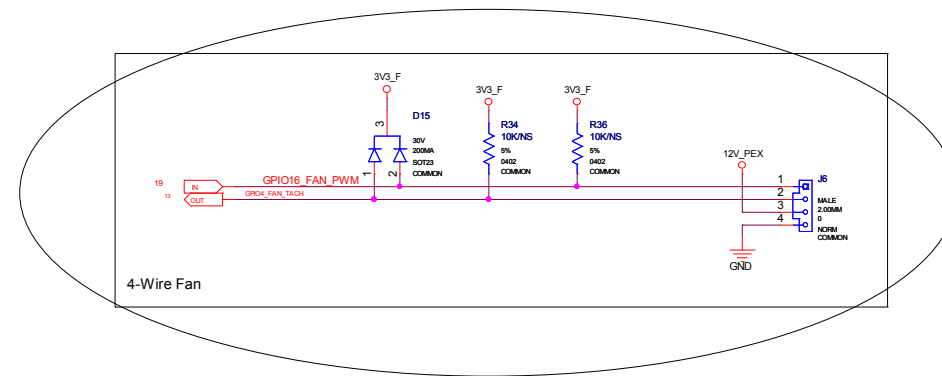
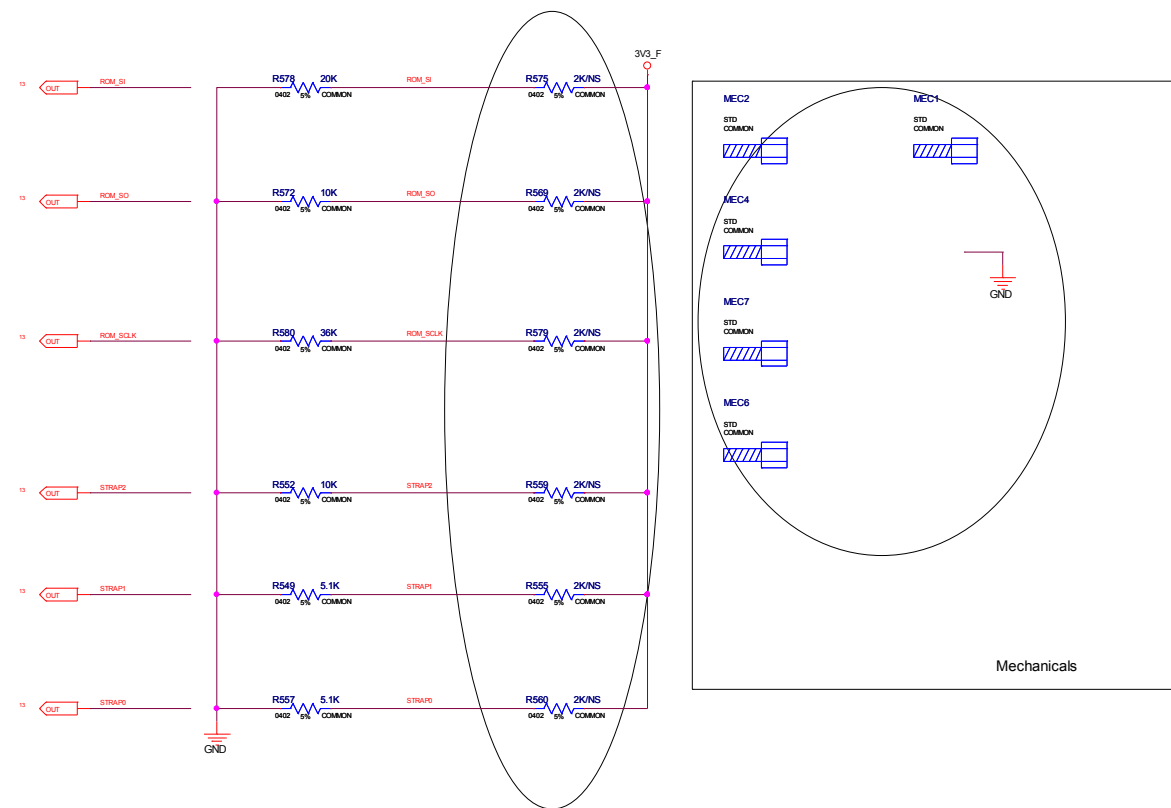
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Power Supply I: Misc Power, Thermal Shutdown

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## Straps, Thermal, Mechanical



2-Wire Fan



4-Wire Fan

Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
N	GPIO16_FAN_Q		
N	GPIO16_FAN_ADJ		
N	GPIO16_FAN_C		
N	GPIO16_FAN_L		

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Straps, Thermal, Mechanical

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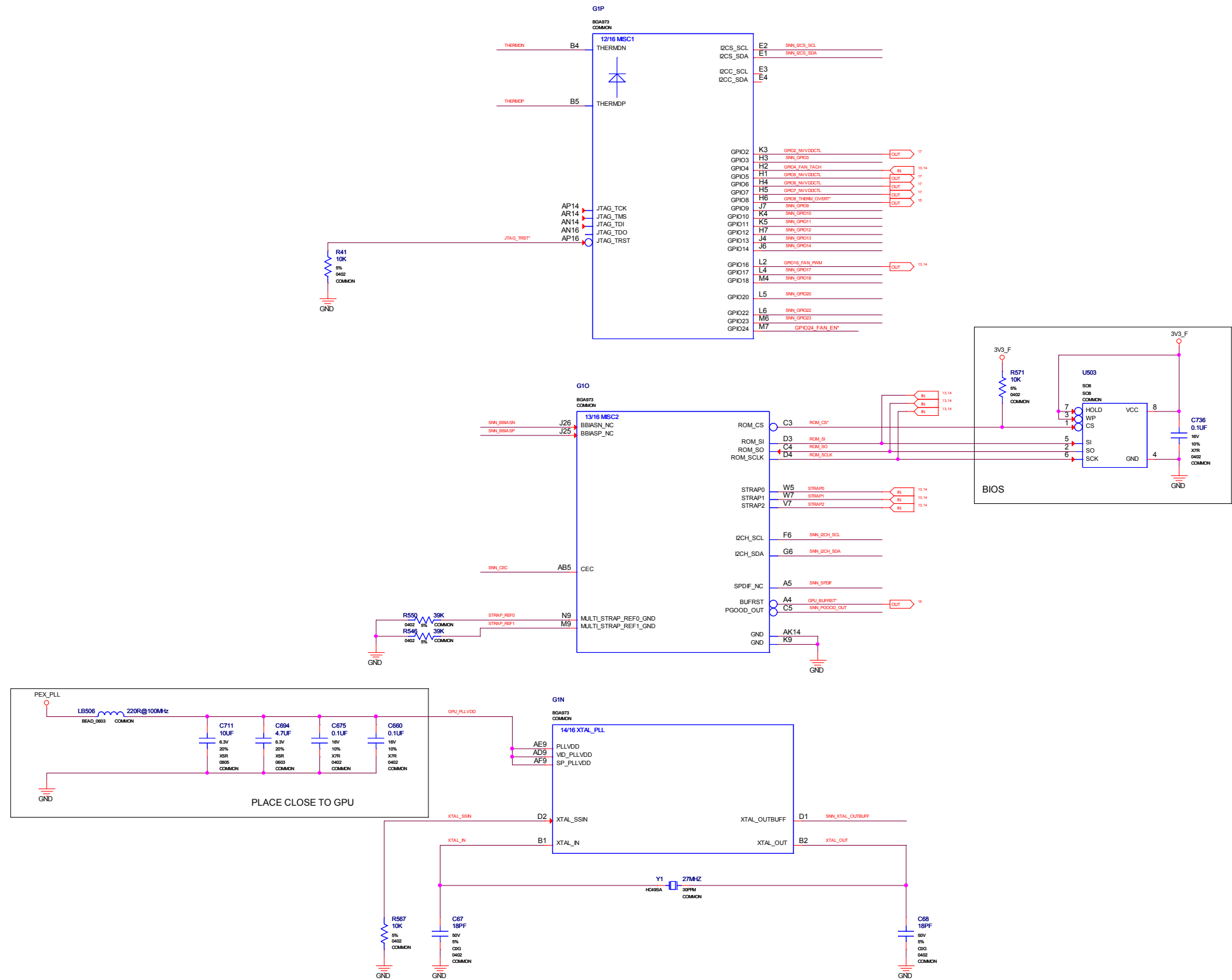
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XTAL, ROM, JTAG, Misc



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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	XTAL_ROM_JTAG_Misc

Net Name	DIFF_PAIR	CRITICAL	IMPEDANCE
XTAL_IN	XTAL	1	50OHM
XTAL_OUT	XTAL	1	50OHM
Net Name	MN_WIDTH	VOLTAGE	MAX_CURRENT
XTAL_SSIN			
THERMON	12ML		
THERMCP	12ML		
JTAG_TCK			
JTAG_TMS			
JTAG_TDI			
JTAG_TDO			
JTAG_TREY			
STRAP_REF0			
STRAP_REF1			
BCC_SCL			
BCC_SDA			
BCC_SCL_N			
BCC_SDA_N			
ROM_VCC	12ML	3.3V	
GPIO4_FAN_TACH			
GPIO4_FAN_PWM			
ROM_CS*			
ROM_SI			
ROM_SO			
ROM_SCLK			
STRAP0			
STRAP1			
STRAP2			
GPU_FLLVDD	12ML	1.05V	0.05A

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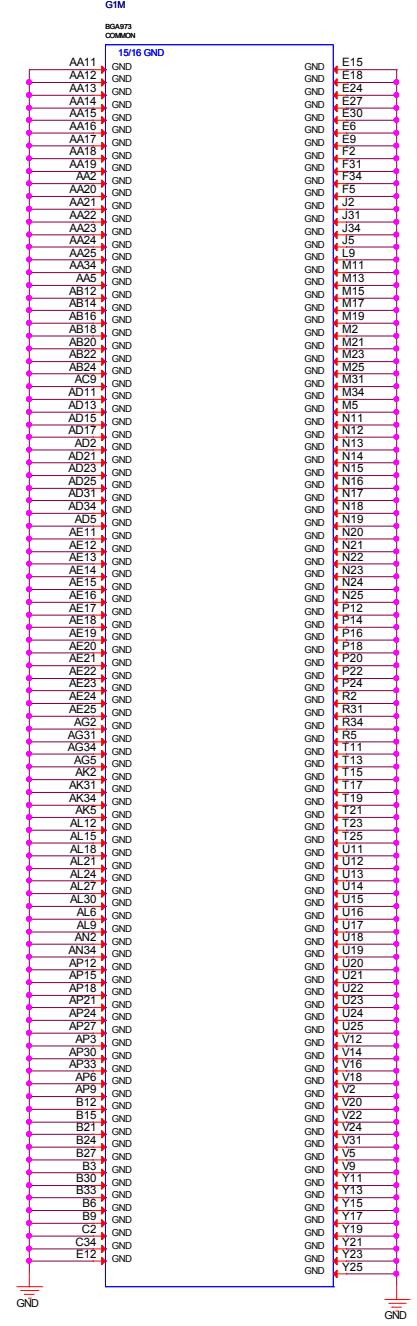
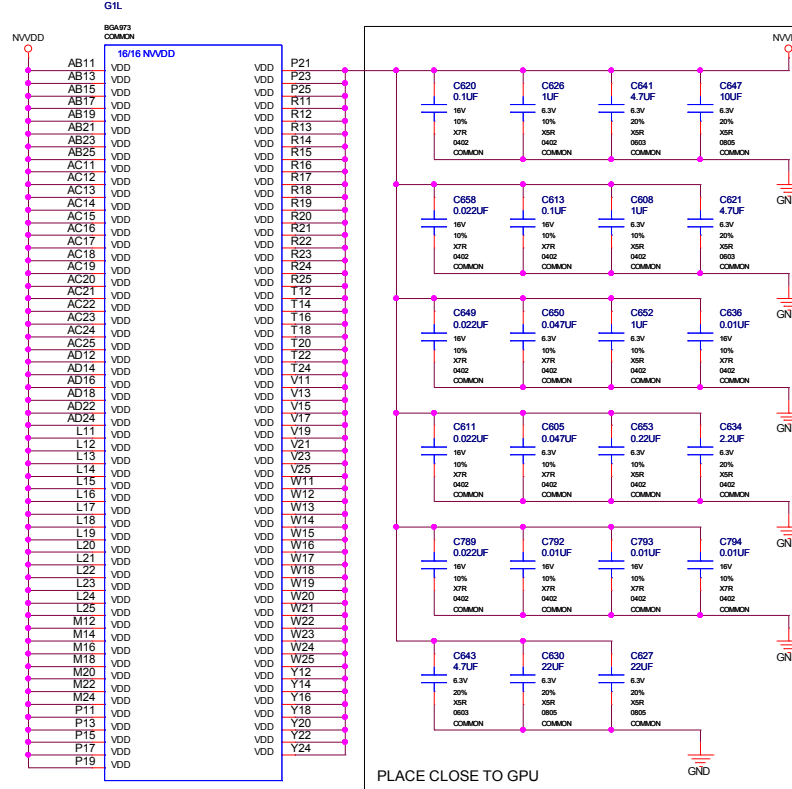
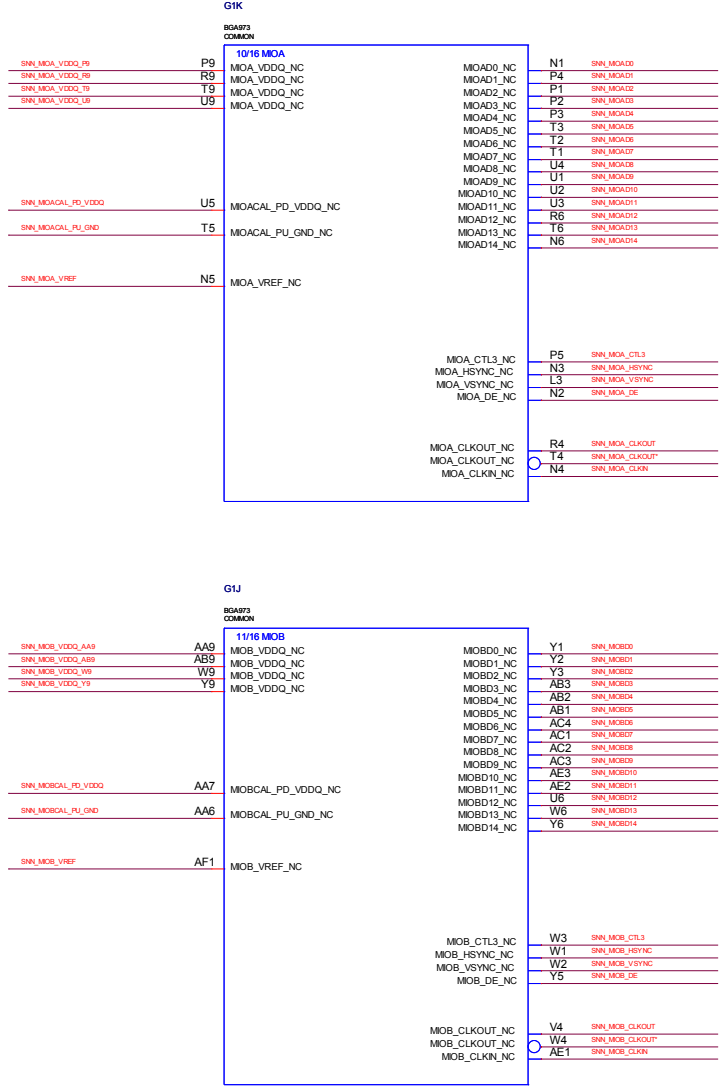
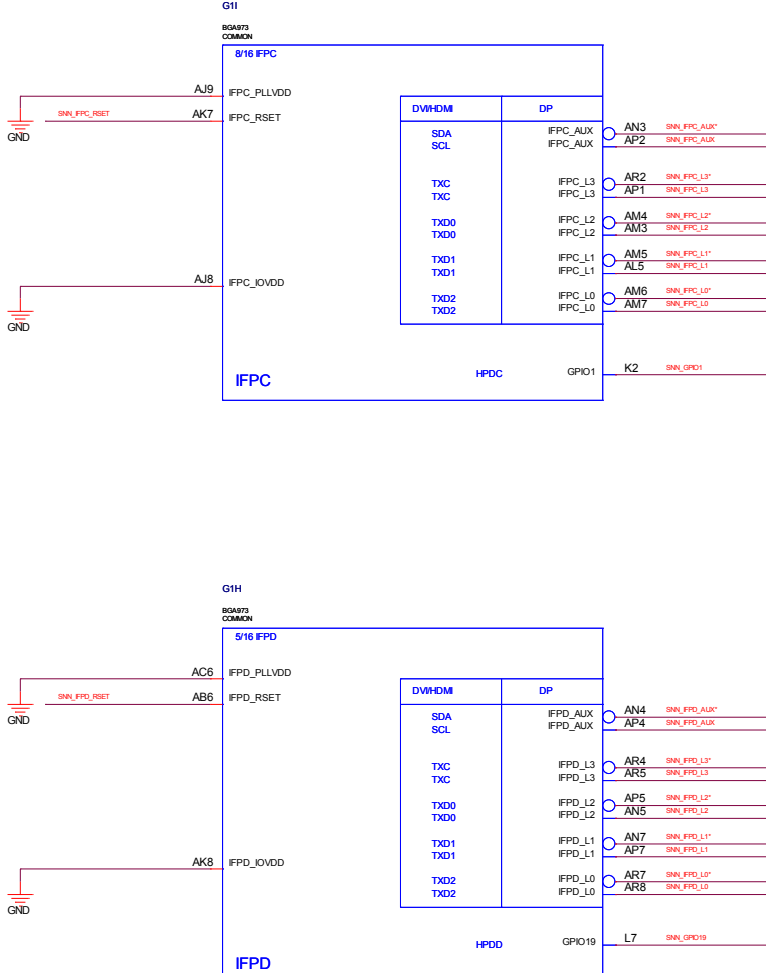
A

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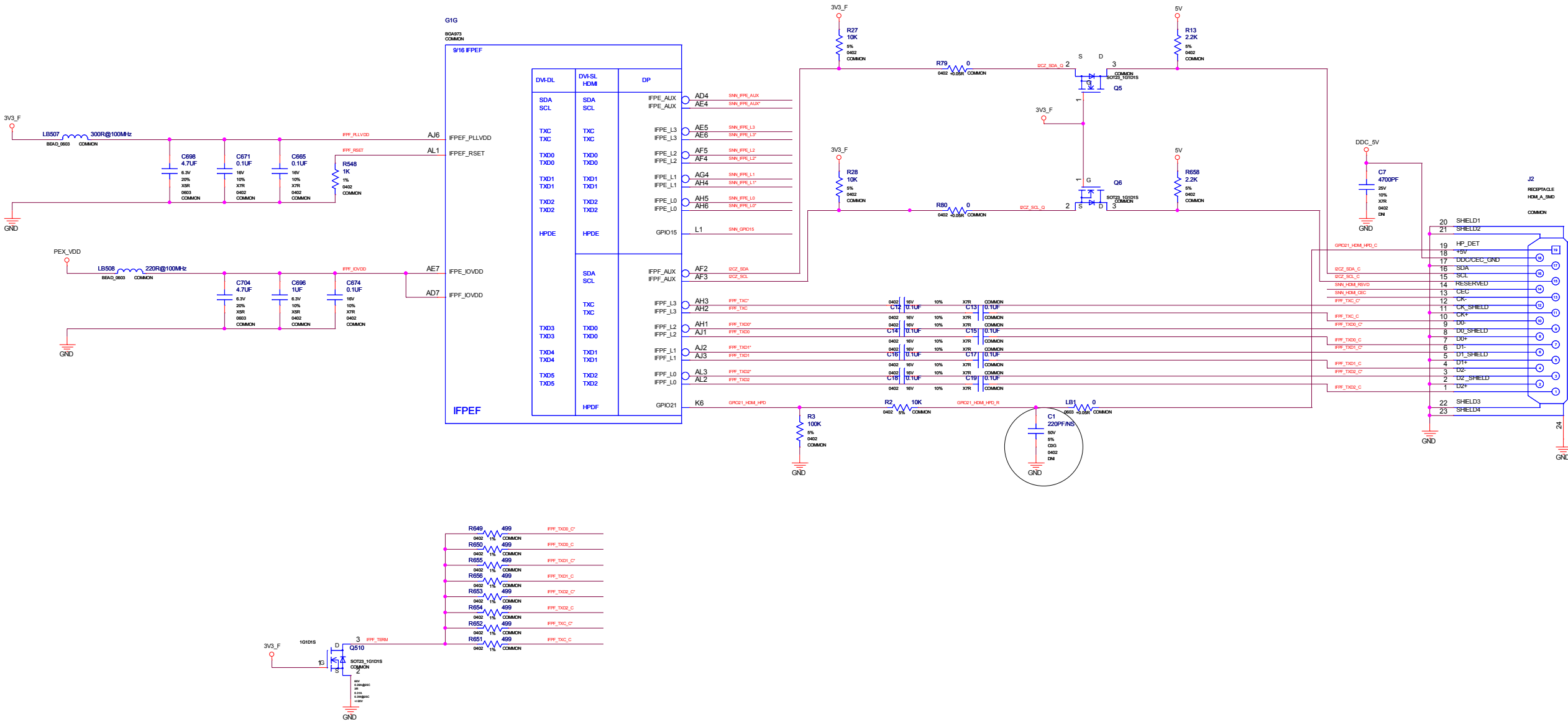
## Unused Interfaces, NVVDD Decoupling



HDMI Interface

Net Name	DIFF_PAIR	CRITICAL	IMPEDANCE
IFPE_TXD0	IFPE_TXD0	1	100Ω
IFPE_TXD0*	IFPE_TXD0	1	100Ω
IFPE_TXD1	IFPE_TXD1	1	100Ω
IFPE_TXD1*	IFPE_TXD1	1	100Ω
IFPE_TXD2	IFPE_TXD2	1	100Ω
IFPE_TXD2*	IFPE_TXD2	1	100Ω
IFPE_TXC	IFPE_TXC	1	100Ω
IFPE_TXC*	IFPE_TXC	1	100Ω
IFPE_TXD0_C	IFPE_TXD0_C	1	100Ω
IFPE_TXD0_C*	IFPE_TXD0_C	1	100Ω
IFPE_TXD1_C	IFPE_TXD1_C	1	100Ω
IFPE_TXD1_C*	IFPE_TXD1_C	1	100Ω
IFPE_TXD2_C	IFPE_TXD2_C	1	100Ω
IFPE_TXD2_C*	IFPE_TXD2_C	1	100Ω
IFPE_TXC_C	IFPE_TXC_C	1	100Ω
IFPE_TXC_C*	IFPE_TXC_C	1	100Ω
IFPE_TXD0_C	IFPE_TXD0_C	1	100Ω
IFPE_TXD0_C*	IFPE_TXD0_C	1	100Ω
IFPE_TXD1_C	IFPE_TXD1_C	1	100Ω
IFPE_TXD1_C*	IFPE_TXD1_C	1	100Ω
IFPE_TXD2_C	IFPE_TXD2_C	1	100Ω
IFPE_TXD2_C*	IFPE_TXD2_C	1	100Ω
IFPE_TXC_C	IFPE_TXC_C	1	100Ω
IFPE_TXC_C*	IFPE_TXC_C	1	100Ω

Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
DC2_SDA			
DC2_SCL			
DC2_SDA_C			
DC2_SCL_C			
GPIO21_HDM_HPD			
GPIO21_HDM_HPD_R			
GPIO21_HDM_HPD_C			
IFPE_TERM	12Ω	0V	
IFPE_RESET	12Ω		
IFPE_PLLVDD	12Ω	3.3V	0.050A
IFPE_IOVDD	12Ω	1.05V	0.100A

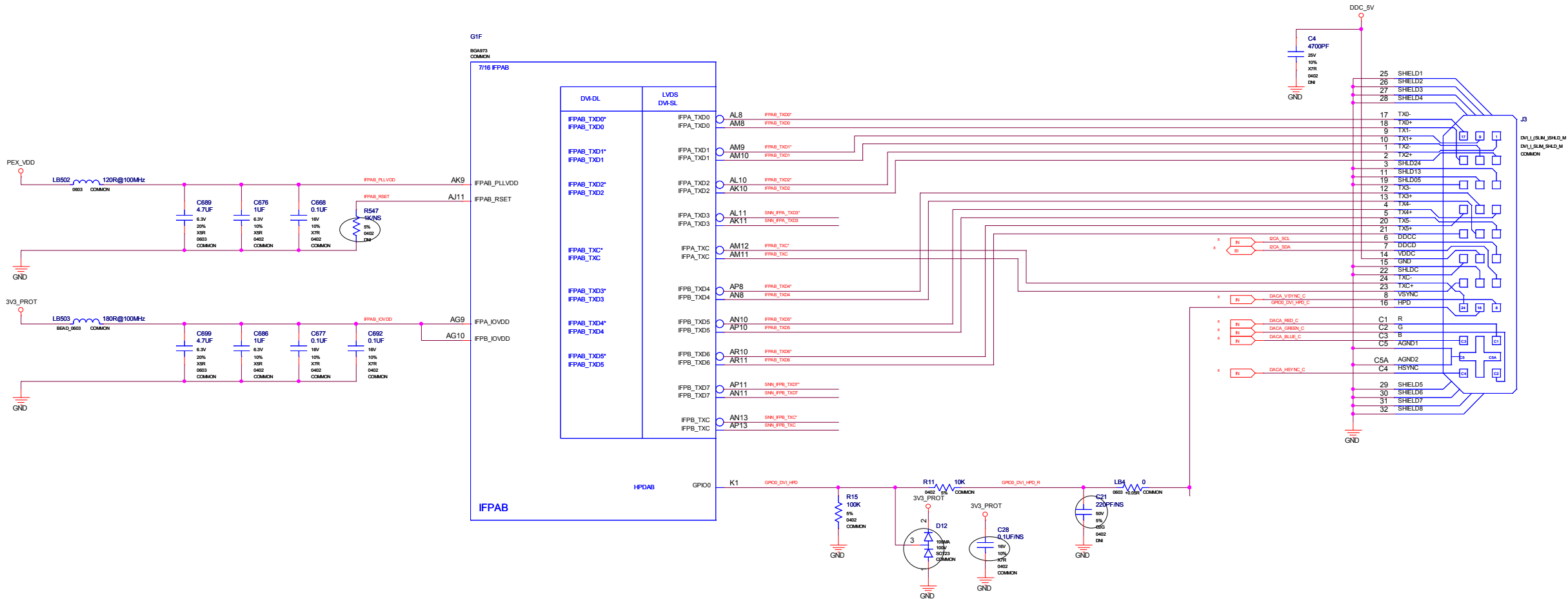


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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	HDMI Interface

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TMDS Interface



Net Name	DIFF_PAIR	CRITICAL	IMPEDANCE
IFPAB_TX00	IFPAB_TX00	1	90Ω
IFPAB_TX00*	IFPAB_TX00	1	90Ω
IFPAB_TX01	IFPAB_TX01	1	90Ω
IFPAB_TX01*	IFPAB_TX01	1	90Ω
IFPAB_TX02	IFPAB_TX02	1	90Ω
IFPAB_TX02*	IFPAB_TX02	1	90Ω
IFPAB_TX04	IFPAB_TX04	1	90Ω
IFPAB_TX04*	IFPAB_TX04	1	90Ω
IFPAB_TX05	IFPAB_TX05	1	90Ω
IFPAB_TX05*	IFPAB_TX05	1	90Ω
IFPAB_TX06	IFPAB_TX06	1	90Ω
IFPAB_TX06*	IFPAB_TX06	1	90Ω
IFPAB_TXC	IFPAB_TXC	1	90Ω
IFPAB_TXC*	IFPAB_TXC	1	90Ω

Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
GP00_DV1_HPD			
GP00_DV1_HPD_R			
GP00_DV1_HPD_C			
IFPAB_RSET	12M		
IFPAB_PLLVDD	12M	1.0V	0.130A
IFPAB_IQVDD	12M	3.3V	0.300A

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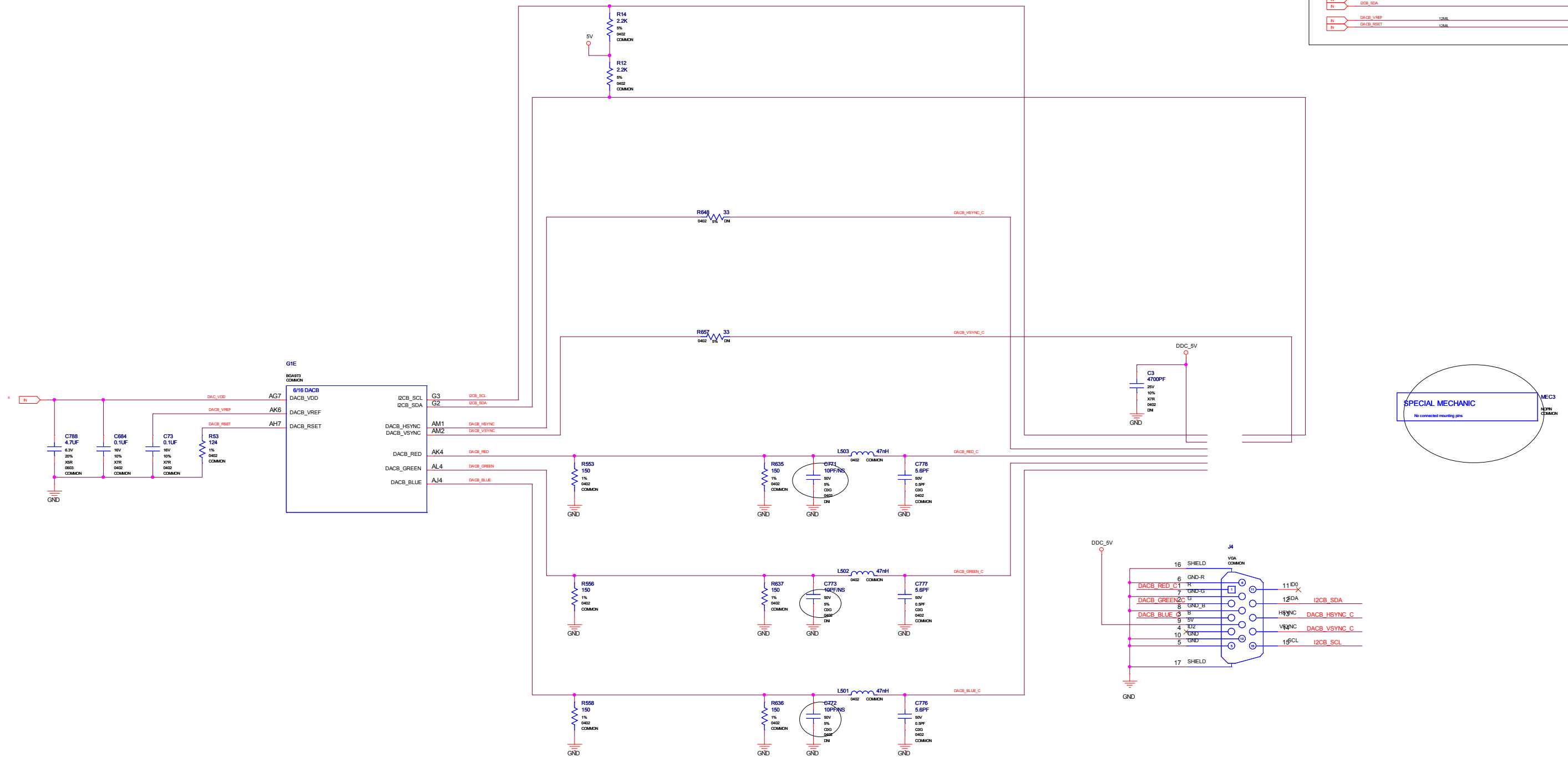
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PAGE DETAIL	TMDS Interface



# DACB VGA Header




Net Name		DIFF_PAIR	CRITICAL	IMPEDANCE
R1	DACB_RED		1	50OHM
R1	DACB_RED_C		1	50OHM
R1	DACB_GREEN		1	50OHM
R1	DACB_GREEN_C		1	50OHM
R1	DACB_BLUE		1	50OHM
R1	DACB_BLUE_C		1	50OHM
R1	DACB_HSYNC		1	50OHM
R1	DACB_HS_BLP		2	50OHM
R1	DACB_HSYNC_C		2	50OHM
R1	DACB_VSYNC		2	50OHM
R1	DACB_VS_BLP		2	50OHM
R1	DACB_VSYNC_C		2	50OHM


  


Net Name		MIN_WIDTH	VOLTAGE	MAX_CURRENT
R1	SCB_SCL			
R1	SCB_SDA			
R1	DACB_VREF	12uM		
R1	DACB_RESET	12uM		

## SPECIAL MECHANIC

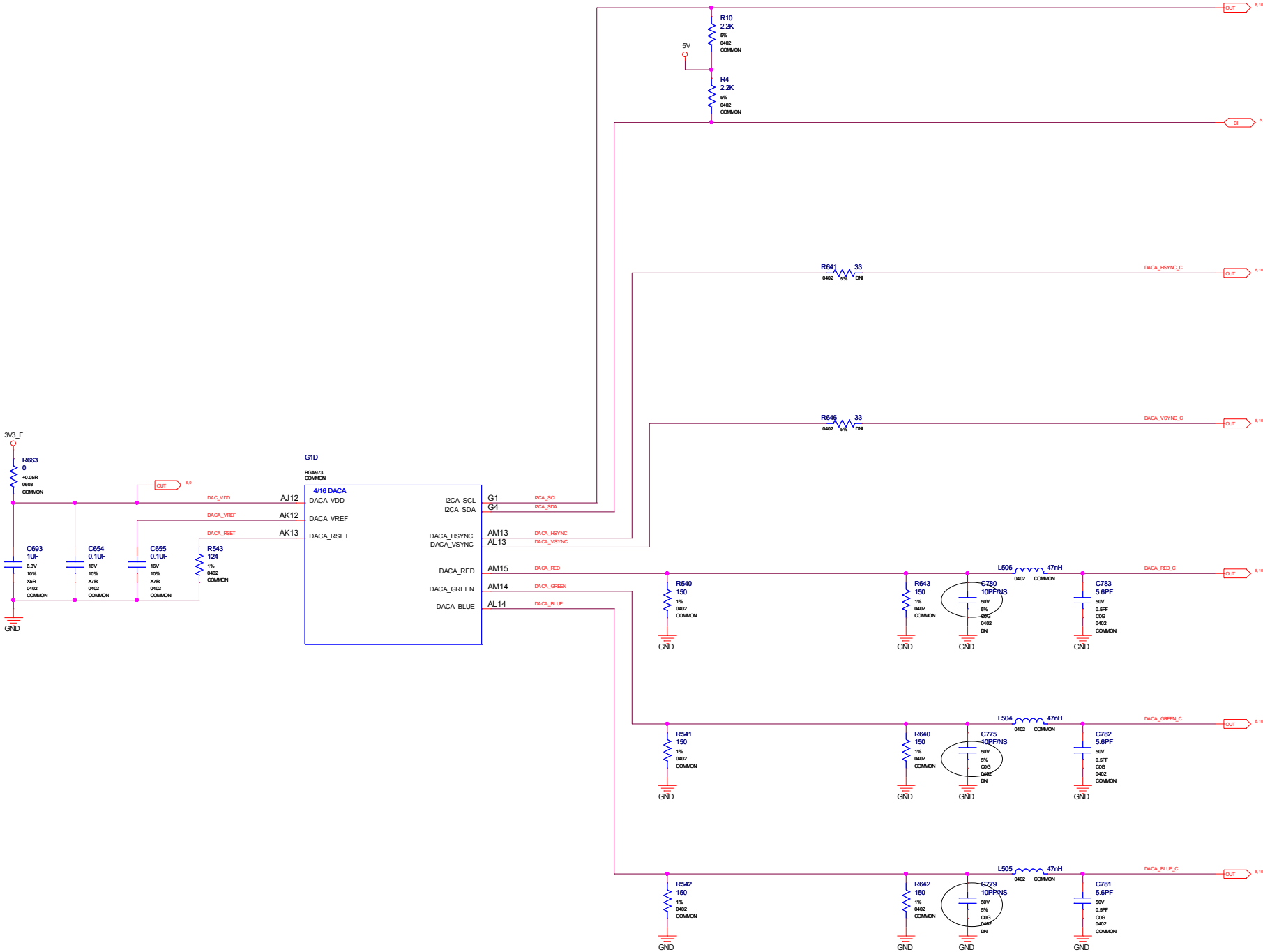
No connected mounting pins

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DACA VGA



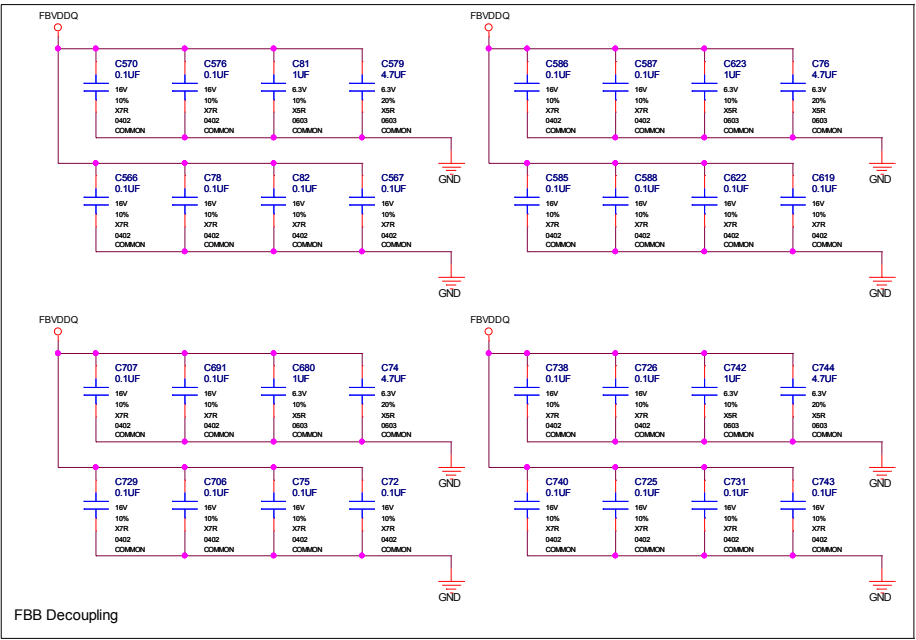
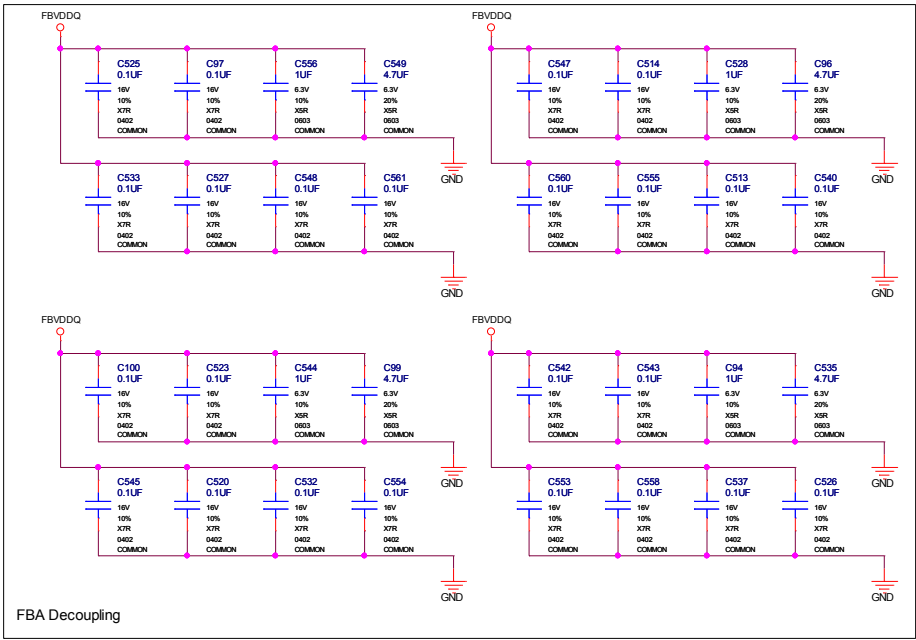
Net Name		DIFF_PAIR	CRITICAL	IMPEDANCE
8.10	IN	DACA_RED	1	50ohm
	OUT	DACA_RED_C	1	50ohm
8.10	IN	DACA_GREEN	1	50ohm
	OUT	DACA_GREEN_C	1	50ohm
8.10	IN	DACA_BLUE	1	50ohm
	OUT	DACA_BLUE_C	1	50ohm
8.10	IN	DACA_HSYNC	2	50ohm
	OUT	DACA_HSYNC_C	2	50ohm
8.10	IN	DACA_VSYNC	2	50ohm
	OUT	DACA_VSYNC_C	2	50ohm
Net Name		MIN_WIDTH	VOLTAGE	MAX_CURRENT
8.10	OUT	DACA_SCL	12mV	
8.10	IN	DACA_SDA	12mV	
8.10	OUT	DACA_VDD	12mV	
8.10	IN	DACA_VREF	12mV	
8.10	IN	DACA_RESET	12mV	

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PAGE DETAIL	DACA VGA

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Memory Decoupling



Net Name		DIFF_PAIR	CRITICAL	IMPEDANCE
FBA_DQS_WP0	FBA_DQS_WP0	1	NODEF	
FBA_DQS_WP1	FBA_DQS_WP1	1	NODEF	
FBA_DQS_WP2	FBA_DQS_WP2	1	NODEF	
FBA_DQS_WP3	FBA_DQS_WP3	1	NODEF	
FBA_DQS_WP4	FBA_DQS_WP4	1	NODEF	
FBA_DQS_WP5	FBA_DQS_WP5	1	NODEF	
FBA_DQS_WP6	FBA_DQS_WP6	1	NODEF	
FBA_DQS_WP7	FBA_DQS_WP7	1	NODEF	
FBA_DQS_WP8	FBA_DQS_WP8	1	NODEF	
FBA_DQS_WP9	FBA_DQS_WP9	1	NODEF	
FBA_DQS_WP10	FBA_DQS_WP10	1	NODEF	
FBA_DQS_WP11	FBA_DQS_WP11	1	NODEF	
FBA_DQS_WP12	FBA_DQS_WP12	1	NODEF	
FBA_DQS_WP13	FBA_DQS_WP13	1	NODEF	
FBA_DQS_WP14	FBA_DQS_WP14	1	NODEF	
FBA_DQS_WP15	FBA_DQS_WP15	1	NODEF	
FBA_DQS_WP16	FBA_DQS_WP16	1	NODEF	
FBA_DQS_WP17	FBA_DQS_WP17	1	NODEF	
FBA_DQS_WP18	FBA_DQS_WP18	1	NODEF	
FBA_DQS_WP19	FBA_DQS_WP19	1	NODEF	
FBA_DQS_WP20	FBA_DQS_WP20	1	NODEF	
FBA_DQS_WP21	FBA_DQS_WP21	1	NODEF	
FBA_DQS_WP22	FBA_DQS_WP22	1	NODEF	
FBA_DQS_WP23	FBA_DQS_WP23	1	NODEF	
FBA_DQS_WP24	FBA_DQS_WP24	1	NODEF	
FBA_DQS_WP25	FBA_DQS_WP25	1	NODEF	
FBA_DQS_WP26	FBA_DQS_WP26	1	NODEF	
FBA_DQS_WP27	FBA_DQS_WP27	1	NODEF	
FBA_DQS_WP28	FBA_DQS_WP28	1	NODEF	
FBA_DQS_WP29	FBA_DQS_WP29	1	NODEF	
FBA_DQS_WP30	FBA_DQS_WP30	1	NODEF	
FBA_DQS_WP31	FBA_DQS_WP31	1	NODEF	
FBA_DQS_WP32	FBA_DQS_WP32	1	NODEF	
FBA_DQS_WP33	FBA_DQS_WP33	1	NODEF	
FBA_DQS_WP34	FBA_DQS_WP34	1	NODEF	
FBA_DQS_WP35	FBA_DQS_WP35	1	NODEF	
FBA_DQS_WP36	FBA_DQS_WP36	1	NODEF	
FBA_DQS_WP37	FBA_DQS_WP37	1	NODEF	
FBA_DQS_WP38	FBA_DQS_WP38	1	NODEF	
FBA_DQS_WP39	FBA_DQS_WP39	1	NODEF	
FBA_DQS_WP40	FBA_DQS_WP40	1	NODEF	
FBA_DQS_WP41	FBA_DQS_WP41	1	NODEF	
FBA_DQS_WP42	FBA_DQS_WP42	1	NODEF	
FBA_DQS_WP43	FBA_DQS_WP43	1	NODEF	
FBA_DQS_WP44	FBA_DQS_WP44	1	NODEF	
FBA_DQS_WP45	FBA_DQS_WP45	1	NODEF	
FBA_DQS_WP46	FBA_DQS_WP46	1	NODEF	
FBA_DQS_WP47	FBA_DQS_WP47	1	NODEF	
FBA_DQS_WP48	FBA_DQS_WP48	1	NODEF	
FBA_DQS_WP49	FBA_DQS_WP49	1	NODEF	
FBA_DQS_WP50	FBA_DQS_WP50	1	NODEF	
FBA_DQS_WP51	FBA_DQS_WP51	1	NODEF	
FBA_DQS_WP52	FBA_DQS_WP52	1	NODEF	
FBA_DQS_WP53	FBA_DQS_WP53	1	NODEF	
FBA_DQS_WP54	FBA_DQS_WP54	1	NODEF	
FBA_DQS_WP55	FBA_DQS_WP55	1	NODEF	
FBA_DQS_WP56	FBA_DQS_WP56	1	NODEF	
FBA_DQS_WP57	FBA_DQS_WP57	1	NODEF	
FBA_DQS_WP58	FBA_DQS_WP58	1	NODEF	
FBA_DQS_WP59	FBA_DQS_WP59	1	NODEF	
FBA_DQS_WP60	FBA_DQS_WP60	1	NODEF	
FBA_DQS_WP61	FBA_DQS_WP61	1	NODEF	
FBA_DQS_WP62	FBA_DQS_WP62	1	NODEF	
FBA_DQS_WP63	FBA_DQS_WP63	1	NODEF	
FBA_DQS_WP64	FBA_DQS_WP64	1	NODEF	
FBA_DQS_WP65	FBA_DQS_WP65	1	NODEF	
FBA_DQS_WP66	FBA_DQS_WP66	1	NODEF	
FBA_DQS_WP67	FBA_DQS_WP67	1	NODEF	
FBA_DQS_WP68	FBA_DQS_WP68	1	NODEF	
FBA_DQS_WP69	FBA_DQS_WP69	1	NODEF	
FBA_DQS_WP70	FBA_DQS_WP70	1	NODEF	
FBA_DQS_WP71	FBA_DQS_WP71	1	NODEF	
FBA_DQS_WP72	FBA_DQS_WP72	1	NODEF	
FBA_DQS_WP73	FBA_DQS_WP73	1	NODEF	
FBA_DQS_WP74	FBA_DQS_WP74	1	NODEF	
FBA_DQS_WP75	FBA_DQS_WP75	1	NODEF	
FBA_DQS_WP76	FBA_DQS_WP76	1	NODEF	
FBA_DQS_WP77	FBA_DQS_WP77	1	NODEF	
FBA_DQS_WP78	FBA_DQS_WP78	1	NODEF	
FBA_DQS_WP79	FBA_DQS_WP79	1	NODEF	
FBA_DQS_WP80	FBA_DQS_WP80	1	NODEF	
FBA_DQS_WP81	FBA_DQS_WP81	1	NODEF	
FBA_DQS_WP82	FBA_DQS_WP82	1	NODEF	
FBA_DQS_WP83	FBA_DQS_WP83	1	NODEF	
FBA_DQS_WP84	FBA_DQS_WP84	1	NODEF	
FBA_DQS_WP85	FBA_DQS_WP85	1	NODEF	
FBA_DQS_WP86	FBA_DQS_WP86	1	NODEF	
FBA_DQS_WP87	FBA_DQS_WP87	1	NODEF	
FBA_DQS_WP88	FBA_DQS_WP88	1	NODEF	
FBA_DQS_WP89	FBA_DQS_WP89	1	NODEF	
FBA_DQS_WP90	FBA_DQS_WP90	1	NODEF	
FBA_DQS_WP91	FBA_DQS_WP91	1	NODEF	
FBA_DQS_WP92	FBA_DQS_WP92	1	NODEF	
FBA_DQS_WP93	FBA_DQS_WP93	1	NODEF	
FBA_DQS_WP94	FBA_DQS_WP94	1	NODEF	
FBA_DQS_WP95	FBA_DQS_WP95	1	NODEF	
FBA_DQS_WP96	FBA_DQS_WP96	1	NODEF	
FBA_DQS_WP97	FBA_DQS_WP97	1	NODEF	
FBA_DQS_WP98	FBA_DQS_WP98	1	NODEF	
FBA_DQS_WP99	FBA_DQS_WP99	1	NODEF	
FBA_DQS_WP100	FBA_DQS_WP100	1	NODEF	

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PAGE DETAIL	Memory Decoupling

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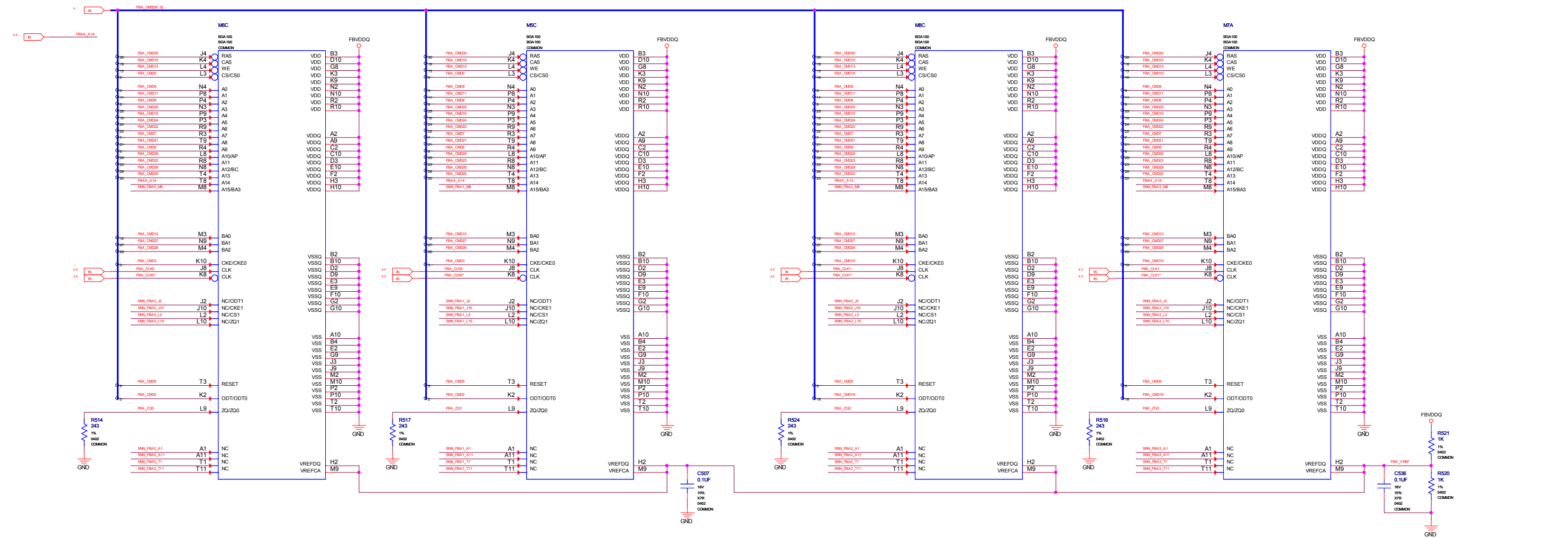
DATE

01-APR-2010

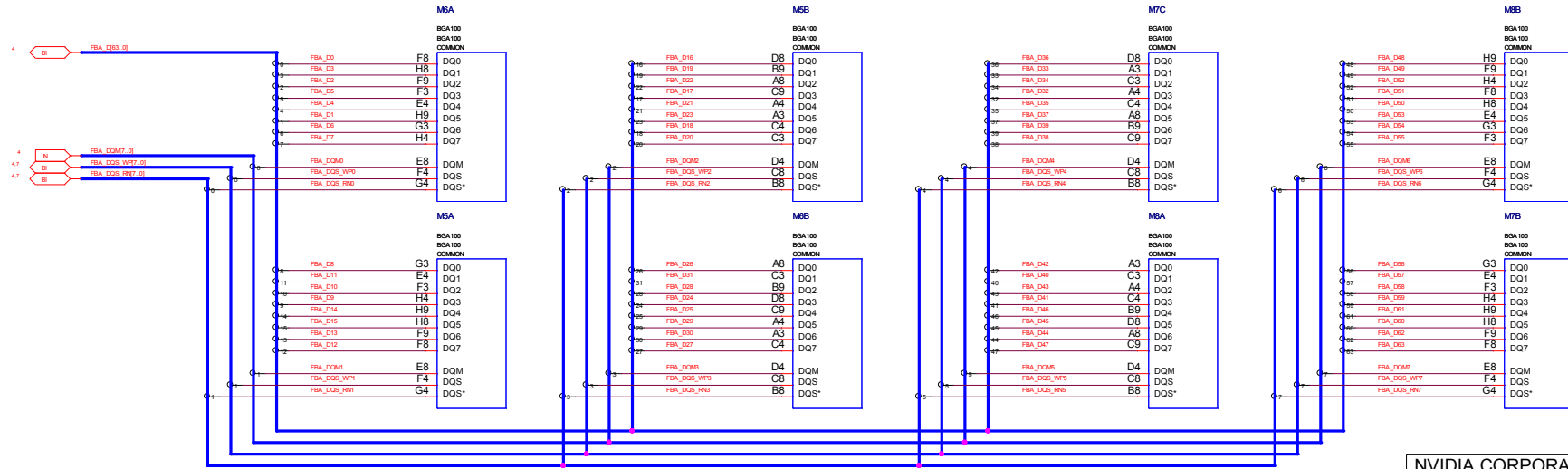
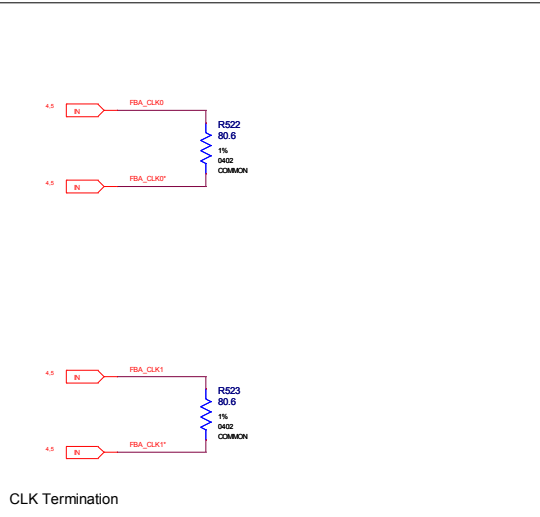


FBA Partition

Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
FBA_CLK0_T		0.75V	
FBA_CLK1_T		0.75V	
FBA_VREF	120M		
FBA_Z00	120M		
FBA_Z01	120M		
FBA_Z02	120M		
FBA_Z03	120M		



CMD Mapping		
CMD	<0..31>	<32..63>
CMD0	CS0*	
CMD1		
CMD2	ODT	
CMD3	CKE	
CMD4		RES
CMD5	A0	A0
CMD6	A7	A7
CMD8	A2	A2
CMD9	A0	A0
CMD10	A4	A4
CMD11	A1	A1
CMD12	BA0	BA0
CMD13	WE*	WE*
CMD14		
CMD15	CA0*	CA0*
CMD16		CS0*
CMD17		
CMD18	ODT	CKE
CMD19		
CMD20	A13	A13
CMD21	A8	A8
CMD22	A6	A6
CMD23	A11	A11
CMD24	A5	A5
CMD25	A3	A3
CMD26	BA2	BA2
CMD27	BA1	BA1
CMD28	A12	A12
CMD29	A10	A10
CMD30	RA0*	RA0*
CMD31		



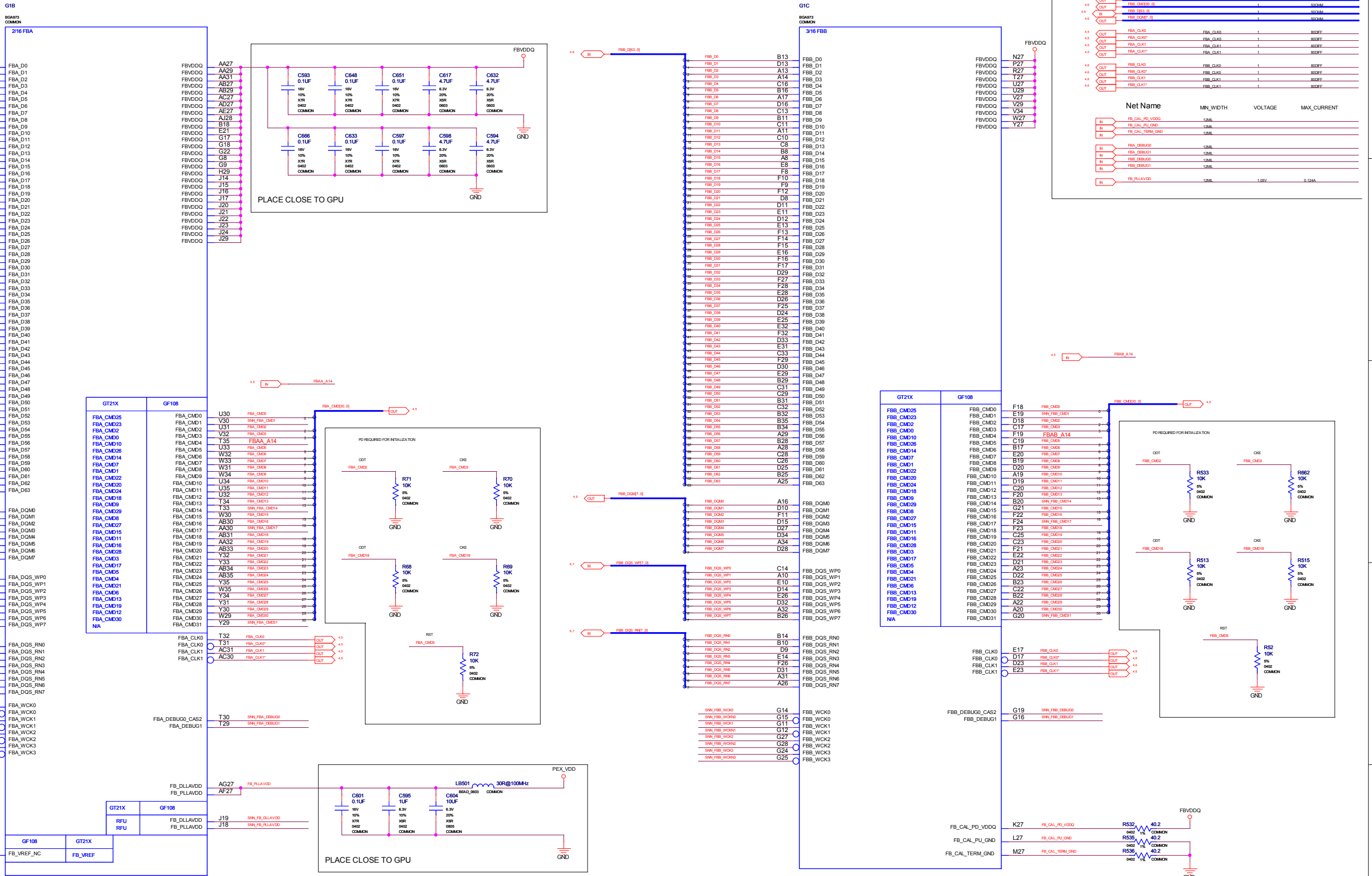
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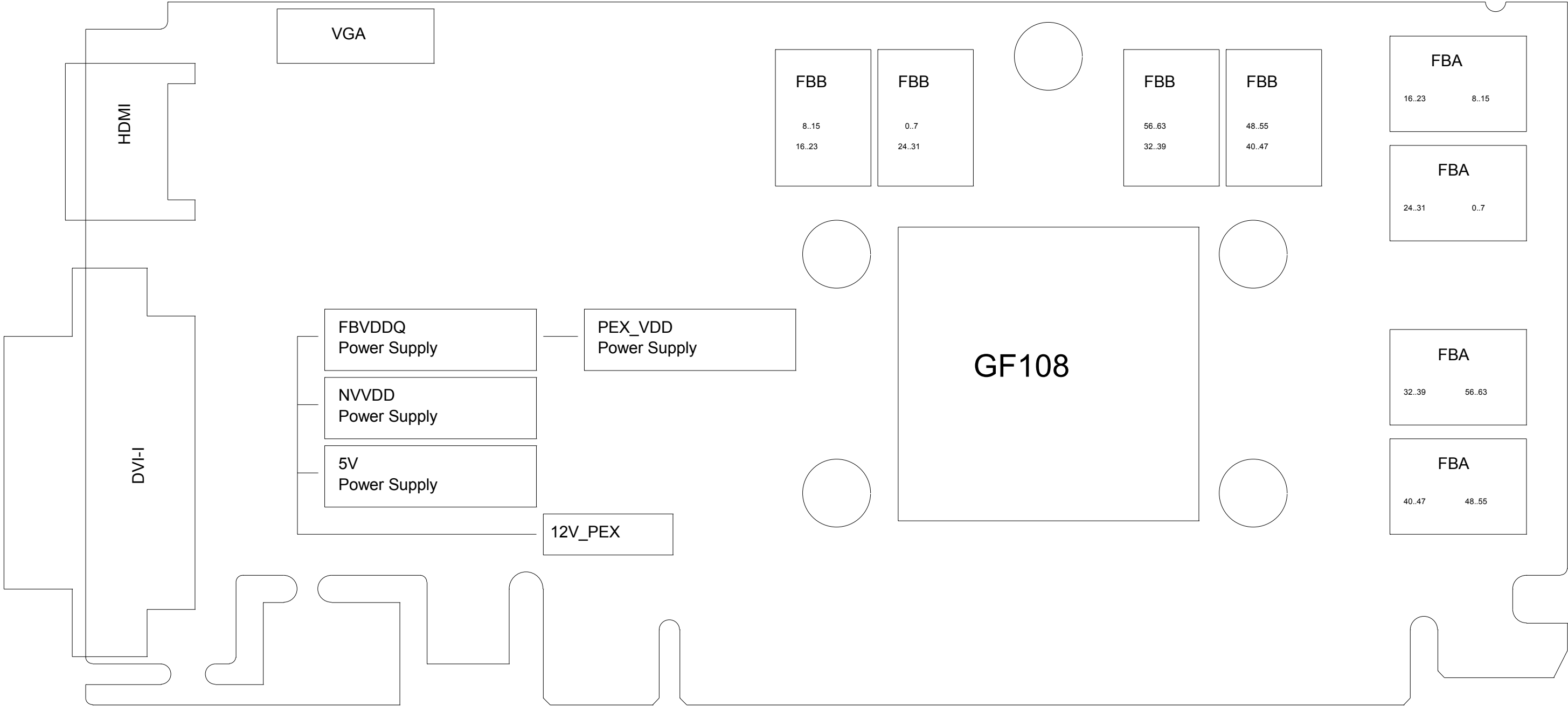
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PAGE DETAIL	FBA Partition

# GPU Frame Buffer





Block Diagram



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PAGE DETAIL	Board Block Diagram

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P1071: GF108, 1024MB x16 DDR3, DVI+HDMI+VGA

- Page 1: Overview
- Page 2: Board Block Diagram
- Page 3: PCI Express Interface
- Page 4: GPU Frame Buffer
- Page 5: FBA Partition
- Page 6: FBB Partition
- Page 7: Memory Decoupling
- Page 8: DACA VGA
- Page 9: DACB VGA Header
- Page 10: TMDS Interface
- Page 11: HDMI Interface
- Page 12: Unused Interfaces, NVVDD Decoupling
- Page 13: XTAL, ROM, JTAG, Misc
- Page 14: Straps, Thermal, Mechanical
- Page 15: Power Supply I: Misc Power, Thermal Shutdown
- Page 16: Power Supply II: FBVDDQ
- Page 17: Power Supply III: NVVDD

SKU	VARIANT	NVPN	ASSEMBLY
B	BASE	600-11071-BASE-SCH	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU0001	600-11071-0001-100	GF108-300-A1 625/1250/900MHz 1024MB/128bit DDR3, DVI-DL+HDMI+VGA
2	SKU0002	600-11071-0002-100	GF108-200-A1 650/1300/900MHz 1024MB/128bit DDR3, DVI-DL+HDMI+VGA
3	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
4	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
5	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
6	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
7	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
8	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
9	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
10	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
11	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
12	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
13	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
14	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
15	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>


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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
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