

HISTORY:

MS8882-0A

P70, NV17, 2M/4MX32 DDR, 32MB/64MB, Dual RGB, TV-In, VIVO (AV/S), Audio In/Out, AGP4X

PCI DEVICE ID 0X0-0X171 FOR NV17-128D.

first version for layout start. No video capture, no stereo. NO DRC CHECK.

change to target release A02

changed I2C assignments

added I2C filter components for VVo (L506,L507,R521-R524,D506,D507,C531,C532)

added PCB footprints for IC, S014, 150ML, and CON_DVH_RA

added end-termination i-packs for address-status-bus

changed RGB filter to integrated 5-pole only

added video capture, BIOS, Strapping

changed RefDes

NO DRC CHECK

added missing end termination for FBBAx

changed stereo 3pin header

added clamp diodes for TV-out and video capture

changed topology for 2nd DDC (VGA / DVI)

NO DRC CHECK

split analog/digital supply for SAA7113 (no component change)

Fixed some RefDes

Changed the strapping resistors cases to 0402

NO DRC CHECK

changed C617 to SMD0603

changed U502 and U503 to TSSOP14

added CKE connection to the memories

added D505 to the stereo section

changed L501, L502, L503 to 0603

NO DRC CHECK

changed U601 to SOT223-DPAK-D2PAK

added bracket

changed L600 to IND_CDR73

added VIP connector

NO DRC CHECK

deleted VIP

changed TP's to HOLE_48C28P

changed switcher gate circuit (no component change)

added C541 and C542 to the linear voltage regulator

pinwrap of memory termination

NO DRC CHECK

deleted page 10: 5.b VVo

changed filter topology for RGB

added return traces for RGB

NO DRC CHECK

added TV filter caps with reference to GND (C511,C517,C522)

separated RGB returns (NOTE: there will be a change for DAC2)

NO DRC CHECK

added net names to unused panel signals (test points)

NO DRC CHECK

added decoupling for DVO rail (C529 and C530)

added GND connection to pin 1 of U700

added R502 pull down for DVOCLKIN

DRC CHECK DONE

changed caps in power section to the combined footprints

data bus swappings

swapped CLKs for U201 and U202

connected the RGB returns of VGA2 together

update materials

update stuffings

X17-A01:

release A01

changed DVOVDD from VID3.3V to A3.3V

add linear regulator for NV17's VDD033

connected the I/FP-RSETs to GND

deleted C134 and C135

PAGE OVERVIEW

1 top (this) page

2 1. AGP interface, core decoupling

3 2.a NV17 Frame Buffer

4 2.b Frame Buffer 0..63

5 2.c Frame Buffer 64..127

6 3. Dual DAC, 1st VGA

7 4. Panel

8 5. TV-out, video capture

9 6. Power supply

10 7. BIOS, Strapping

11 8. Hardware Monitor

STUFFING OPTIONS

COMMON

NO_STUFF

XTAL_27

XTAL_14

VIVO

VIDCAP

TV_OUT

DVI-I

DVI

2NDVGA

NO_DVI

SYNC_DRIVER2

SYNC_DRIVER

NO_SYNC_DRIVER2

NO_SYNC_DRIVER

STEREOSYNCS

STEREOSYNCS2

STEREOSYNCS1

STEREO

SI2C_PROT

RGB_PROT

2NDVGA_RGB_PROT

2NDSI2C_PROT

SET_NVVDD_ISL

SET_NVVDD_IRU

SET_FBVDDQ_ISL

SET_FBVDDQ_IRU

NO_1117

ISL

IRU

FBVDD_3.3

FBVDD_2.5

1117_ADJ

1117

PASSIVE_HS

FAN

BRCKT_VGA_TV_VGA

BRCKT_VGA_TV_DVI

BRCKT_VGA_TV

BRCKT_VGA

MEMSTRAP1111

MEM-64B

MEM-128B

NV17-128D

MEANING

common components for all skus

no assembly at all

stuff the 27MHz xtal for the GPU

stuff the 14.318MHz xtal for the GPU

stuff video I/O

stuff video capture

stuff TV-out

stuff the DVI-I option

stuff the DVI-D option

stuff a second VGA

don't stuff any DVI

stuff the termination resistors for the DACB syncs

stuff the sync driver chip

bypass the sync driver for the DAC B syncs

bypass the sync driver for the DAC A syncs

stuff the switchable stereo sync circuit

sync stereo to DAC B

sync stereo to DAC A

stuff the generic stereo circuit

stuff the protection diodes for syncs and I2C at DAC A

stuff the protection diodes for RGB at DAC A

stuff the protection diodes for RGB at DAC B

stuff the protection diodes for syncs and I2C at DAC B

set NVVDD voltage at Intersil switcher

set NVVDD voltage at IR/Uniseml switcher

set FBVDDQ voltage at Intersil switcher

set FBVDDQ voltage at IR/Uniseml switcher

bypass the A3.3V regulator to AGP3.3V

stuff the Intersil switcher

stuff the IR/Unisem switcher

connect FBVDD with AGP3.3V

connect FBVDD with FBVDDQ

stuff the adjustable regulator for A3,3V

stuff the fixed regulator for A3,3V

have passive heatsink

fan sink and components

Bracket VGA-TV-VGA

Bracket VGA-TV-DVI

Bracket VGA-TV

Bracket VGA

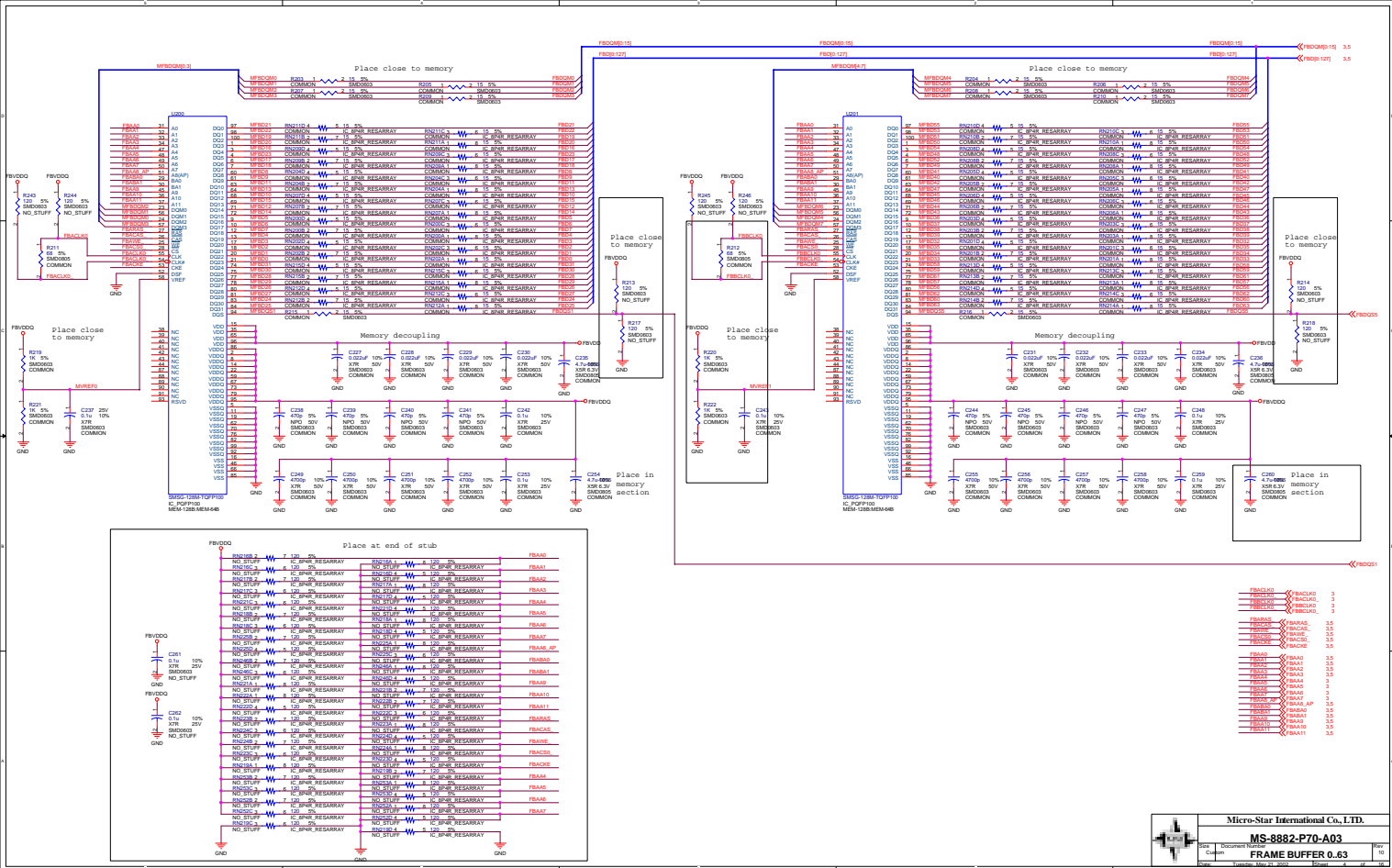
strap memory to 1111

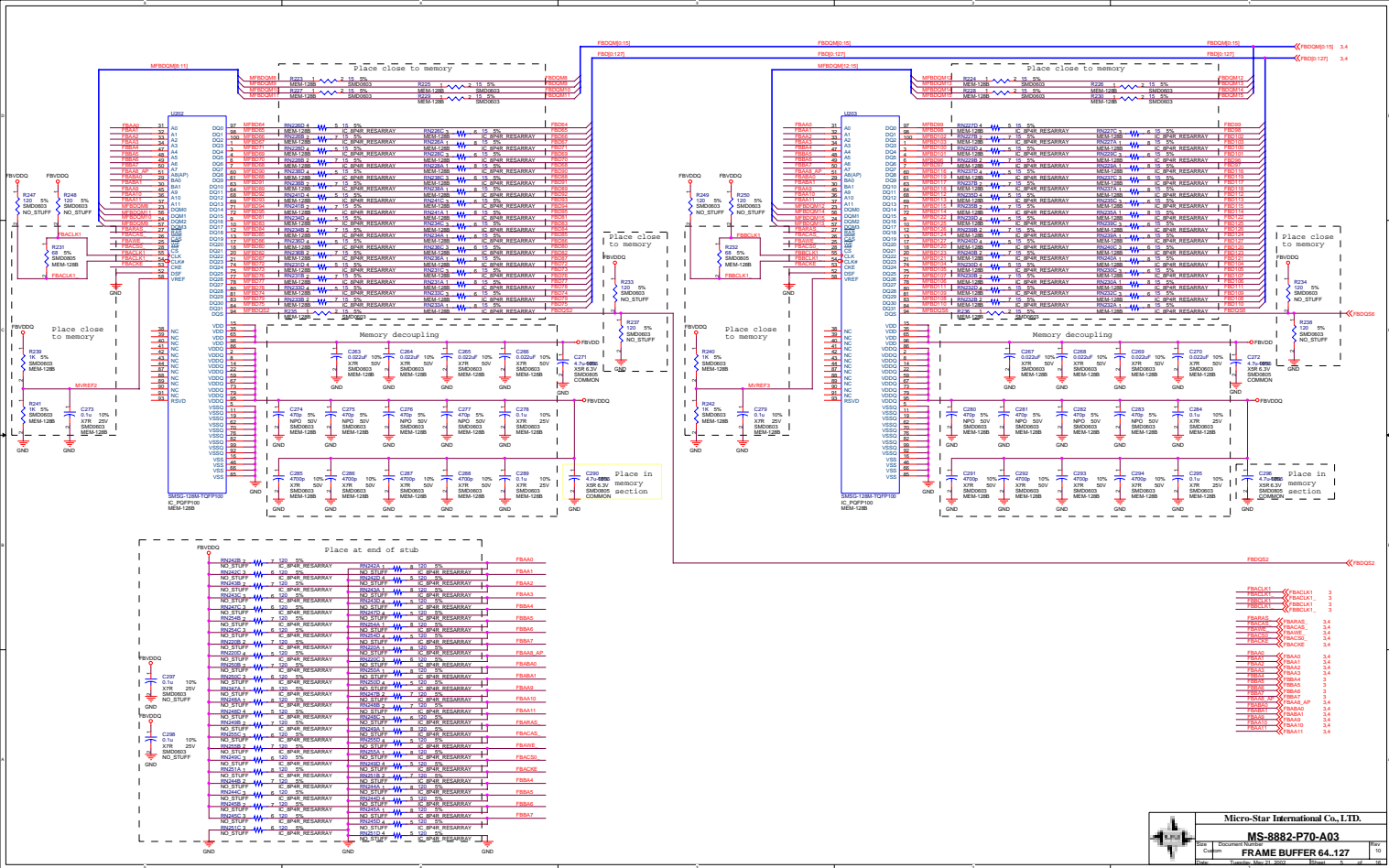
stuff 64 bit memory - 2pcs

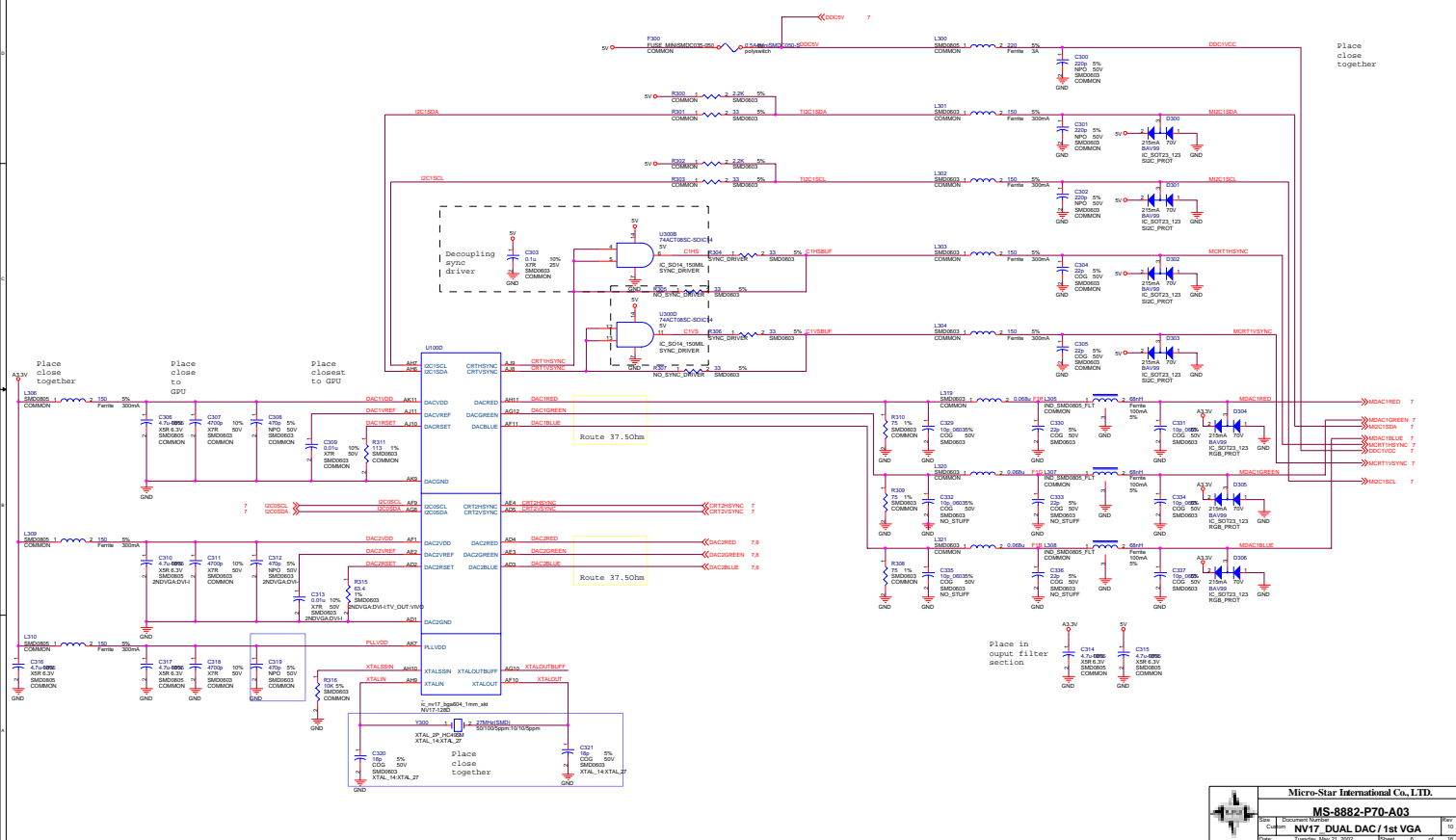
stuff 128 bit memory - 4pcs

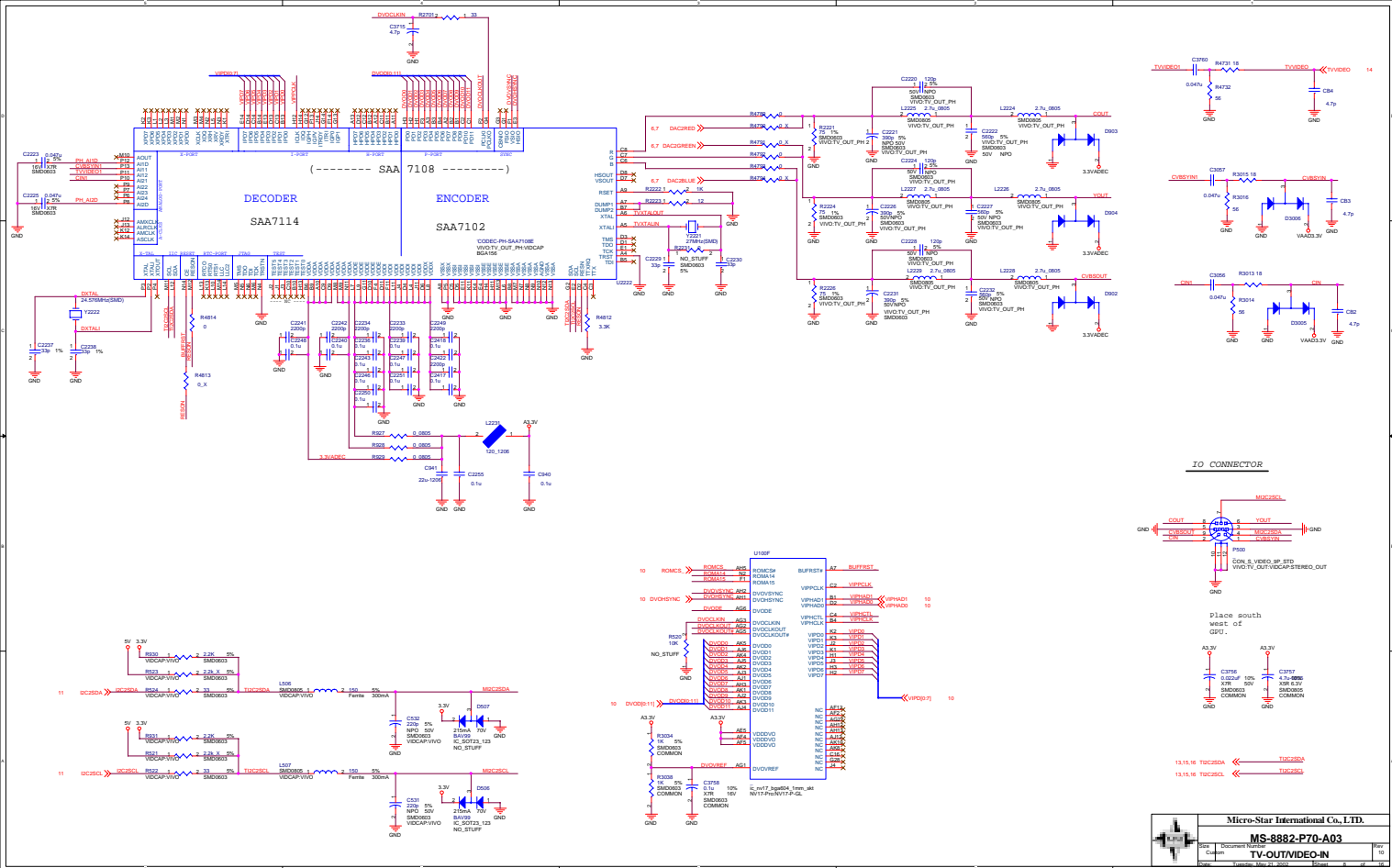
stuff NV17-128D

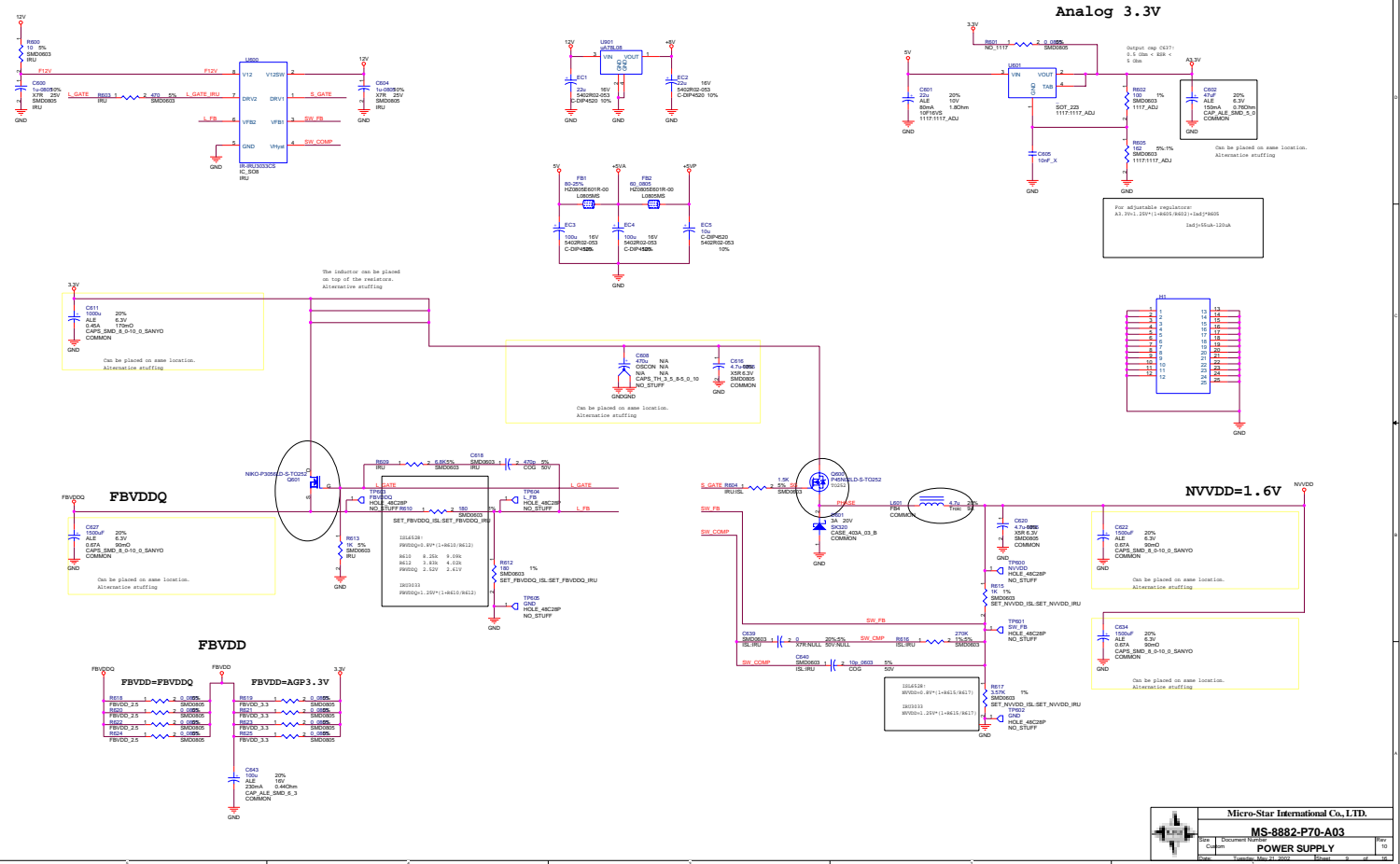
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CE2-CE17 for EMI

