NV20, 4MX16 DDR, RGB, EXTERNAL DVI-I, TV-DOWN, TV IF , AGP4X

PCI DEVICE ID 0X0=0X200 FOR NV20.

NVVDD SET TO: 1.52V
FBVDD SET TO: 3.47V
FBVDDQ SET TO: 2.59V

HISTORY REVISION:

X00: Based on P50-A06
- See change list in 149- file.
- Set FBVDDQ=2.59V

P50-A07-X01:

- Changed all memory clk/clk# diff pair resistors to 68R 5% (from 47R)

P50-A08:

X04: - Delay PLL_VDD to come up after NVVDD.
X05: - Added 1UF accross R257.
X06: - Removed X04-5 above, added a switcher generated PLL delay option.
- SSENA cap for 2nd SW changed to 1UF.
- A05 Si, NVVDD=1.52V

P50-A09:

X02: - Changed PLL VDD and DAC VDD to be gated by Fet controlled by FBVDD power good signal.
X03: - Added option to pull up power good to 12V

ECO1235: - Changed R841 PU to 10K (from 4.7K)

ECO1373: - Moved 75 ohm termination closer to filter for noise level reduction. Replaced C303,306,309 with 75ohm and no stuffed R208-210.

{Item}\t{Quantity}\t{Reference}\t{Source Package} {USR DFINE} {Value}\t{Assembly}
{Assembly}{Source Package} {Value}{PN}{AVL1}{AVL2}{AVL3}{AVL4}{AVL5}

Micro-Star International Co., LTD.
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Size: C | Document Number: TOP PAGE | Rev: 0A
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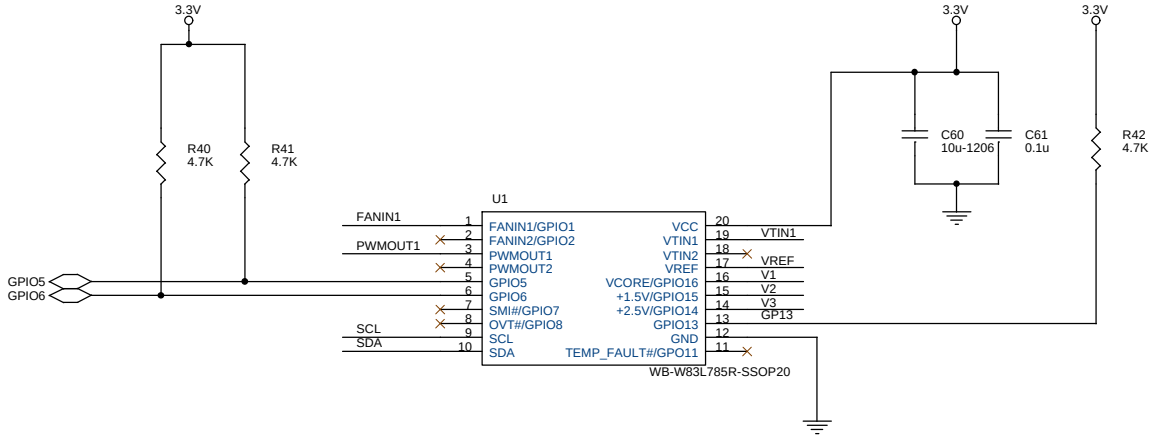
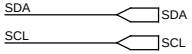
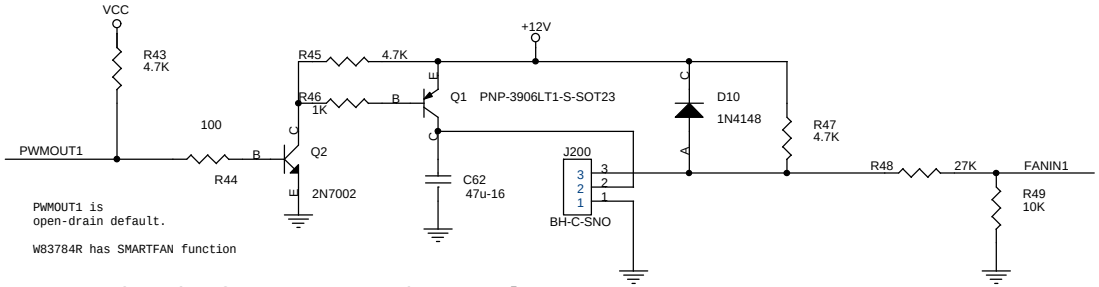
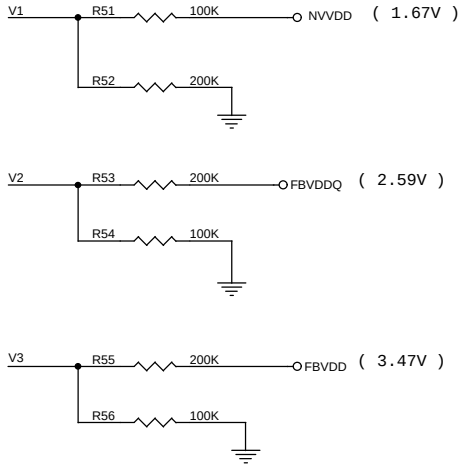


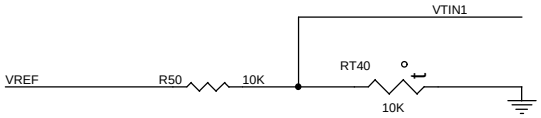




PWM1 Circuit for FAN1 speed Control

FOR 12V FAN





TEMPERATURE SENSORING CIRCUIT

VOLTAGE SENSORING CIRCUIT

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Size: B	Document Number: HARDWARE MONITOR	Rev: 0A
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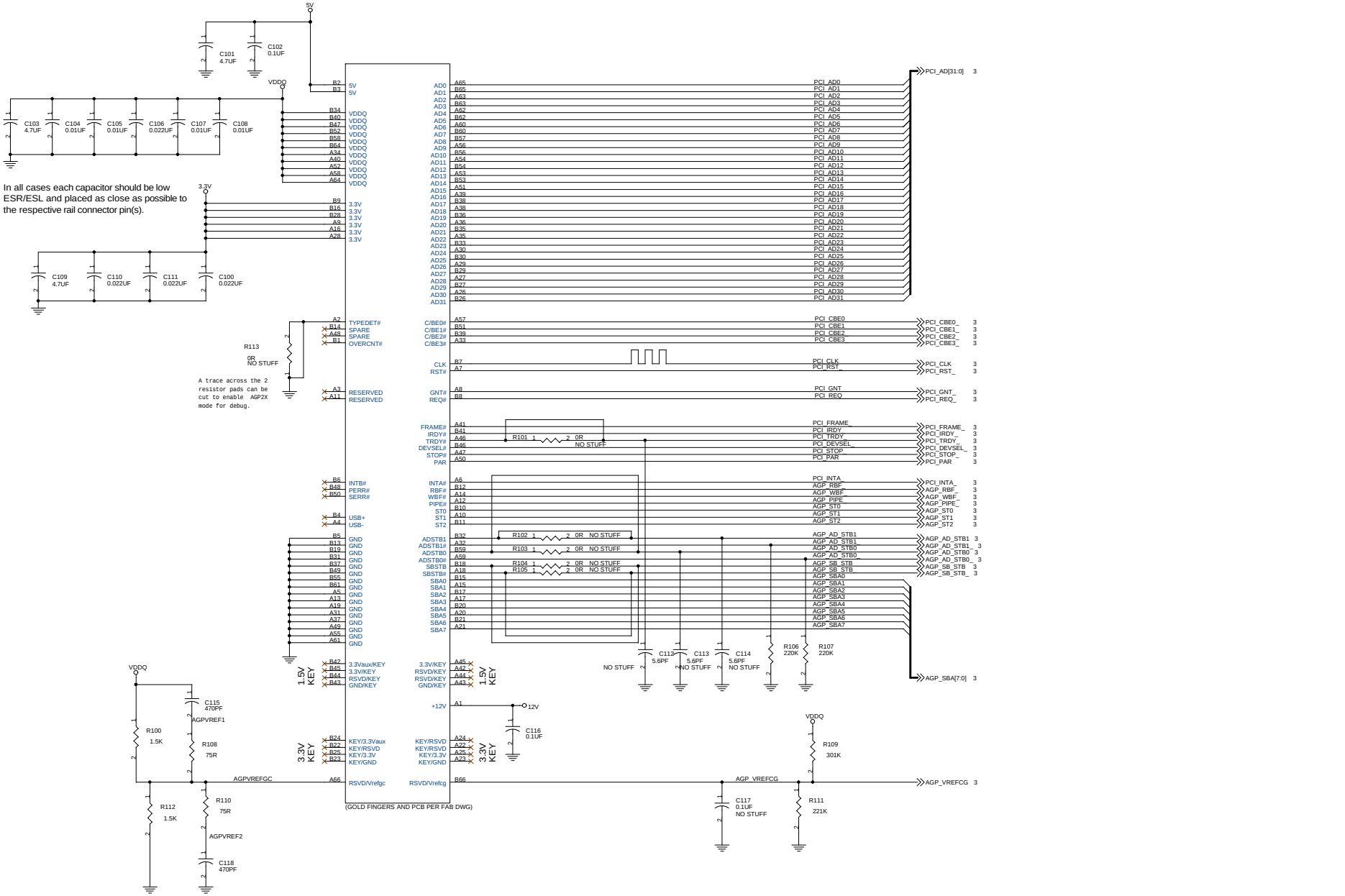

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Size: C	Document Number: BUS I/O
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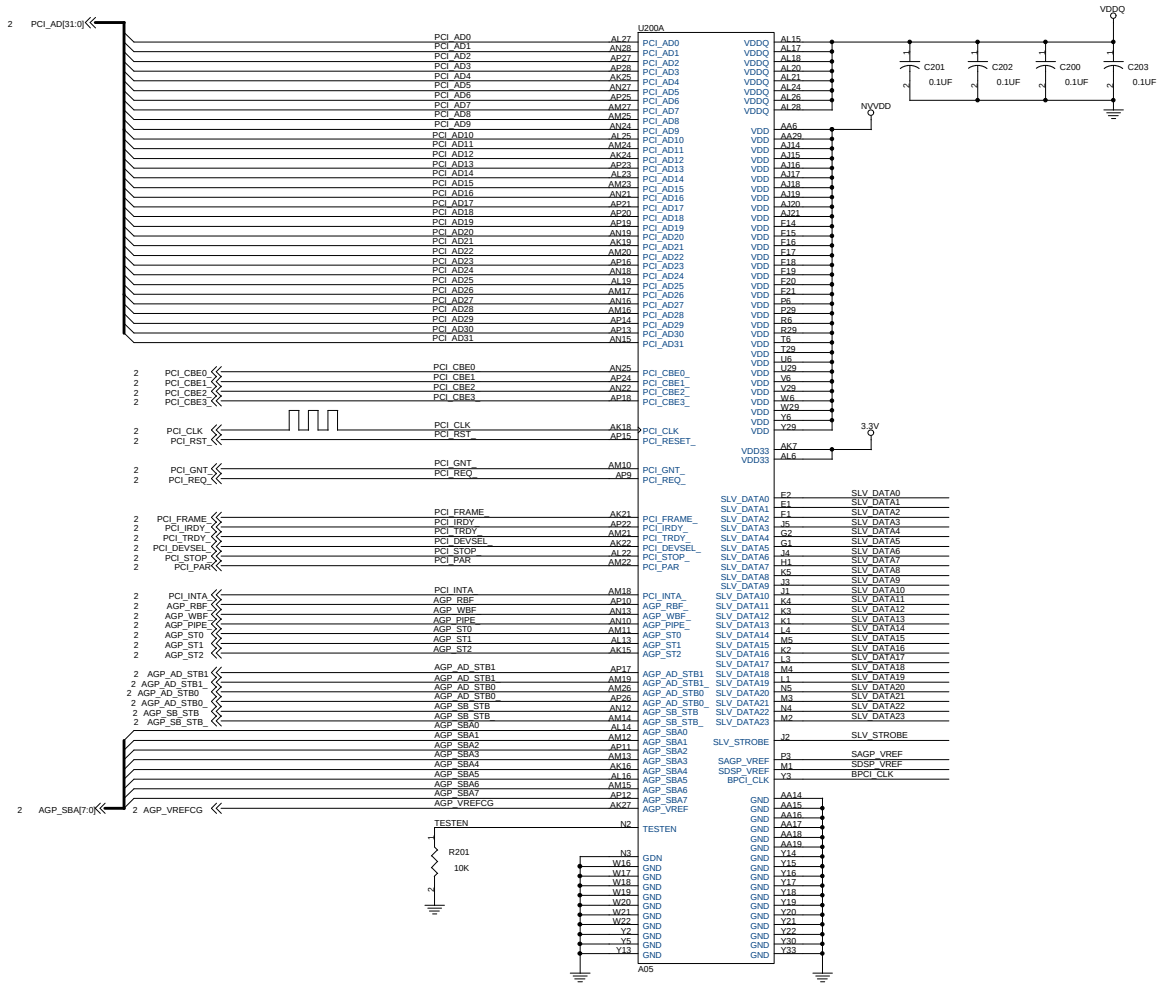

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MS-8838 based on P50A09		
Size: C	Document Number: NV20 HOST	Rev: 0A
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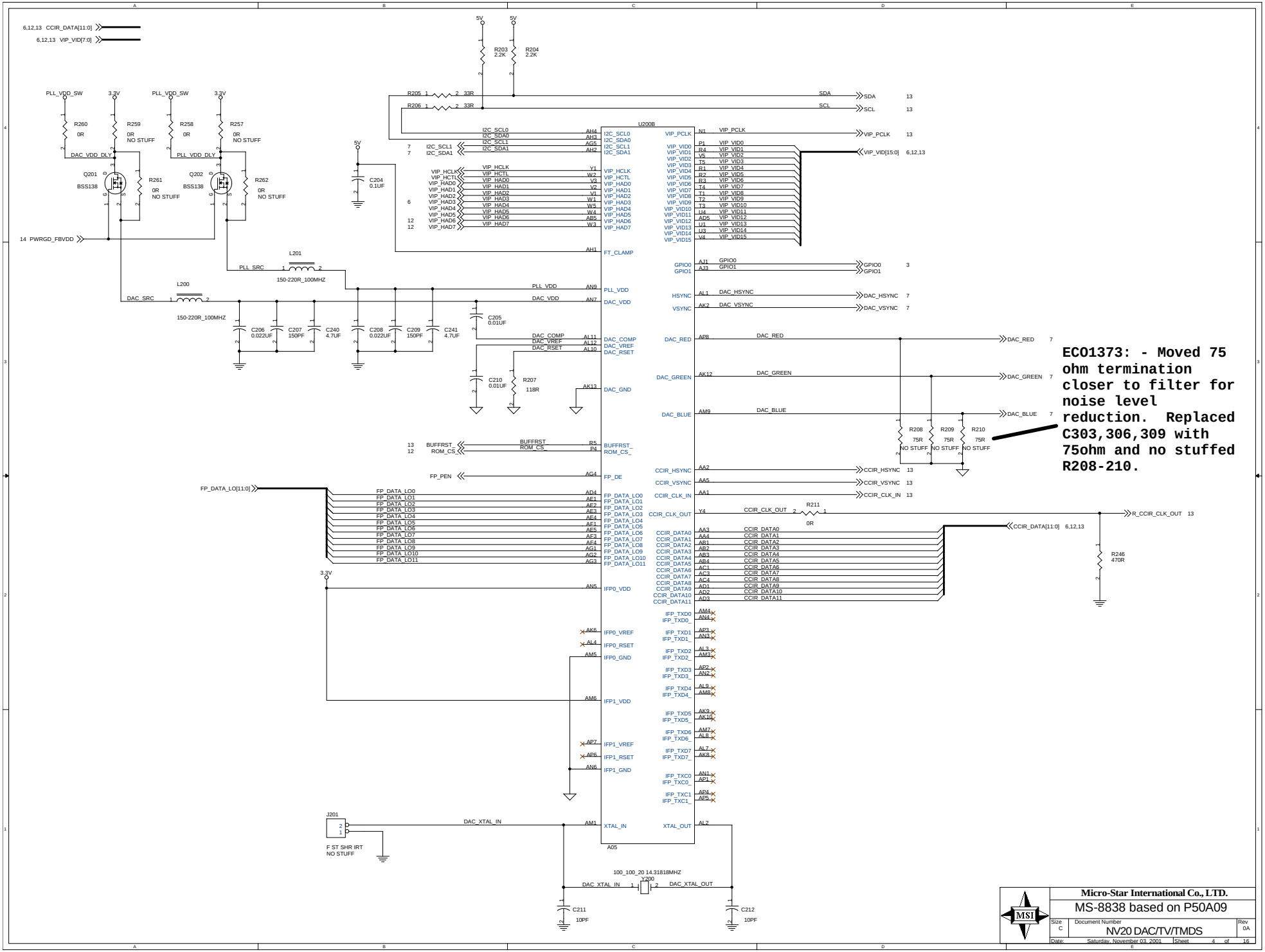
6,12,13 CCIR_DATA[11:0]
6,12,13 VIP_VID[7:0]
U200B
A05
ECO1373: - Moved 75 ohm termination closer to filter for noise level reduction. Replaced C303,306,309 with 75ohm and no stuffed R208-210.
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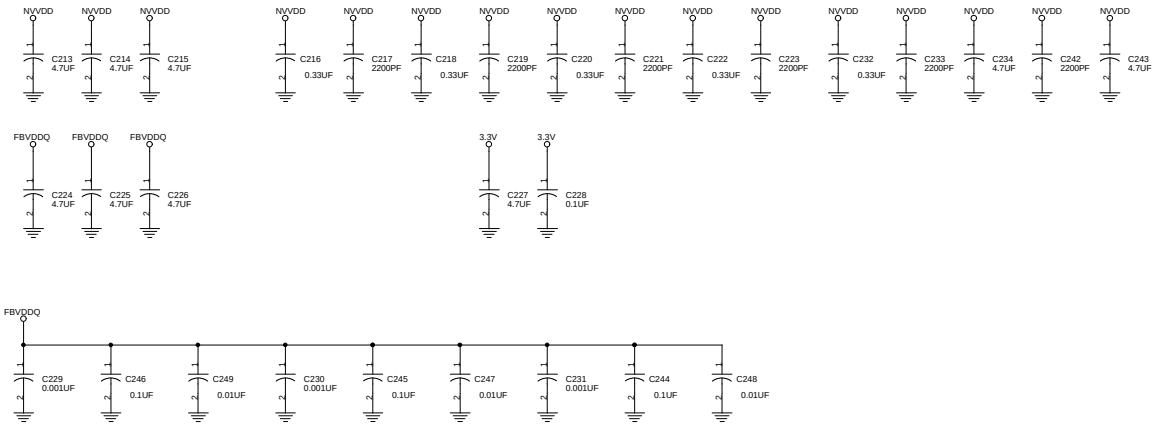
Use thick (non-impedance controlled) traces on XTALIN/OUT



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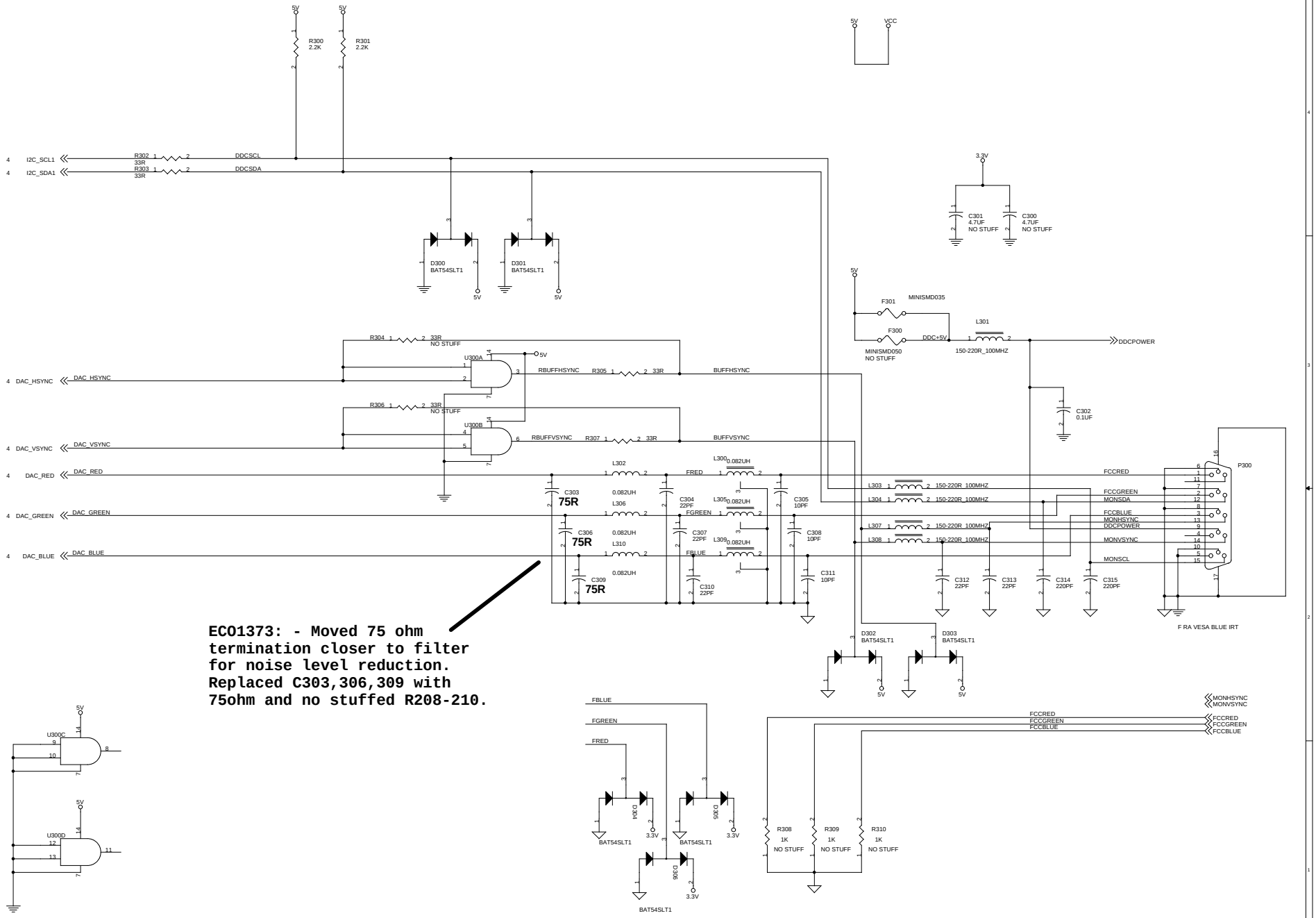

ECO1373: - Moved 75 ohm termination closer to filter for noise level reduction. Replaced C303,306,309 with 75ohm and no stuffed R208-210.

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Size: C	Document Number: DAC I/O
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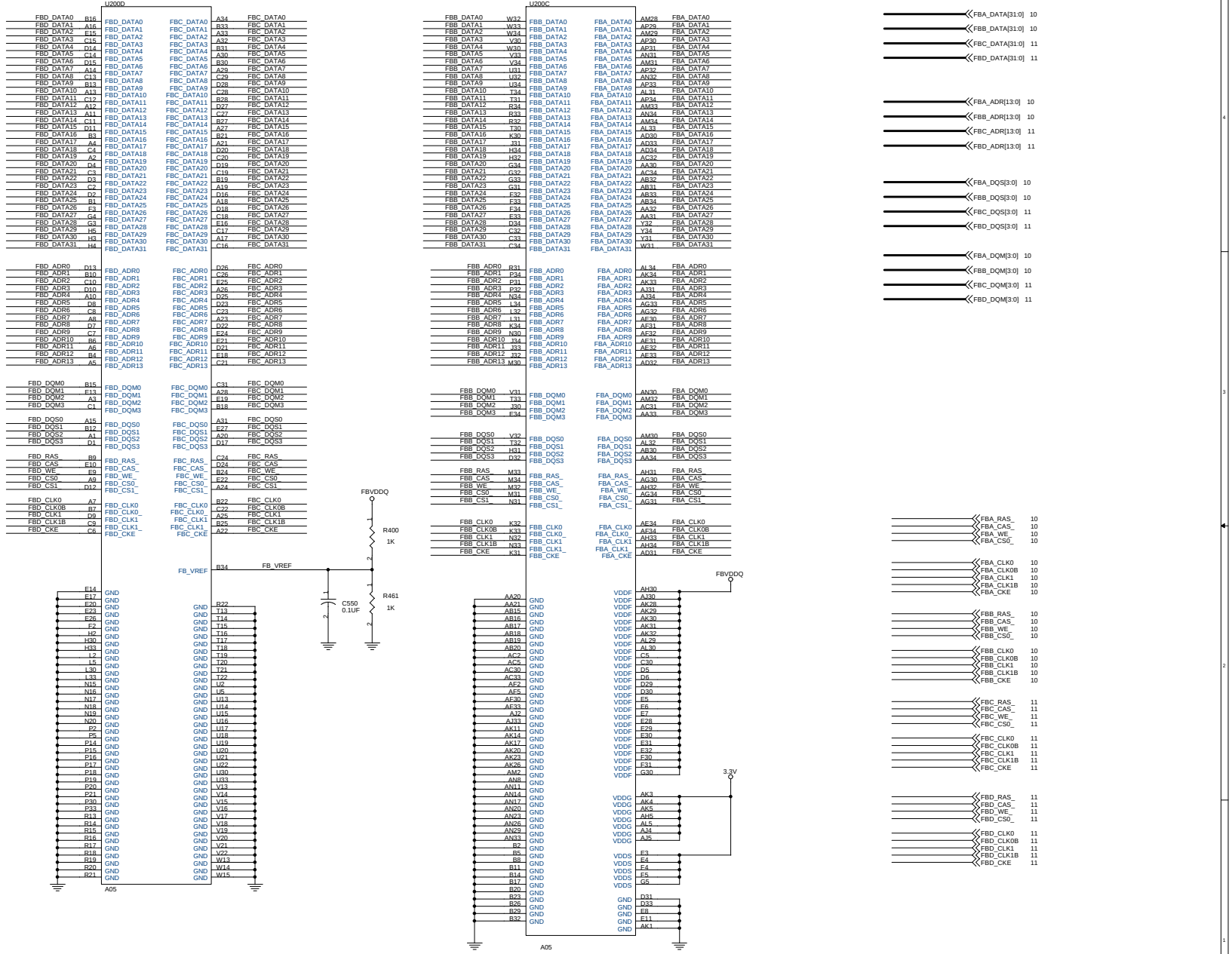

MSI	Micro-Star International Co., LTD.	
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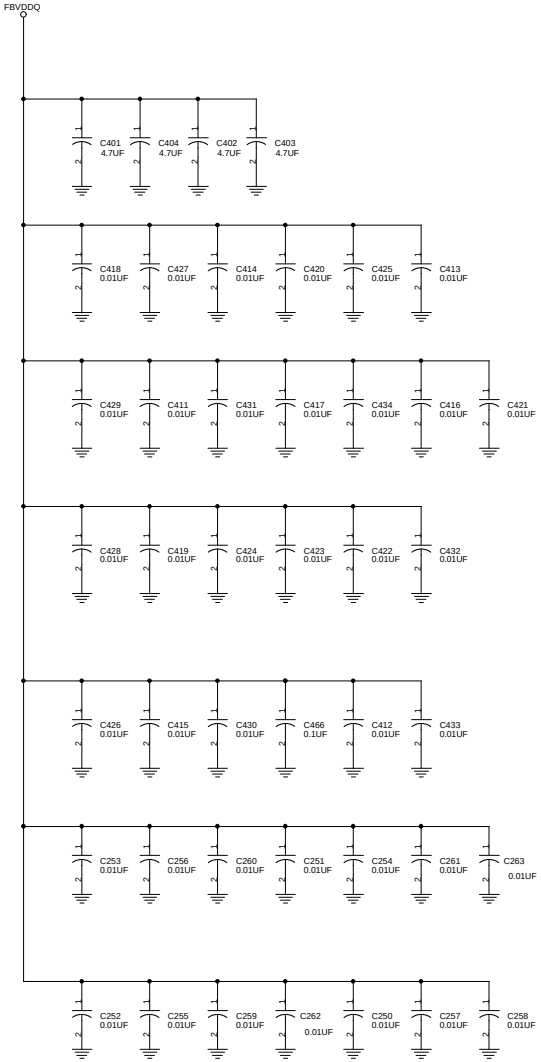

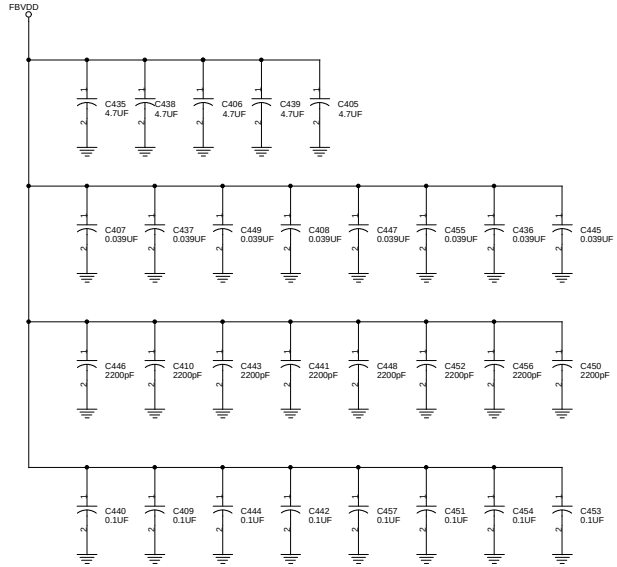

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MS-8838 based on P50A09	
Size: C	Document Number: FB DECOUPLING 4MX16 SDRAM
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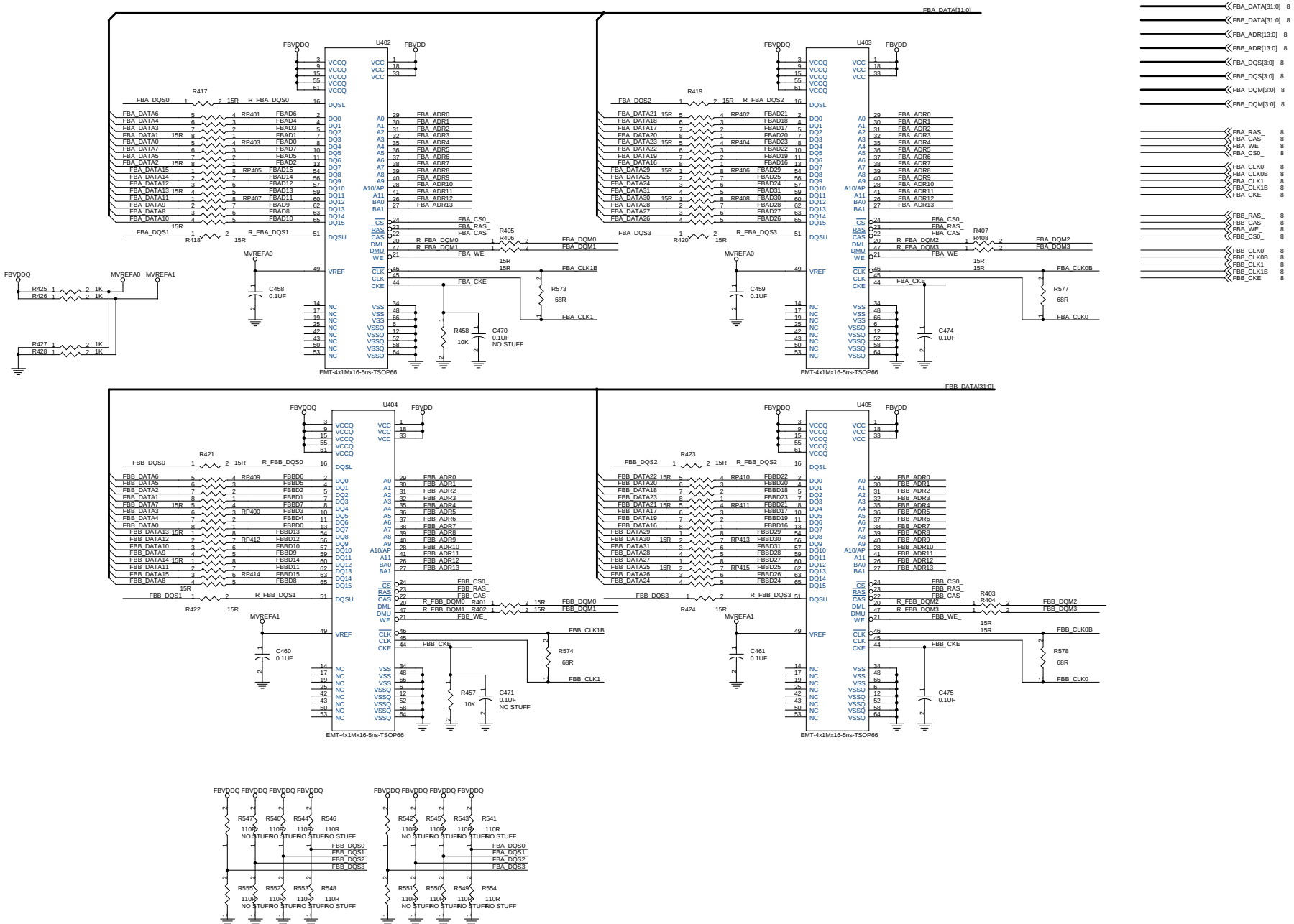

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Size: C	Document Number: FBA/B DDR 4MX16 SDRAM
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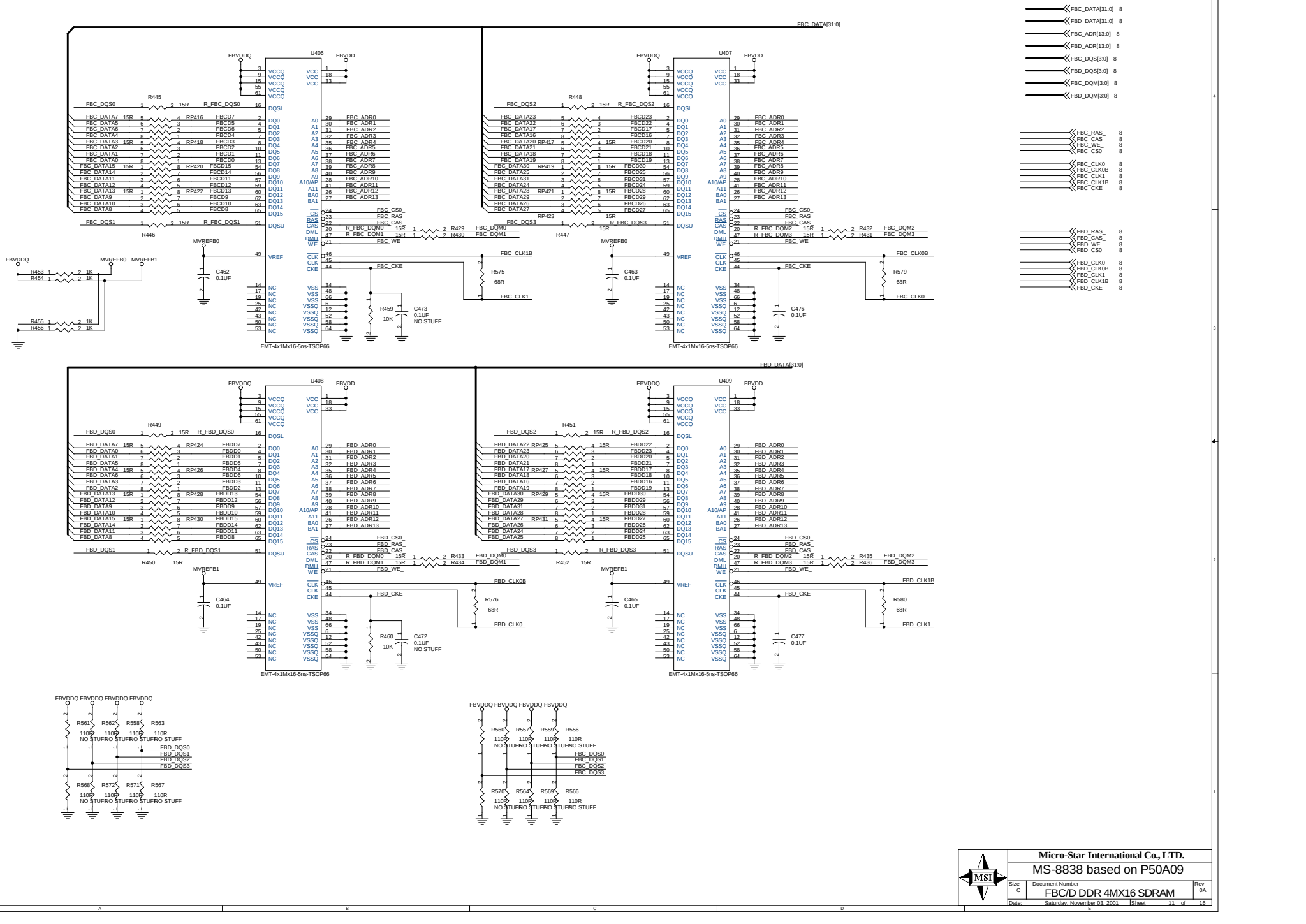

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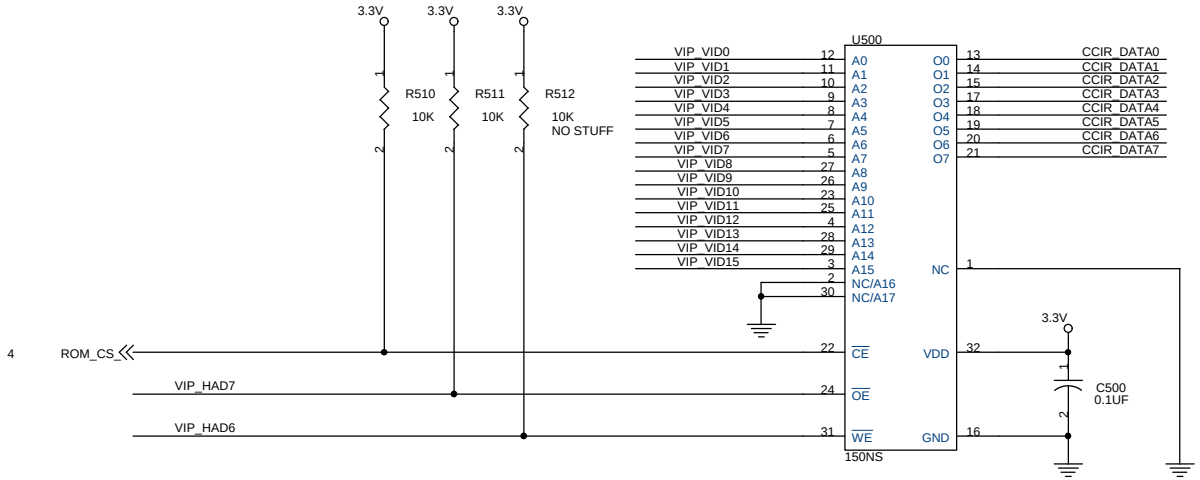
4,6,13 VIP_VID[15:0]
4,13 VIP_PCLK
4 VIP_HCLK
4 VIP_HCTL
4 VIP_HAD0
4 VIP_HAD1
4 VIP_HAD2
4,6 VIP_HAD3
4 VIP_HAD4
4 VIP_HAD5
4 VIP_HAD6
4 VIP_HAD7
4,6,13 CCIR_DATA[11:0]
4,13 CCIR_HSYNC
4,13 CCIR_VSYNC
4,13 CCIR_CLK_IN
4,13 R_CCIR_CLK_OUT
4,13 BUFFRST_
4,13 SCL
4,13 SDA



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Size: B	Document Number: ROM VIP & VIDEO IF	Rev: 0A
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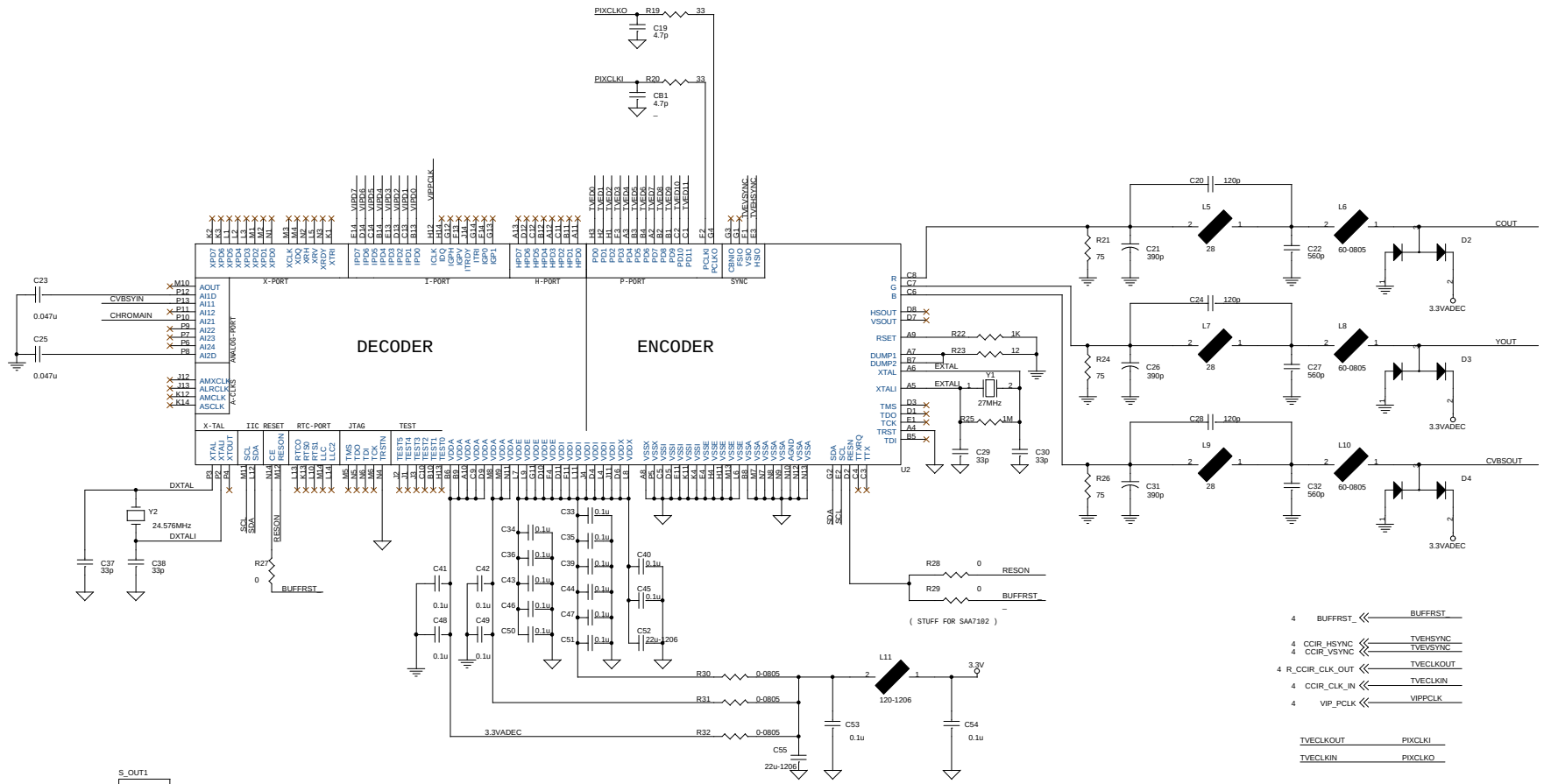

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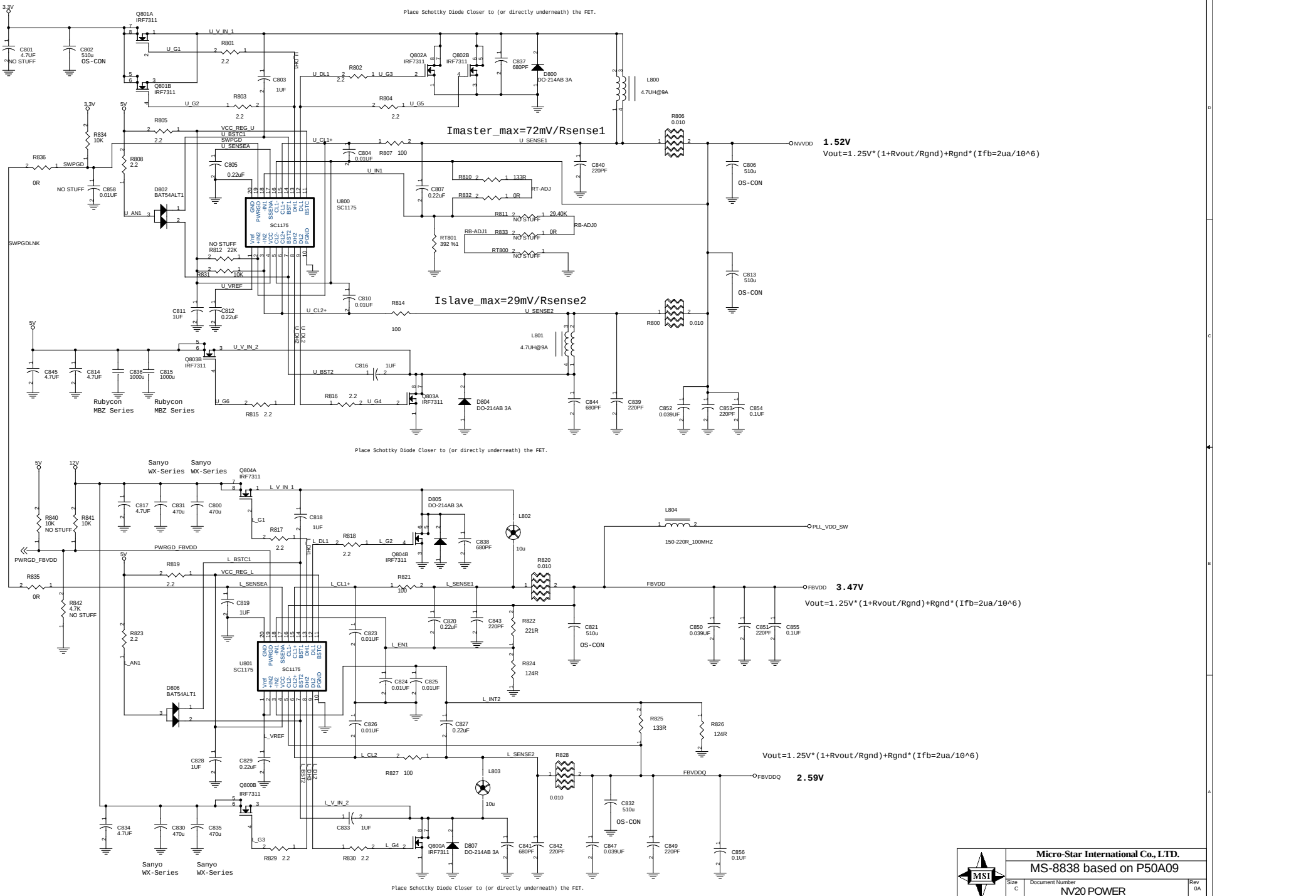
Place Schottky Diode Closer to (or directly underneath) the FET.
Imaster_max=72mV/Rsense1
Islave_max=29mV/Rsense2
1.52V
Vout=1.25V*(1+Rvout/Rgnd)+Rgnd*(Ifb=2ua/10^6)
3.47V
Vout=1.25V*(1+Rvout/Rgnd)+Rgnd*(Ifb=2ua/10^6)
2.59V
Vout=1.25V*(1+Rvout/Rgnd)+Rgnd*(Ifb=2ua/10^6)
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MS-8838 based on P50A09
NV20 POWER
Saturday, November 03, 2001

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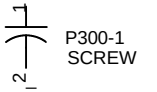
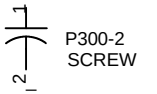
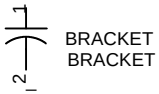


P300-1
SCREW



P300-2
SCREW

HEATSINK
HEATSINK



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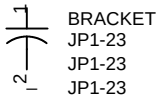

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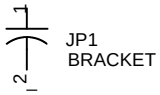
JP1-23
JP1-23
JP1-23



JP1
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Size: A	Document Number: <Doc>	Rev: 0A
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