P489-A02 DESIGN - G73, 128/256/512 MB DDR2

VGA, DVI-I, HDMI, SDTV, HDTV, SCART

PAGE SUMMARY:

V058-0A History

Page 2: reserved PCI-E B12 for M/B SPDIF IN
Page 10: add DACC SYNC BUFFER parts for new slim VGA
Page 14: add SCART J15 for 4073, removed SVIDEO/COMPOSIT connectors.
Page 16: change J6_1 for MSI SPDIF IN housing
Page 19: L10 co-layout L27 dip choke
Page 11: add common mode choke

Page 13: removed CN1 SLI CONNECTOR, change MIOA_IOVDD to 3V3_PEX
Page 17: change MIOAD0/MIOAD2 to 3V3_PEX

Page 12: add HDMI using common mode choke

VAR	VARIANT	NVPN	ASSEMBLY
B	BASE	600-10489-9998-200	G73 ALL COMPONENT BOM
1	0000	600-10489-0000-200	G73 400/400Mhz 256Mb 128bit DDR2, DVI-I+HDMI+SDTV+SCART
2	0001	600-10489-0001-200	G73 400/400Mhz 256Mb 128bit DDR2, DVI-I+HDMI+SDTV+YPrPb
3	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
4	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
5	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
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15	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>

ASSEMBLY	G73 ALL COMPONENT BOM
PAGE DETAIL	TABLE OF CONTENTS

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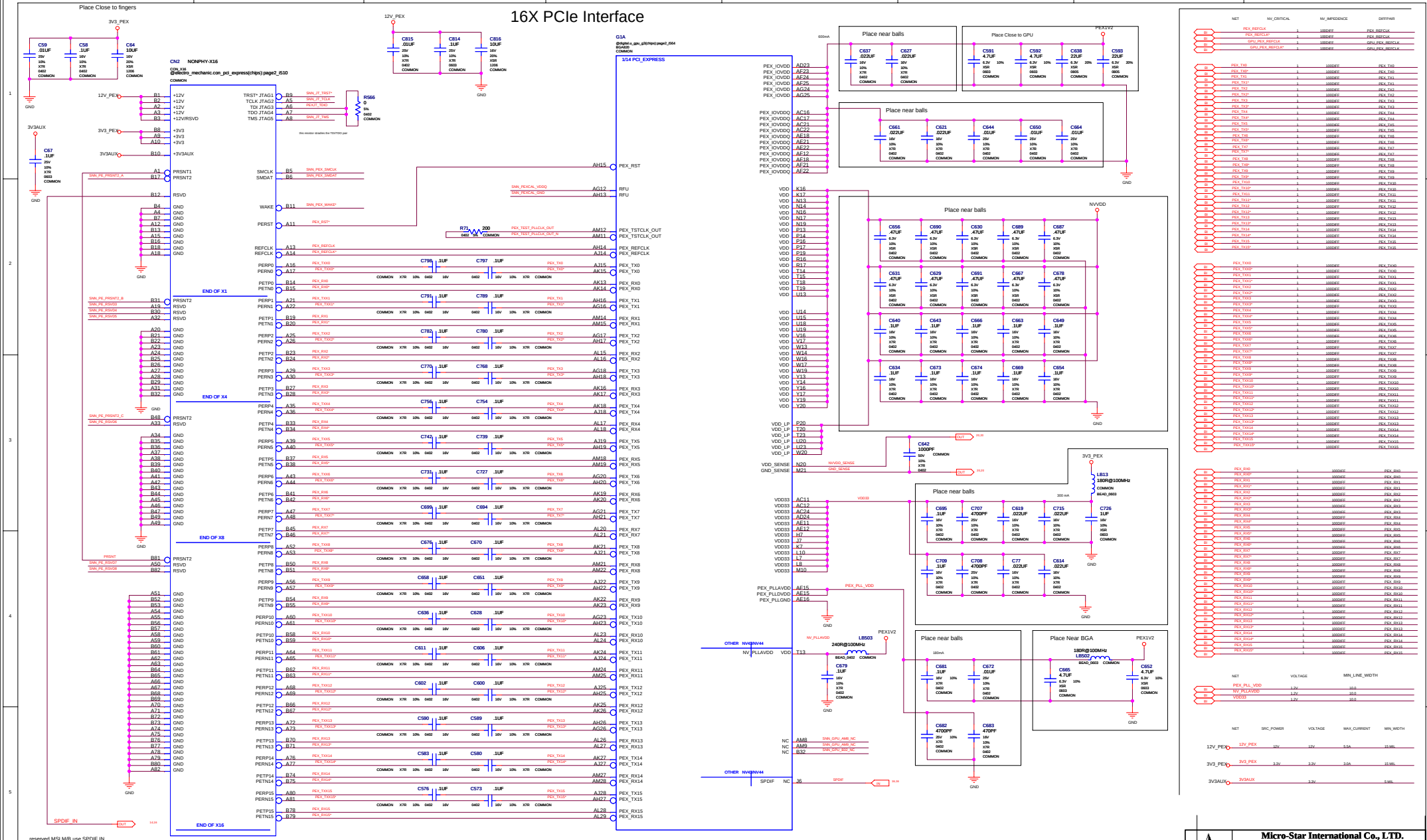
16X PCIe Interface



ASSEMBLY	G73 ALL COMPONENT BOM
PAGE DETAIL	PCI EXPRESS 16X, NVVDD DECOUPLING CAPS,PEX_IOVDD/Q DECOUPLING CAPS

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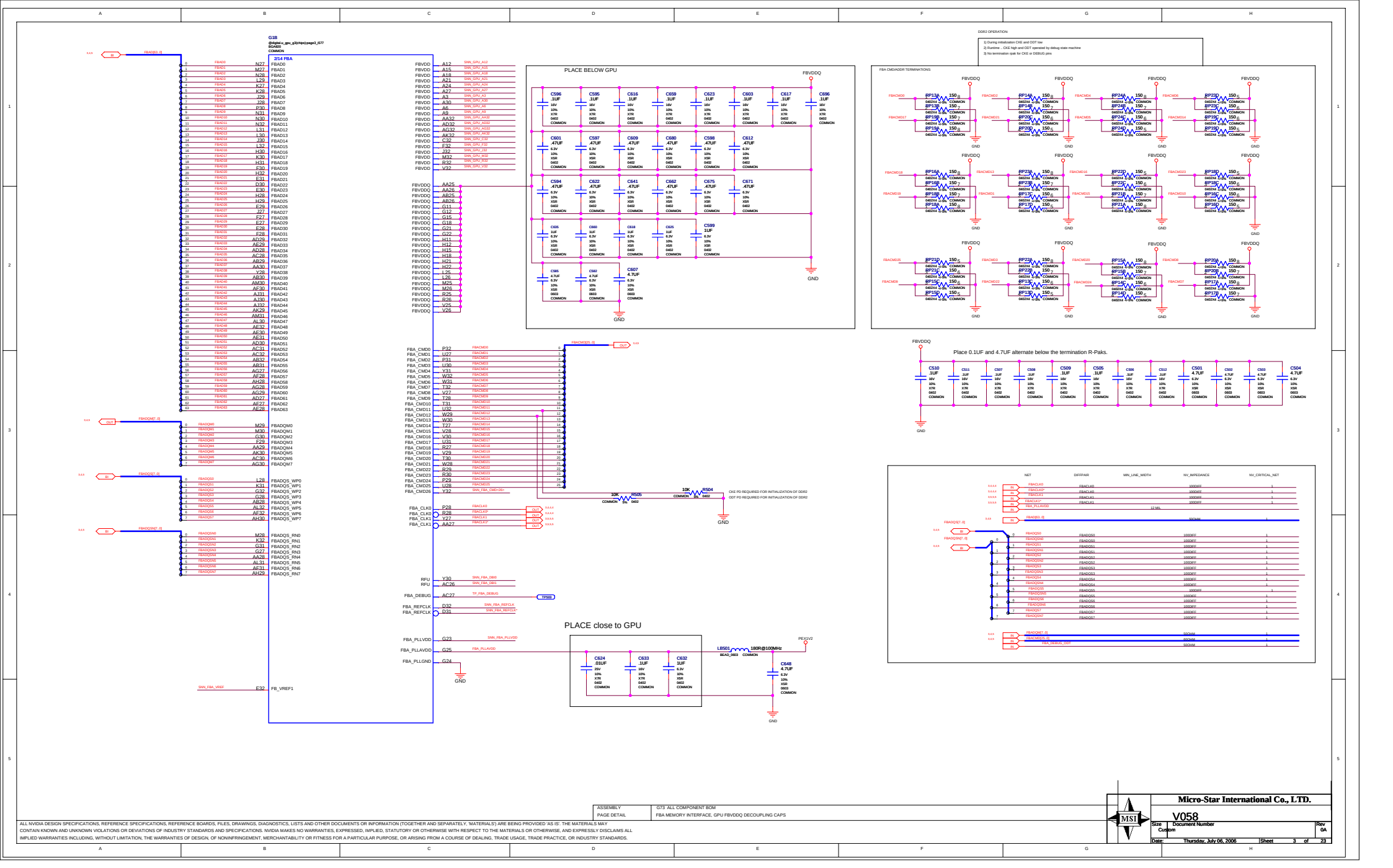

ASSEMBLY	G73_ALL COMPONENT BOM
PAGE DETAIL	FBA MEMORY INTERFACE, GPU FBVDDQ DECOUPLING CAPS

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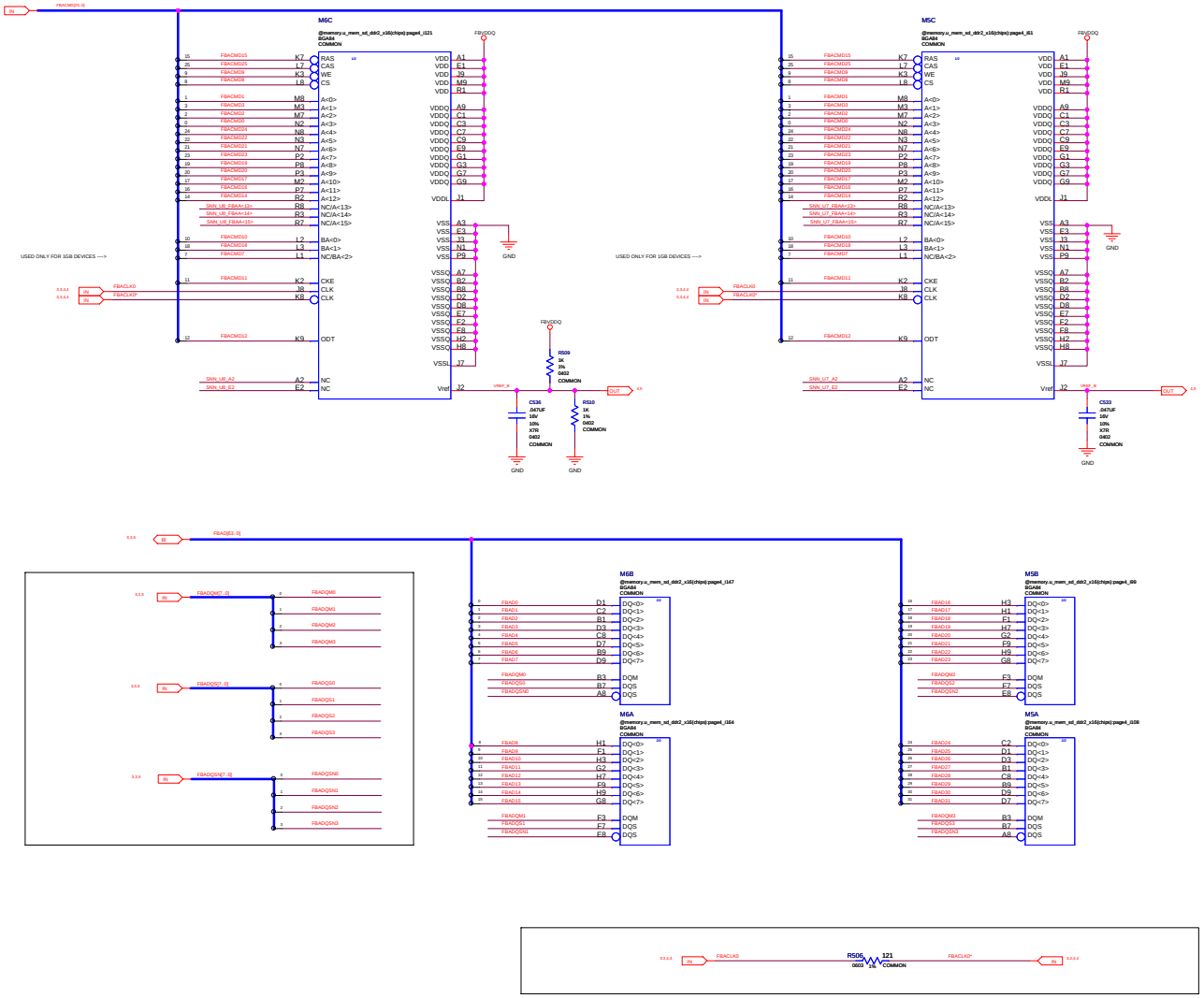
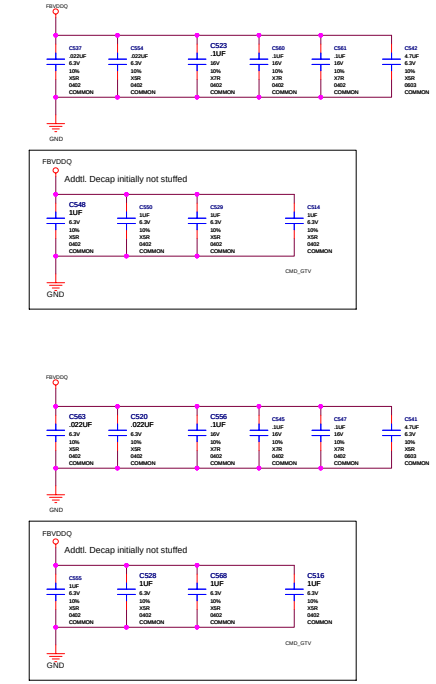
FBA MEMORY 1st bank 0..31

PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY





ASSEMBLY	G73 ALL COMPONENT BOM
PAGE DETAIL	FBA 16Mx16 DDR2 MEMORIES, 1ST BANK 0..31

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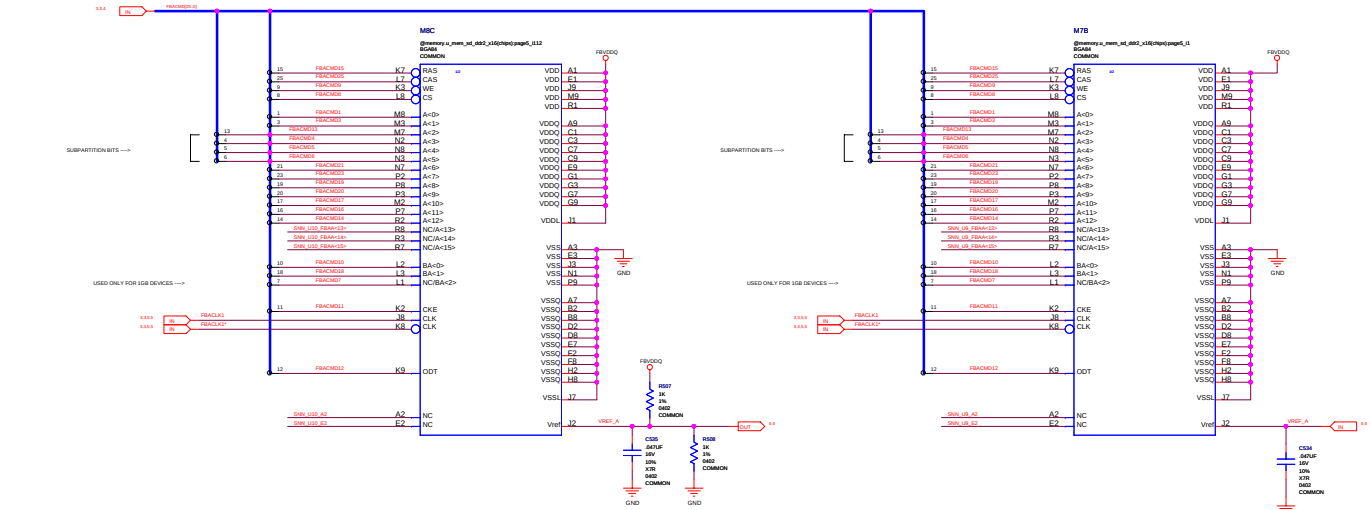
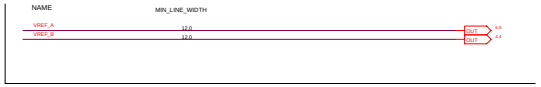
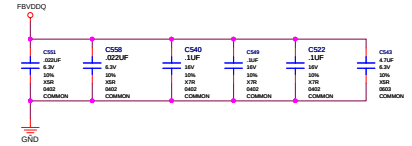
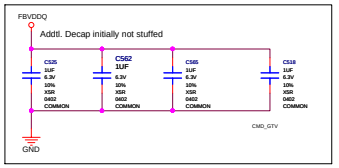
FBA MEMORY 1st bank 32..63

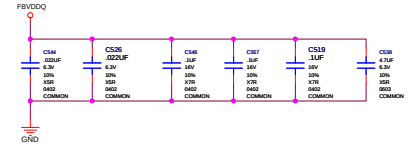
PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY

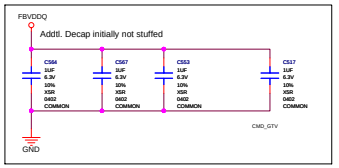












ASSEMBLY	G73 ALL COMPONENT BOM
PAGE DETAIL	FBA 16Mx16 DDR2 MEMORIES, 1ST BANK 32..63

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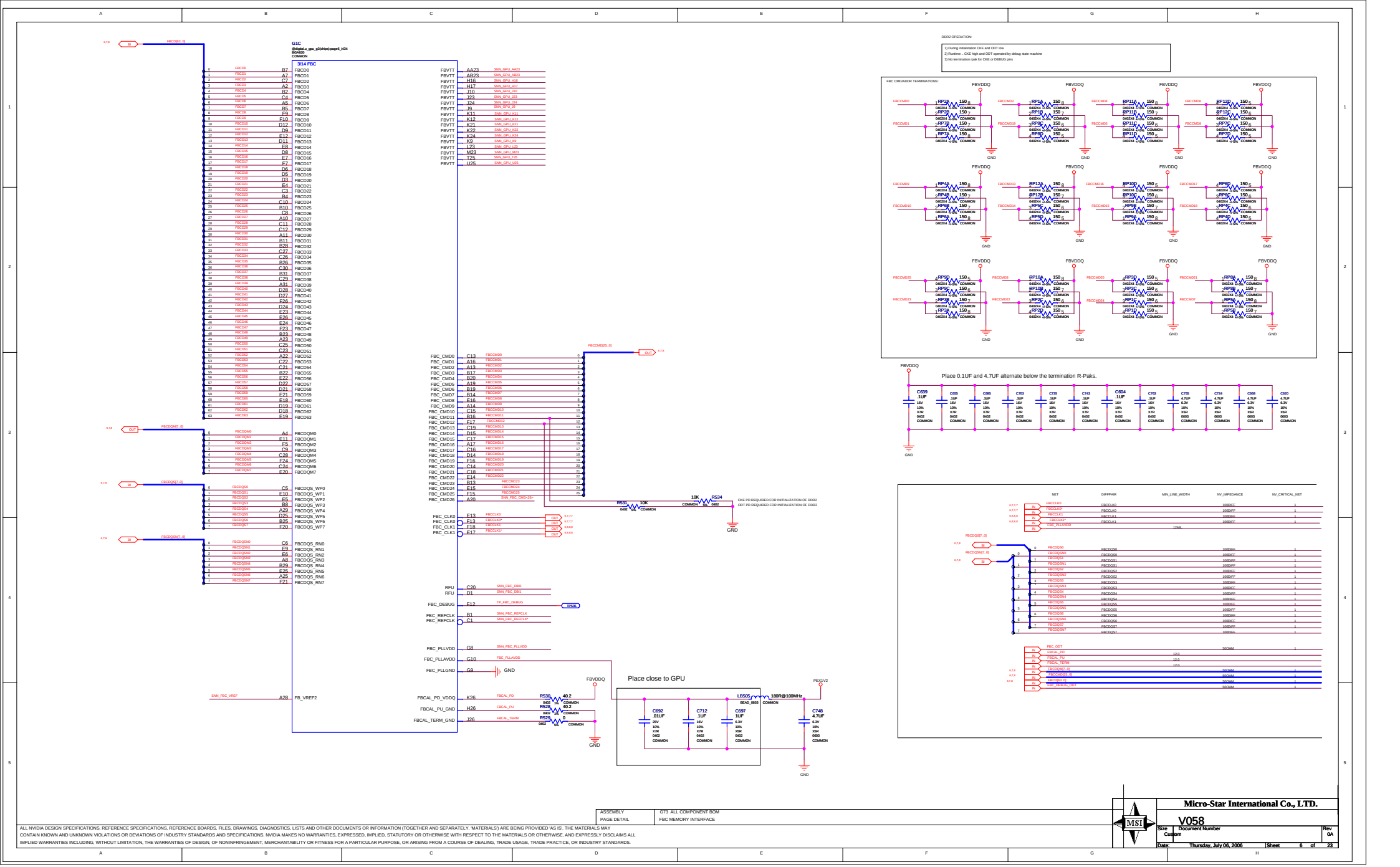

ASSEMBLY	G73 ALL COMPONENT BOM
PAGE DETAIL	FBC MEMORY INTERFACE

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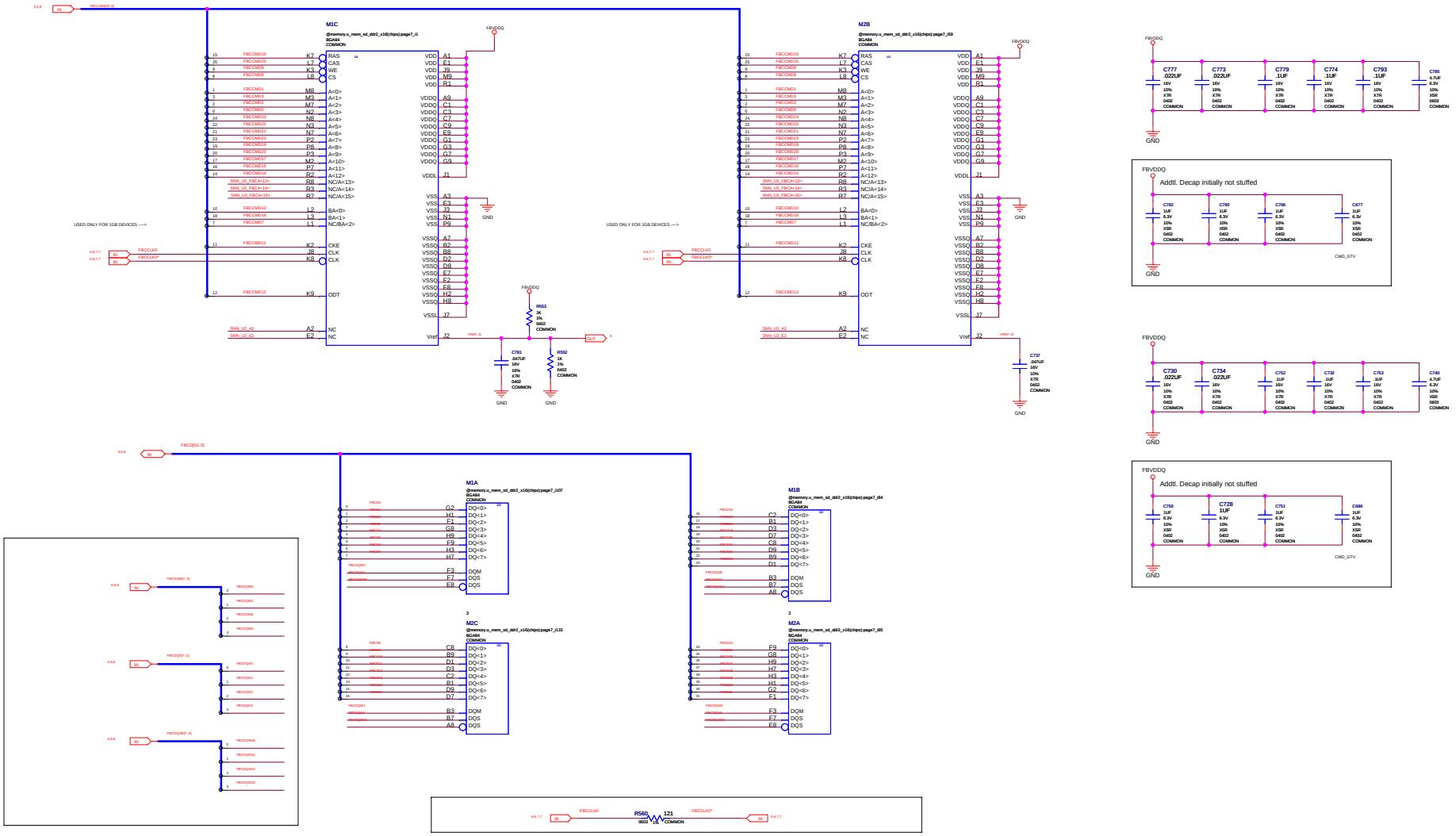
FBC MEMORY 2nd bank 0..31

PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY



ASSEMBLY	G73 ALL COMPONENT BOM
PAGE DETAIL	FBC 16MX16 DDR2 MEMORIES, 2ND BANK 0..31

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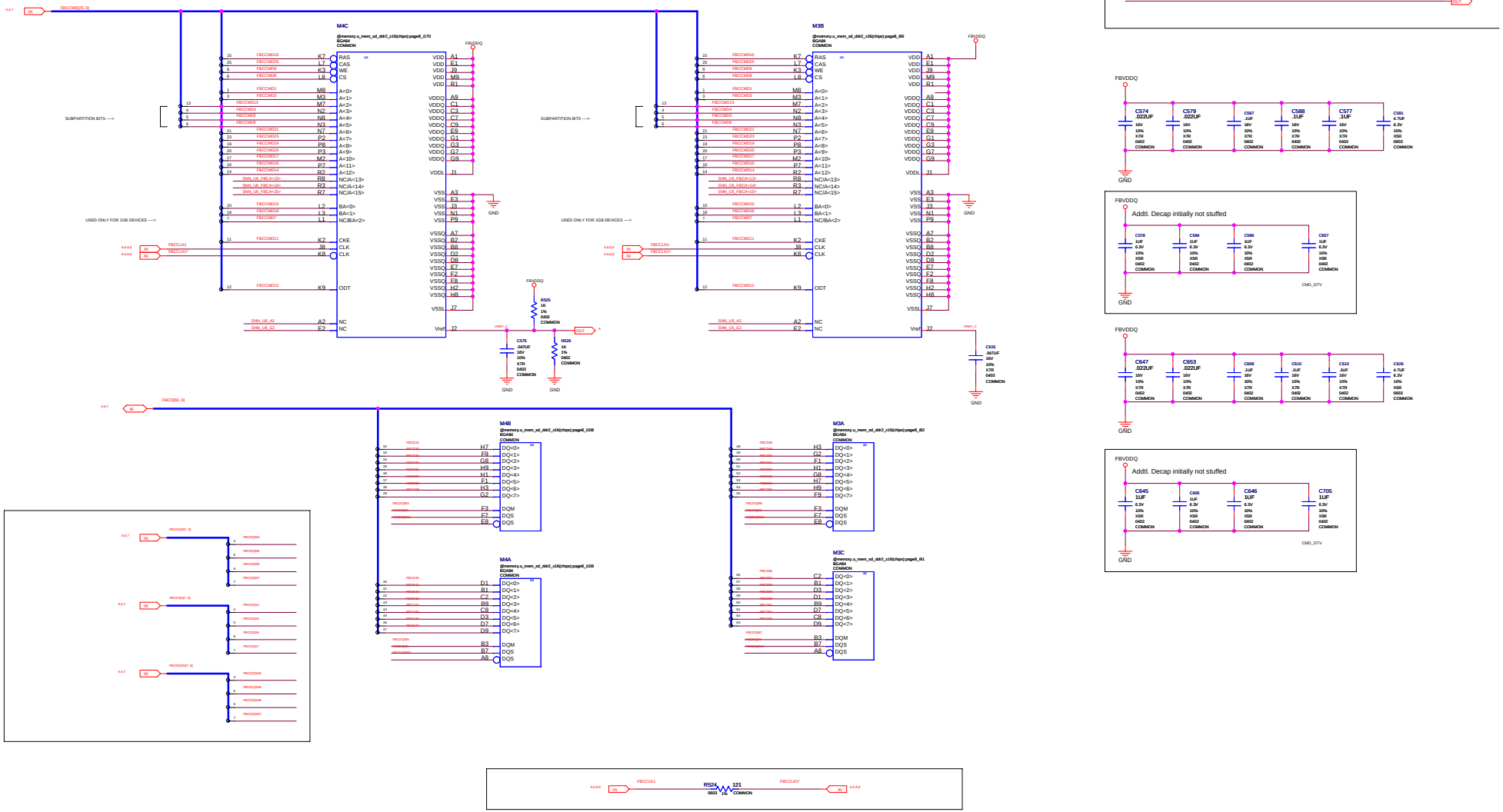
FBC MEMORY 2nd bank 32..63

PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY



NAME MIN_LINE_WIDTH
VREF_C 12.0
VREF_D 12.0

FBVDDQ

C574 .022UF 16V 10% X7R 0402 COMMON
C579 .022UF 16V 10% X7R 0402 COMMON
C587 .1UF 16V 10% X7R 0402 COMMON
C588 .1UF 16V 10% X7R 0402 COMMON
C577 1UF 16V 10% X7R 0402 COMMON
C581 4.7UF 6.3V 10% X5R 0603 COMMON

Addtl. Decap initially not stuffed

C578 1UF 6.3V 10% X5R 0402 COMMON
C584 1UF 6.3V 10% X5R 0402 COMMON
C585 1UF 6.3V 10% X5R 0402 COMMON
C657 1UF 6.3V 10% X5R 0402 COMMON
CMD_GTV

C647 .022UF 16V 10% X7R 0402 COMMON
C653 .022UF 16V 10% X7R 0402 COMMON
C608 .1UF 16V 10% X7R 0402 COMMON
C610 .1UF 16V 10% X7R 0402 COMMON
C613 .1UF 16V 10% X7R 0402 COMMON
C626 4.7UF 6.3V 10% X5R 0603 COMMON

Addtl. Decap initially not stuffed

C645 1UF 6.3V 10% X5R 0402 COMMON
C605 1UF 6.3V 10% X5R 0402 COMMON
C646 1UF 6.3V 10% X5R 0402 COMMON
C705 1UF 6.3V 10% X5R 0402 COMMON
CMD_GTV

ASSEMBLY: G73 ALL COMPONENT BOM
PAGE DETAIL: FBC 16Mx16 DDR2 MEMORIES, 2ND BANK 32..63

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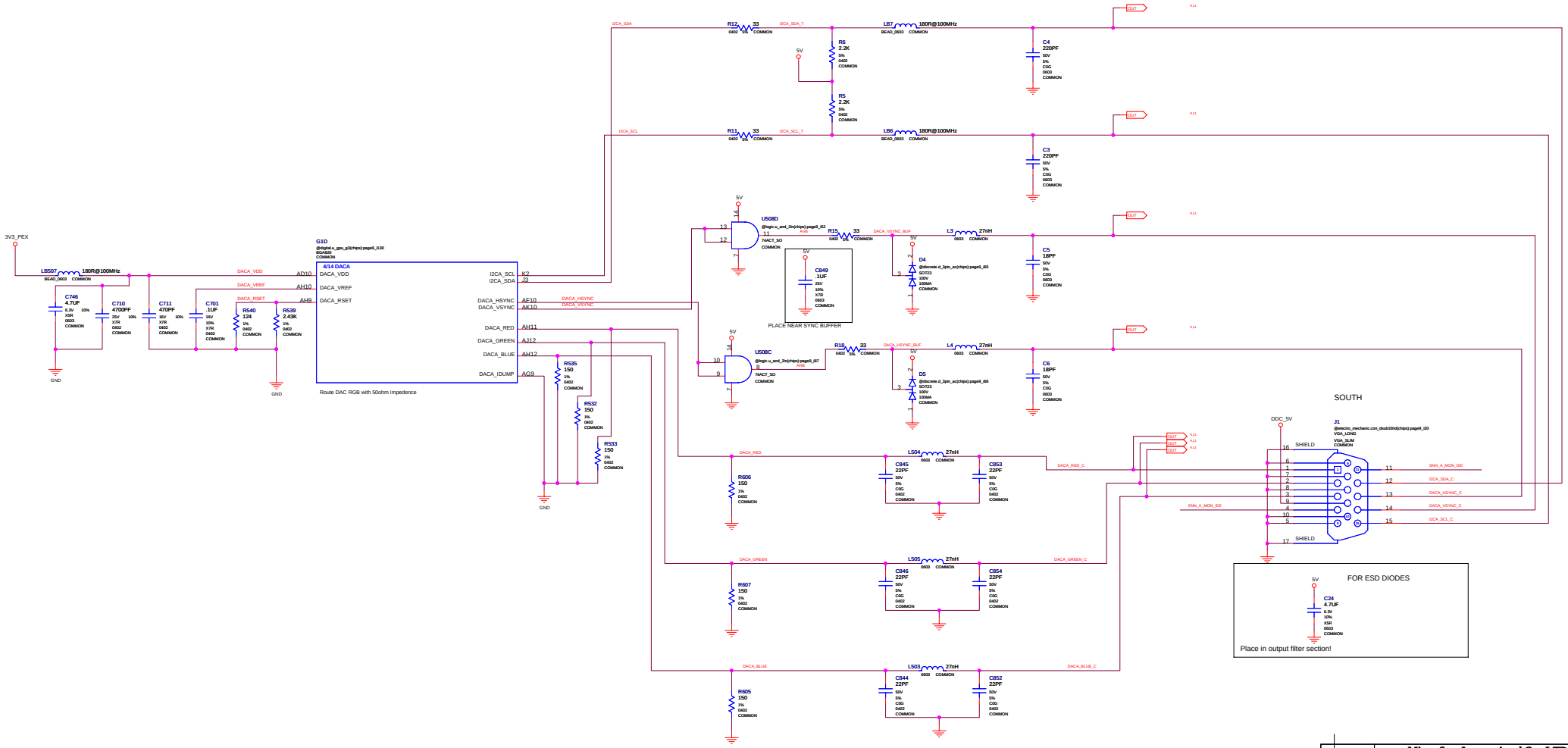
Primary Display (DACA), Slim DB15

DACA NET RULES

NET	NV_CRITICAL	NV_IMPEDANCE	DIFFPAIR
DACA_RED	1	75OHM	
DACA_GREEN	1	75OHM	
DACA_BLUE	1	75OHM	
DACA_RED_C	1	75OHM	
DACA_GREEN_C	1	75OHM	
DACA_BLUE_C	1	75OHM	

NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
DACA_VDD	3.30000	0.07	16.0

DACA RGB-FILTER



ASSEMBLY	G73_ALL COMPONENT BOM
PAGE DETAIL	DACA FILTERS, DACA SYNC BUFFERS & DB15 SOUTH

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DACB for TVout, DACC unused.

DACC NET RULES

NET	NV_CRITICAL	NV_IMPEDANCE	DIFFPAIR
DACC_RED	1	75OHM	
DACC_GREEN	1	75OHM	
DACC_BLUE	1	75OHM	
DACC_RED_C	1	75OHM	
DACC_GREEN_C	1	75OHM	
DACC_BLUE_C	1	75OHM	

NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
DACC_VDD	3.30000	0.07	16.0

Page14 I2C_C parts

remove r538
add DAC C parts.

SOUTH

FOR ESD DIODES

Place in output filter section!

Route DAC RGB with 50ohm impedance

PLACE NEAR SYNC BUFFER

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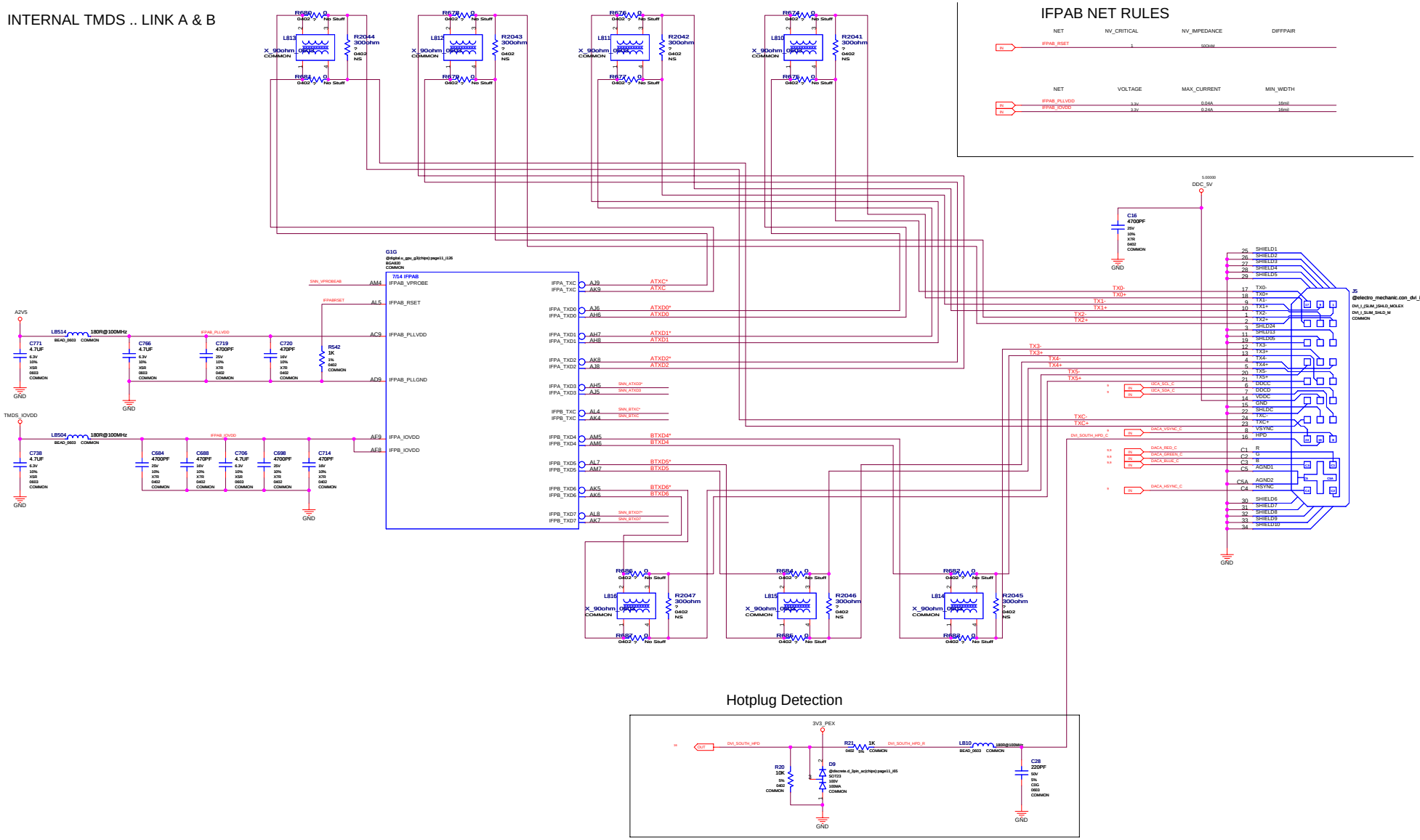
INTERNAL TMDS .. LINK A & B



Hotplug Detection

ASSEMBLY: G73_ALL COMPONENT BOM
PAGE DETAIL: TMDS LINK A & B, DVI CONNECTOR

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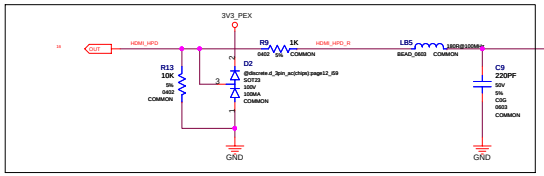
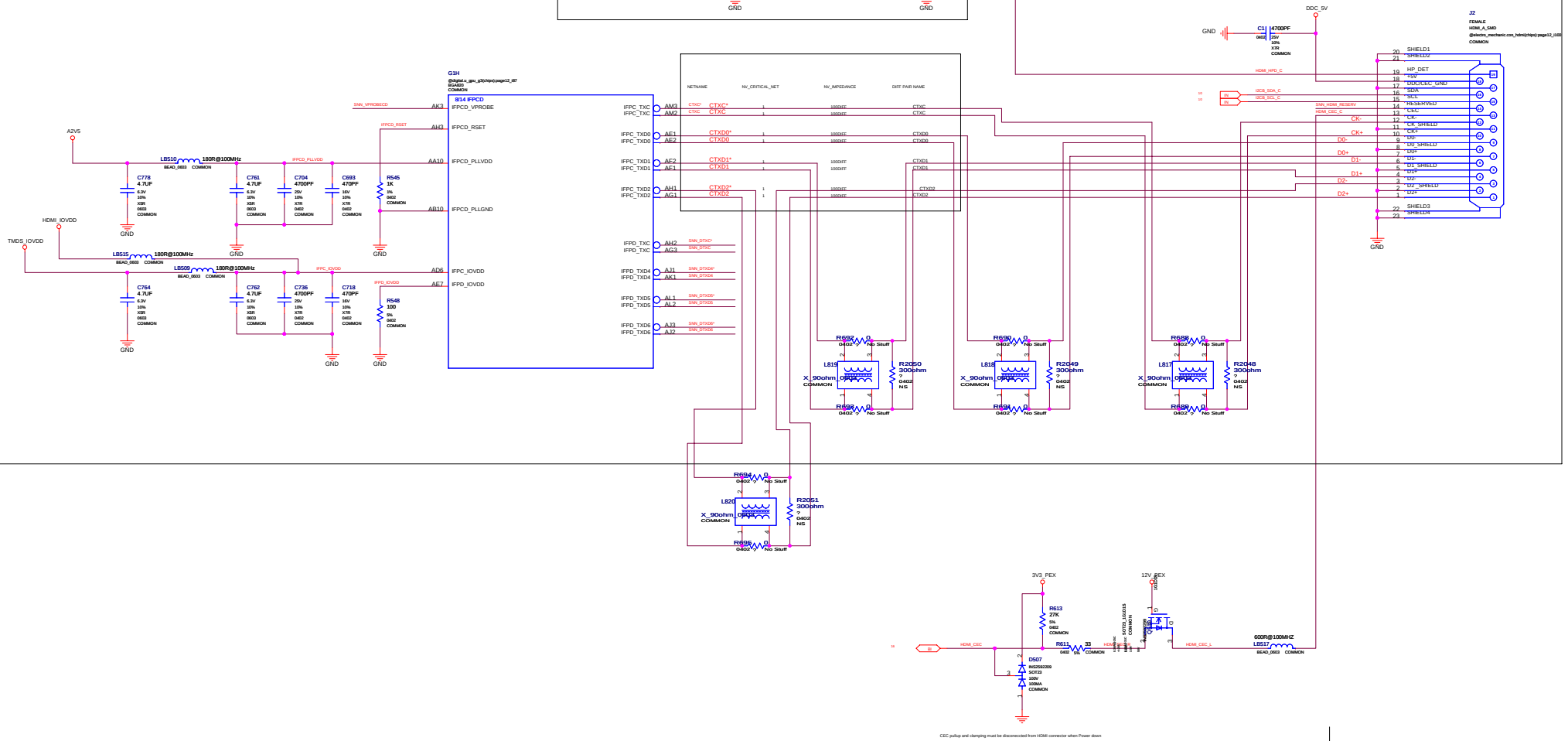
INTERNAL TMDS .. LINK C

Hotplug Detection



NET RULES

NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
IFPCD_PLLVDD	3.30000	0.04	10.0
IFPD_IOVDD	0.000000	0	10.0
IFPC_IOVDD	3.30000	0.15	10.0



CEC pullup and clamping must be disconnected from HDMI connector when Power down

ASSEMBLY	G73 ALL COMPONENT BOM
PAGE DETAIL	TMDS LINK C, HDMI CONNECTOR MID

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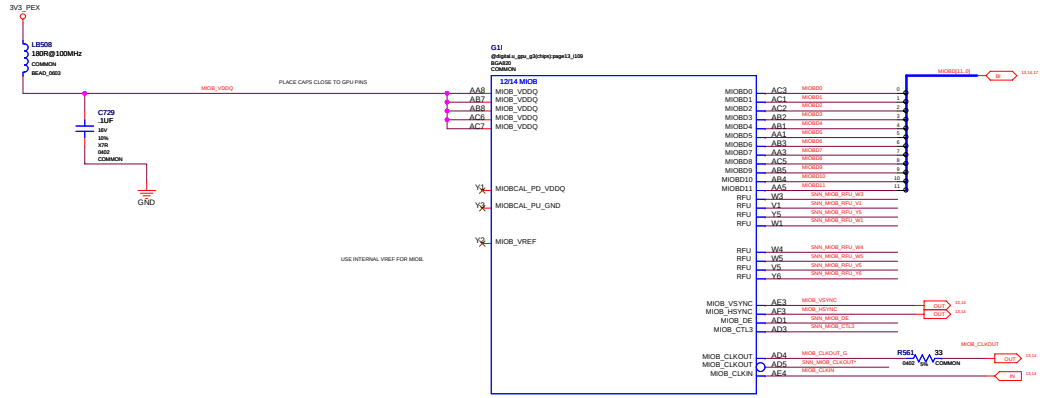
G73 VIP/MIOB

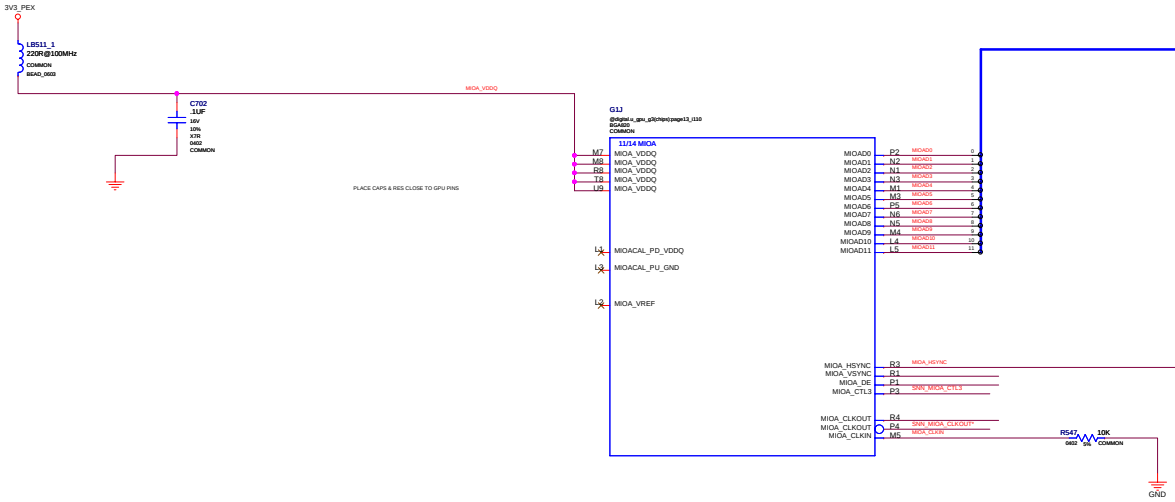


MIO NET RULES

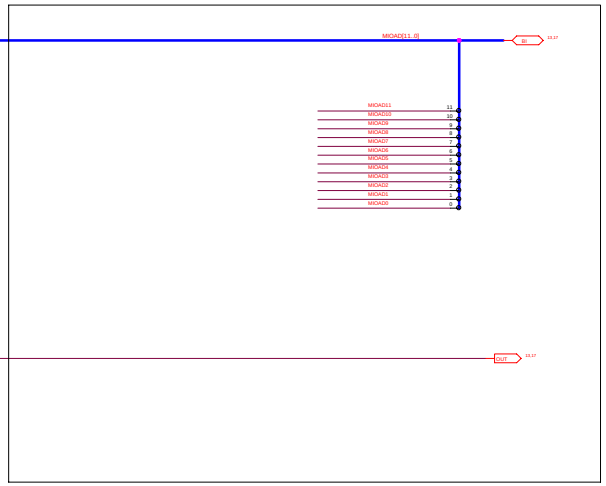
NET	NV_CRITICAL	NV_IMPEDANCE	DIFFPAIR
MIOAD[11..0]	2	50OHM	
MIOA_CLKIN	2	50OHM	
MIOA_CLKOUT	2	50OHM	
MIOA_DE	2	50OHM	
MIOA_HSYNC	2	50OHM	
MIOA_VSYNC	2	50OHM	
MIOBD[11..0]	2	50OHM	
MIOB_CLKIN	2	50OHM	
MIOB_CLKIN_G	2	50OHM	
MIOB_CLKOUT	2	50OHM	
MIOB_CLKOUT_G	2	50OHM	
MIOB_HSYNC	2	50OHM	
MIOB_VSYNC	2	50OHM	

NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
MIOA_VDDQ	2.5V	0.8A	12mil
MIOA_VREF	1.25V		12mil
MIOACAL_PD_VDDQ	2.5V		12mil
MIOACAL_PU_GND	0V		12mil
MIOB_VDDQ	3.3V	0.05A	12mil
MIOBCAL_PD_VDDQ	3.3V		12mil
MIOBCAL_PU_GND	0V		12mil

G73 MIOA



SLI Connector



ASSEMBLY	G73_ALL COMPONENT BOM
PAGE DETAIL	MIOA & MIOB, SLI CONNECTOR

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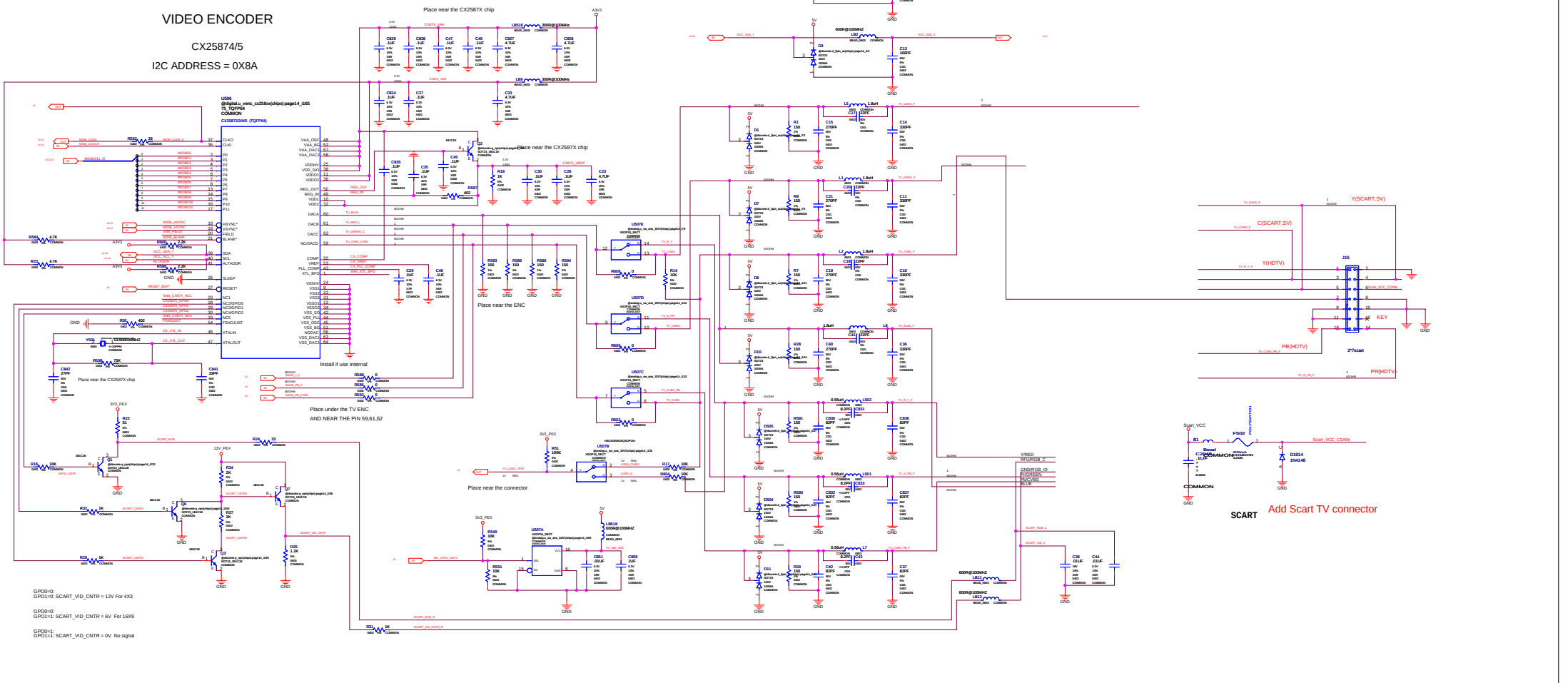
External TV Encoder

VIDEO ENCODER

CX25874/5

I2C ADDRESS = 0X8A



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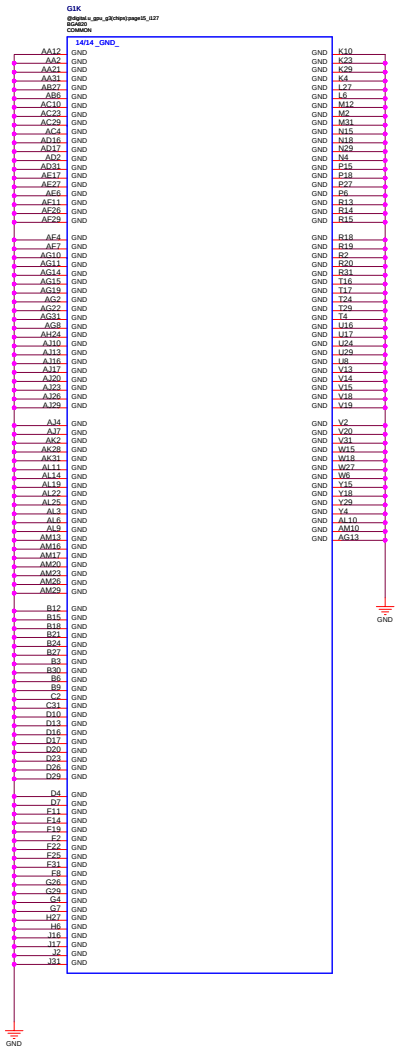

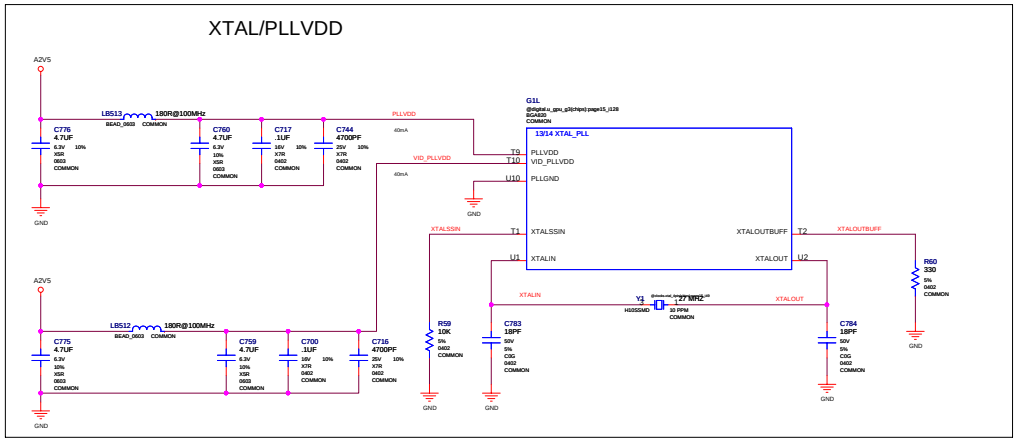

NET	NV_CRITICAL	NV_IMPEDANCE	DIFFPAIR
XTALSSIN	1	90OHM	
XTALIN	1	90OHM	
XTALOUT	1	90OHM	
XTALOUTBUFF	1	90OHM	

NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
PLL_VDD	2.5V	0.3	12mil
VID_PLLVDD	2.5V	0.3	12mil

ASSEMBLY	G73 ALL COMPONENT BOM
PAGE DETAIL	GPU GND CONNECTION, XTAL

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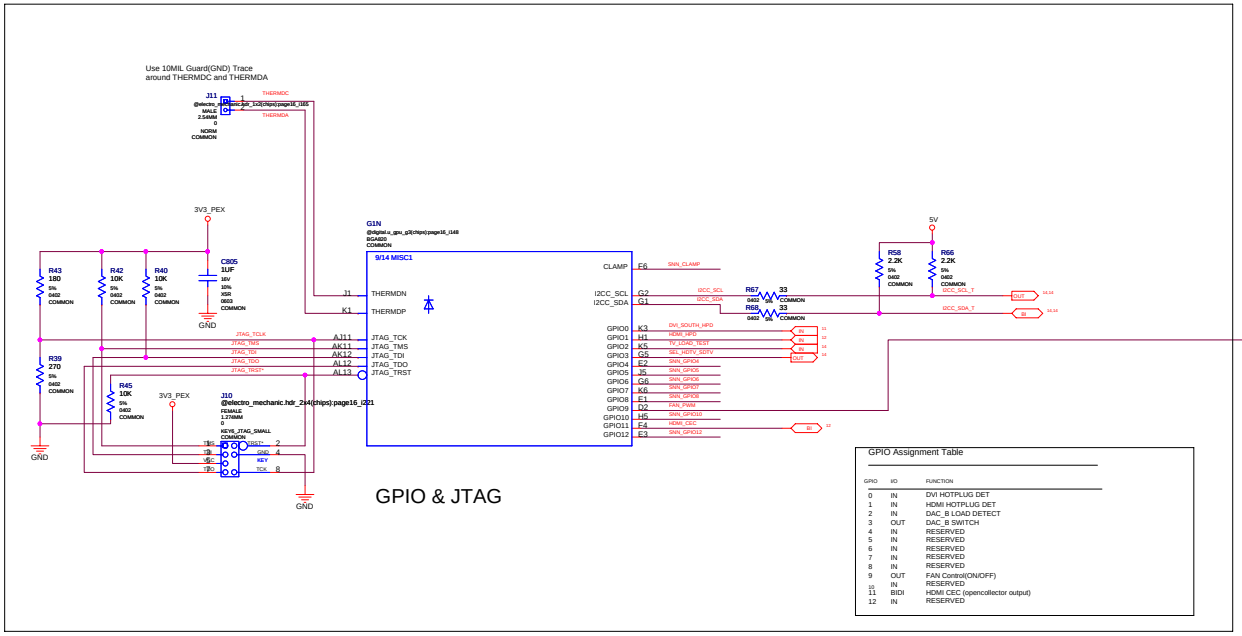

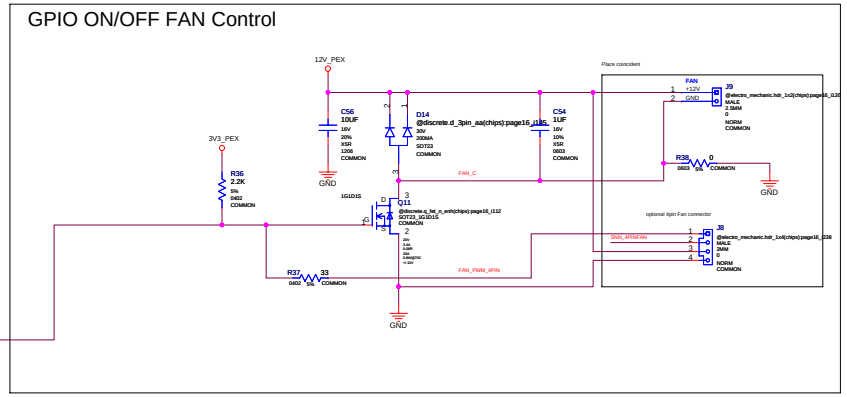

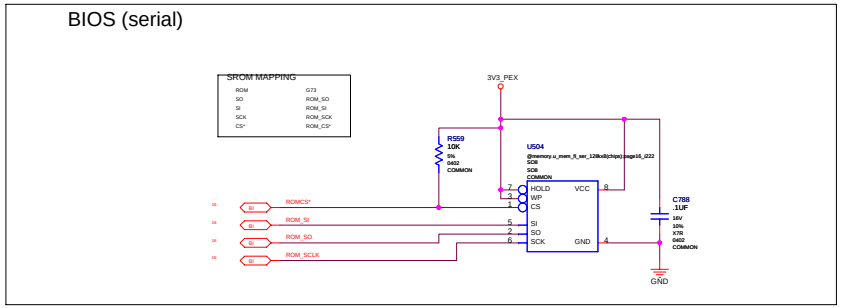

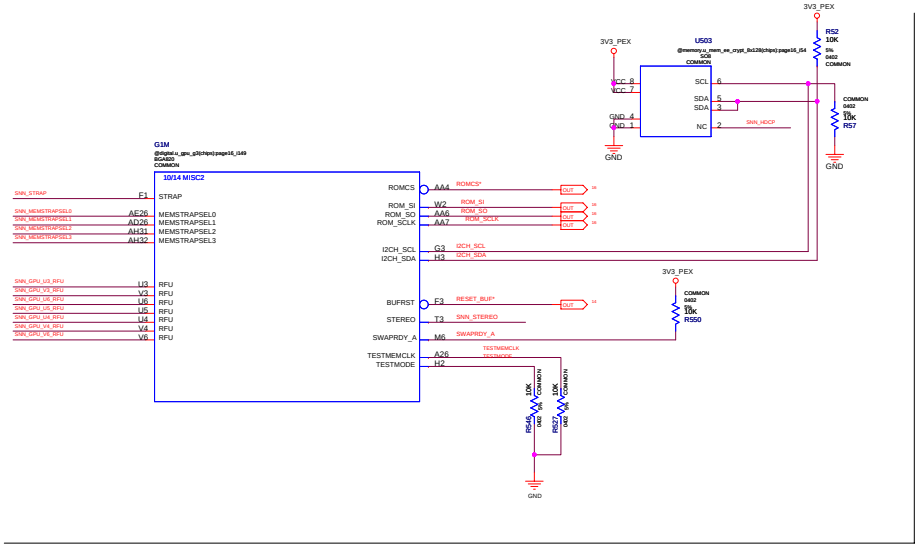

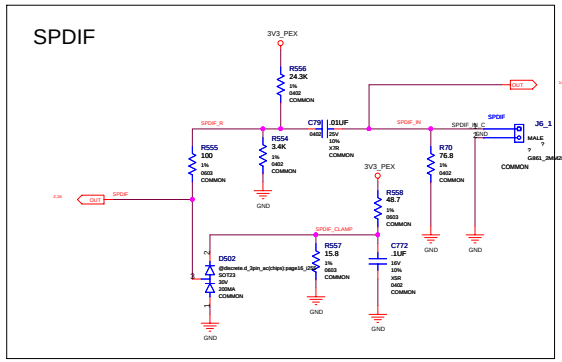

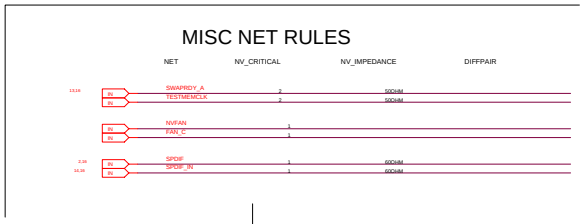

ASSEMBLY: G73 ALL COMPONENT BOM
PAGE DETAIL: JTAG, BIOS ROM, HDCP ROM, FAN CONTROL, GPIO's

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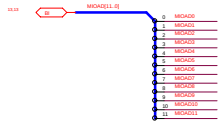
BIOS, Strapping options, Misc

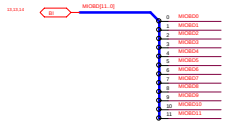
Straps

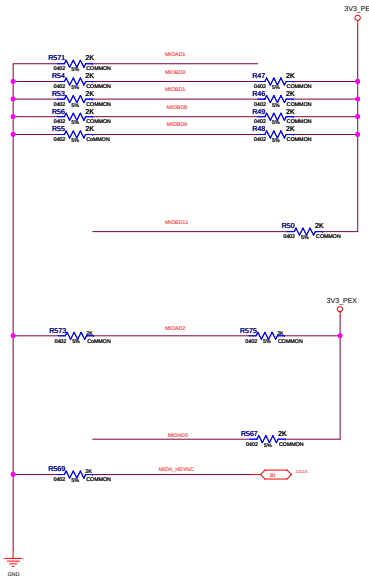
Assembly: BIOS

Pullups to 3V3



Pullups to 2V5





REG: NV_PEXTDEV_BOOT_0

Bit	Signal	VALUE_ID	VALUEs
01: SUB_VENDOR	SUB_VENDOR	0 NO_BIOS 1 read from BIOS	
02: RAM_CFG_0	RAM_CFG[3:0]	0000 64Mx16 DDR2 128MT	1000 64Mx16 DDR2 64BIT
		0001 RFU	1001 RFU
03: RAM_CFG_1		0010 128Mx16 DDR2 128MT	1010 128Mx16 DDR2 64BIT
		0011 RFU	1011 RFU
04: RAM_CFG_2		0100 RFU	1100 RFU
		0101 RFU	1101 RFU
05: RAM_CFG_3		0110 RFU	1110 RFU
		0111 256Mx16 DDR2 128BIT	1111 32Mx16 DDR2 64BIT

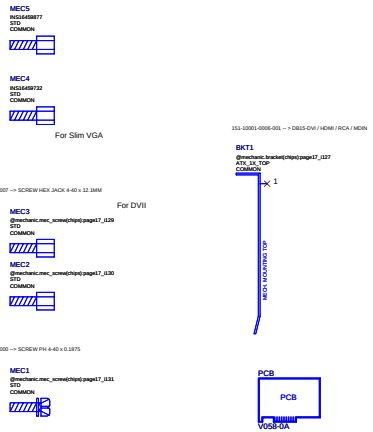
12: PCI_DEVID_0	PCI_DEVID[3:0]	0000 0x140 ... 1111 0x014F 0000 (default 0x140)
13: PCI_DEVID_1		0101 (0x145)
20: PCI_DEVID_2		
21: PCI_DEVID_3		

16: USER_0	STRAP_USER[3:0]	0000 (default)
17: USER_1		
18: USER_2		
19: USER_3		

REG: NV_STRAP_1

Bit	Signal	VALUE_ID	VALUEs
11: PEX_PLL_EN_TERM100			0 (default – internal term on) 1 internal term off
16: SLOT_CLOCK_CFG			0 COMMON PCIE REF CLK enabled 1 COMMON PCIE REF CLK disabled

Mechanical parts



ASSEMBLY	G73 ALL COMPONENT BOM
PAGE DETAIL	BIOS STRAPS & MECHANICALS

Micro-Star International Co., LTD.

V058

Size	Document Number	Rev
Custom		0A

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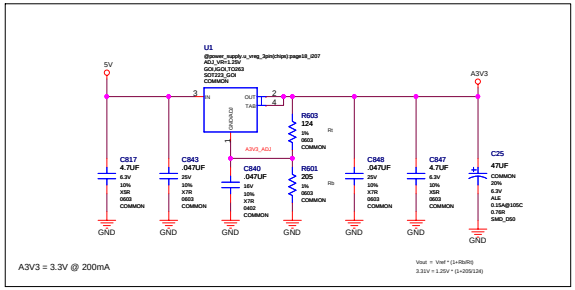
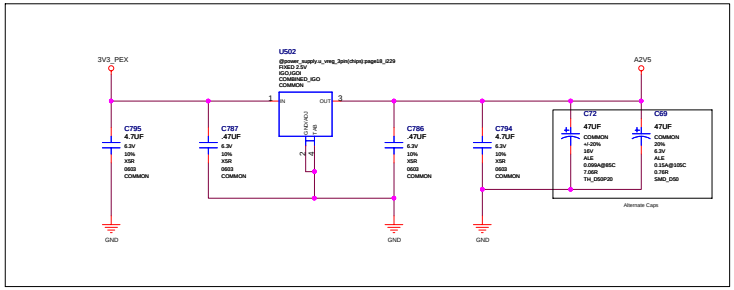
Power Supply I: TMDS/A3V3/A2V5

NETNAME	MAX_CURRENT	MIN_LINE_WIDTH	VOLTAGE
5V_FUSED	0.1	12 MIL	5V
DDC_5V	0.1	12 MIL	5V
A2V5	0.96	15 MIL	2.5V
HDMI_IOVDD	0.15	15 MIL	3.3V
TMDS_IOVDD	0.24	15 MIL	3.3V
A3V3	0.4	15 MIL	3.3V
GND		15 MIL	0V
A3V3_ADJ		10 MIL	1.25V

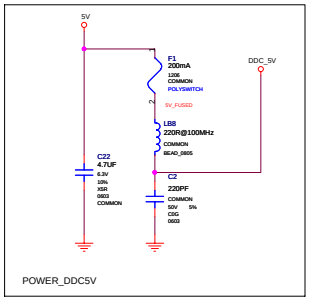
A3V3 Power Supply



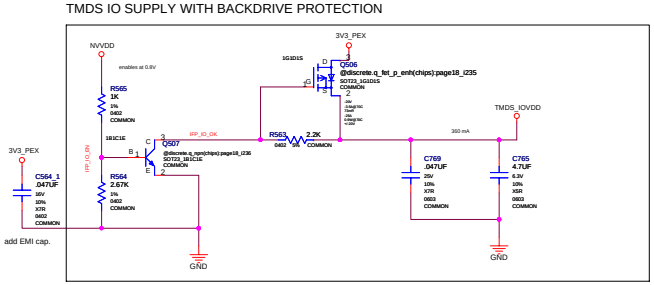
A2V5 Power Supply



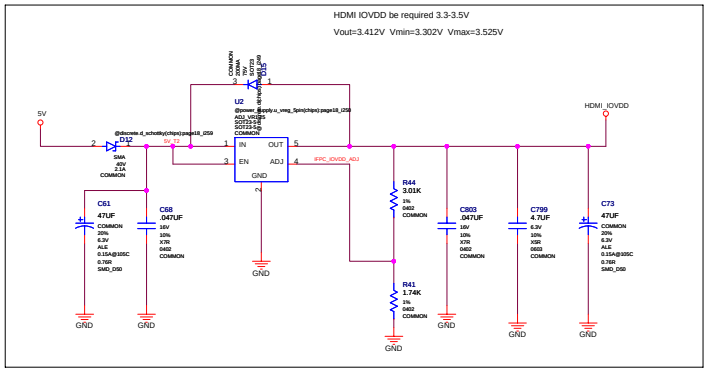
DDC 5V



TMDS IO SUPPLY WITH BACKDRIVE PROTECTION



HDMI IO SUPPLY WITH BACKDRIVE PROTECTION



ASSEMBLY	G73_ALL COMPONENT BOM
PAGE DETAIL	POWER SUPPLY I: DDC5V, TMDS_IO_VDD, A2V5, A3V3

Micro-Star International Co., LTD.

V058

Size	Document Number	Rev
Custom		0A

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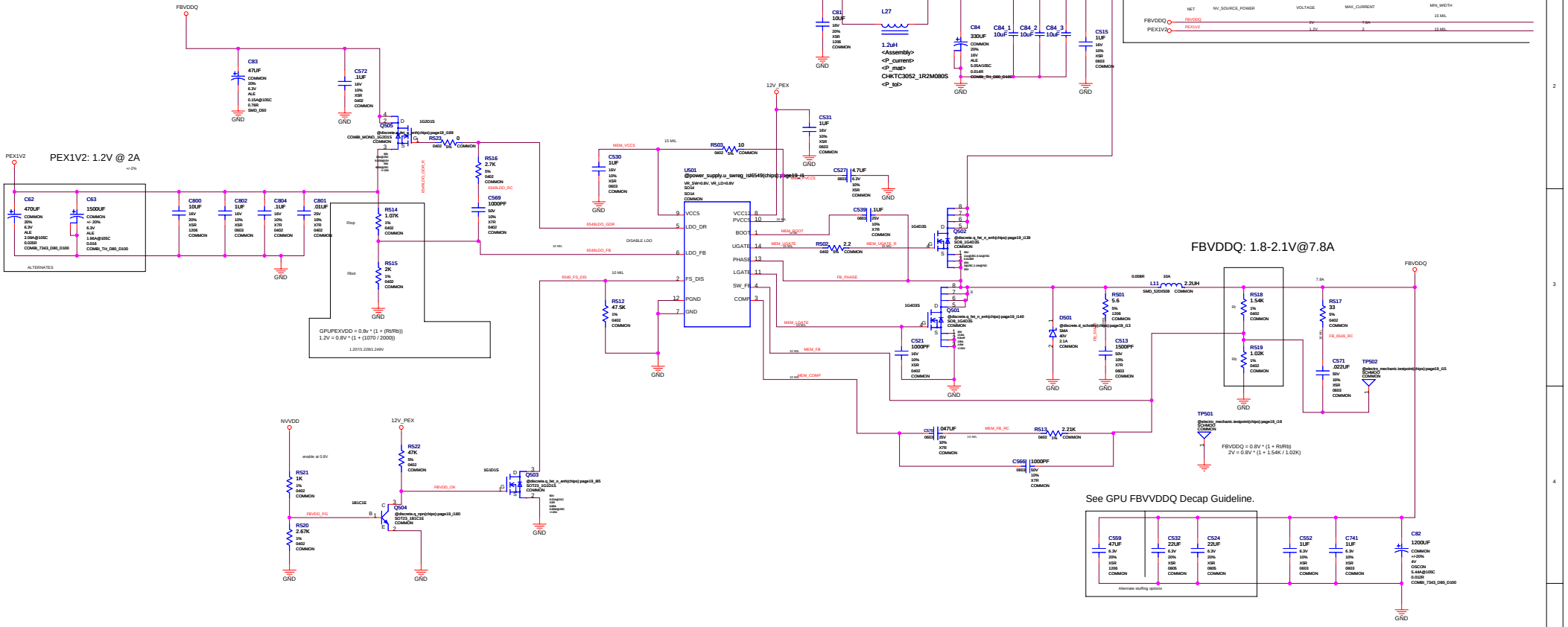
PowerSupply II: ISL6549 FBVDDQ/PEX1V2



ASSEMBLY	G73 ALL COMPONENT BOM
PAGE DETAIL	POWER SUPPLY II: ISL6549 FBVDDQ, PEX1V2

Micro-Star International Co., LTD.

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Size	Document Number	Rev
Custom		0A

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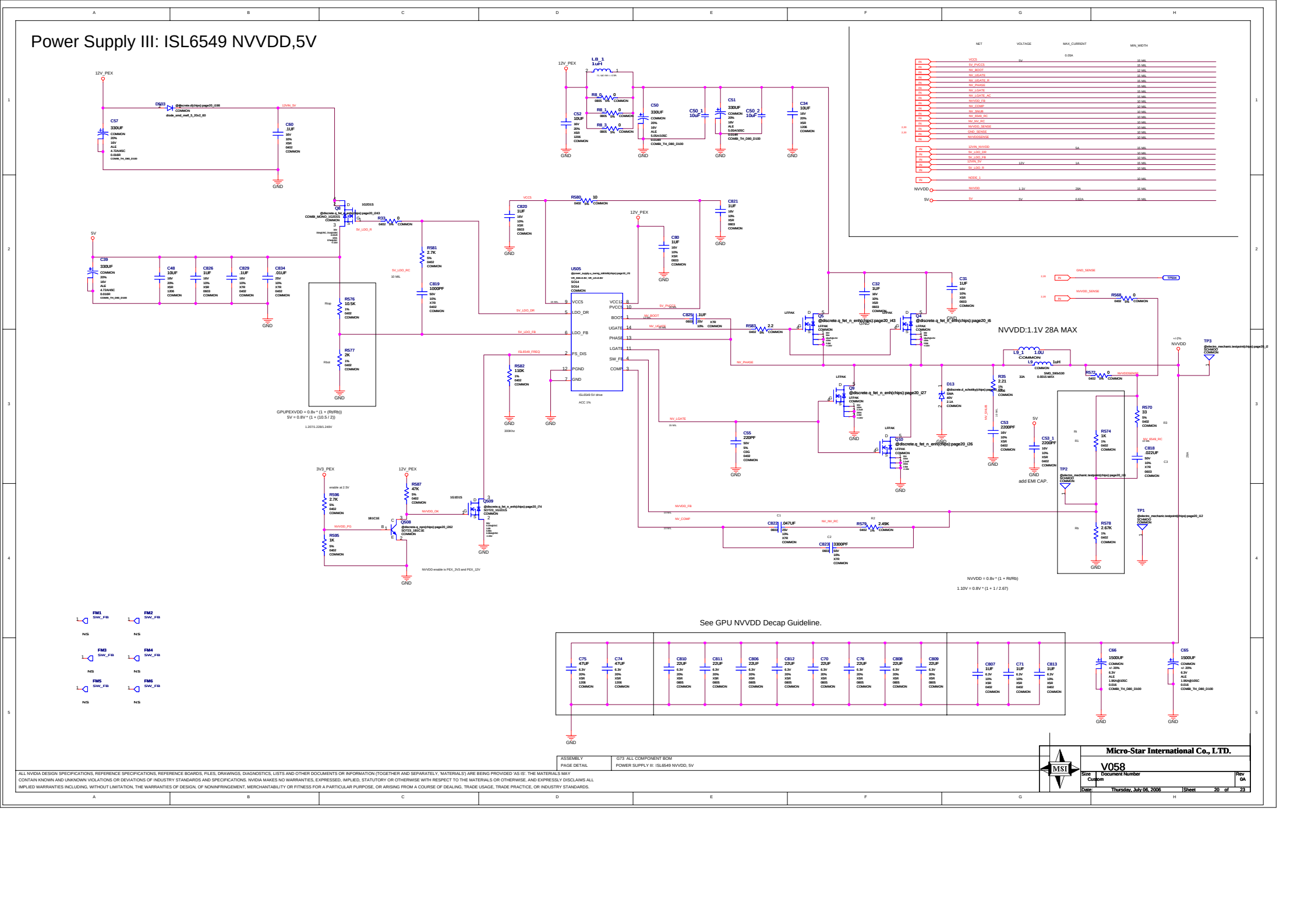
Power Supply III: ISL6549 NVVDD,5V



Title: Basenet Report
Design: design
Date: Jun 9 15:43:26 2006

Base nets and synonyms for
p489_lib.DESIGN(@p489_lib.design(sch_1))
Base Signal Location([Zone][dir])

3V3AUX 2.5G
3V3_PEX 2.5G
5V 20.2G
5V_FUSED 18.1G< 18.4B
5V_LDO_DR 20.1F< 20.2D
5V_LDO_FB 20.1F< 20.3D
5V_LDO_R 20.1F< 20.2C
5V_LDO_RC 20.2C
5V_PVCCS 20.1F< 20.2E
5V_T2 18.4F
12VIN_5V 20.1B 20.1F<
12VIN_FBVDDQ 19.1F< 19.2E
12VIN_NVVDD 20.1E 20.1F<
12V_PEX 2.5G
6549LDO_FB 19.1F< 19.3D
6549LDO_GDR 19.1F< 19.3D
6549LDO_GDR_R 19.1F< 19.3C
6549LDO_RC 19.1F< 19.3C
6549_FS_DIS 19.1F< 19.3C
A2V5 18.1G
A3V3 18.1G
A3V3_ADJ 18.1G< 18.2B
AHB 9.3E
ALTADDR 14.3B
ATXC 11.2D
ATXC* 11.2D
ATXD0 11.2D
ATXD0* 11.2D
ATXD1 11.2D
ATXD1* 11.2D
ATXD2 11.3D
ATXD2* 11.3D
AVB 9.3E
BTXD4 11.3D
BTXD4* 11.3D
BTXD5 11.3D
BTXD5* 11.3D
BTXD6 11.3D
BTXD6* 11.3D
CTXC 12.2D
CTXC* 12.2D
CTXD0 12.2D
CTXD0* 12.2D
CTXD1 12.2D
CTXD1* 12.2D
CTXD2 12.3D
CTXD2* 12.2D
CX87X_VAA 14.1C
CX87X_VDD 14.1C
CX87X_VDDC 14.2D
CX25875_GPO0 14.3B
CX25875_GPO1 14.3B
CX25875_GPO2 14.3B
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CX_PLL_COMP 14.3C
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FAN_PWM_4PIN 16.2G
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