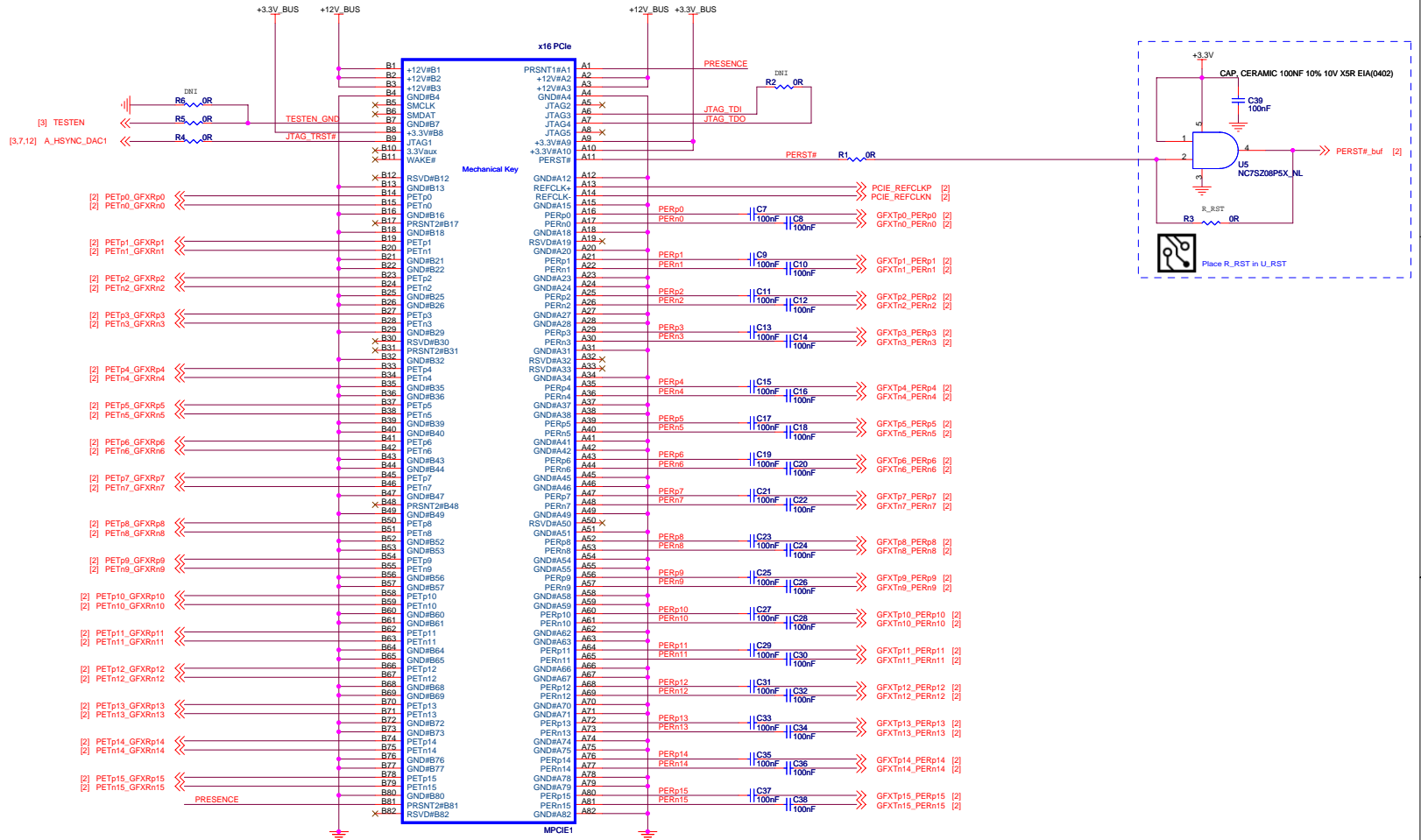
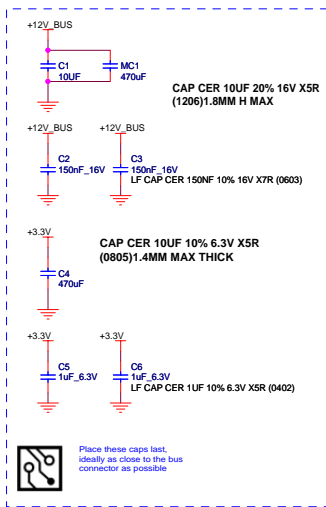
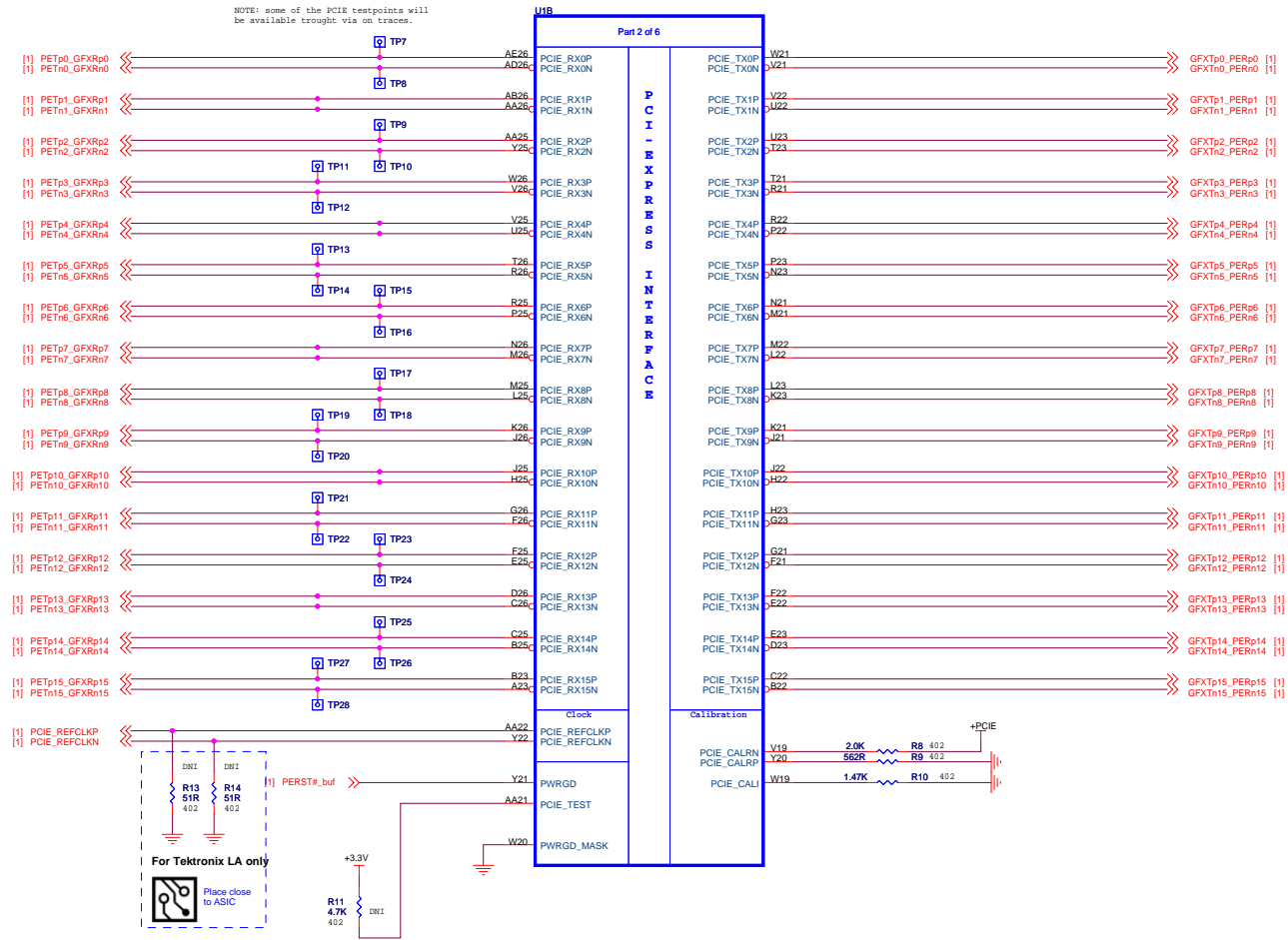


PCI-EXPRESS EDGE CONNECTOR



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Canada L3T 7X6
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File		RH RV505CE 256MB DDR2 LP 4L	
Size	Document Number	105-A85831-00A	Rev 0
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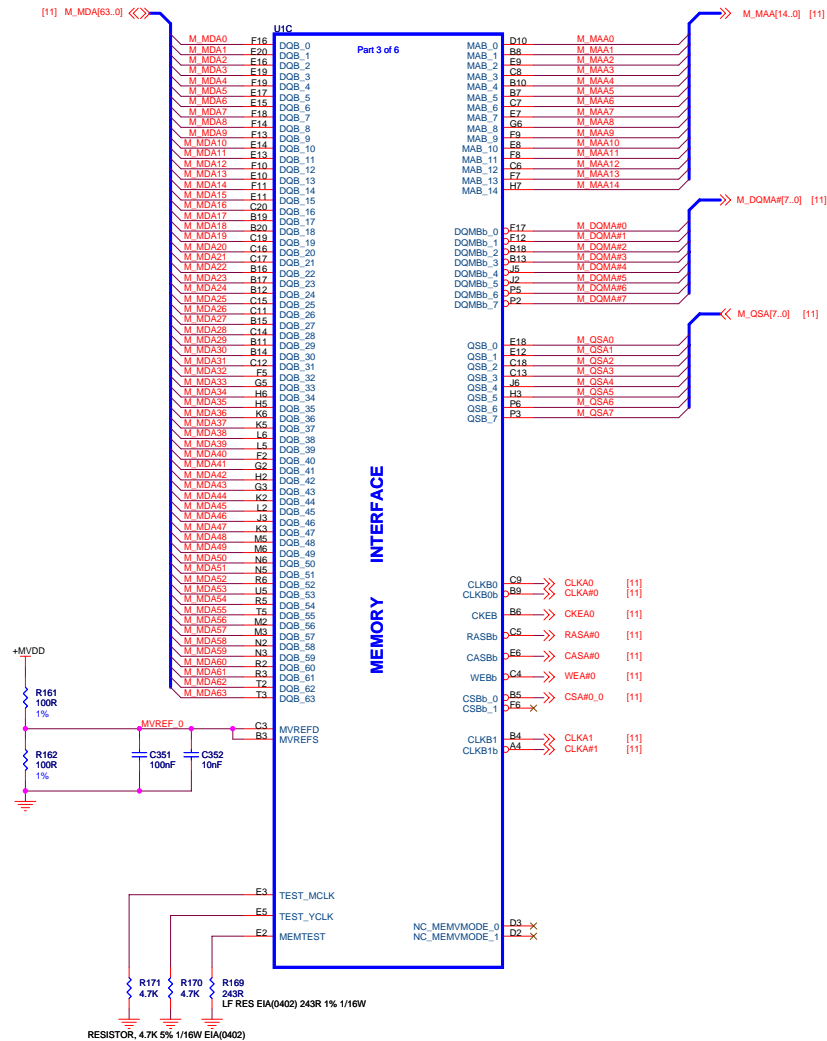


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Channel A

Register control is from Registers named CHANNEL B although this ASIC package only has 1 channel



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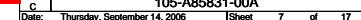
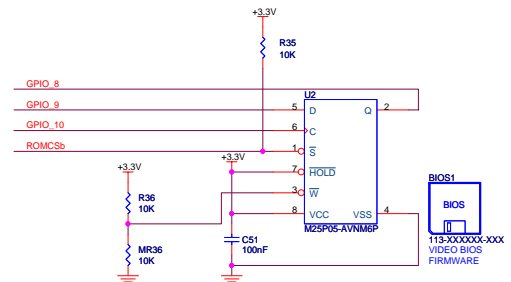
1 Commerce Valley Drive East
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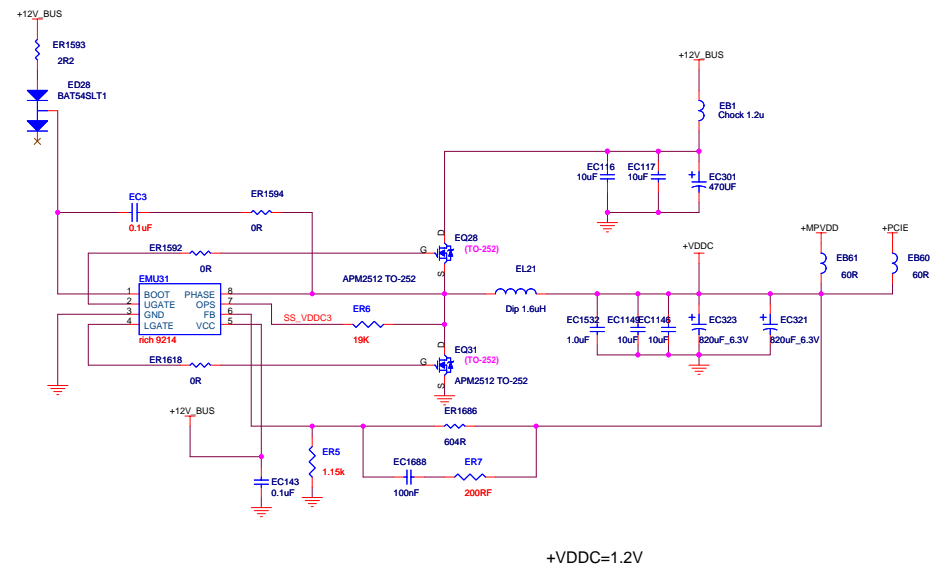
Title	RH RV505CE 256MB DDR2 LP 4L
-------	-----------------------------

Size	Document Number
6	105-A85831-00A

C	105-A85831-00A			
Date:	Thursday, September 14, 2006	Sheet	5	of 17

0

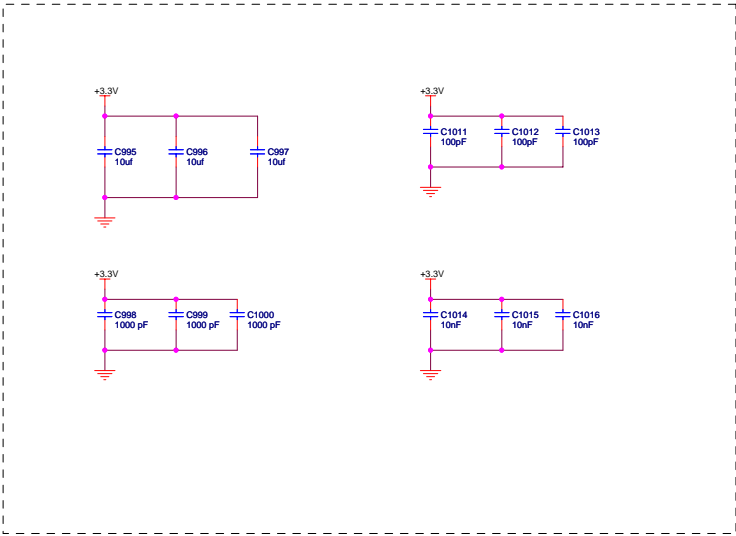
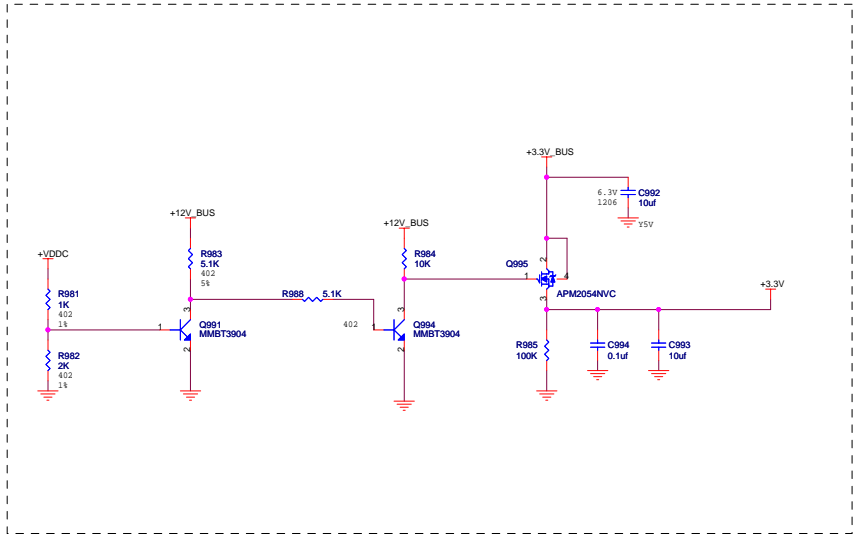




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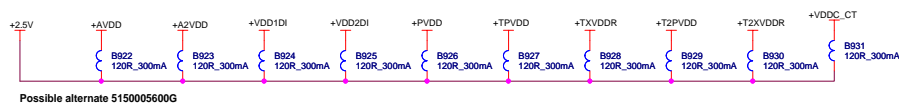
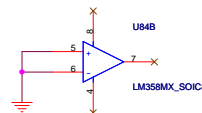
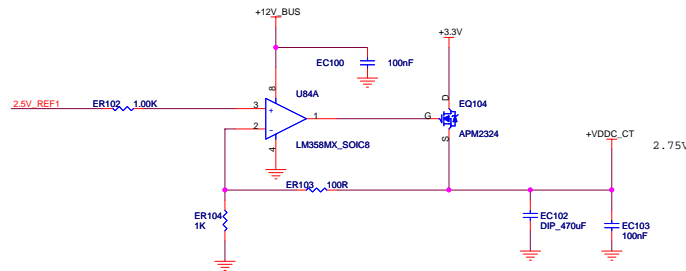
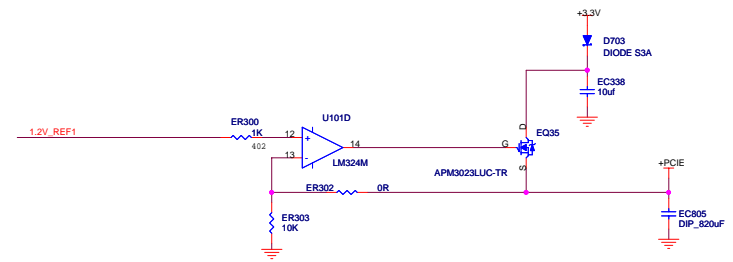
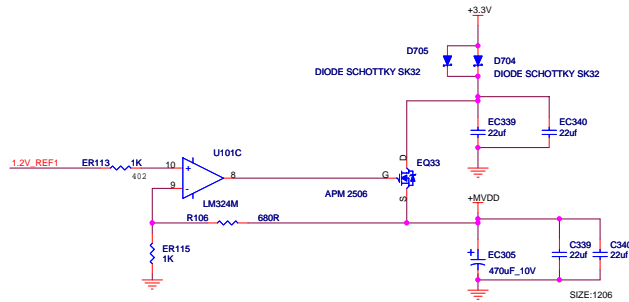
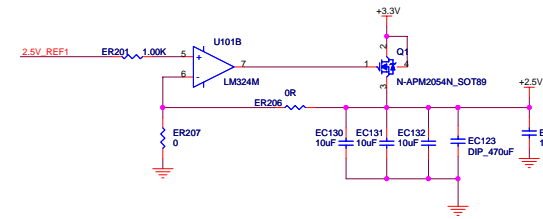
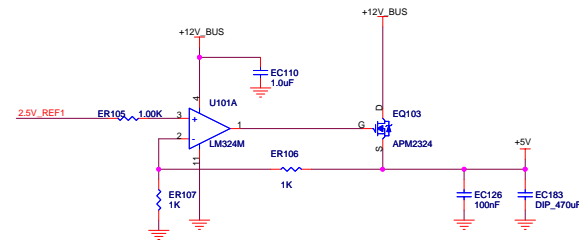
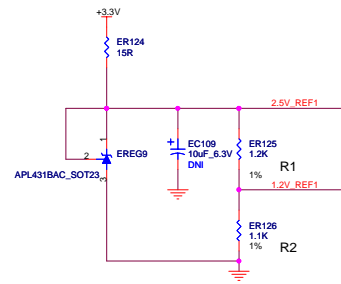
Title		RH RV505CE 256MB DDR2 LP 4L	
Size	Custom	Document Number	105-A85831-00A
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power
sequence



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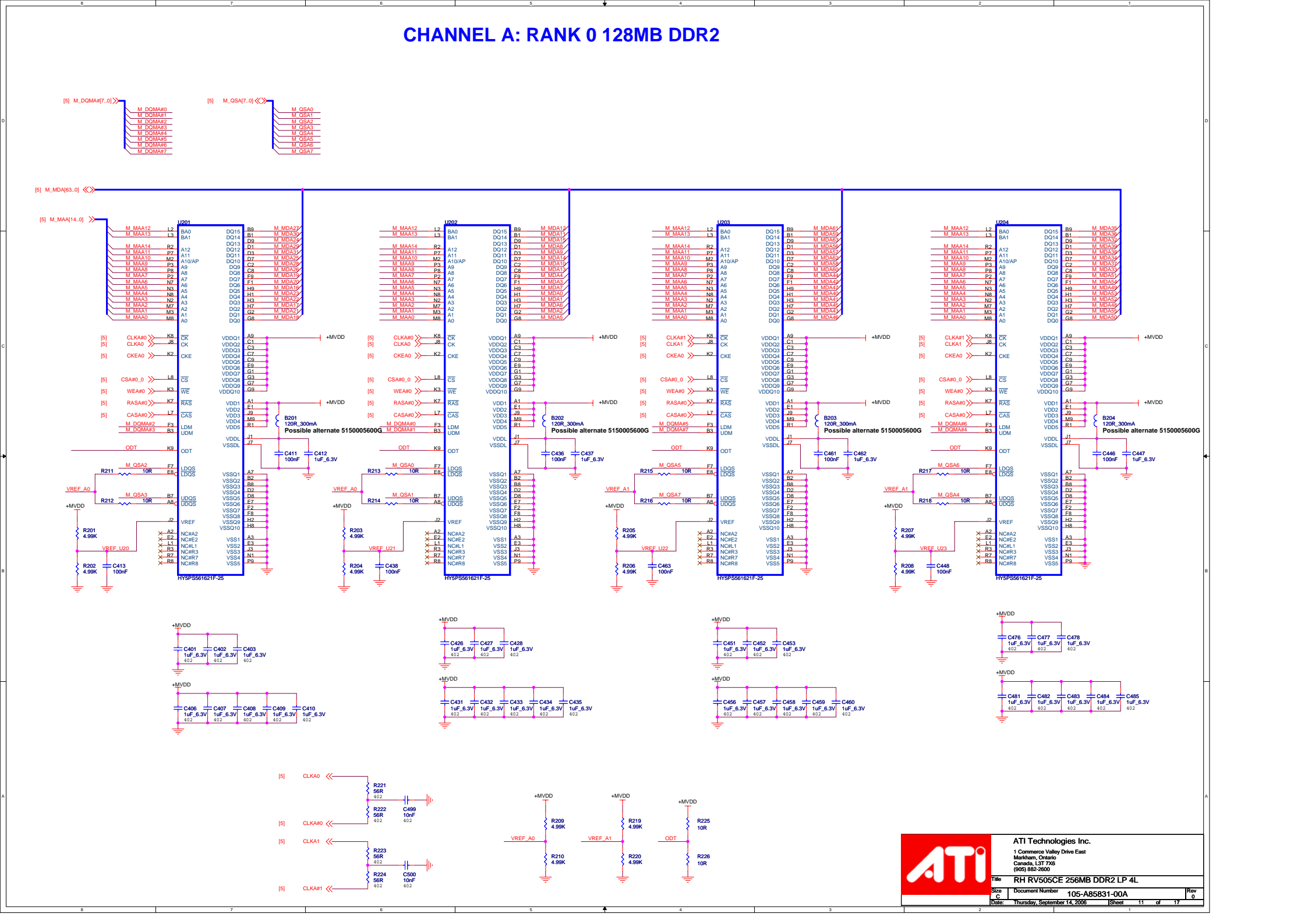
Title	RH RV505CE 256MB DDR2 LP 4L		
Size	Document Number	105-A85831-00A	Rev
Customer			0
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Title	RH RV505CE 256MB DDR2 LP 4L			Rev
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CHANNEL A: RANK 0 128MB DDR2



CHANNEL A: RANK 0 128MB DDR2

The diagram illustrates the electrical connections for Channel A, Rank 0, 128MB DDR2 memory. It shows four memory modules (U201, U202, U203, U204) connected to a central bus. Each module has 16 data lines (DQ0-DQ15), 4 address lines (A0-A3), 2 control lines (CSA0_0, WEA0), and 2 status lines (ODT, VREF). The modules are connected to a common +MVDD and -MVSS supply. The diagram includes detailed pin connections, component values (resistors, capacitors), and a legend for the components.

Legend:

- U201, U202, U203, U204: HY6PS561621F-25
- R201, R202, R203, R204, R205, R206, R207, R208, R209, R210, R211, R212, R213, R214, R215, R216, R217, R218, R219, R220, R221, R222, R223, R224, R225, R226: 4.99K
- C401, C402, C403, C404, C405, C406, C407, C408, C409, C410, C411, C412, C413, C414, C415, C416, C417, C418, C419, C420, C421, C422, C423, C424, C425, C426, C427, C428, C429, C430, C431, C432, C433, C434, C435, C436, C437, C438, C439, C440, C441, C442, C443, C444, C445, C446, C447, C448, C449, C450, C451, C452, C453, C454, C455, C456, C457, C458, C459, C460, C461, C462, C463, C464, C465, C466, C467, C468, C469, C470, C471, C472, C473, C474, C475, C476, C477, C478, C479, C480, C481, C482, C483, C484, C485, C486, C487, C488, C489, C490, C491, C492, C493, C494, C495, C496, C497, C498, C499, C500: 100nF
- C401, C402, C403, C404, C405, C406, C407, C408, C409, C410, C411, C412, C413, C414, C415, C416, C417, C418, C419, C420, C421, C422, C423, C424, C425, C426, C427, C428, C429, C430, C431, C432, C433, C434, C435, C436, C437, C438, C439, C440, C441, C442, C443, C444, C445, C446, C447, C448, C449, C450, C451, C452, C453, C454, C455, C456, C457, C458, C459, C460, C461, C462, C463, C464, C465, C466, C467, C468, C469, C470, C471, C472, C473, C474, C475, C476, C477, C478, C479, C480, C481, C482, C483, C484, C485, C486, C487, C488, C489, C490, C491, C492, C493, C494, C495, C496, C497, C498, C499, C500: 1uF_6.3V

[illegible]

CHANNEL A: RANK 0 128MB DDR2

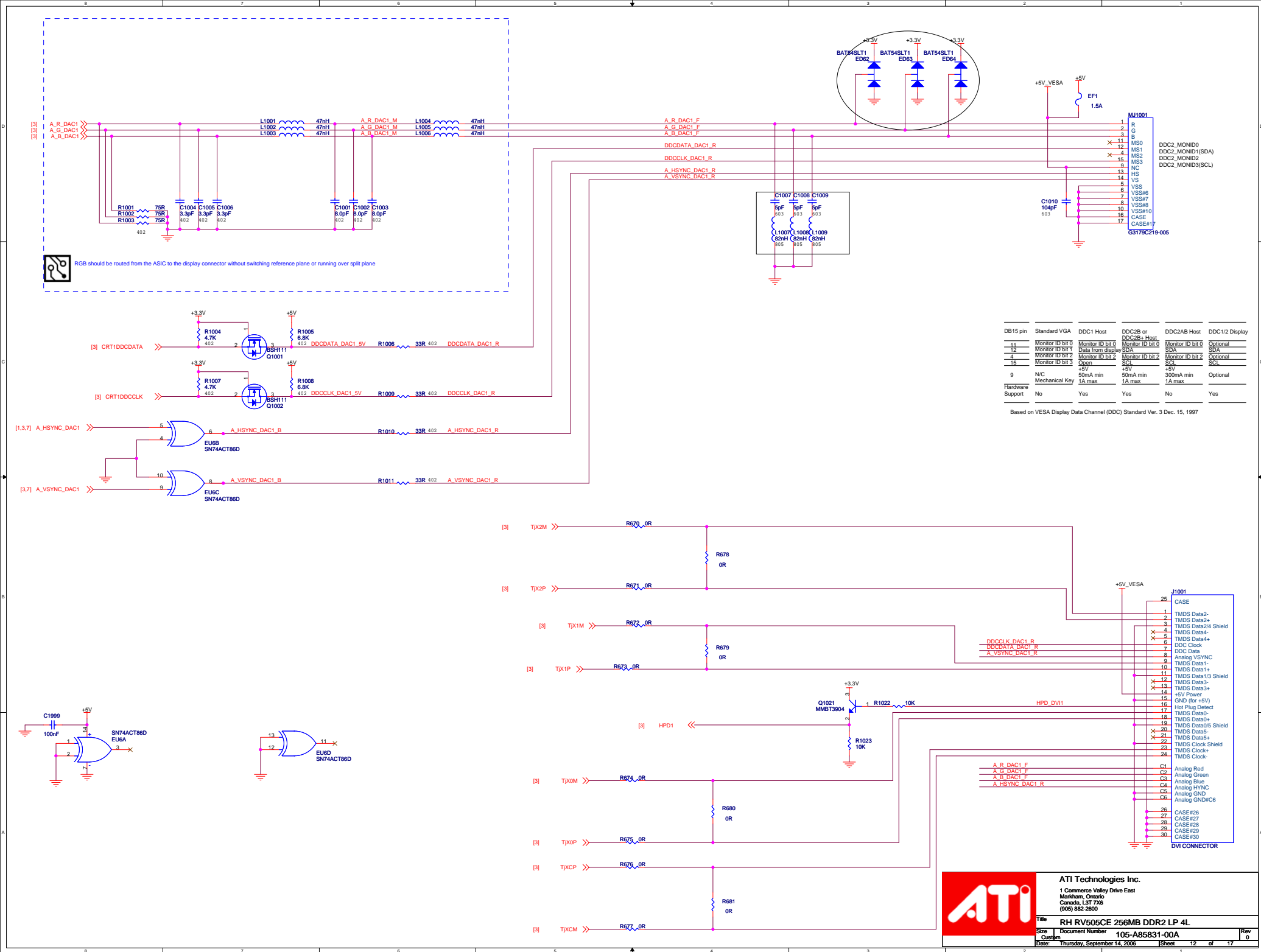
The diagram illustrates the electrical connections for Channel A, Rank 0, 128MB DDR2 memory. It shows four memory modules (U201, U202, U203, U204) connected to a central bus. Each module has 16 data lines (DQ0-DQ15), 2 address lines (A0-A1), 2 control lines (CSA0_0, WEA0), 2 clock lines (CLKA0, CLKA1), and 2 power lines (VDDQ1, VDDQ2). The modules are connected to a central bus via 100nF capacitors (C411-C414, C436-C437, C451-C452, C466-C467). The bus is connected to a central controller (U200) via 100nF capacitors (C401-C403, C426-C427, C451-C452, C466-C467). The diagram also shows power and ground connections for the modules and the bus, including 100nF capacitors (C401-C403, C426-C427, C451-C452, C466-C467) and 100nF capacitors (C401-C403, C426-C427, C451-C452, C466-C467).

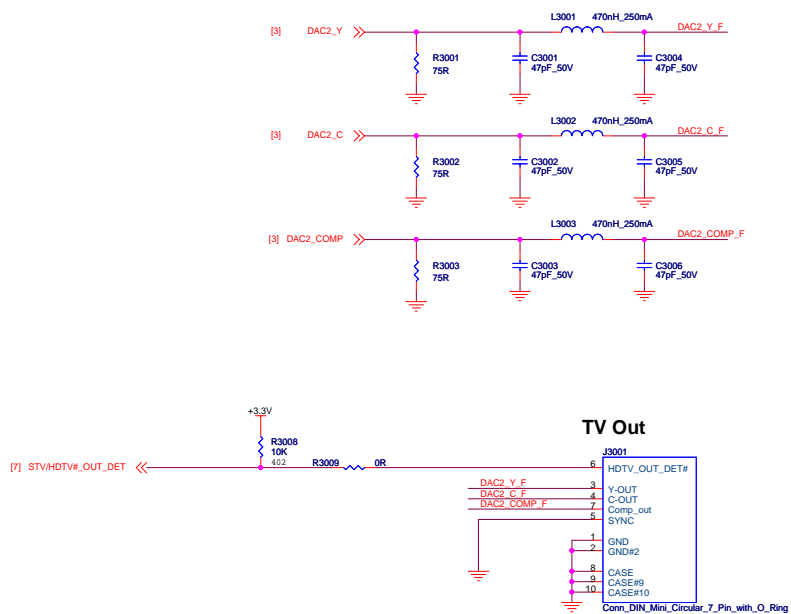
Legend:

- [S] M_DQMA[7..0]
- [S] M_QSA[7..0]
- [S] M_MAA[63..0]
- [S] M_MAA[14..0]
- [S] M_QSA0_0
- [S] M_QSA1
- [S] M_QSA2
- [S] M_QSA3
- [S] M_QSA4
- [S] M_QSA5
- [S] M_QSA6
- [S] M_QSA7
- [S] M_QSA8
- [S] M_QSA9
- [S] M_QSA10
- [S] M_QSA11
- [S] M_QSA12
- [S] M_QSA13
- [S] M_QSA14
- [S] M_QSA15
- [S] M_QSA16
- [S] M_QSA17
- [S] M_QSA18
- [S] M_QSA19
- [S] M_QSA20
- [S] M_QSA21
- [S] M_QSA22
- [S] M_QSA23
- [S] M_QSA24
- [S] M_QSA25
- [S] M_QSA26
- [S] M_QSA27
- [S] M_QSA28
- [S] M_QSA29
- [S] M_QSA30
- [S] M_QSA31
- [S] M_QSA32
- [S] M_QSA33
- [S] M_QSA34
- [S] M_QSA35
- [S] M_QSA36
- [S] M_QSA37
- [S] M_QSA38
- [S] M_QSA39
- [S] M_QSA40
- [S] M_QSA41
- [S] M_QSA42
- [S] M_QSA43
- [S] M_QSA44
- [S] M_QSA45
- [S] M_QSA46
- [S] M_QSA47
- [S] M_QSA48
- [S] M_QSA49
- [S] M_QSA50
- [S] M_QSA51
- [S] M_QSA52
- [S] M_QSA53
- [S] M_QSA54
- [S] M_QSA55
- [S] M_QSA56
- [S] M_QSA57
- [S] M_QSA58
- [S] M_QSA59
- [S] M_QSA60
- [S] M_QSA61
- [S] M_QSA62
- [S] M_QSA63
- [S] M_QSA64
- [S] M_QSA65
- [S] M_QSA66
- [S] M_QSA67
- [S] M_QSA68
- [S] M_QSA69
- [S] M_QSA70
- [S] M_QSA71
- [S] M_QSA72
- [S] M_QSA73
- [S] M_QSA74
- [S] M_QSA75
- [S] M_QSA76
- [S] M_QSA77
- [S] M_QSA78
- [S] M_QSA79
- [S] M_QSA80
- [S] M_QSA81
- [S] M_QSA82
- [S] M_QSA83
- [S] M_QSA84
- [S] M_QSA85
- [S] M_QSA86
- [S] M_QSA87
- [S] M_QSA88
- [S] M_QSA89
- [S] M_QSA90
- [S] M_QSA91
- [S] M_QSA92
- [S] M_QSA93
- [S] M_QSA94
- [S] M_QSA95
- [S] M_QSA96
- [S] M_QSA97
- [S] M_QSA98
- [S] M_QSA99
- [S] M_QSA100
- [S] M_QSA101
- [S] M_QSA102
- [S] M_QSA103
- [S] M_QSA104
- [S] M_QSA105
- [S] M_QSA106
- [S] M_QSA107
- [S] M_QSA108
- [S] M_QSA109
- [S] M_QSA110
- [S] M_QSA111
- [S] M_QSA112
- [S] M_QSA113
- [S] M_QSA114
- [S] M_QSA115
- [S] M_QSA116
- [S] M_QSA117
- [S] M_QSA118
- [S] M_QSA119
- [S] M_QSA120
- [S] M_QSA121
- [S] M_QSA122
- [S] M_QSA123
- [S] M_QSA124
- [S] M_QSA125
- [S] M_QSA126
- [S] M_QSA127
- [S] M_QSA128
- [S] M_QSA129
- [S] M_QSA130
- [S] M_QSA131
- [S] M_QSA132
- [S] M_QSA133
- [S] M_QSA134
- [S] M_QSA135
- [S] M_QSA136
- [S] M_QSA137
- [S] M_QSA138
- [S] M_QSA139
- [S] M_QSA140
- [S] M_QSA141
- [S] M_QSA142
- [S] M_QSA143
- [S] M_QSA144
- [S] M_QSA145
- [S] M_QSA146
- [S] M_QSA147
- [S] M_QSA148
- [S] M_QSA149
- [S] M_QSA150
- [S] M_QSA151
- [S] M_QSA152
- [S] M_QSA153
- [S] M_QSA154
- [S] M_QSA155
- [S] M_QSA156
- [S] M_QSA157
- [S] M_QSA158
- [S] M_QSA159
- [S] M_QSA160
- [S] M_QSA161
- [S] M_QSA162
- [S] M_QSA163
- [S] M_QSA164
- [S] M_QSA165
- [S] M_QSA166
- [S] M_QSA167
- [S] M_QSA168
- [S] M_QSA169
- [S] M_QSA170
- [S] M_QSA171
- [S] M_QSA172
- [S] M_QSA173
- [S] M_QSA174
- [S] M_QSA175
- [S] M_QSA176
- [S] M_QSA177
- [S] M_QSA178
- [S] M_QSA179
- [S] M_QSA180
- [S] M_QSA181
- [S] M_QSA182
- [S] M_QSA183
- [S] M_QSA184
- [S] M_QSA185
- [S] M_QSA186
- [S] M_QSA187
- [S] M_QSA188
- [S] M_QSA189
- [S] M_QSA190
- [S] M_QSA191
- [S] M_QSA192
- [S] M_QSA193
- [S] M_QSA194
- [S] M_QSA195
- [S] M_QSA196
- [S] M_QSA197
- [S] M_QSA198
- [S] M_QSA199
- [S] M_QSA200
- [S] M_QSA201
- [S] M_QSA202
- [S] M_QSA203
- [S] M_QSA204
- [S] M_QSA205
- [S] M_QSA206
- [S] M_QSA207
- [S] M_QSA208
- [S] M_QSA209
- [S] M_QSA210
- [S] M_QSA211
- [S] M_QSA212
- [S] M_QSA213
- [S] M_QSA214
- [S] M_QSA215
- [S] M_QSA216
- [S] M_QSA217
- [S] M_QSA218
- [S] M_QSA219
- [S] M_QSA220
- [S] M_QSA221
- [S] M_QSA222
- [S] M_QSA223
- [S] M_QSA224
- [S] M_QSA225
- [S] M_QSA226
- [S] M_QSA227
- [S] M_QSA228
- [S] M_QSA229
- [S] M_QSA230
- [S] M_QSA231
- [S] M_QSA232
- [S] M_QSA233
- [S] M_QSA234
- [S] M_QSA235
- [S] M_QSA236
- [S] M_QSA237
- [S] M_QSA238
- [S] M_QSA239
- [S] M_QSA240
- [S] M_QSA241
- [S] M_QSA242
- [S] M_QSA243
- [S] M_QSA244
- [S] M_QSA245
- [S] M_QSA246
- [S] M_QSA247
- [S] M_QSA248
- [S] M_QSA249
- [S] M_QSA250
- [S] M_QSA251
- [S] M_QSA252
- [S] M_QSA253
- [S] M_QSA254
- [S] M_QSA255
- [S] M_QSA256
- [S] M_QSA257
- [S] M_QSA258
- [S] M_QSA259
- [S] M_QSA260
- [

[illegible]

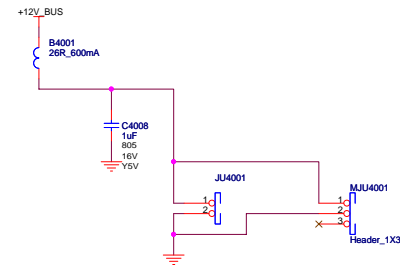
CHANNEL A: RANK 0 128MB DDR2





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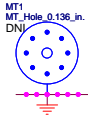
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Title		RH RV505CE 256MB DDR2 LP 4L	
Size	Document Number	105-A85831-00A	Rev
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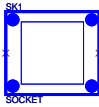
DVI/VGA SCREWS

ASSY-SCREW1
SCREW
JACKSCREW
ASSY
7020000800

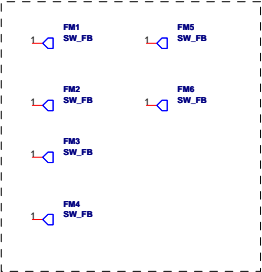
ASSY-SCREW2
SCREW
JACKSCREW
ASSY
7020000800



DNI



PCB DESIGN NOTE:
ASIC REQUIREMENTS
FOR THIS SOCKET:
1. USE ALTERNATE ASIC DECAL
WITH SQUARE VIA PADS.
2. SOCKET & ASIC PADS
REQUIRED GOLD PLATING.
SEE SPEC. FOR DETAILS.



<Variant Name>



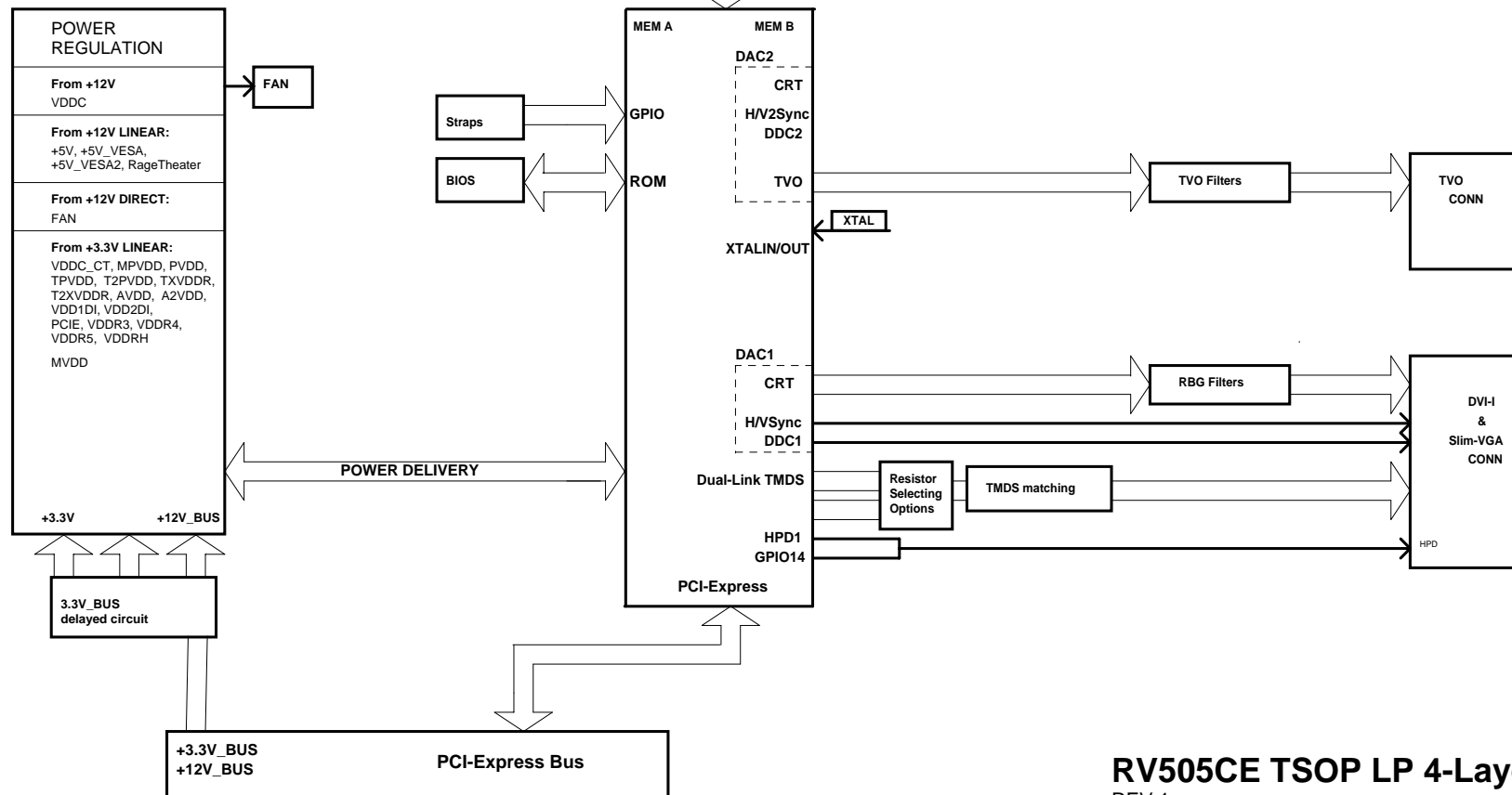
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RV505CE

MEMORY CHANNEL A

DDR2 16M/32Mx16




RV505CE TSOP LP 4-Layer
REV 1



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			Title RH RV505CE 256MB DDR2 LP 4L		Schematic No. 105-A85831-00A		Date: Thursday, September 14, 2006	
REVISION HISTORY							Rev 0	
Sch Rev			PCB Rev			Date		
0			00A			12/5/2005		
			LP design based on LP A857. Changed memory using latest interface from A676-10 Design. Changed memory Bank Addressing, for RV505 CE compliance Remove TMD52 and Dual Link Support. This avoids Shunt resistors for muxing TMD5 transmitters.					



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Title RH RV505CE 256MB DDR2 LP 4L		
Size B	Document Number 105-A85831-00A	Rev 0
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