

ON RESET
GPIO0 - Input
GPIO1 - Input
GPIO2 - Input
GPIO3 - Input

PCI2050APDV

These Capacitors should be placed on each pin of Intel 2050A Bridge Controller

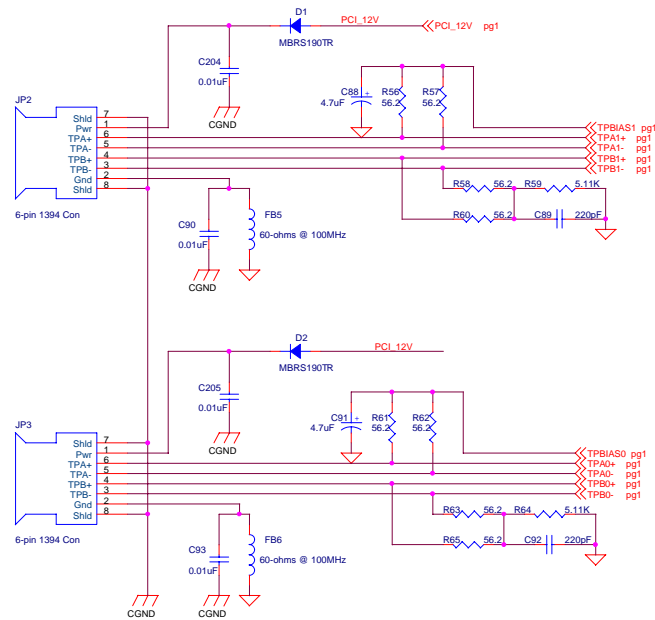
These Capacitors should be placed on each DVDD pin of TSB43AB22

These Capacitors should be placed on each AVDD pin of TSB43AB22

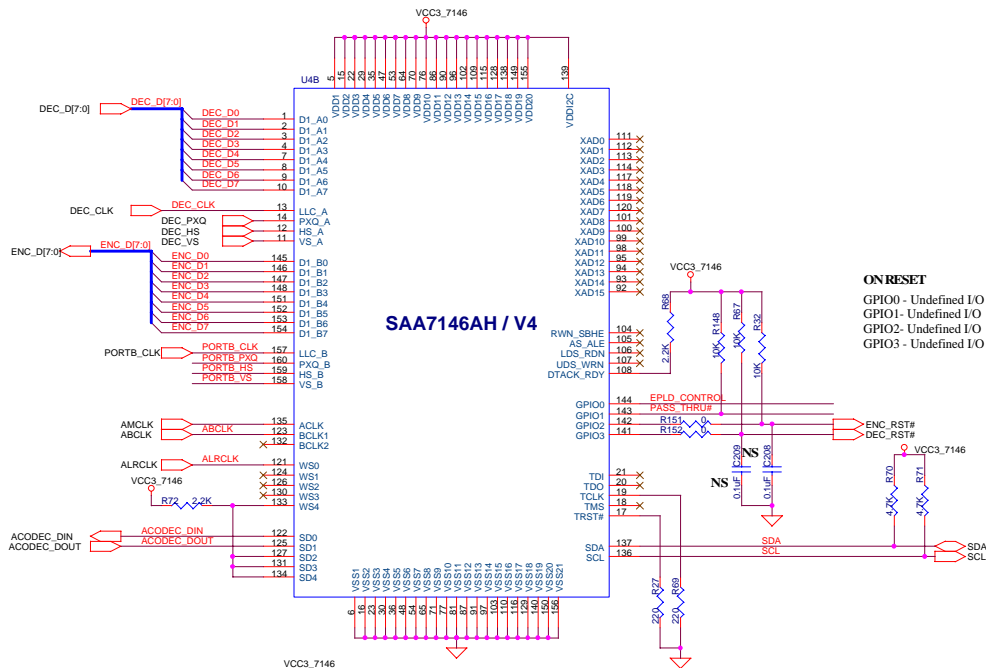
These Capacitors should be placed on each VDDP pin of TSB43AB22

ON RESET
GPIO2 - Input (not used)
GPIO3 - Input (not used)

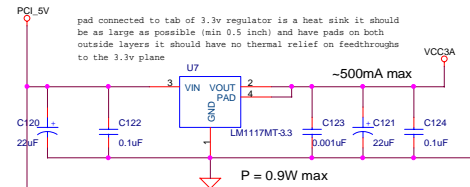
Client	
Product Name	Emuzed Inc.
Client Document Number	PCI Bridge & 1394 Port
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Client		Emuzed Inc.	
Product Name		Sheet Name	
Atlantis		1394 Connector	
Size	Client Document Number	Rev.	
C		0.14	
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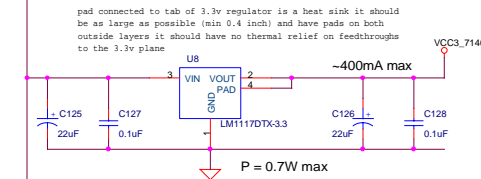


ONRESET
GPIO0 - Undefined I/O
GPIO1 - Undefined I/O
GPIO2 - Undefined I/O
GPIO3 - Undefined I/O

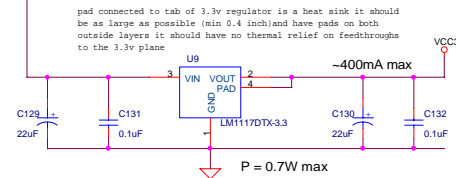


UDA1342
EEPROM
SAA7115
SAA7121
TSB43AB22

40mA
10mA
170mA
100mA
100mA



SAA7146 400mA

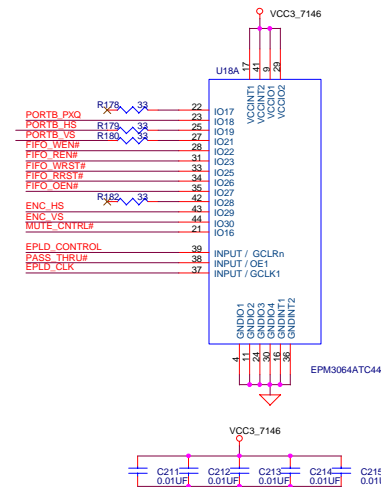


PCI2050A 400mA

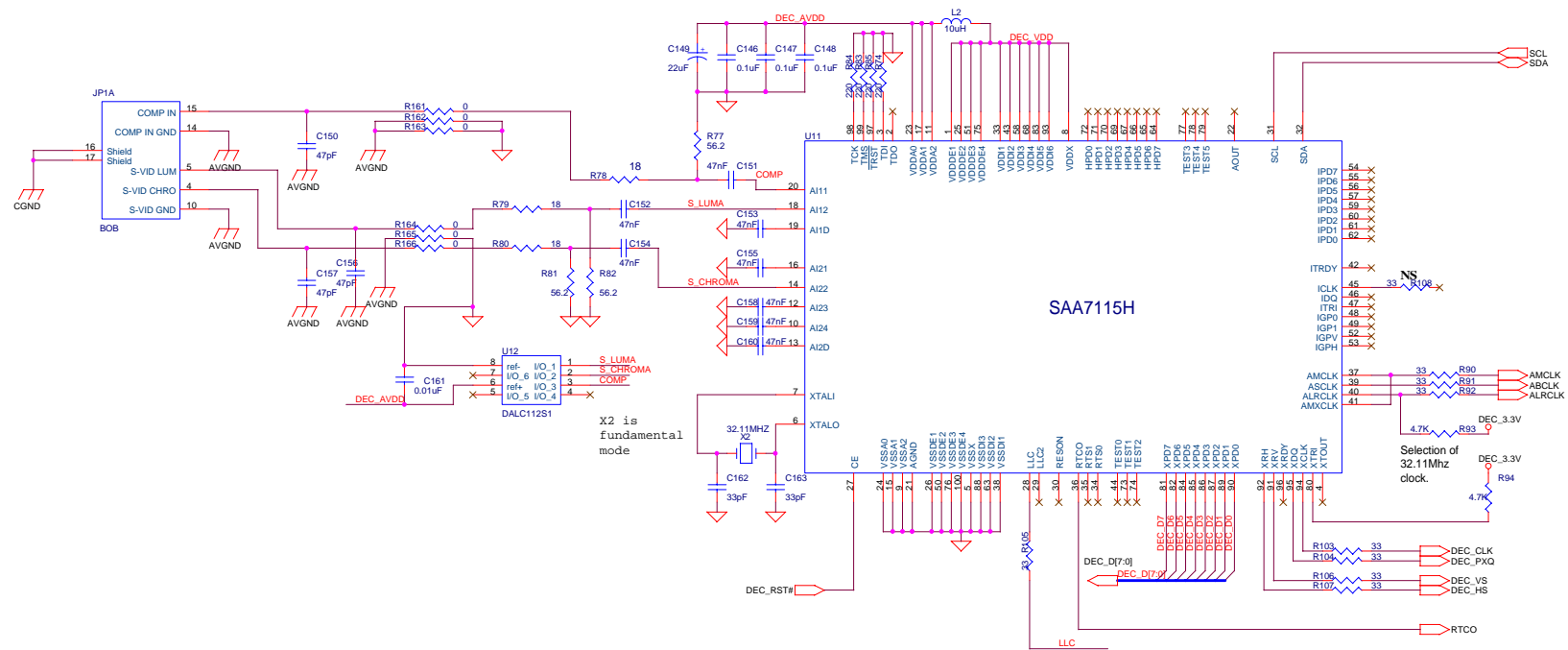
FIFO_WEN#
FIFO_REN#
FIFO_WRS#
FIFO_RRS#
FIFO_OEN#

ENC_HS
ENC_VS
EPLD_CLK
MUTE_CNTRL#

PCI_5V
VCC3
VCC3A

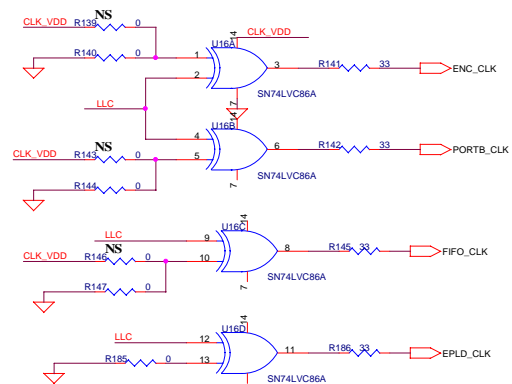
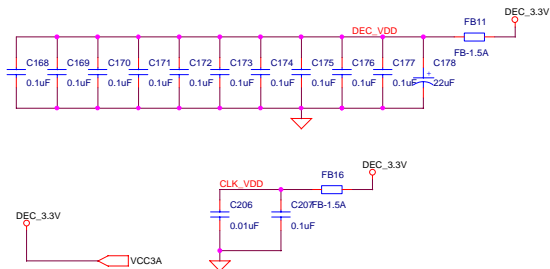


One decoupling cap should be placed on each VCC pin of the EPLD



I2C Address Selection:
 RTCO=0 slave address 42H/43H -Default
 RTCO=1 slave address 40H/41H
 RTCO read during power up only

IIC Address: 0x42, 0x43



Client		Emuzed Inc.	
Product Name		Sheet Name	
Atlantis		Video Decoder	
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