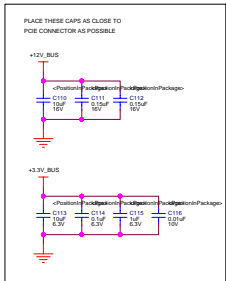
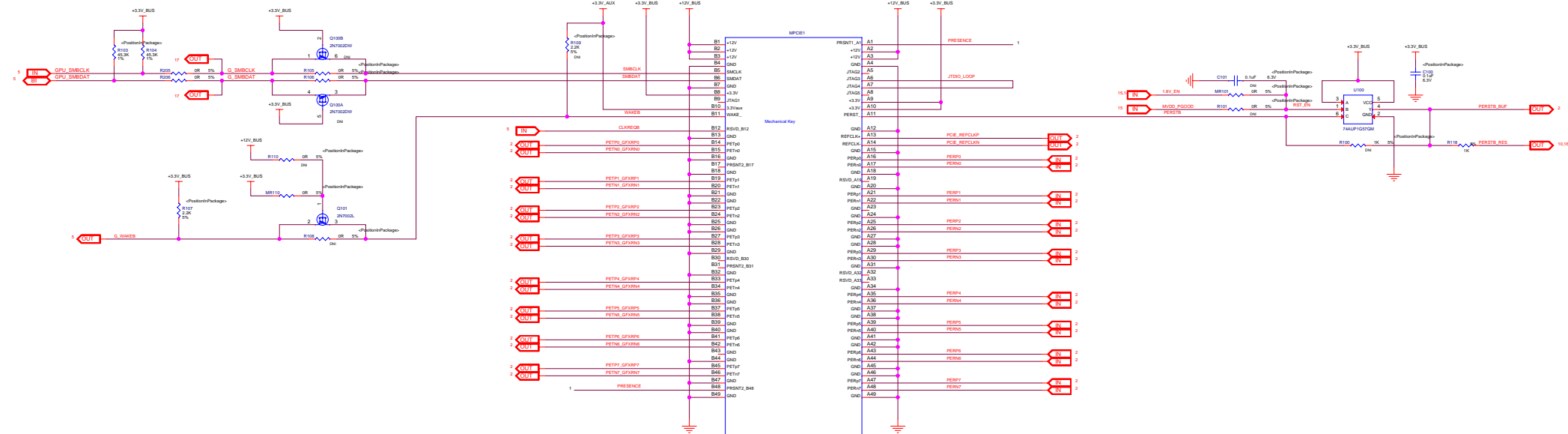



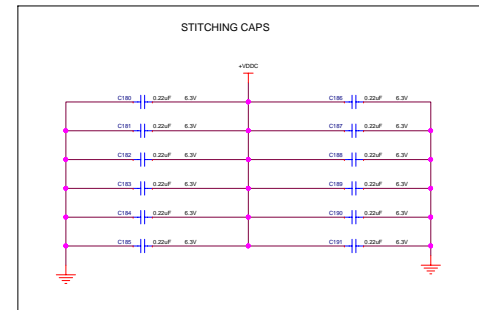
(1) PCIE EDGE CONNECTOR



SYMBOL LEGEND	
DNI	DO NOT INSTALL
b or #	ACTIVE LOW
BUO	BRING UP ONLY
	DIGITAL GROUND
	ANALOG GROUND

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SHEET: PCIE EDGE CONNECTOR				TITLE:	
DATE: Wed Jun 01 06:00:28 2016		REV: 1.0		TITLE	
SHEET NUMBER: 1 OF 19					
DOCUMENT NUMBER: 106_CXX000_00A					

(2) BAFFIN PCIE

[illegible]

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SHEET: BAFFIN PCIE

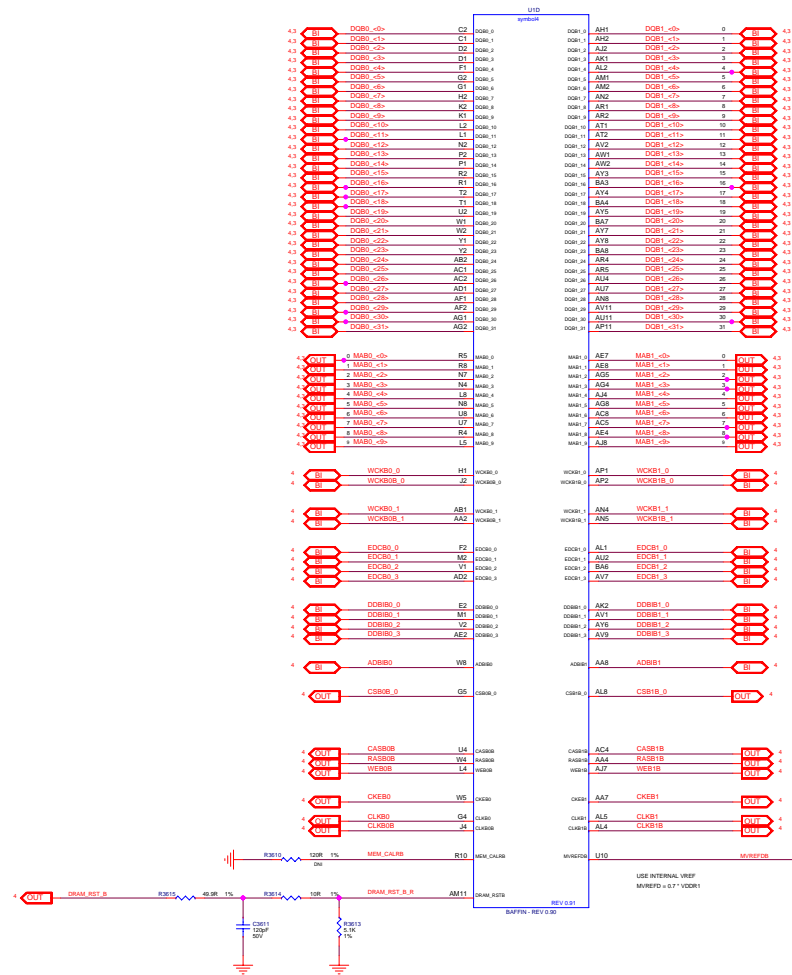
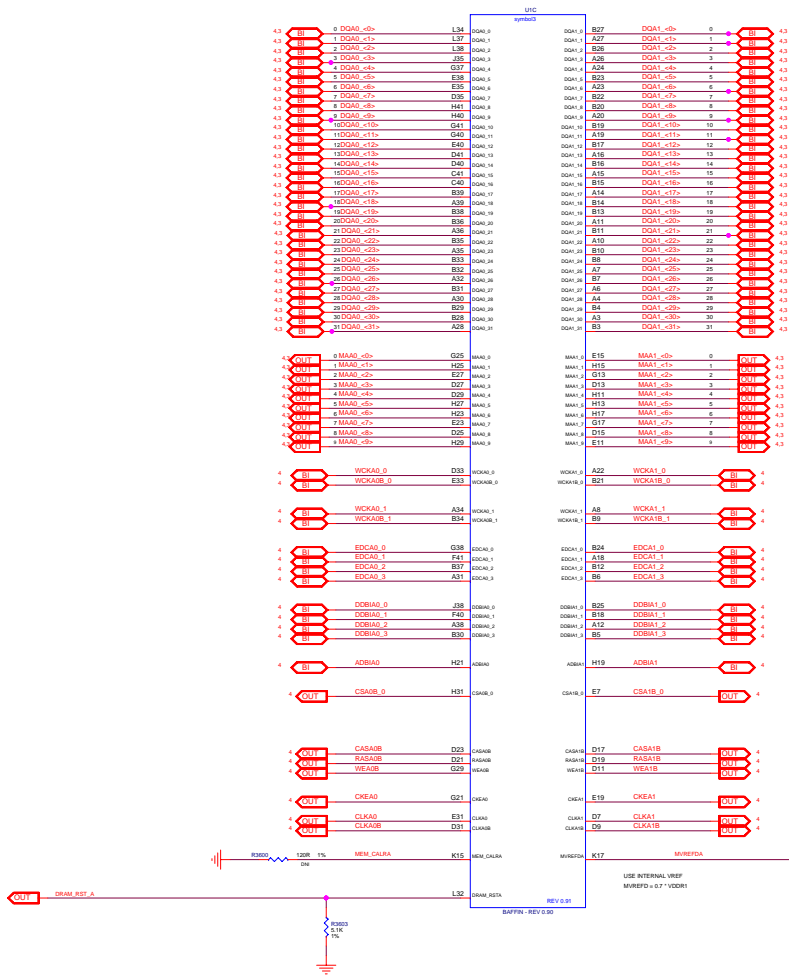
DATE: Wed Jun 01 06:00:28 2016

REV:	1.0
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SHEET NUMBER: 2 OF 19

TITLE:	
	TITLE

(3) RACERX MEMORY INTERFACE



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SHEET: BAFFIN MEMORY

DATE: Wed Jun 01 06:00:28 2016

REV: 1.0

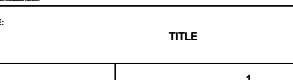
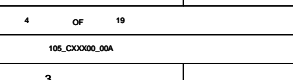
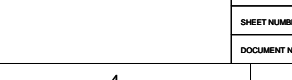
SHEET NUMBER: 3 OF 19

DOCUMENT NUMBER: 106\_CXX000\_00A

TITLE:

TITLE

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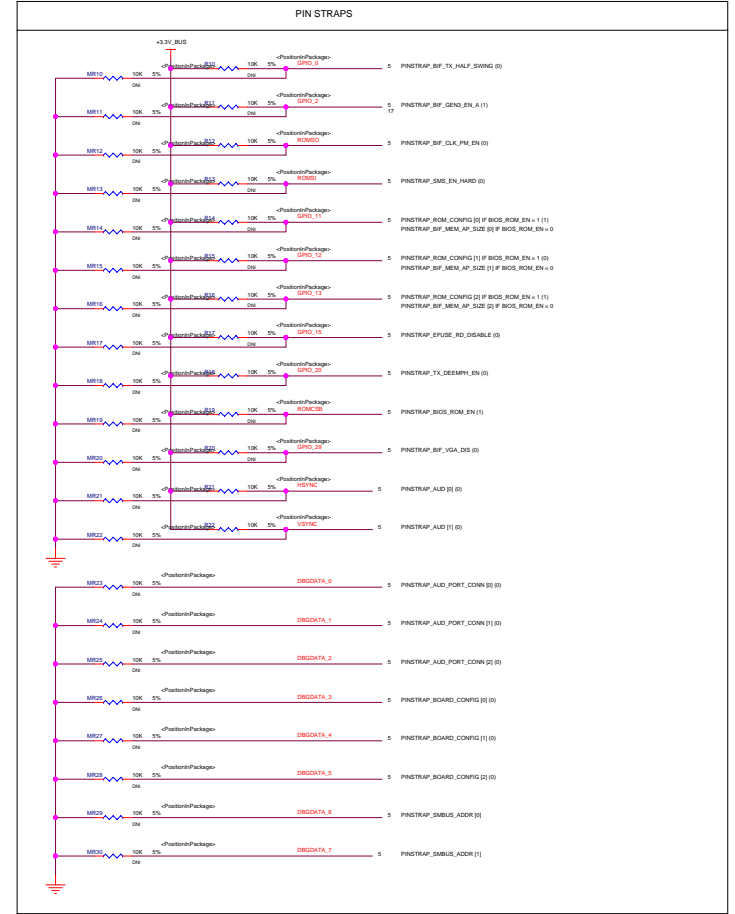
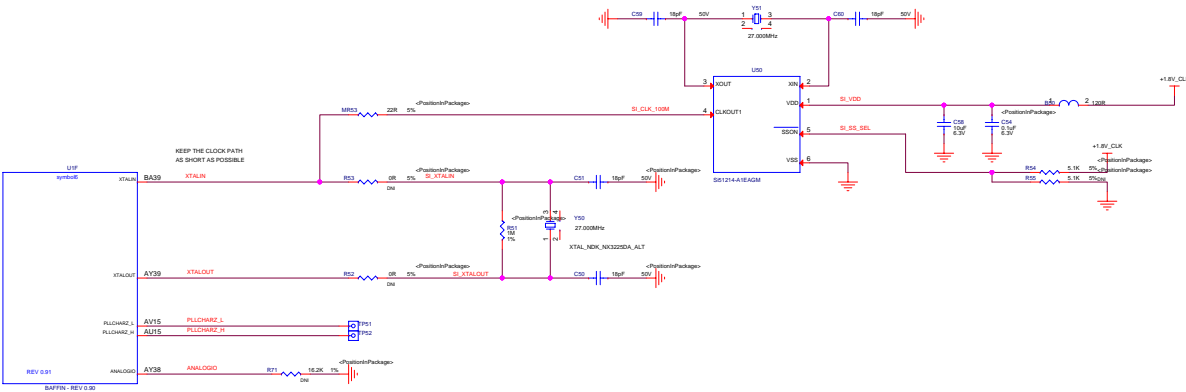
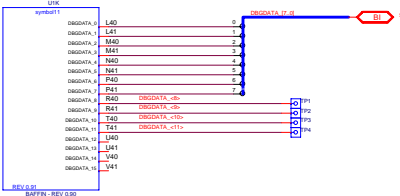
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(5) BAFFIN GPIO STRAPS CLK

DEVICE	ADDRESS
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RT4963	0x02
VT8851	

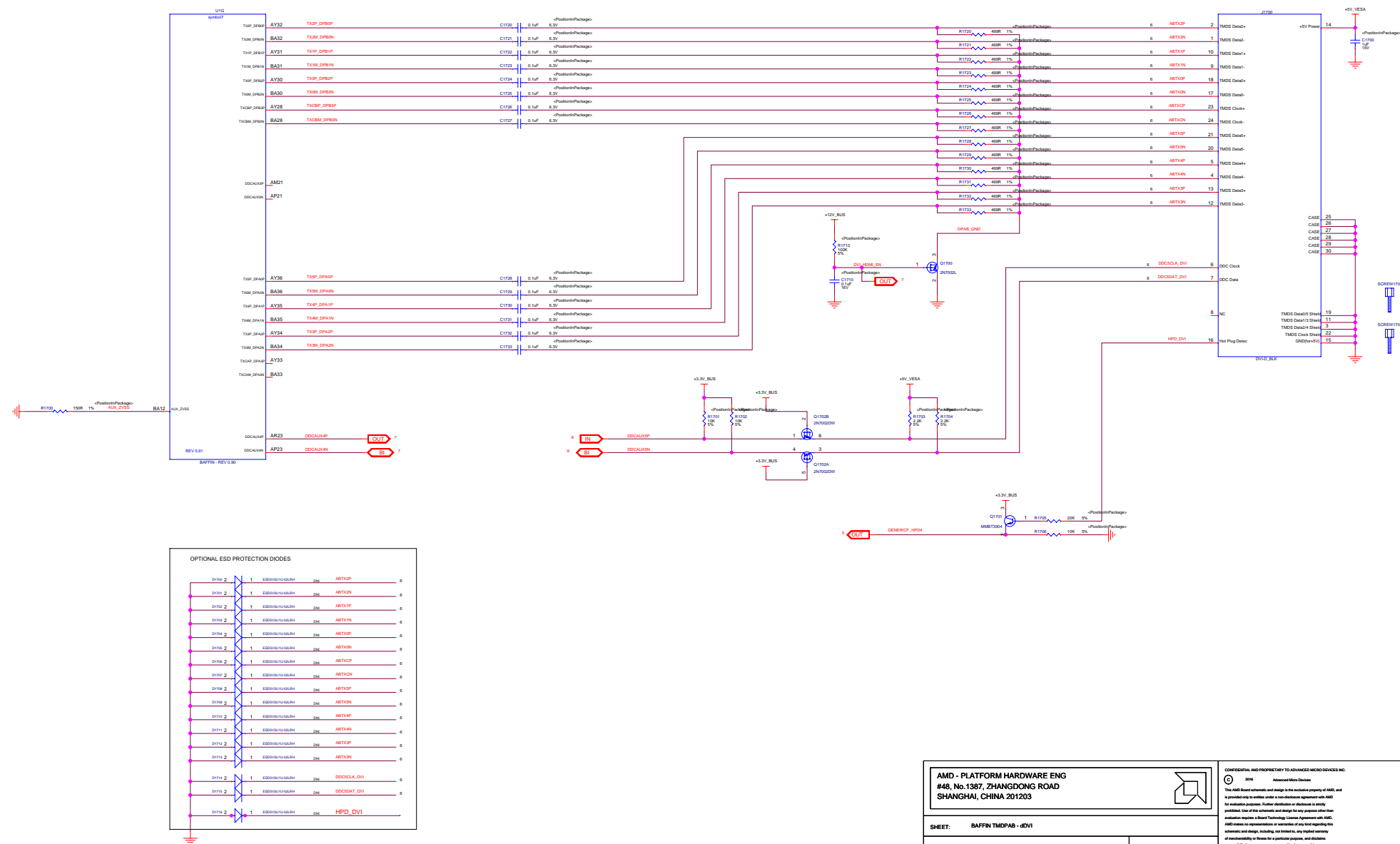
DEVICE	ADDRESS
LM5963	0x00



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SHEET: BAFFIN GPIO STRAPS CLK	
DATE: Wed Jun 01 06:00:29 2016	REV: 1.0
SHEET NUMBER: 5 OF 19	TITLE:
DOCUMENT NUMBER: 106_CXX000_00A	TITLE

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(6) BAFFIN TMDPAB - dDVI



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SHEET: BAFFIN TMDPAB - dJVI

DATE: Wed Jun 01 06:00:29 2016

SHEET NUMBER:

6 OF 15

DOCUMENT NUMBER: 105\_CXXX00\_00

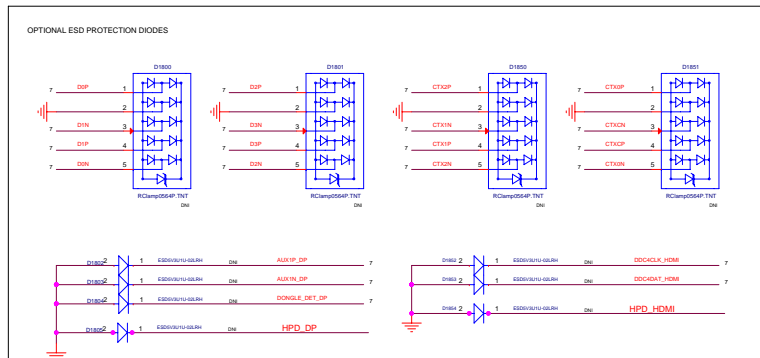
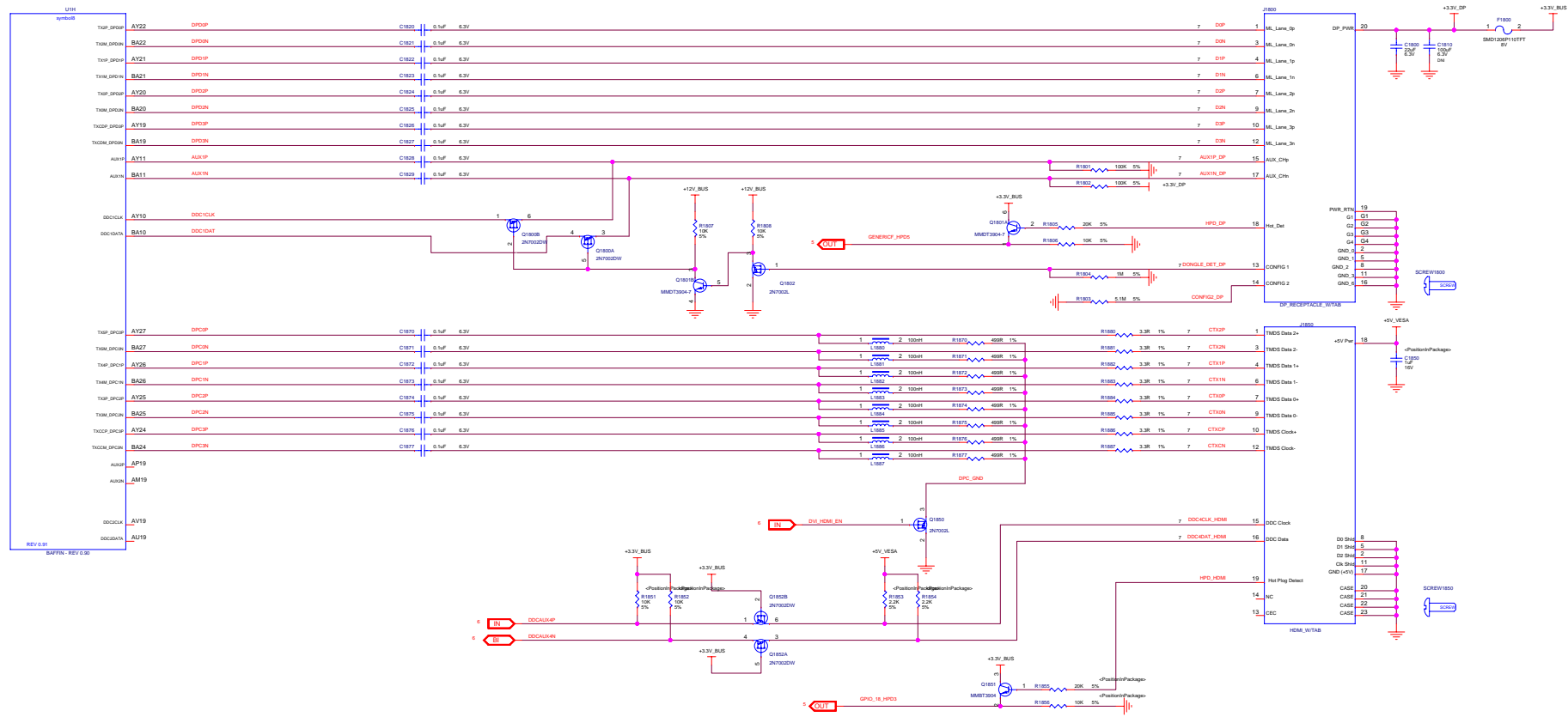
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**TITLE:**

**TITLE**

(7) BAFFIN TMDPCD - HDMI DP



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**SHEET:** BAFFIN TMDPCD - HDMI DP

DATE: Wed Jun 01 06:00:29 2016

SHEET NUMBER: 7 OF 19

DOCUMENT NUMBER: 105\_CXXX00\_00A

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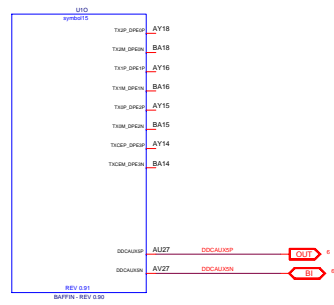
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(8) BAFFIN TMDPE



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SHEET: BAFFIN TMDPE - NC

DATE: Wed Jun 01 06:00:29 2016

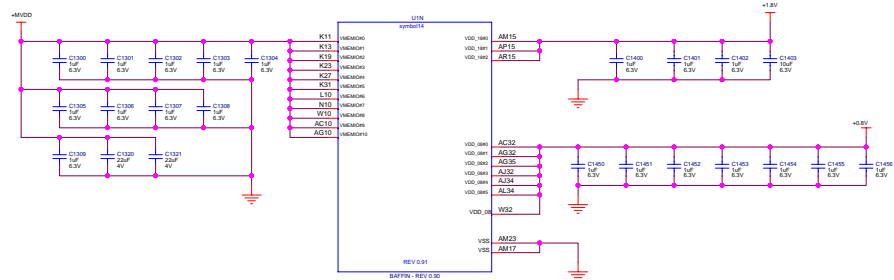
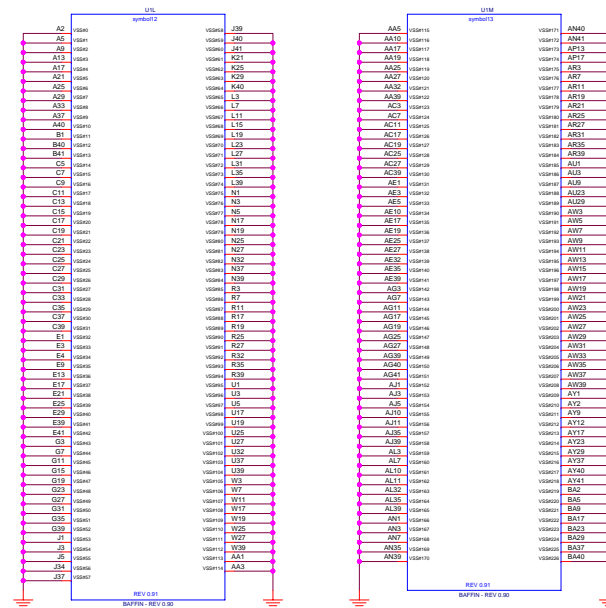
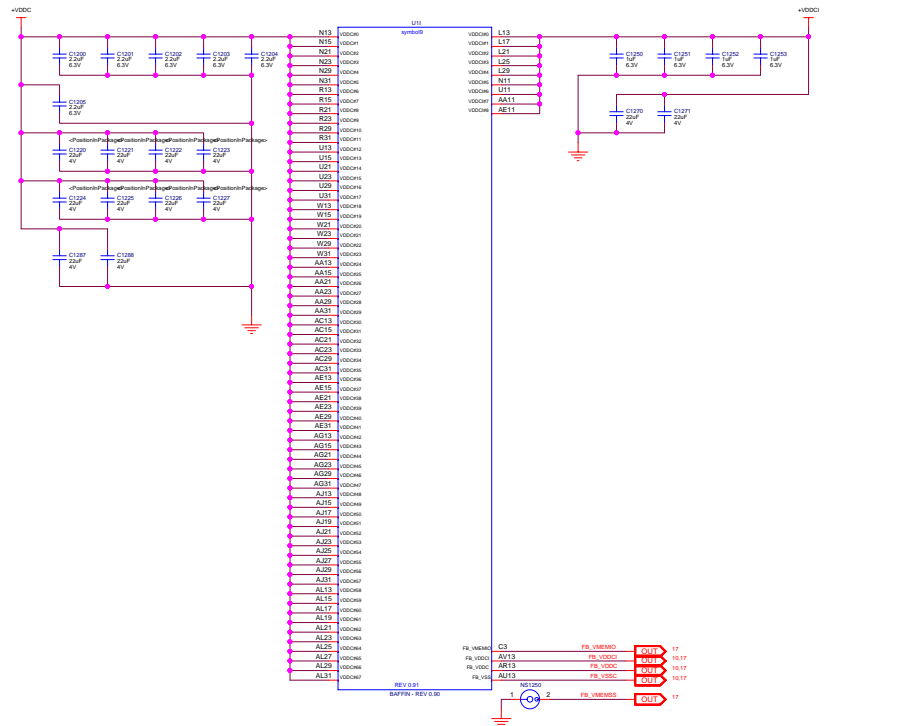
REV: 1.0

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(9) BAFFIN POWER GND



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REV: 1.0

SHEET NUMBER: 9 OF 19

TITLE:	TITLE
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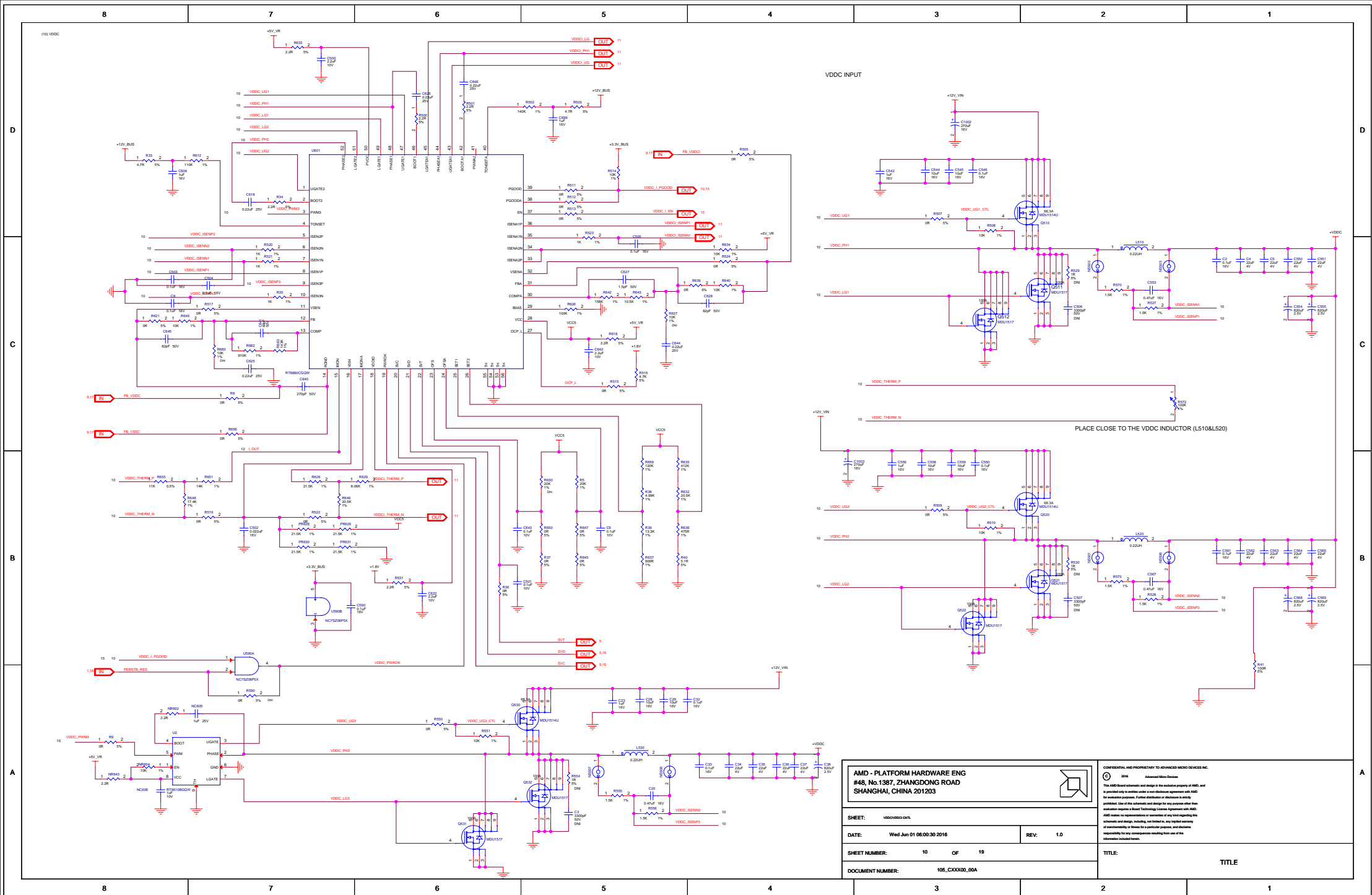
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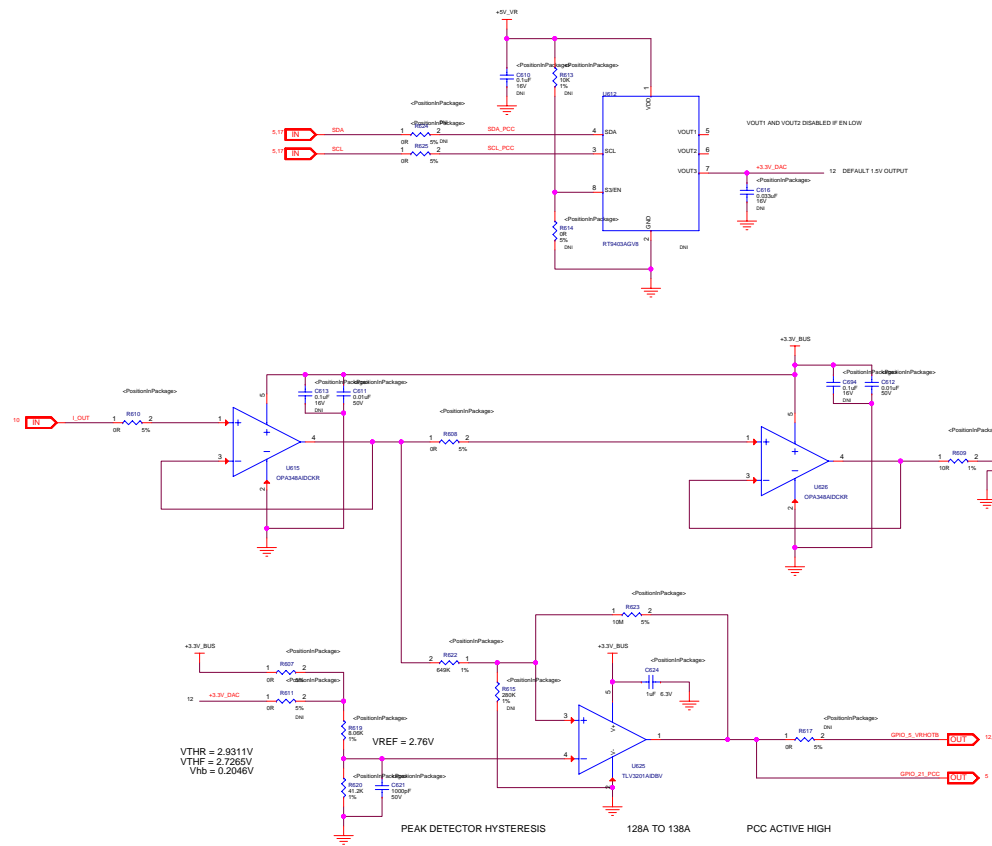
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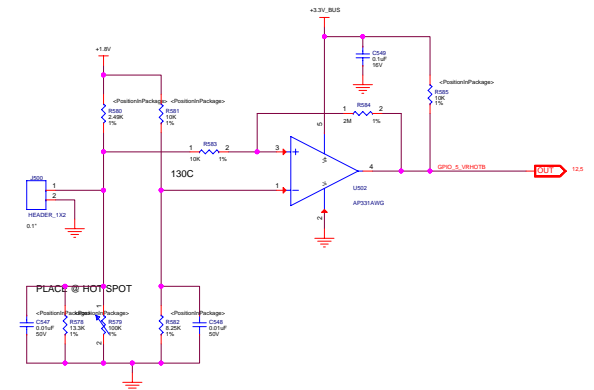


(13) PCC VRHOT

### PEAK CURRENT CONTROL



## VR TEMPERATURE PROTECTION



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SHEET: REG - PCC VRHOT

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REV: 1.0

SHEET NUMBER: 12 OF 19

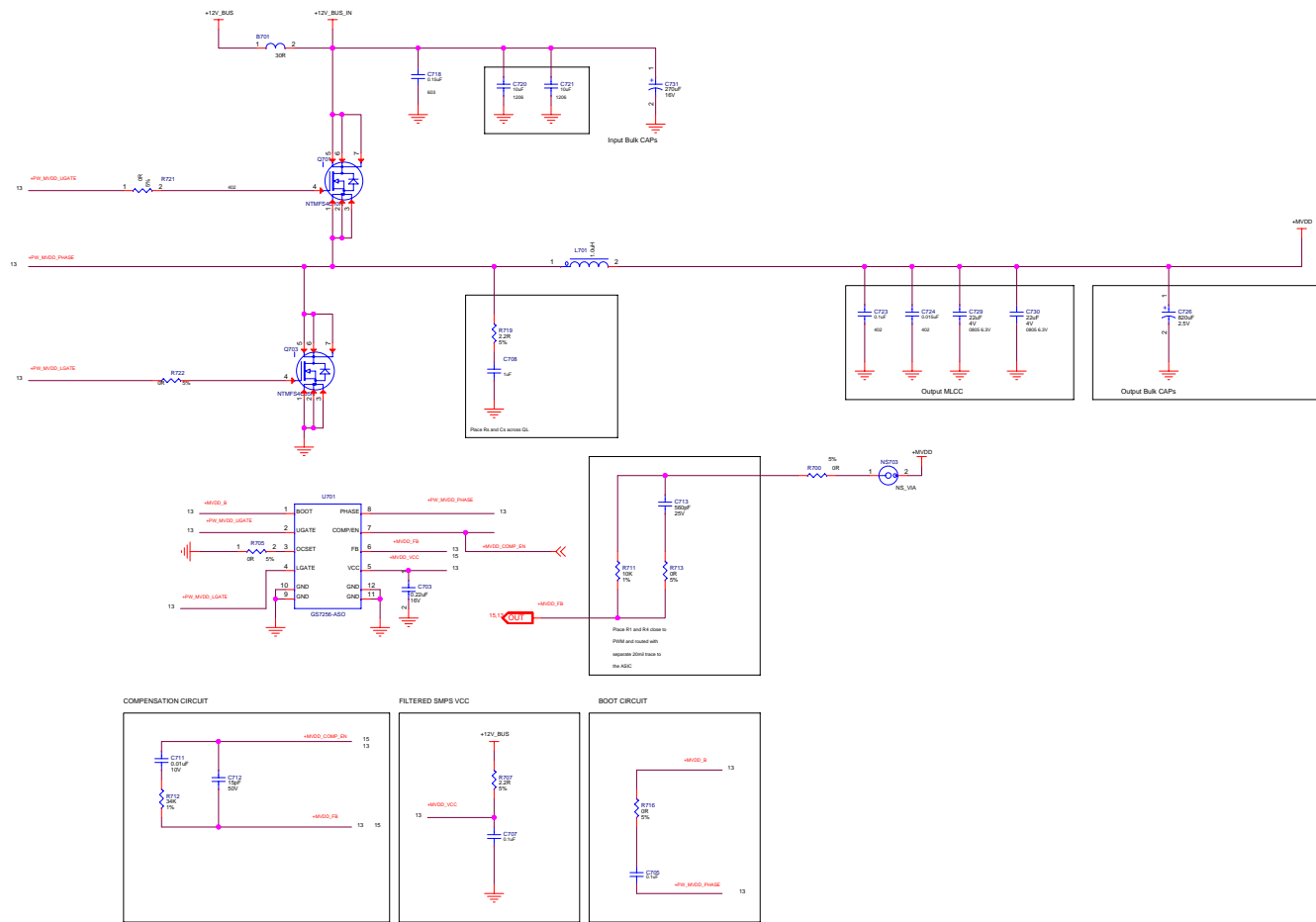
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(18) MVDD



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SHEET: MVD	DATE: Wed Jun 01 06:00:30 2016		REV: 1.0	TITLE:
SHEET NUMBER: 13	OF 19	TITLE		
DOCUMENT NUMBER: 105_CXX000_00A				

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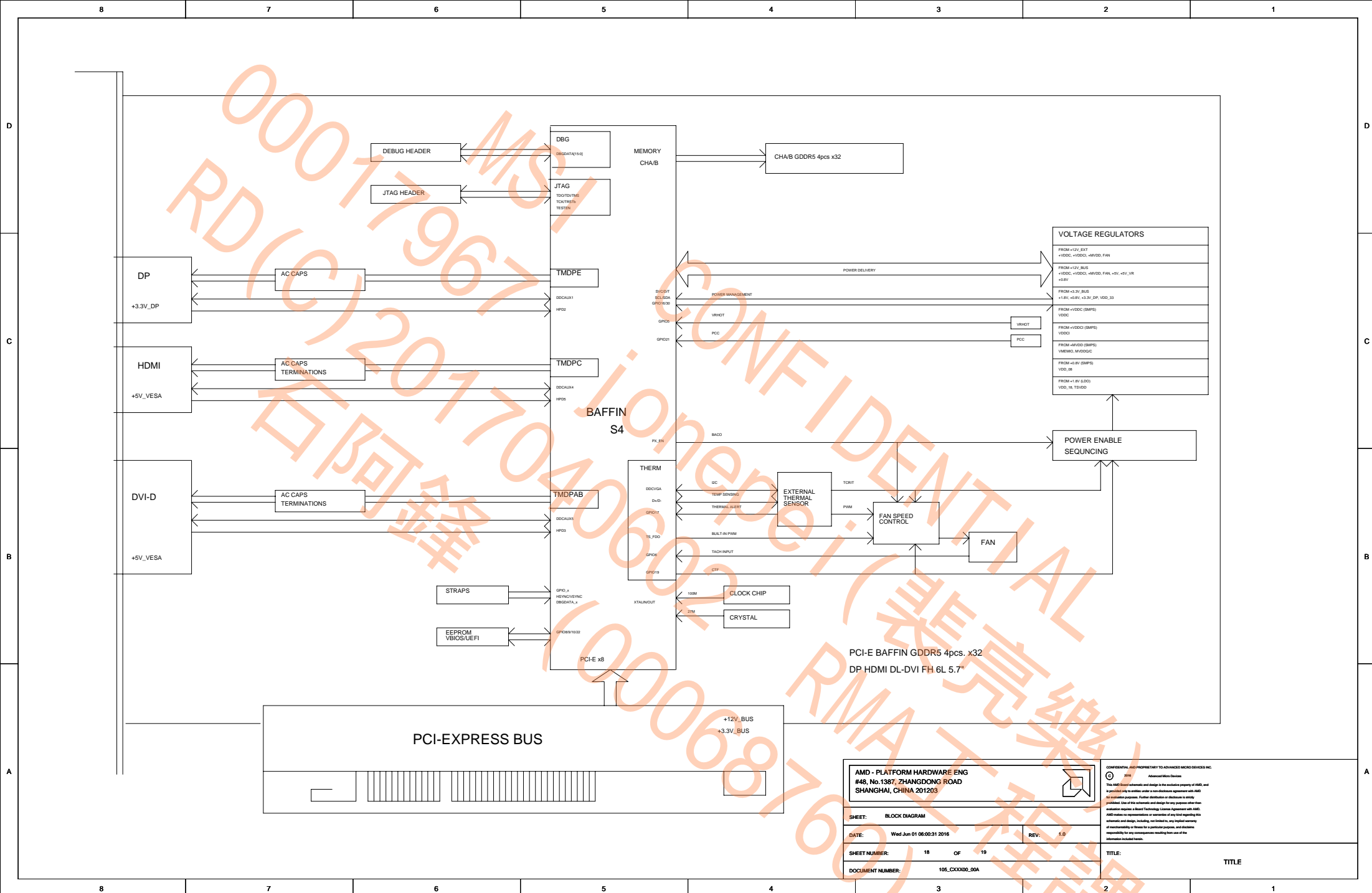
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AMD				TITLE:		DOCUMENT NUMBER: 106_C00000_00A		DATE: Wed Jun 01 06:00:31 2016		SHEET NUMBER: 19 OF 19		REV: 1.0			
REVISION HISTORY				ENGINEER: XXXX		NOTES: NOTE		CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC. This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representation or warranty of any kind regarding this schematic and design, including, not limited to, any implied warranty of non-infringement or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of this information included herein.		AMD - PLATFORM HARDWARE ENG #48, No.1367, ZHANGDONG ROAD SHANGHAI, CHINA 201203					
SCH Rev		PCB Rev		Date		REVISION DESCRIPTION									
1		006		10/20/2015		Initial Design									
1		006		10/20/2015		AMD 1.01 USB to SATA Bridge, AMD 100MHz CLOCK speed Change 0.001 power good pin_06									

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