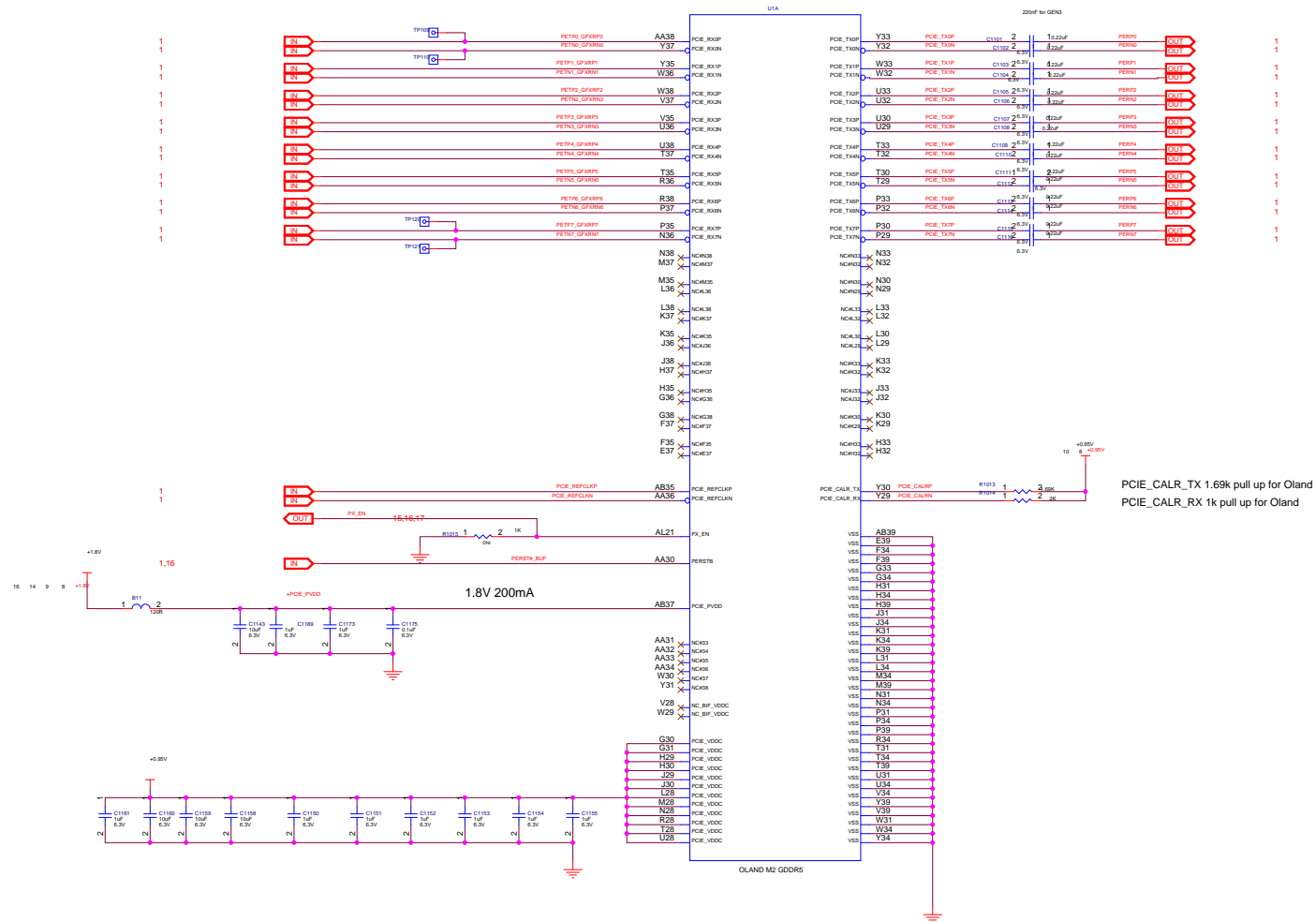
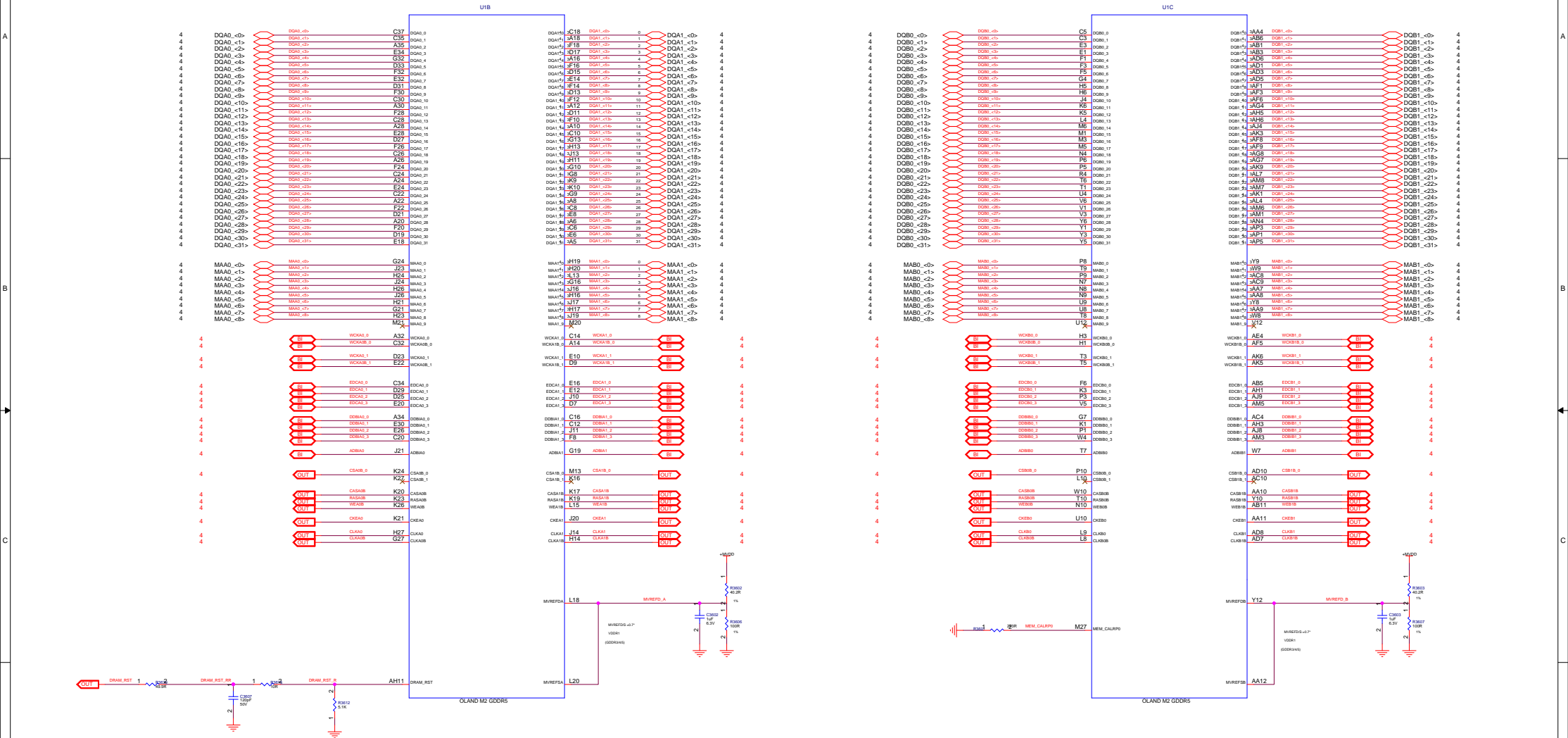


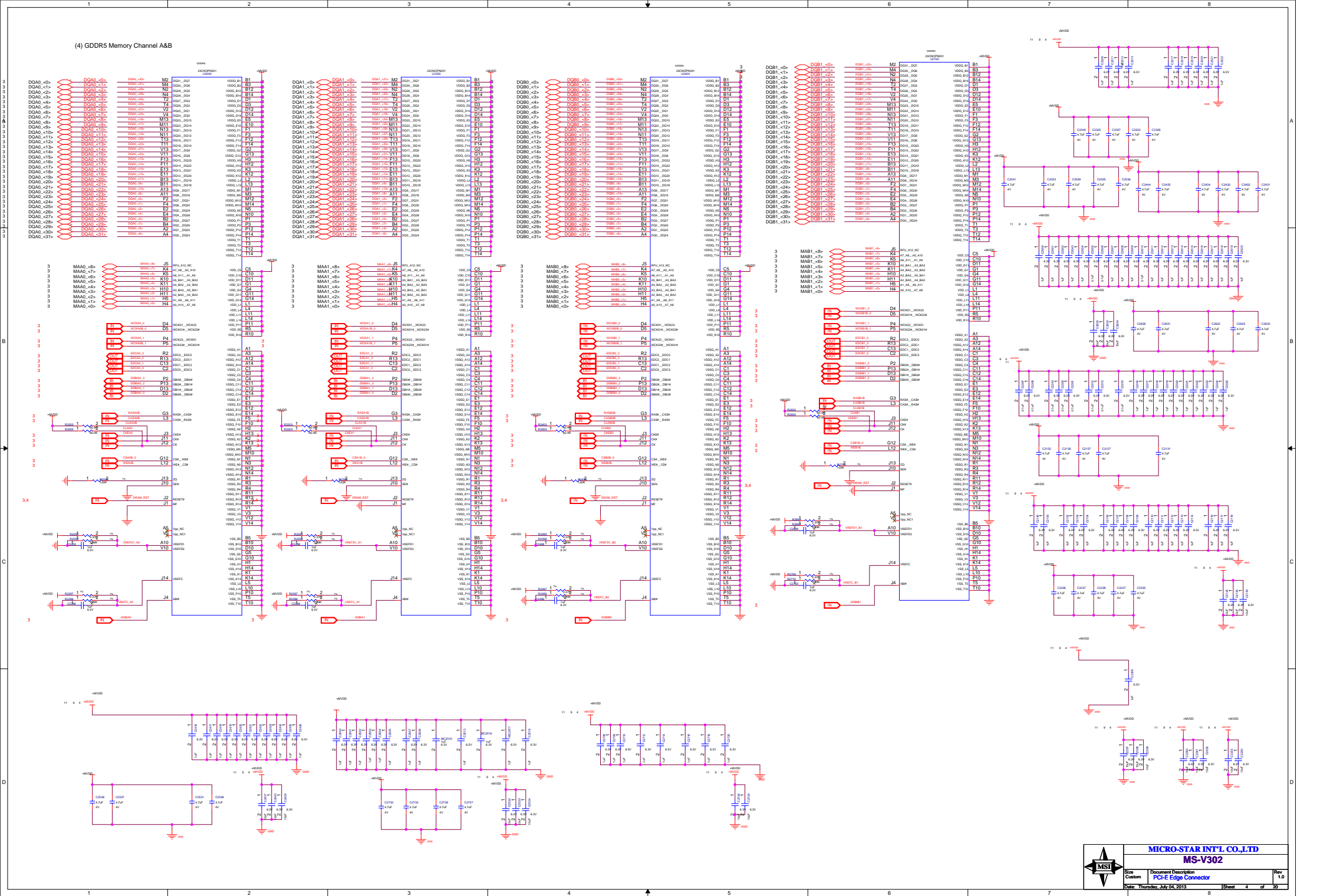
Oland PCIe Interface

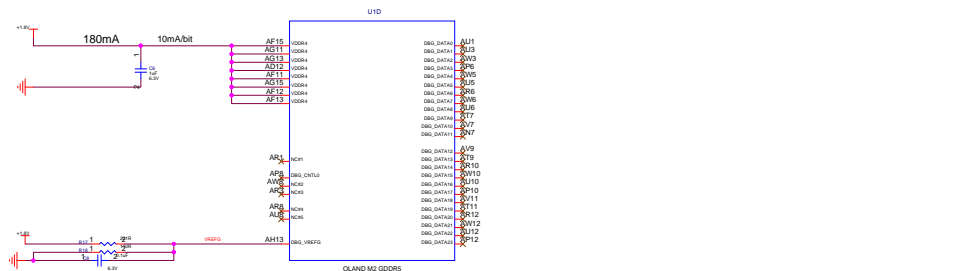
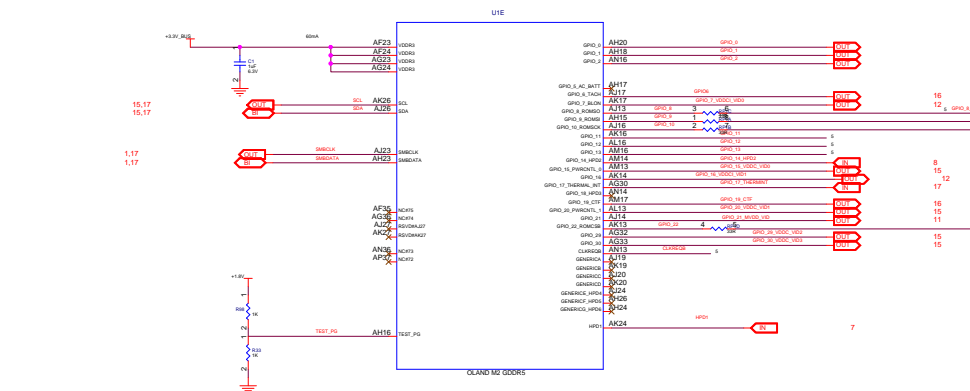
NOTE: Some of the PCIe isopoints will
be available through vias on traces.





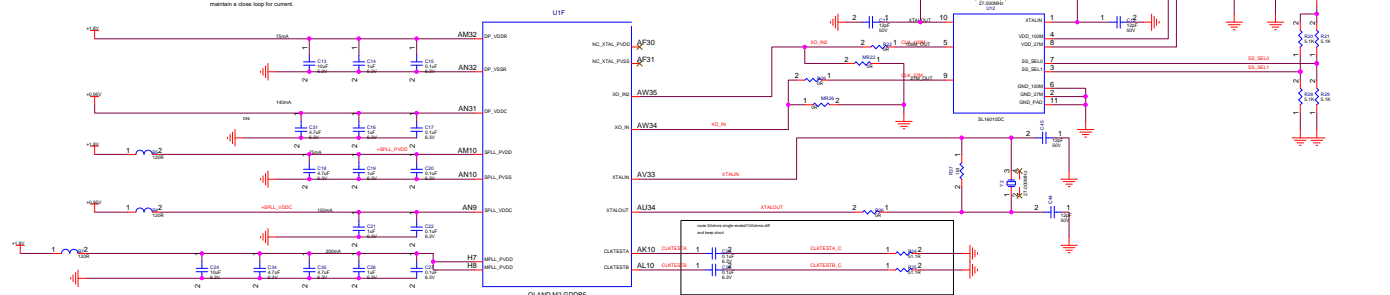
(4) GDDR5 Memory Channel A&B

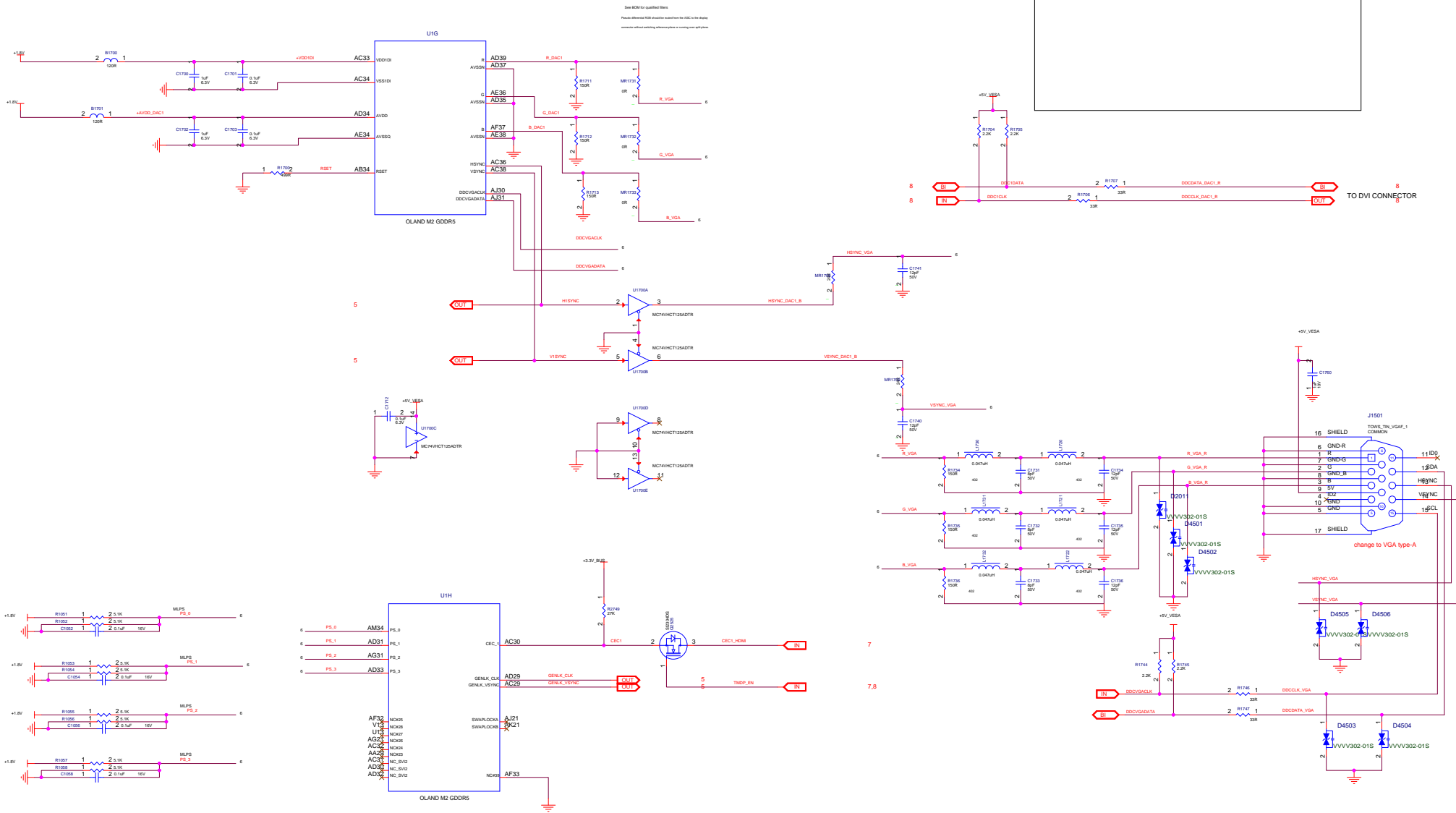


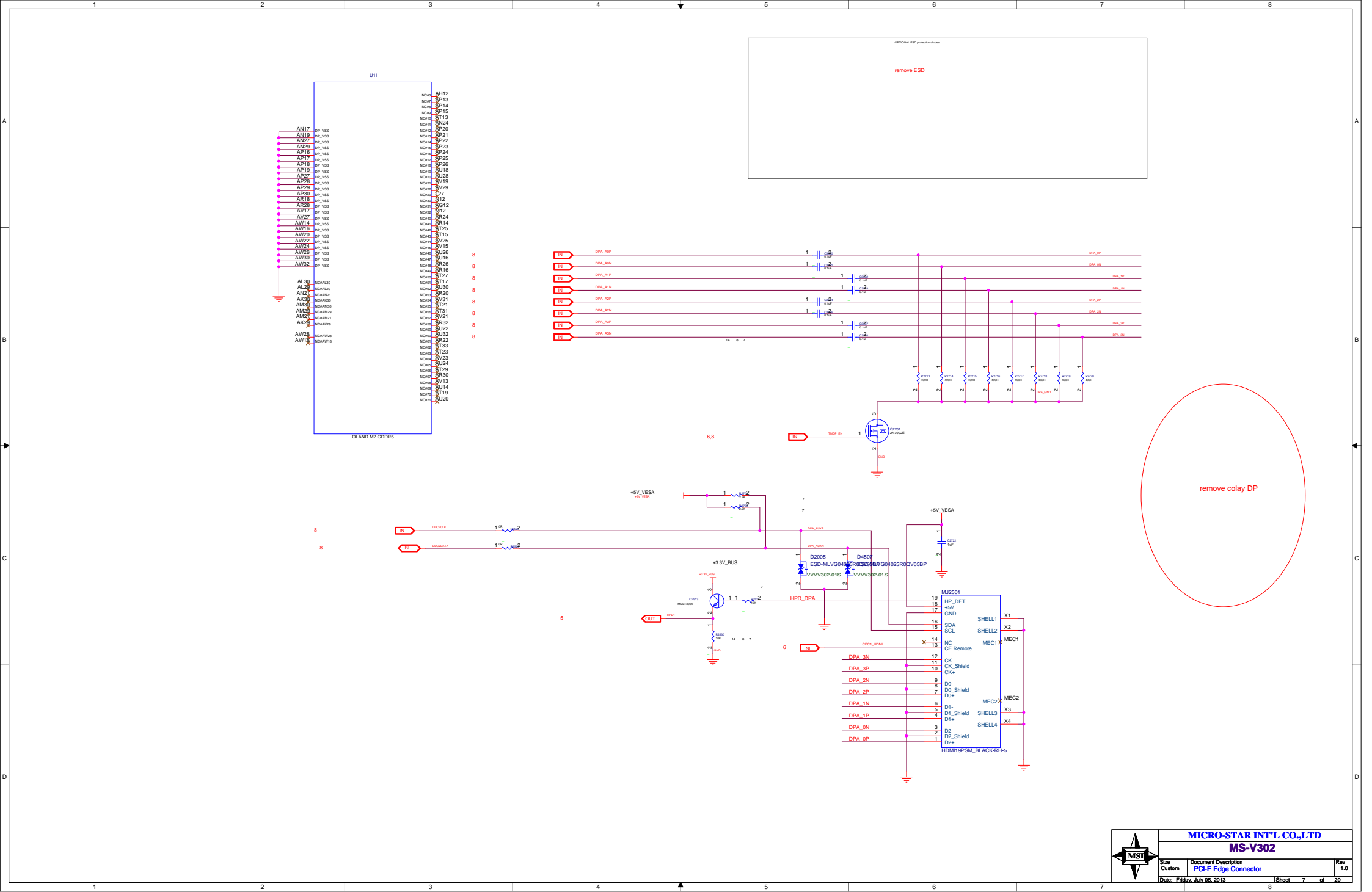


Please place the capacitors
helps to filter the noise and
keep the connection

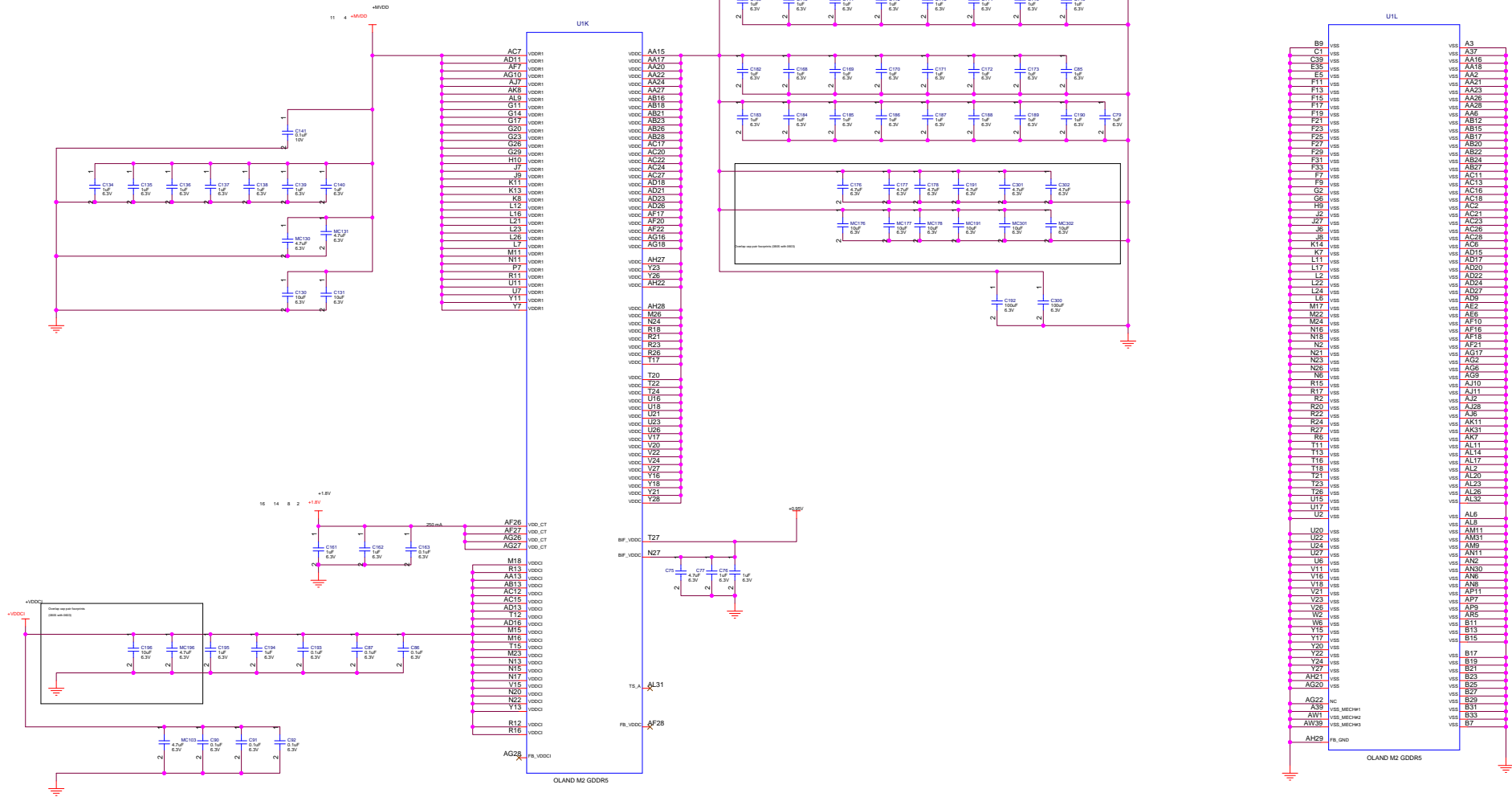
Please pay attention to the grounding
strategies for these filter capacitors to
maintain a close loop for current.





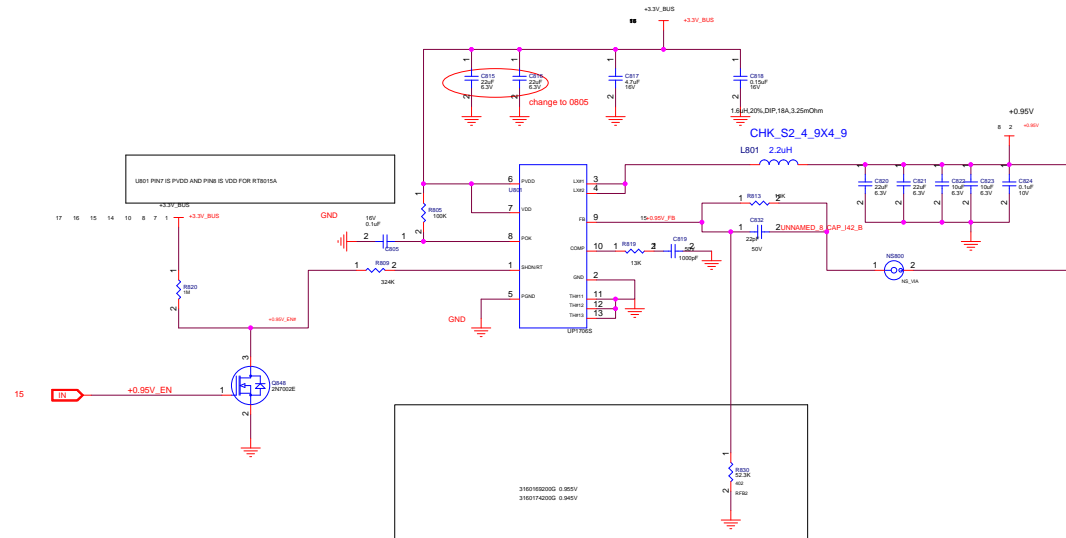


Oland Power & GND

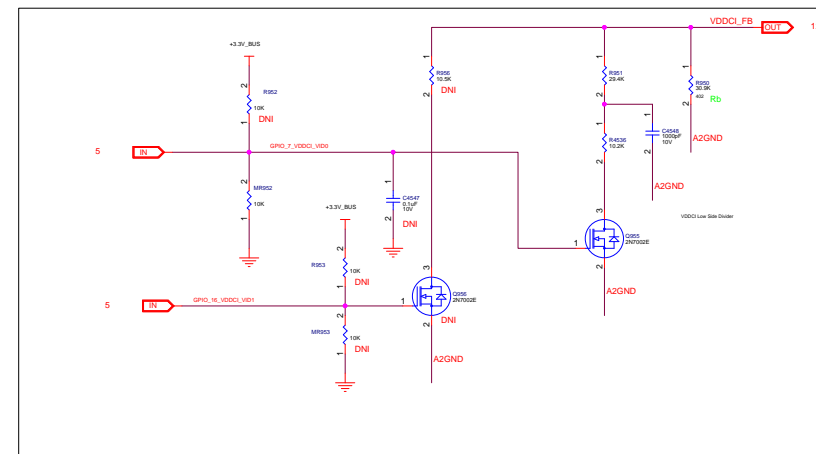
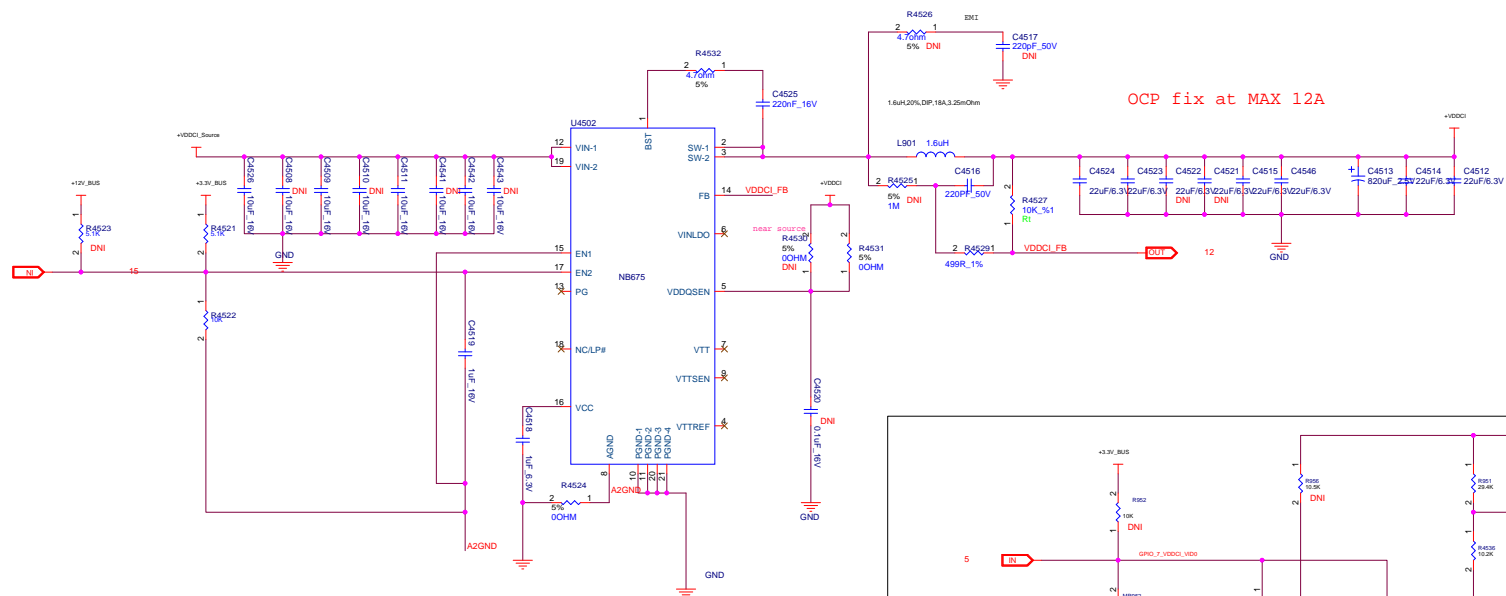




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VDDCI



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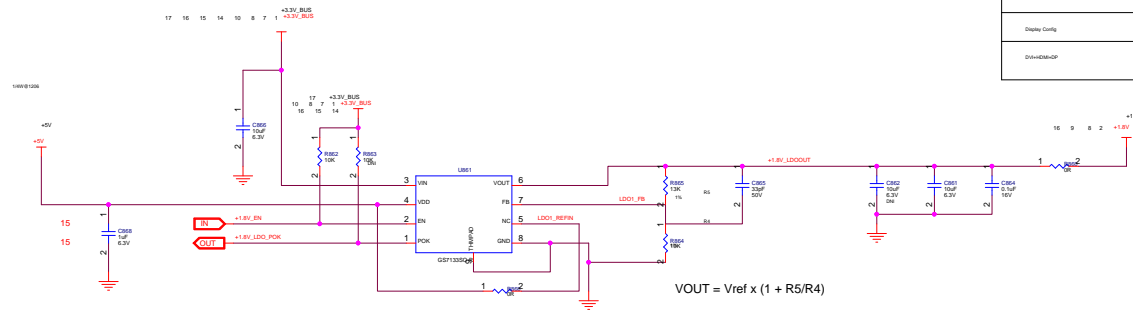
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Linear Regulators

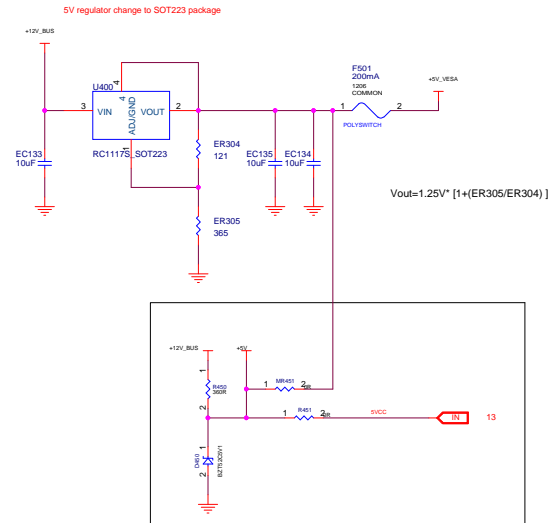
LD0 #1: $V_{in} = 3.00V$ to $3.60V$ ($3.3V \pm 1.9\%$) $V_{out} = +1.8V \pm 2\%$; $I_{out} = 1.6A$ (TYP) RMS MAX

PCB: 50 to 70mm sq. copper area for cooling

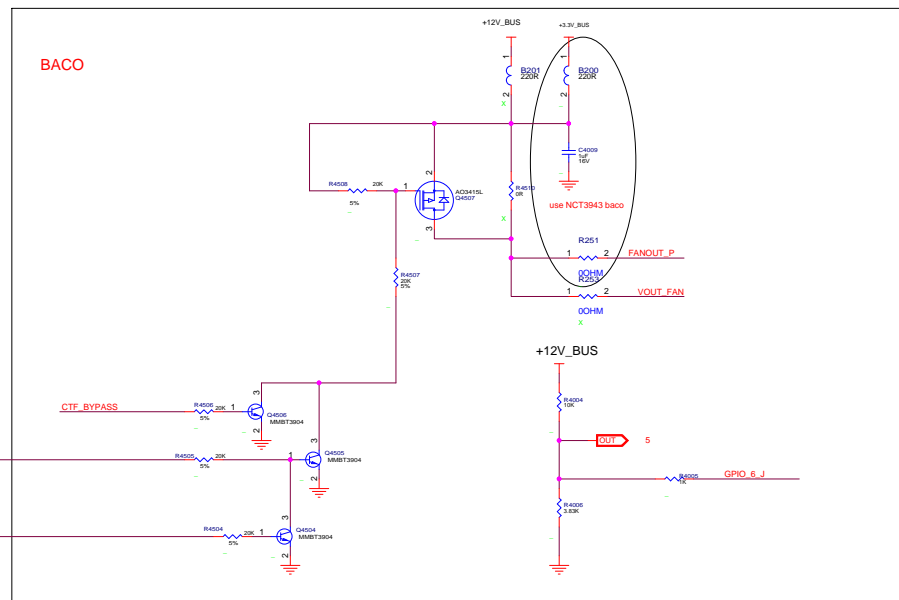
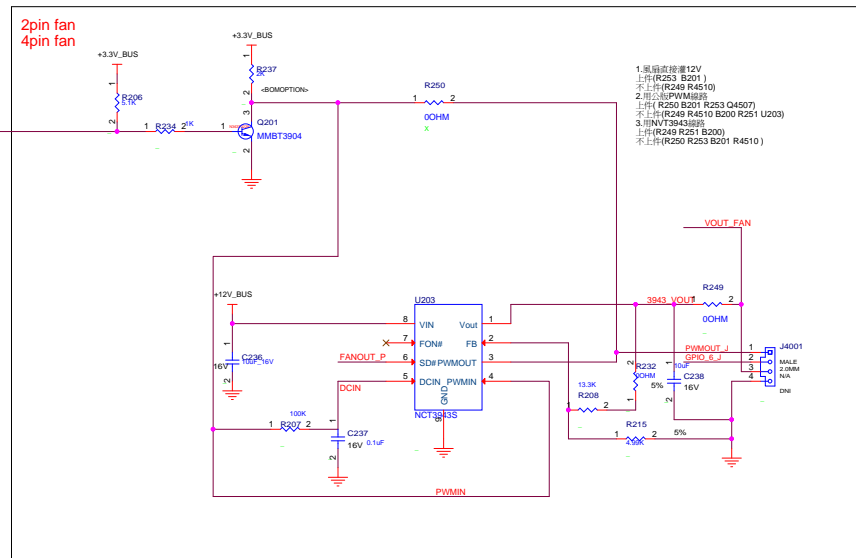
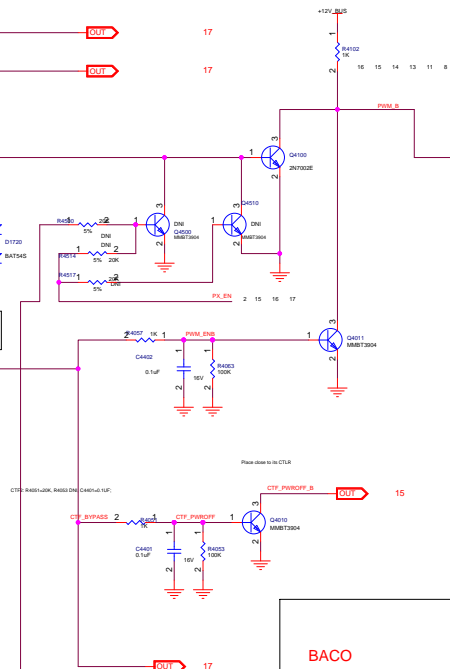
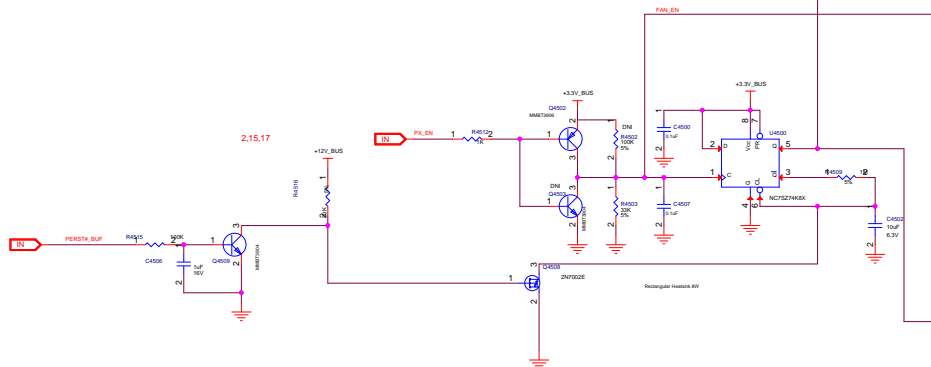
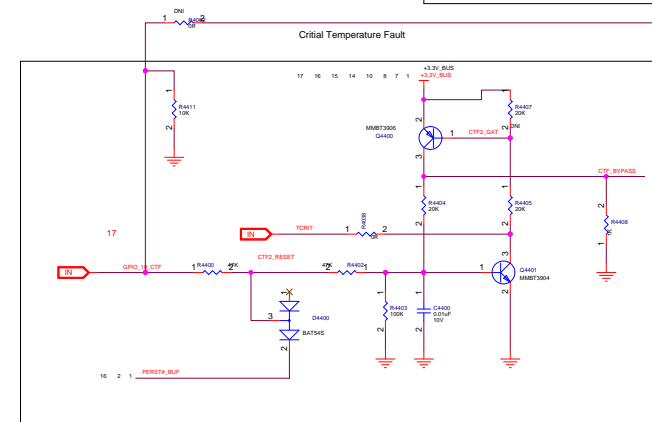


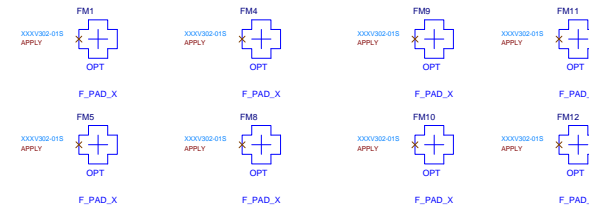
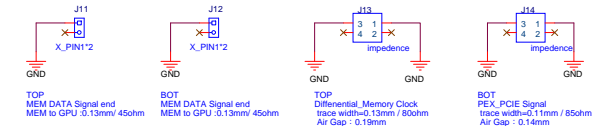
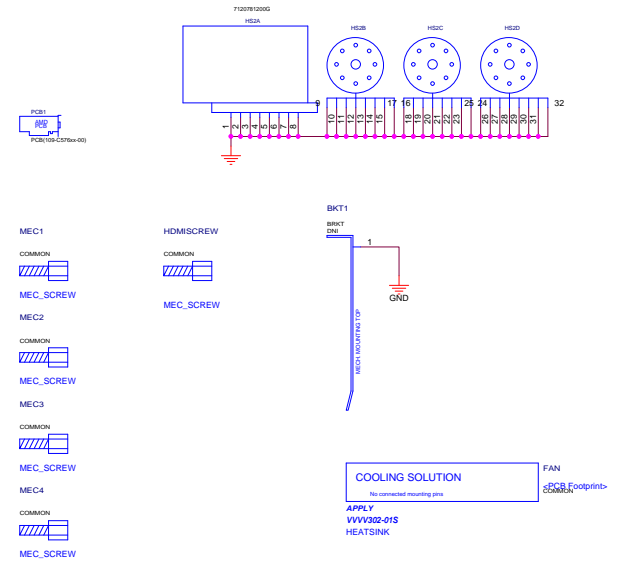
1.8V WORST-CASE REQUIREMENT	
Display Config	Est. Current
D1+H2DM+OP	1300mA

Regulators for +5V, +5V_VESA and +5V_VESA2



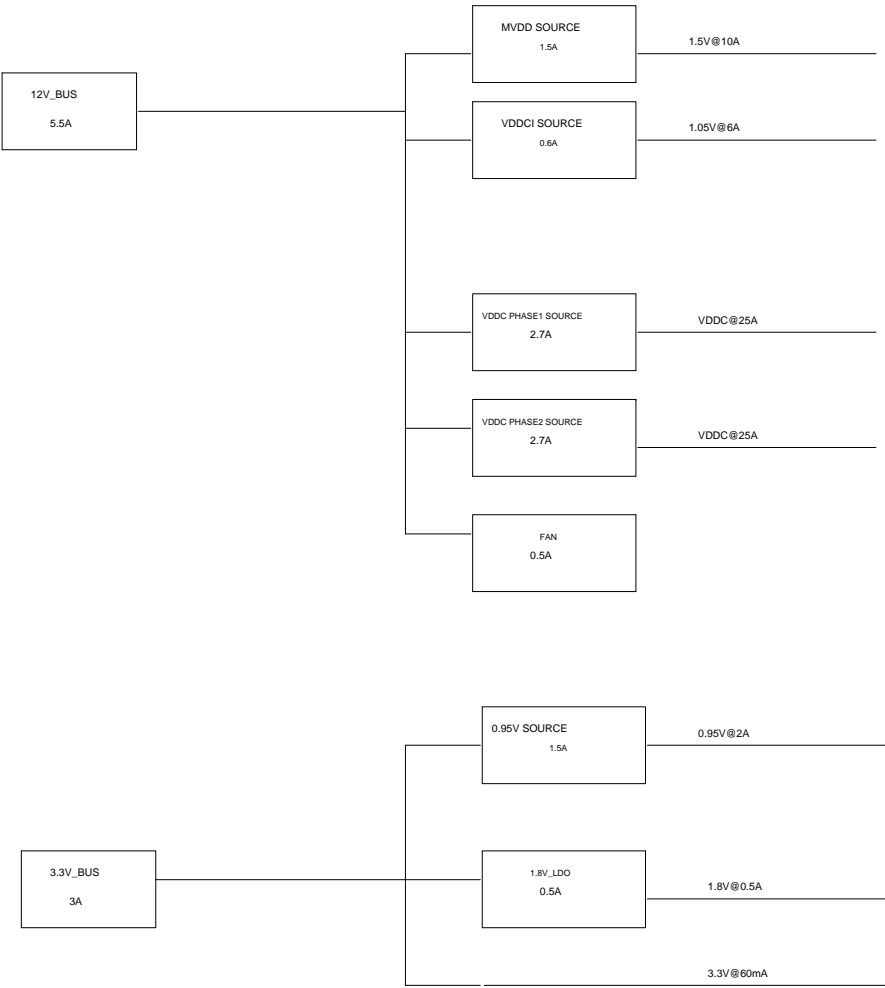
Critical Temperature Fault

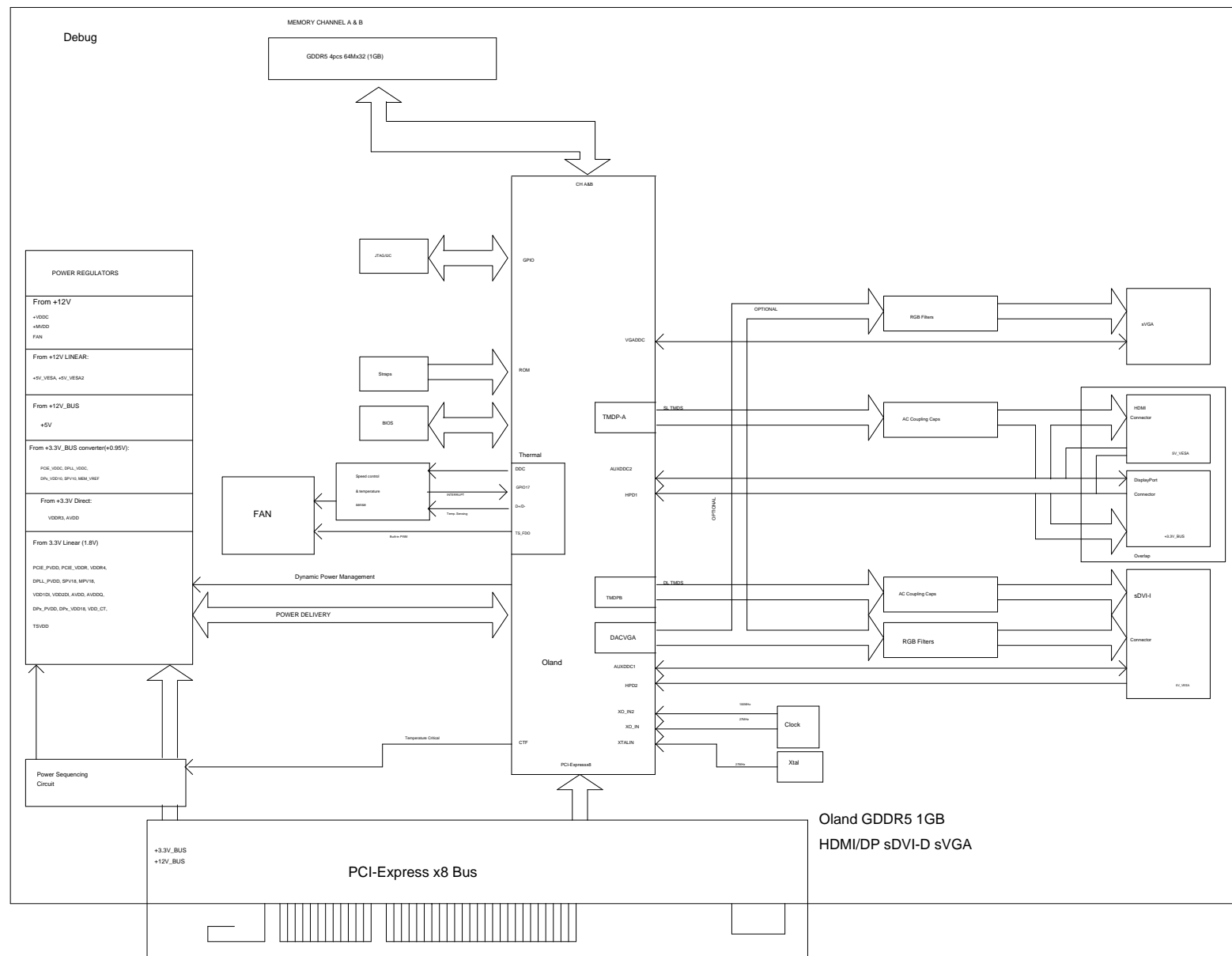


[illegible]

MD01	HPD4	DDC1
HEM00P	HPD2	DDC1AUX2
VGA		DDC1GA

GPIO15	VDDC_VD0	
GPIO30	VDDC_VD1	
GPIO39	VDDC_VD2	
GPIO38	VDDC_VD3	
GPIO7	VDDC_VD0	DEFAULT 1
GPIO16	VDDC_VD1	
GPIO21	MVDD_VD	





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			TITLE Client XT DDR5 1GB 40V14H8M8P-VGA	SCHEMATIC NO 1GB-C3750X-00	DATE
REVISION HISTORY			NOTE: THIS SCHEMATIC REPRESENTS THE PCB. IT DOES NOT REPRESENT ANY SPECIFIC BOM. FOR PRODUCT SPECIFIC EQUIPMENT ISSUES, YOU HAVE TO CONSULT THE PRODUCT SPECIFIC BOM. PLEASE CONTACT HIRE REPRESENTATIVE TO OBTAIN LATEST BOM CORRECT TO THE APPLICATION/VERSION.		
SCM REV	PCB REV	DATE	REVISION DESCRIPTION		
00	00	20130703			
			Page6.change J1501 to type-A and add ESD Page7.remove DP and add ESD Page8.add ESD Page10.change 0.95V converter and change L801 Page11.change MVDD PWM IC to converter and change L701 Page12.change VDDCI PWM IC to converter and change L901 Page13.change L601/L612 Page14.change 5V Regulator Page16.add Fan IC + 4pin Fan		