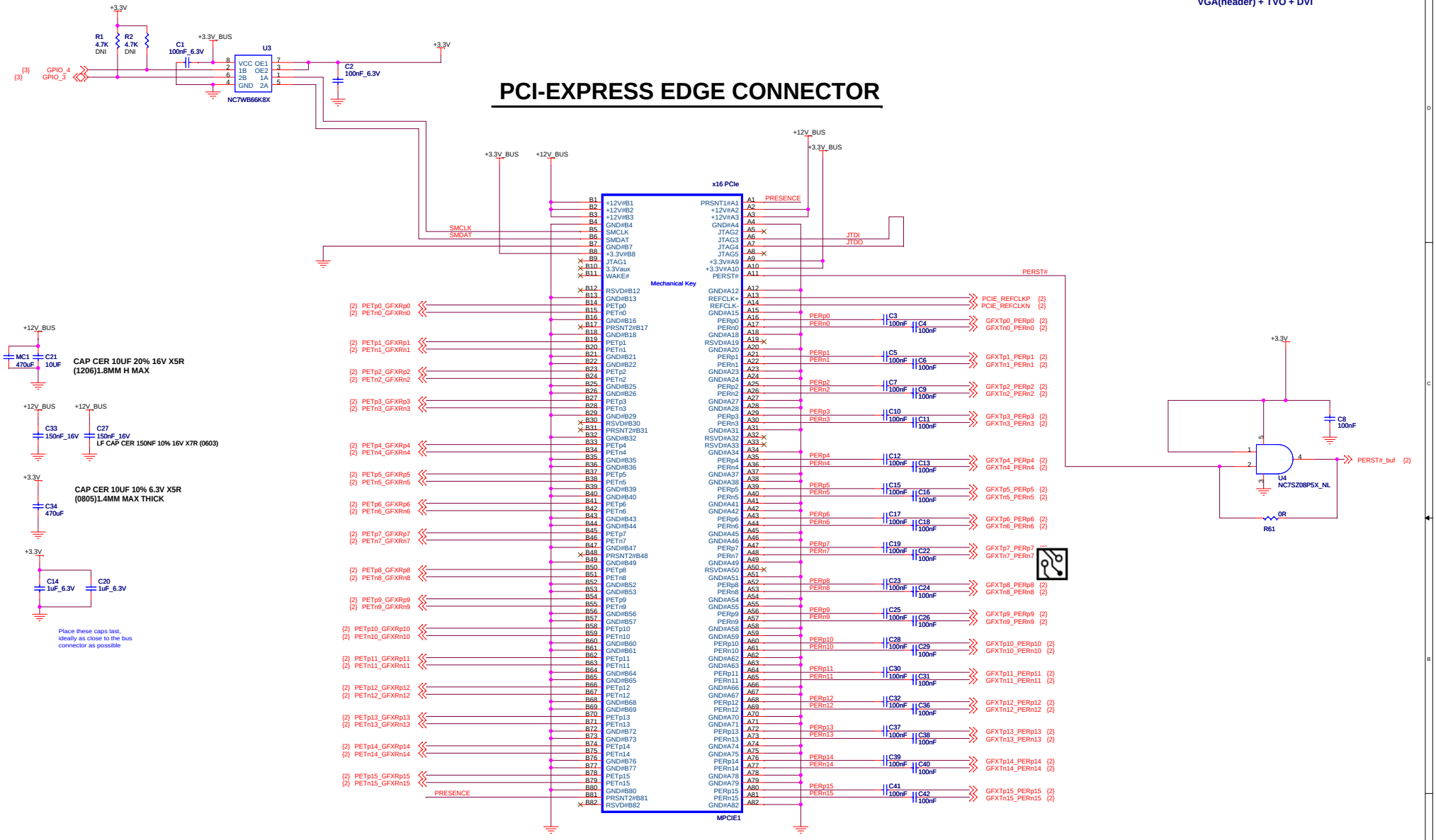
RV710 CUSTOM

VGA(header) + TVO + DVI

PCI-EXPRESS EDGE CONNECTOR



SYMBOL LEGEND

DNI	DO NOT INSTALL
#	ACTIVE LOW
	DIGITAL GROUND
	ANALOG GROUND

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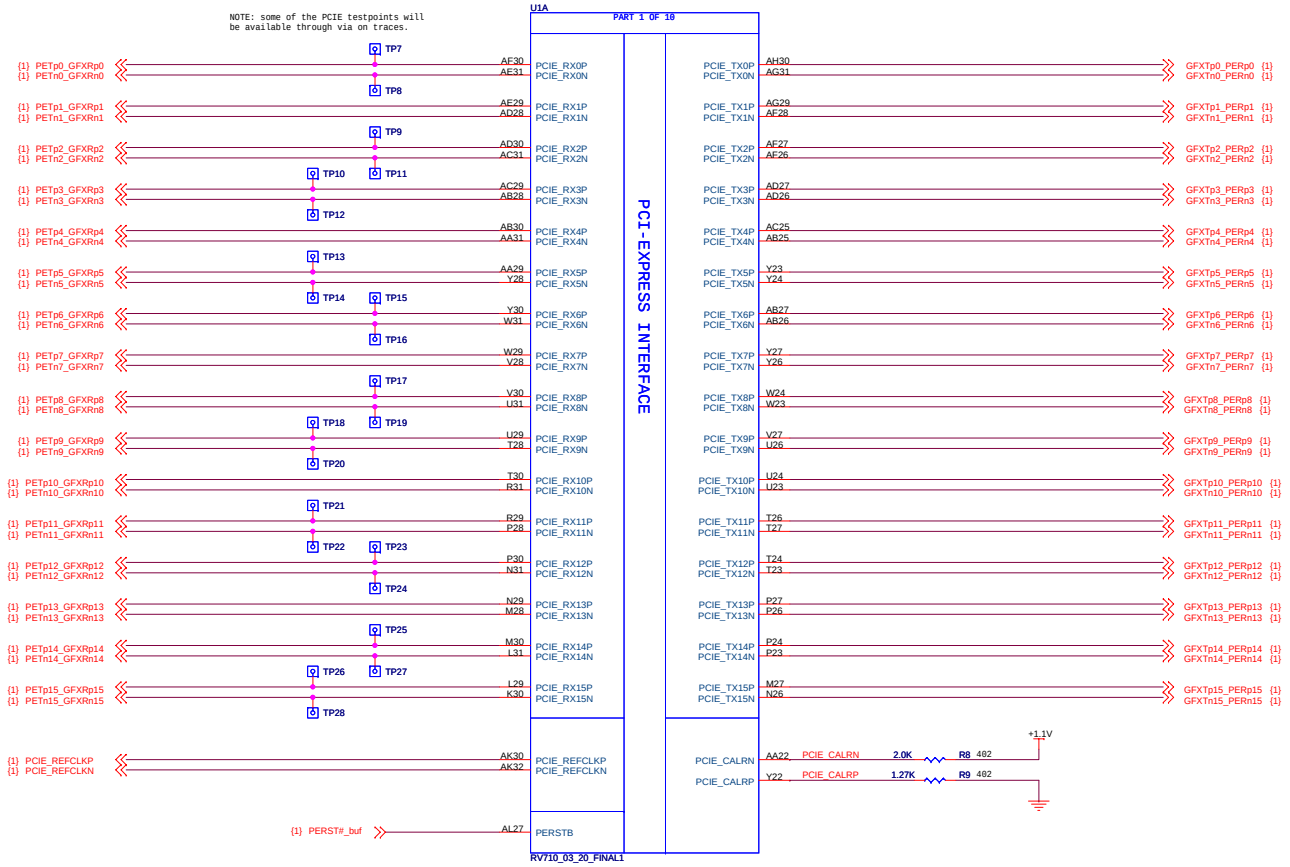

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Sheet	2 of 22
Title	RH LP RV710 DDR2 VGA (header) TVO DVI
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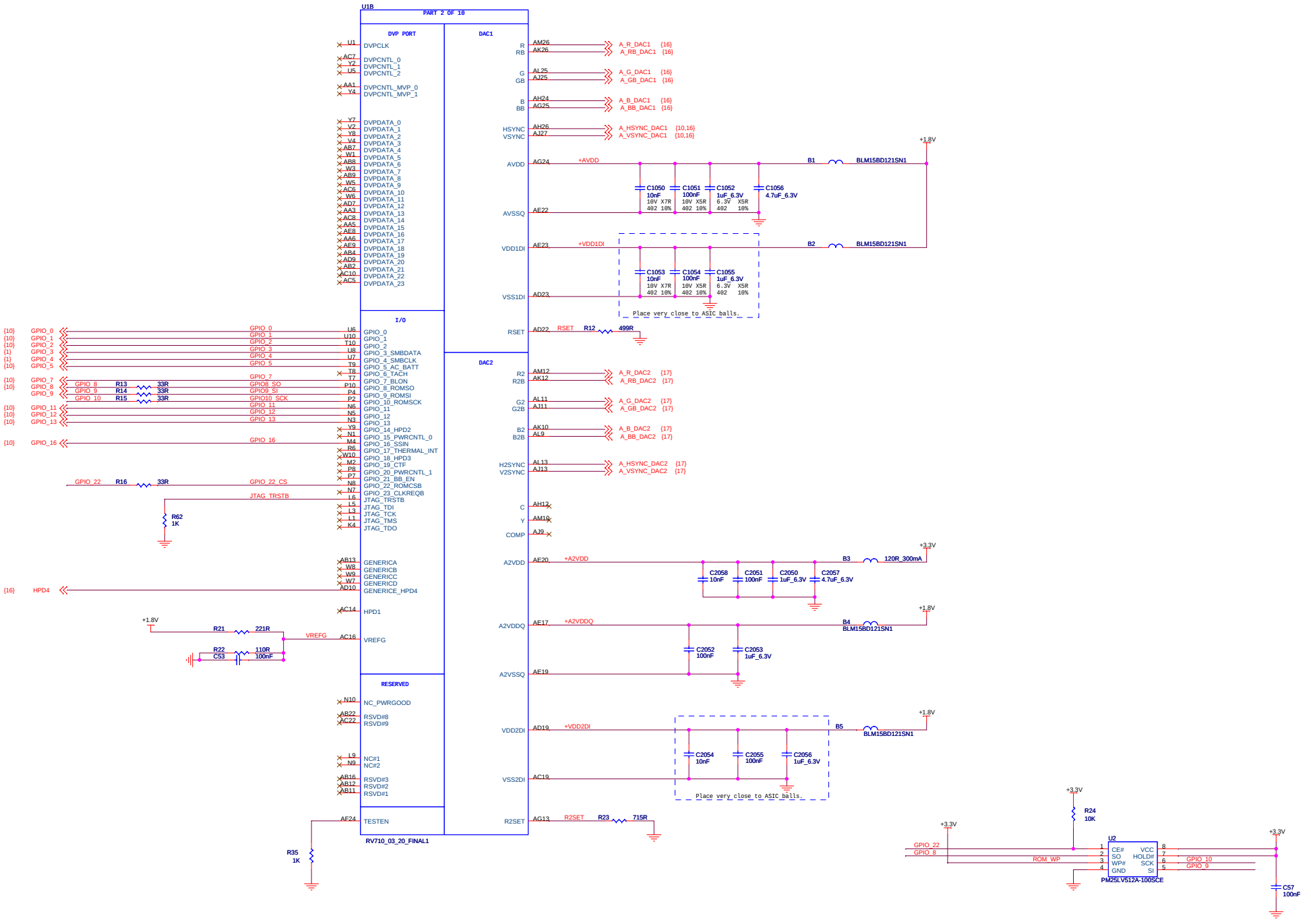
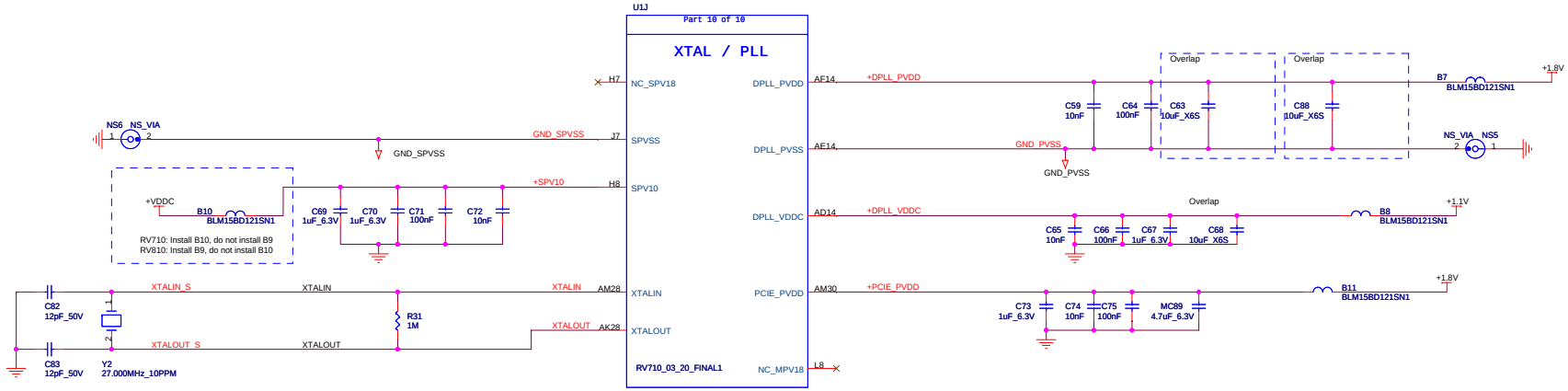


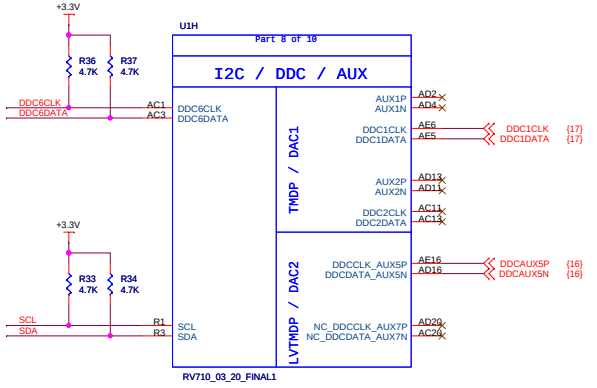


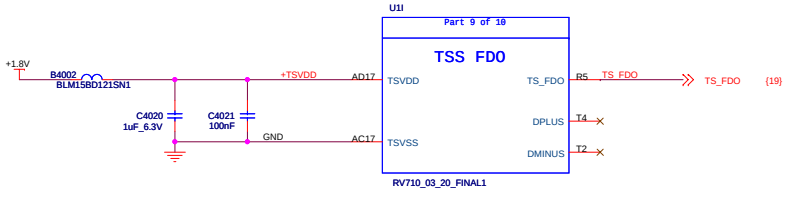
DDC6 BUS:

I2C Address	Function	Device
0x90	I2C VDDC Control	DS4402
0x98	LM63 - External Temperature Sensor	LM63

SCL / SDA BUS:

I2C Address	Function	Device
N/A	N/A	N/A





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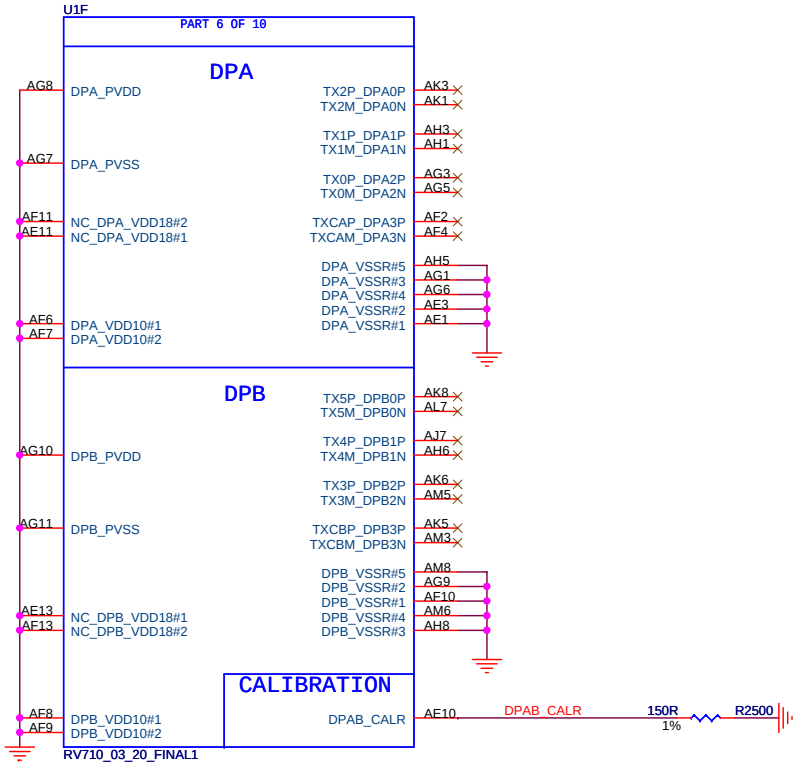
TMDP INTERFACE



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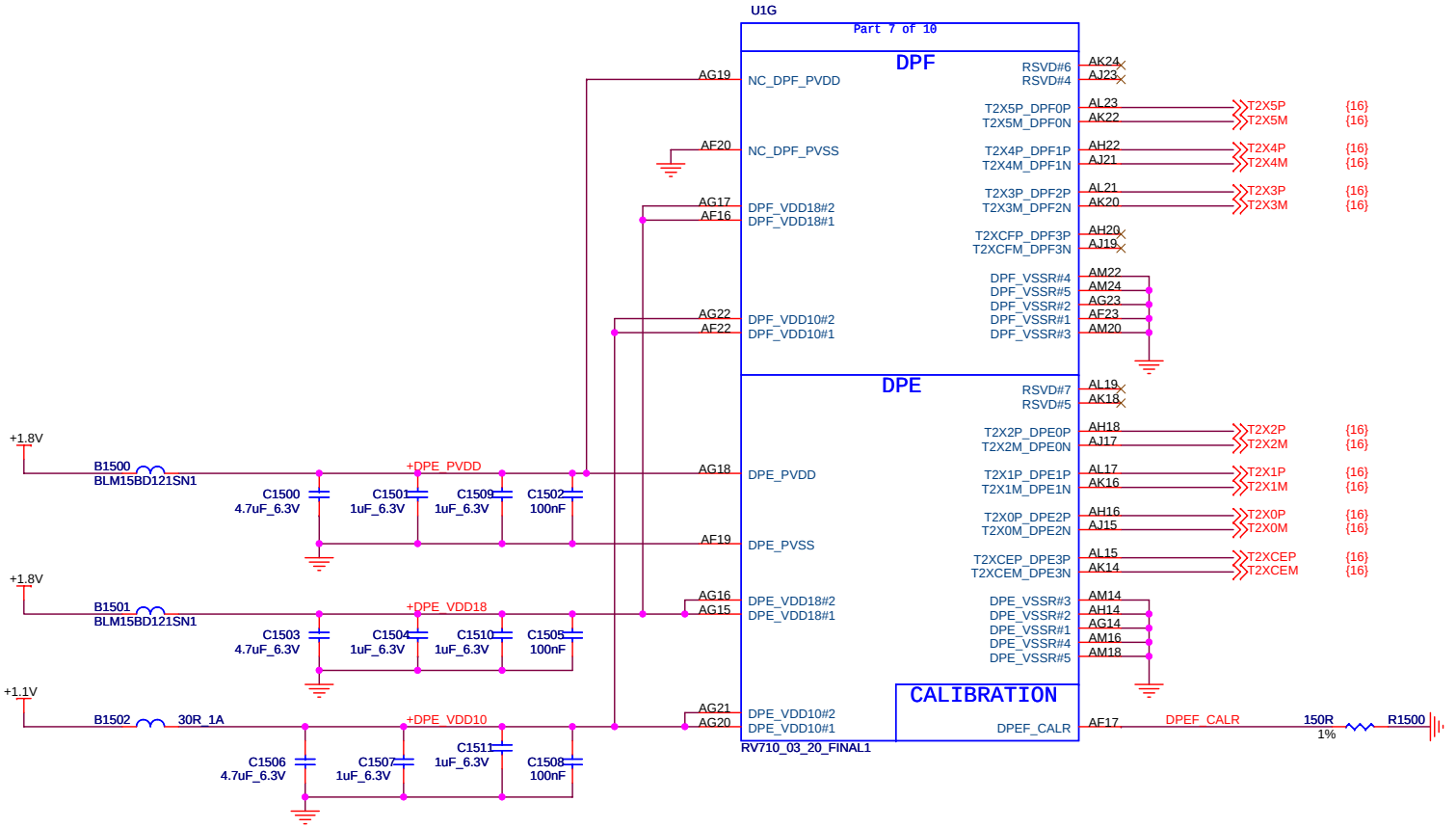
LVTMDP INTERFACE



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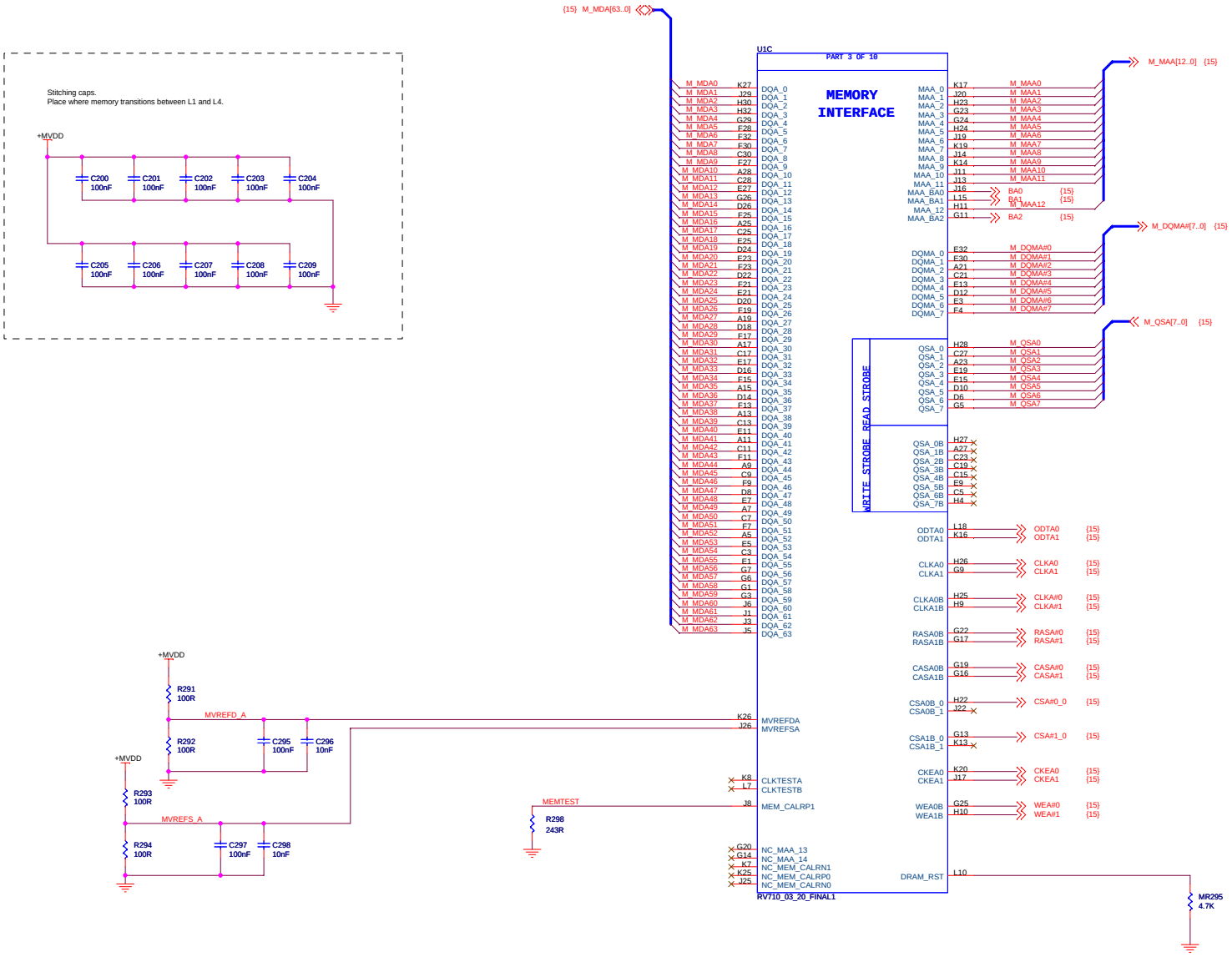
MEMORY INTERFACE



DIVIDER RESISTORS	DDR2
MVREF TO 1.8V	100R
MVREF TO GND	100R

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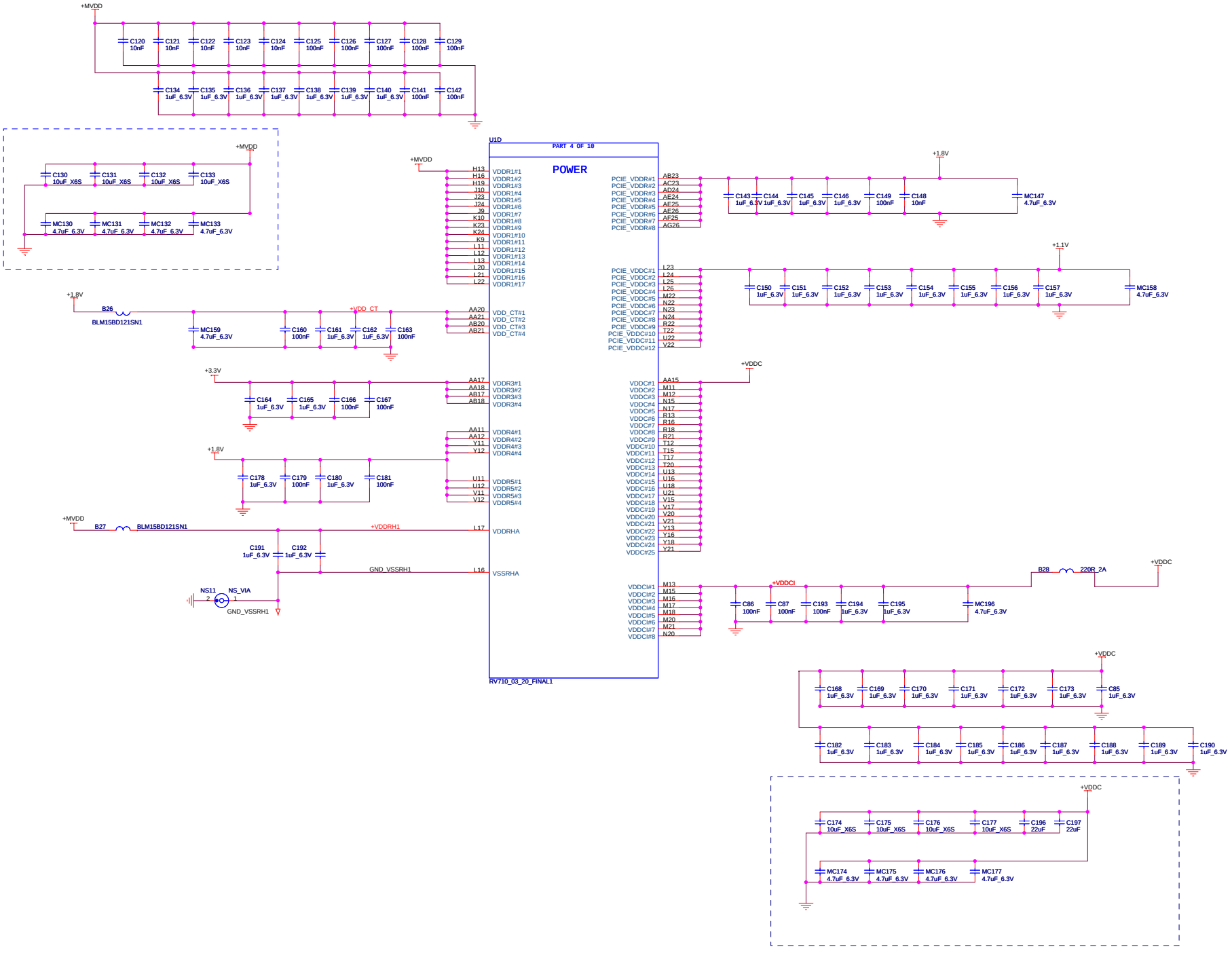

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Title: RH LP RV710 DDR2 VGA (header) TVO DVI

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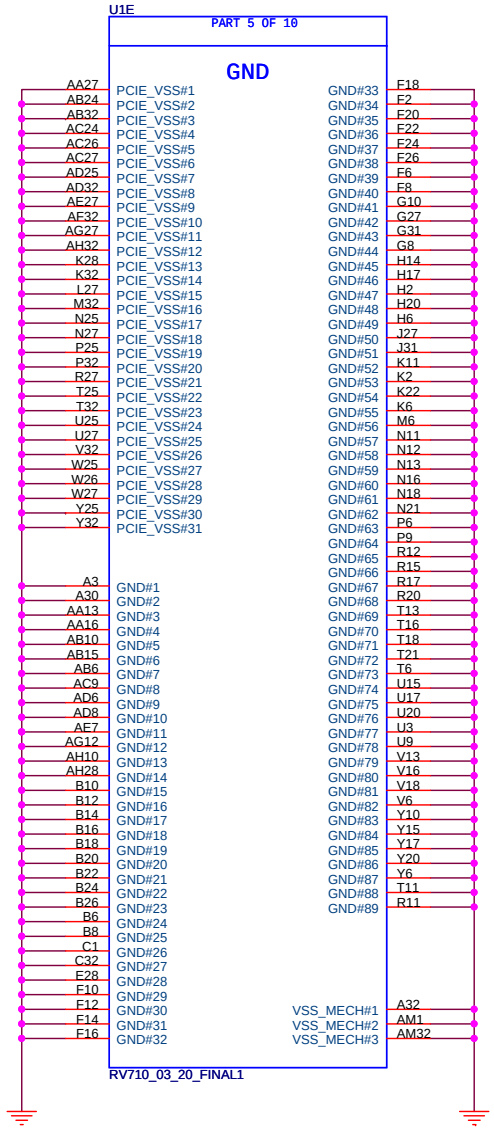

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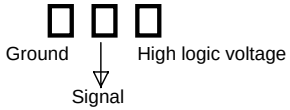
PIN BASED STRAPS

Pull-Down Resistors are for BU until built-in pull-downs are verified.

Overlap pads to save space and to prevent assembly of both resistors.

Layout



PIN BASED STRAPS

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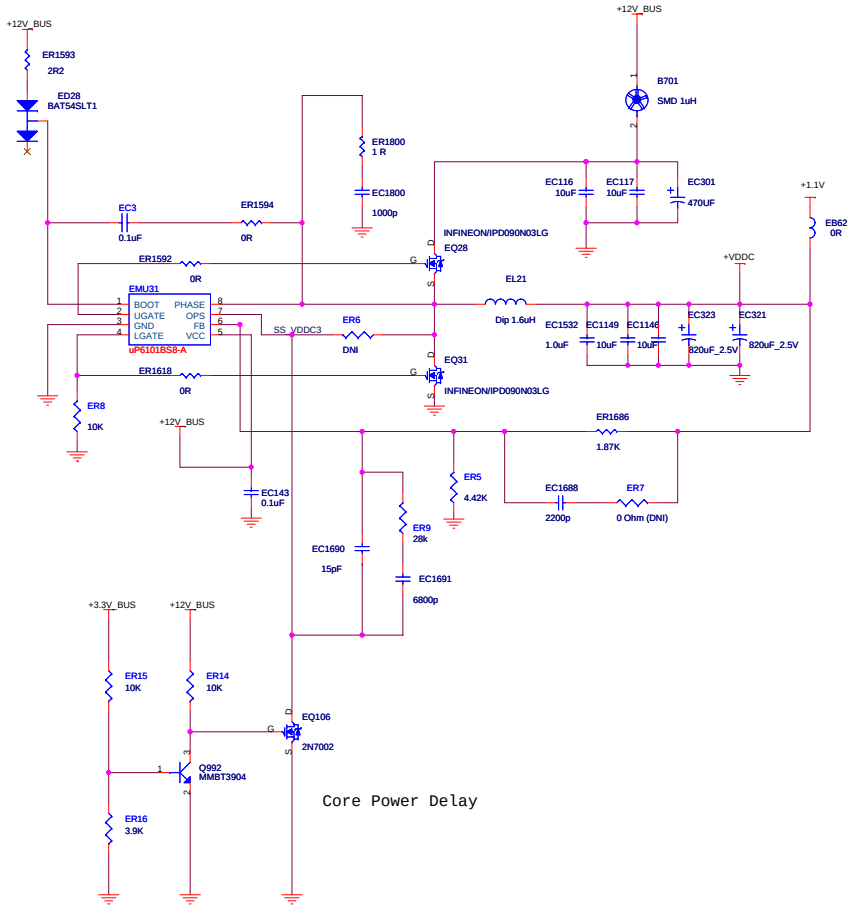

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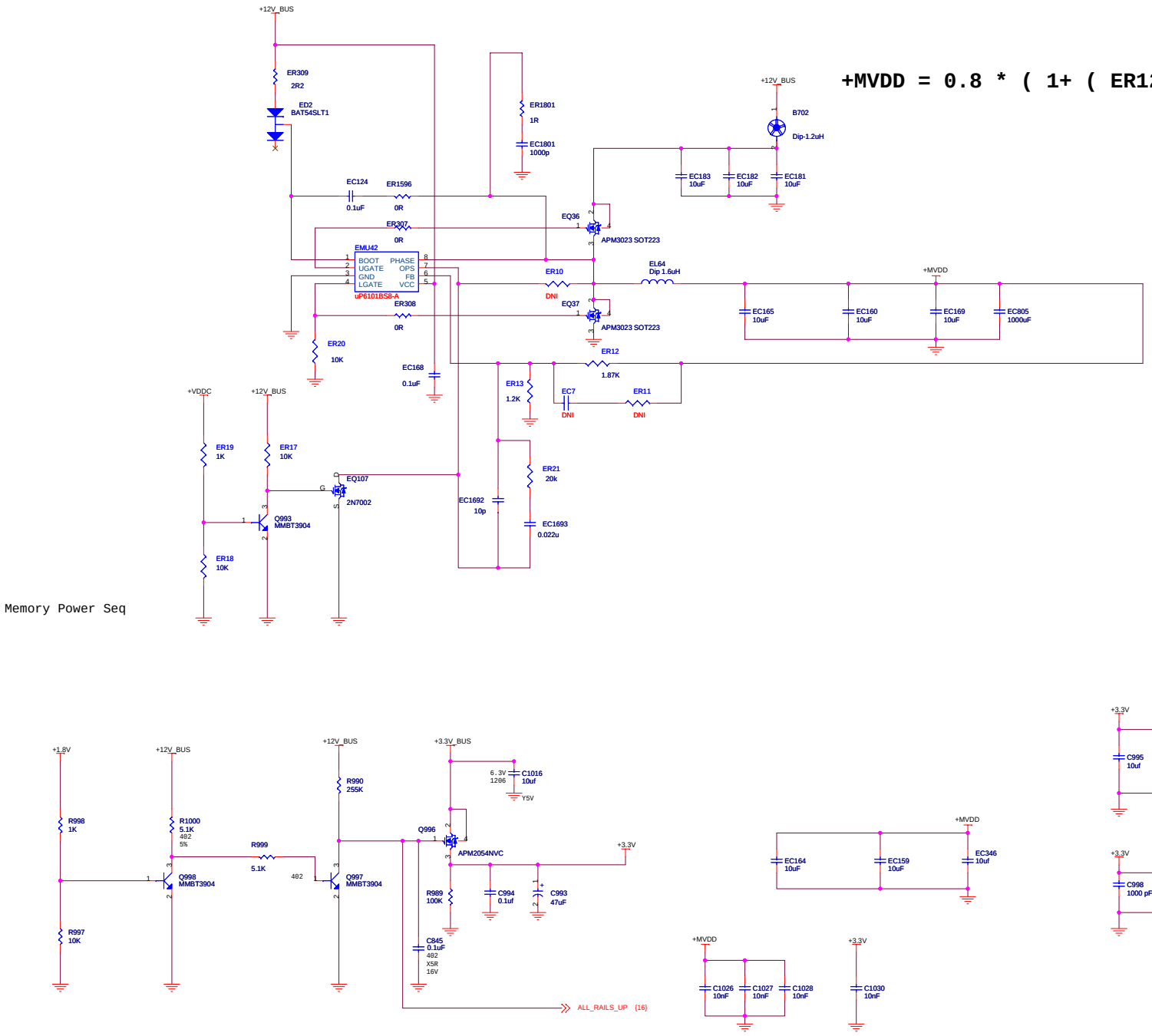

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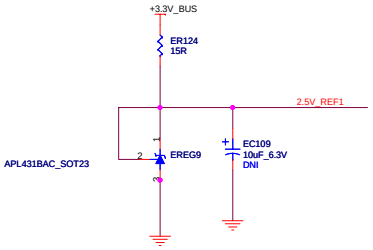
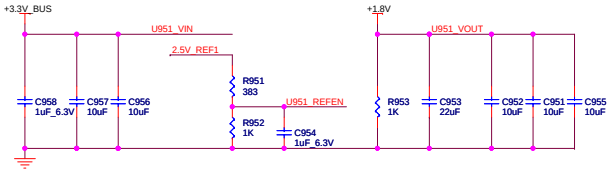
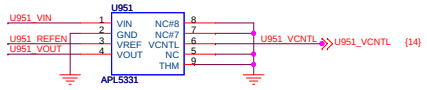
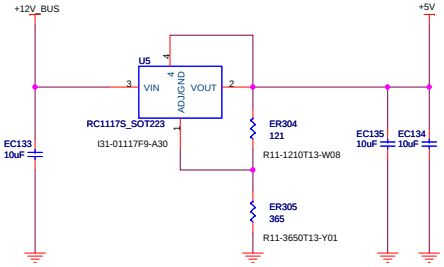
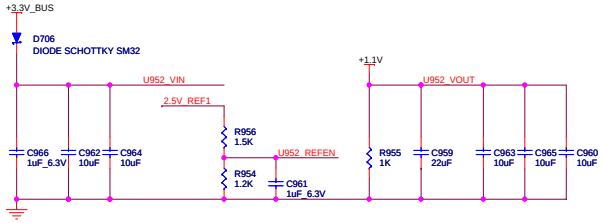


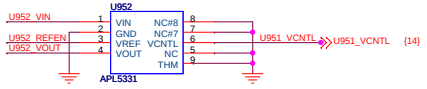






Vout=1.25V* [1+(ER305/ER304)]





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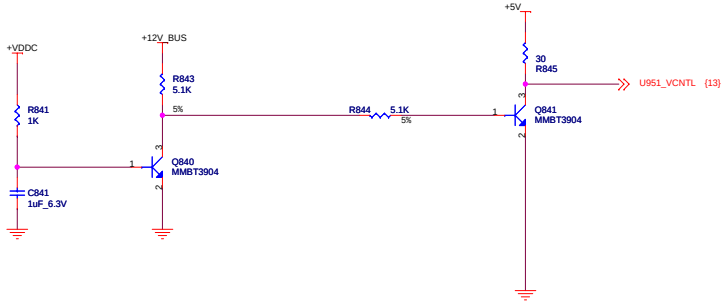

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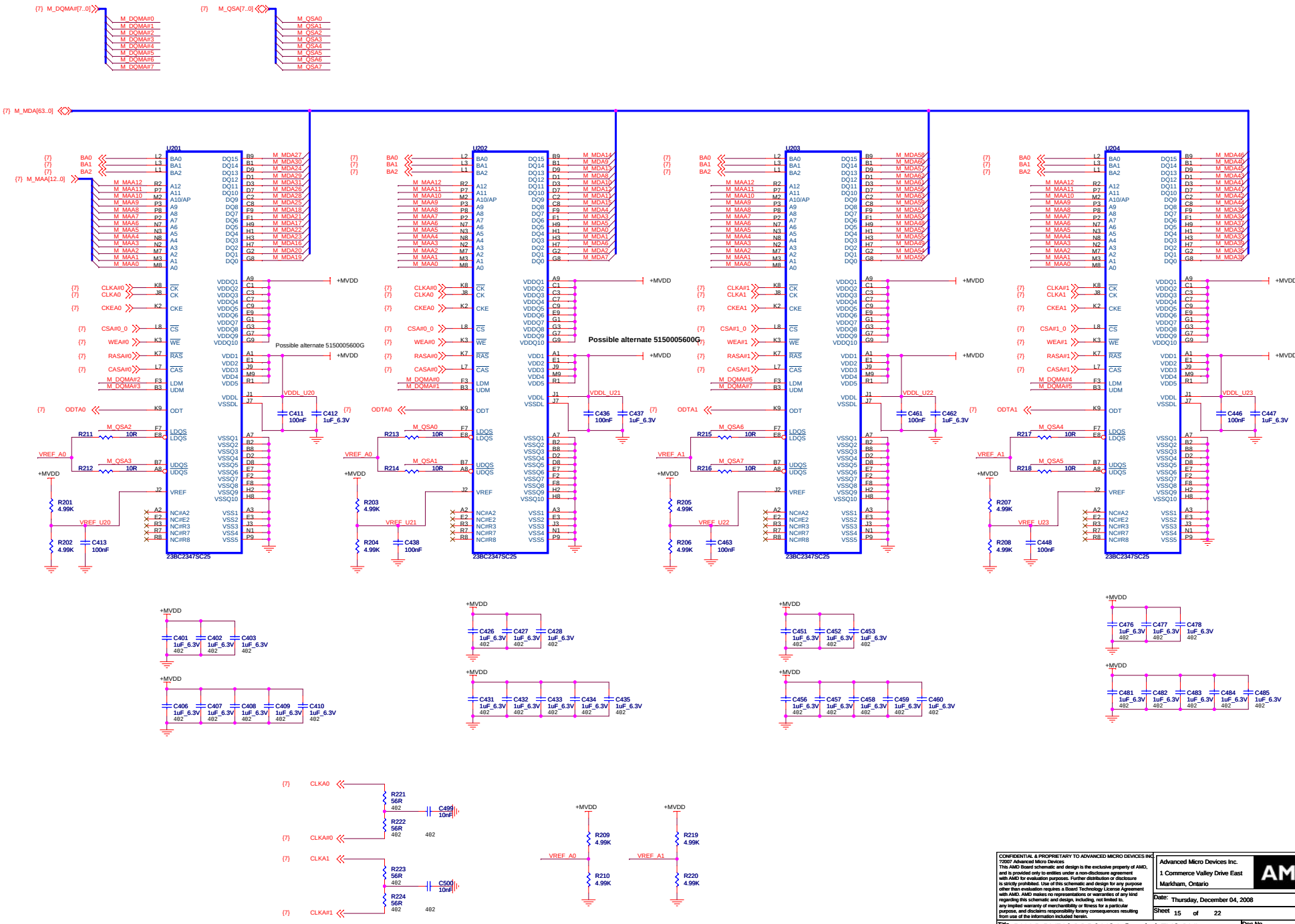
CHANNEL A: RANK 0 512MB DDR2

MAX DENSITY: 64Mx16



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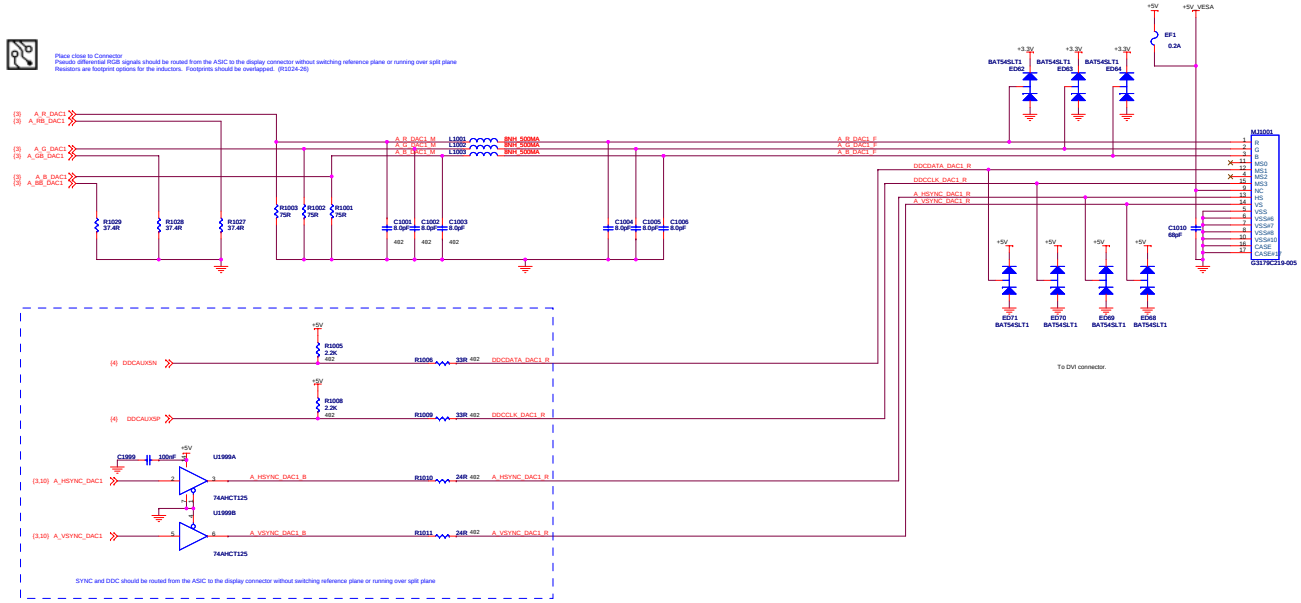
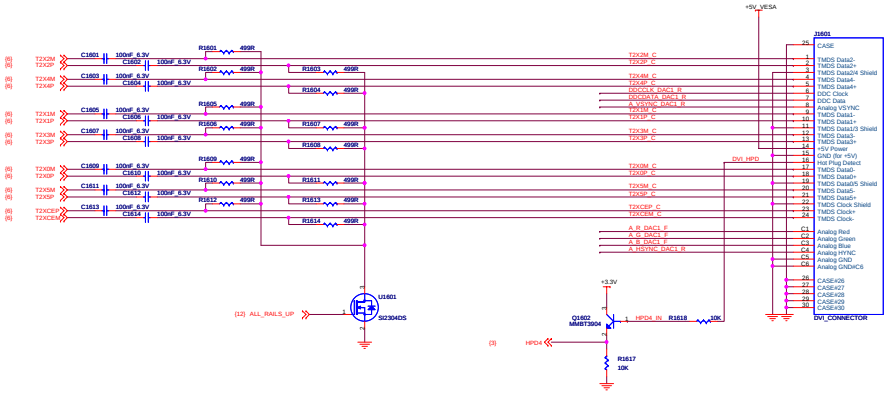
DAC 1 OUTPUT



DPE / DPF OUTPUT



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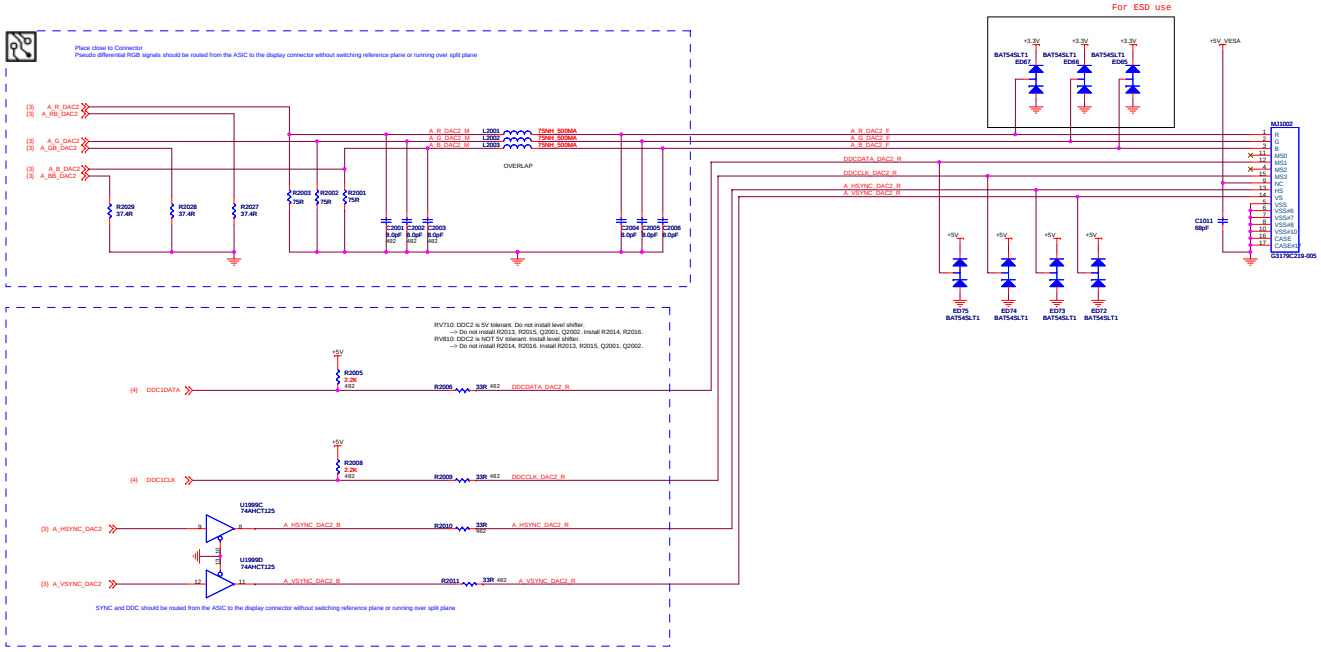
DAC 2 OUTPUT



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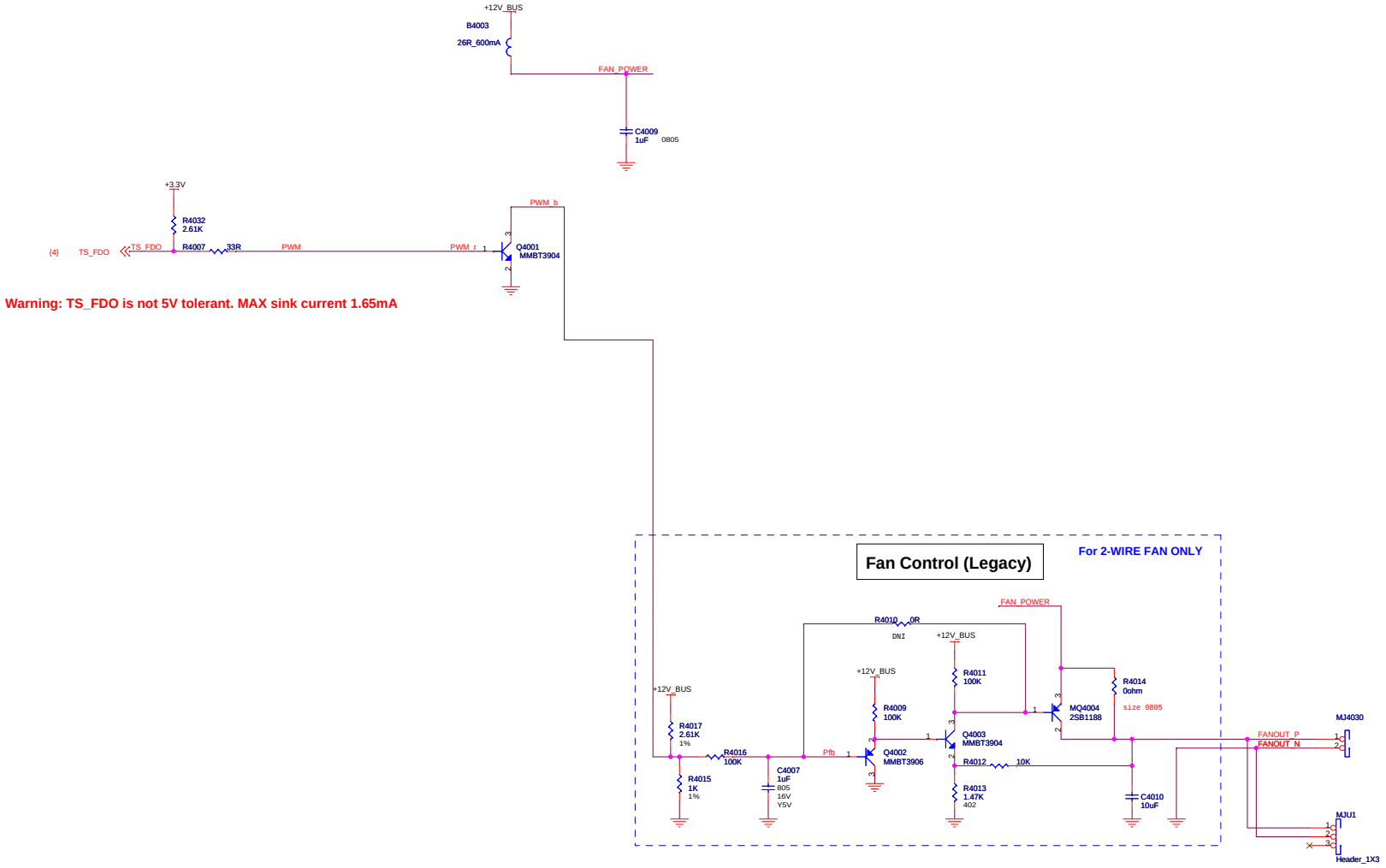
Title RH LP RV710 DDR2 VGA (header) TVO DVI | Doc No. 105-B750XX-00A



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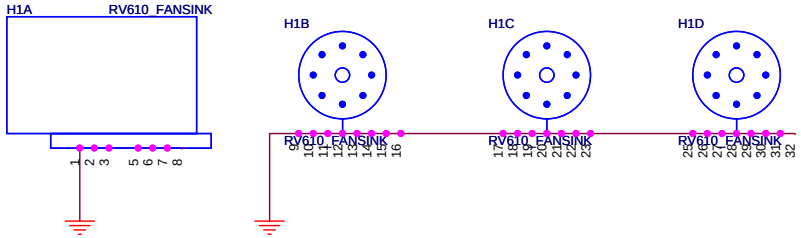
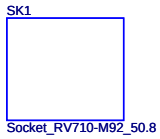
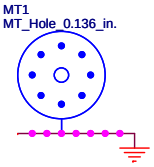
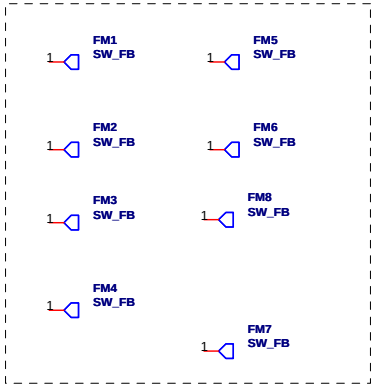
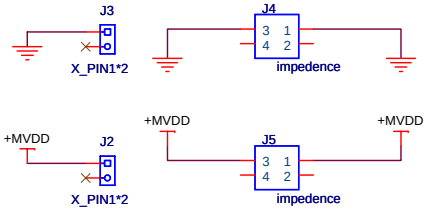
Title RH LP RV710 DDR2 VGA (header) TVO DVI	Doc No. 105-B750XX-00A











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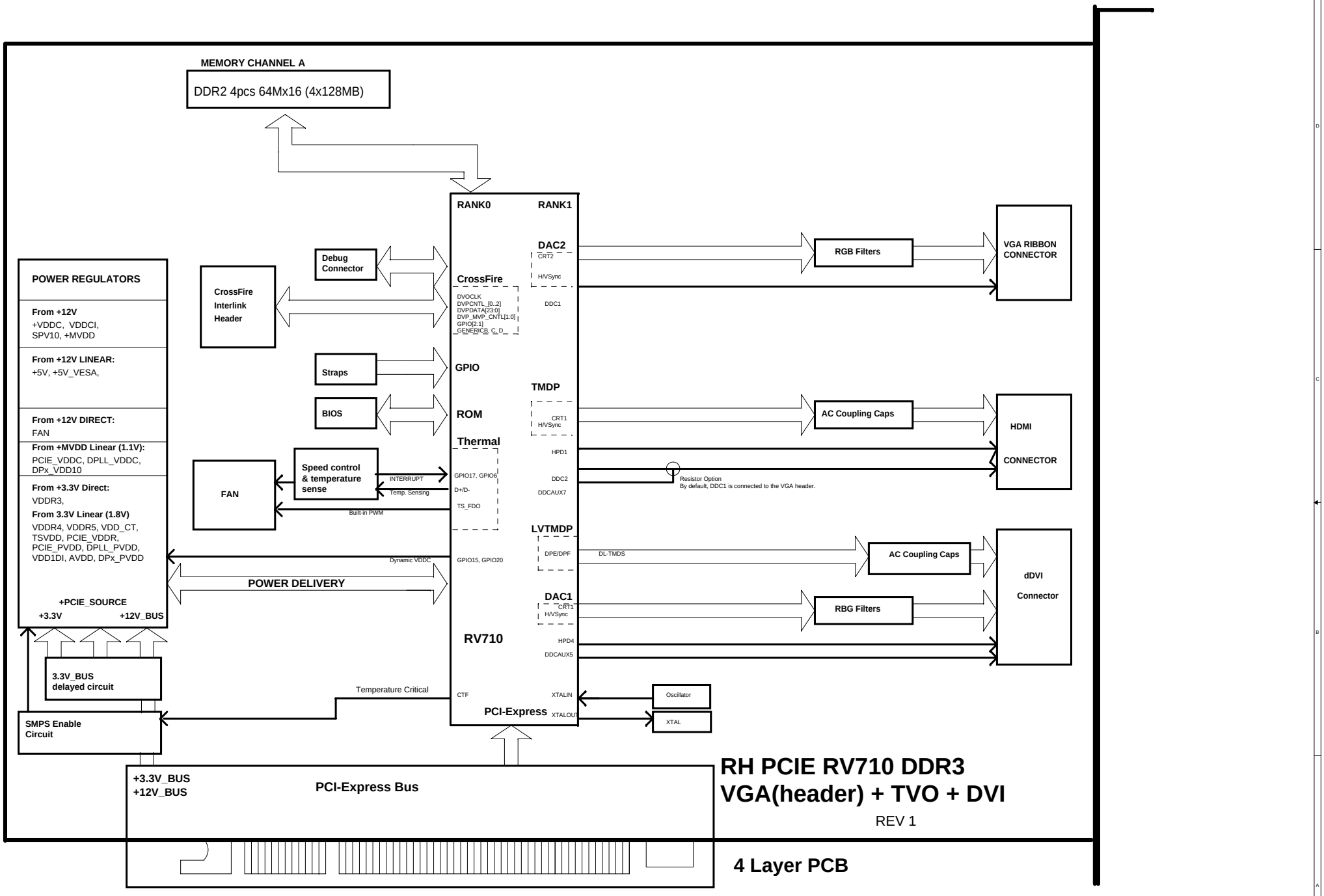

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Title	Schematic No.	Date:
RH LP RV710 DDR2 VGA (header) TVO DVI	105-B750XX-00A	Thursday, December 04, 2008

REVISION HISTORY

NOTE: **This schematic represents the PCB, it does not represent any specific SKU.
For Stuffing options (component values, DNI , ? please consult the product specific BOM.
Please contact AMD representative to obtain latest BOM closest to the application desired.**

Rev 0

RV710 ENGINEERING BOARD

Sch Rev	PCB Rev	Date	REVISION DESCRIPTION
01	00A	2008.04.02	INITIAL RELEASE OF CUSTOM-WIDE BOARD. BASED ON B625 REV06.