

TITLE: Oland G5 2GB VGA/sDVI-I+HDMI/DP

DOCUMENT NUMBER: 105-C869xx-00B

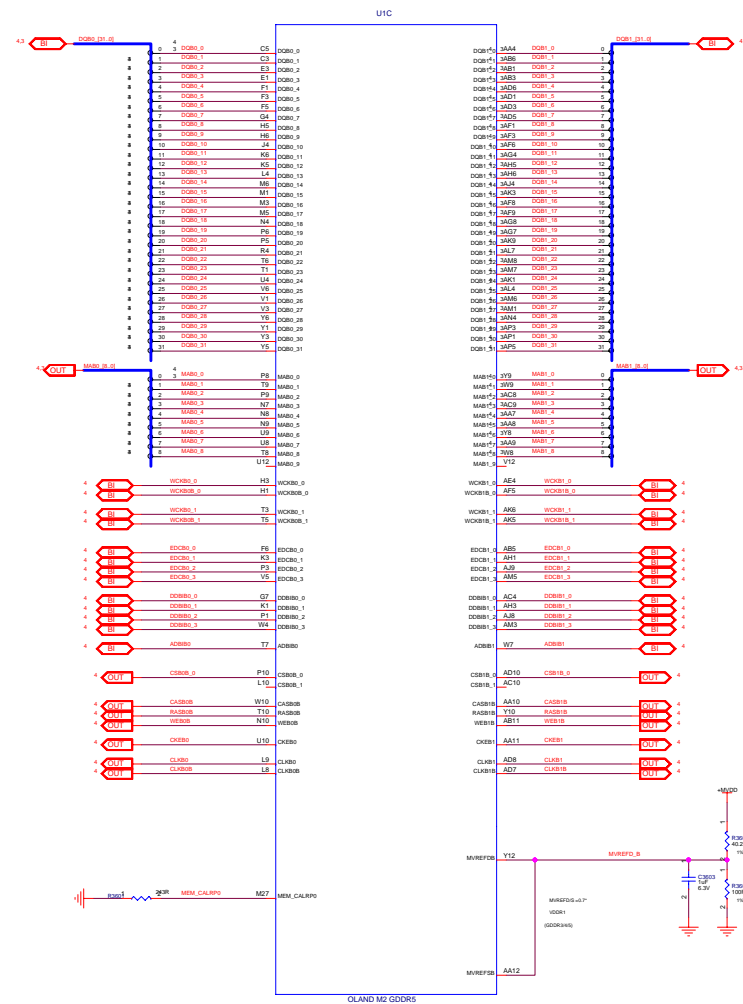
11

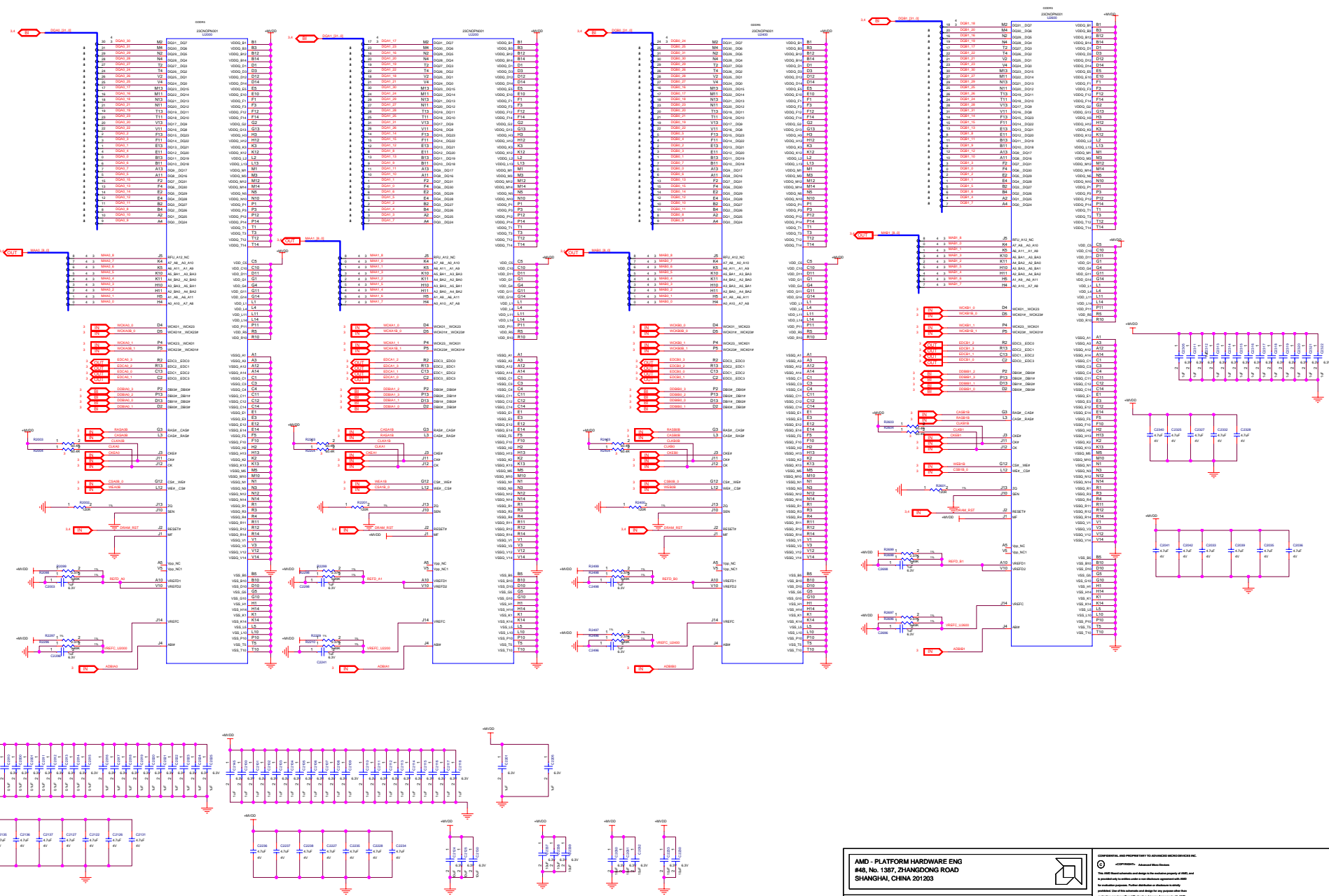
1001

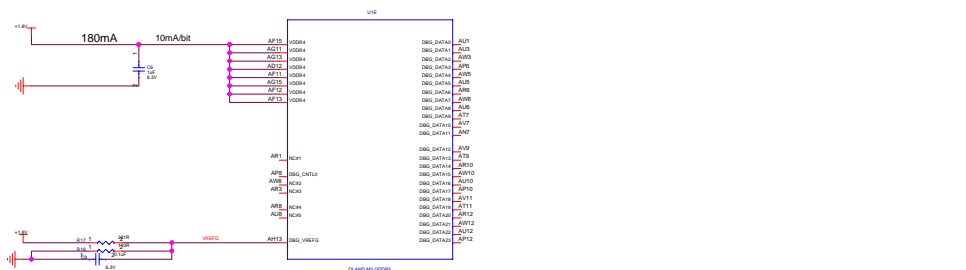
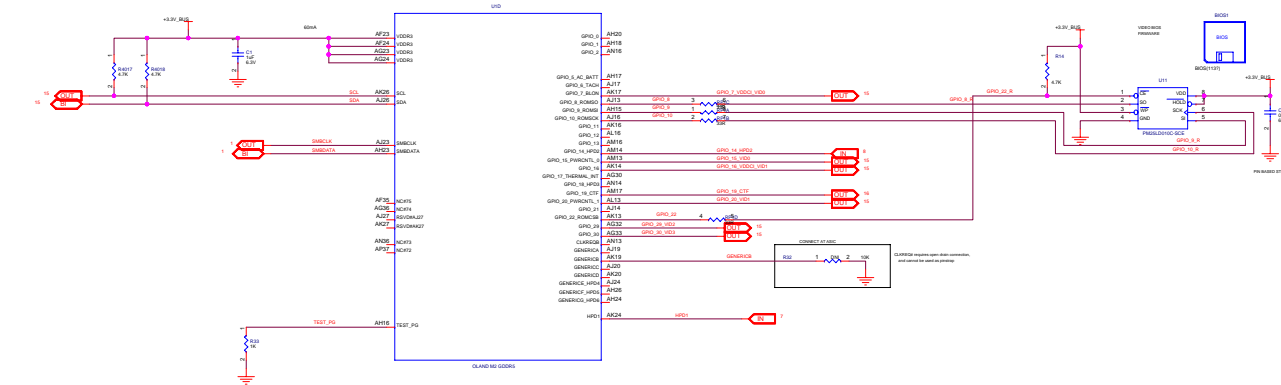


11

8	7	6	5	4	3	2	1
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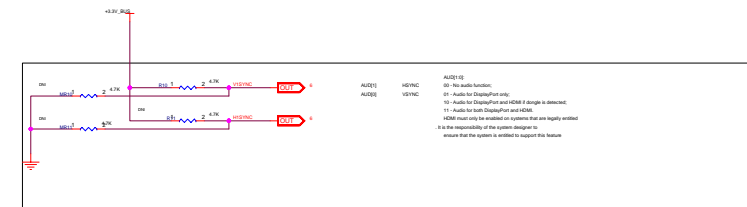
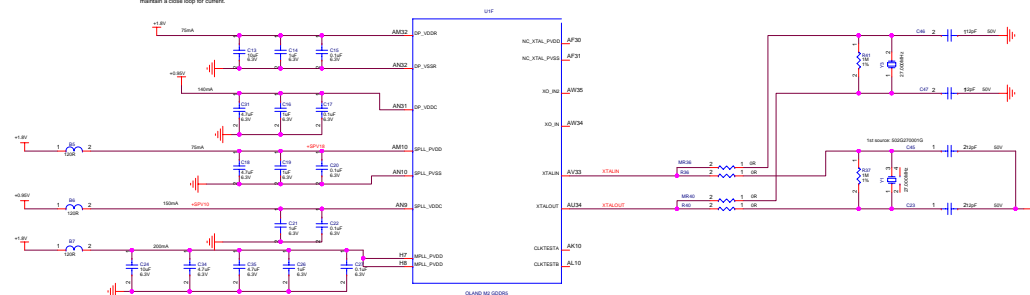






Place the crossfire
testpoints near the ASIC and
not the connector

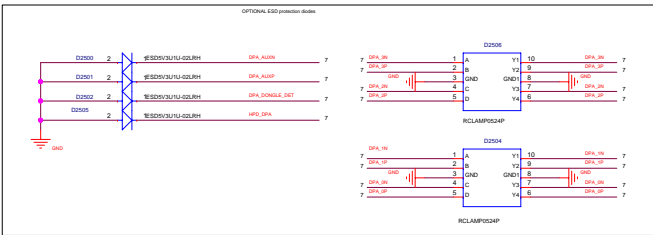
Please pay attention to the grounding strategies for these filter capacitors to maintain a close loop for current.



AUD[1]	HSYNC	AUD[1:0]	00 - No audio function;
AUD[0]	VSYNC		01 - Audio for DisplayPort only;
			10 - Audio for DisplayPort and HDMI if dangle is detected;
			11 - Audio for both DisplayPort and HDMI.

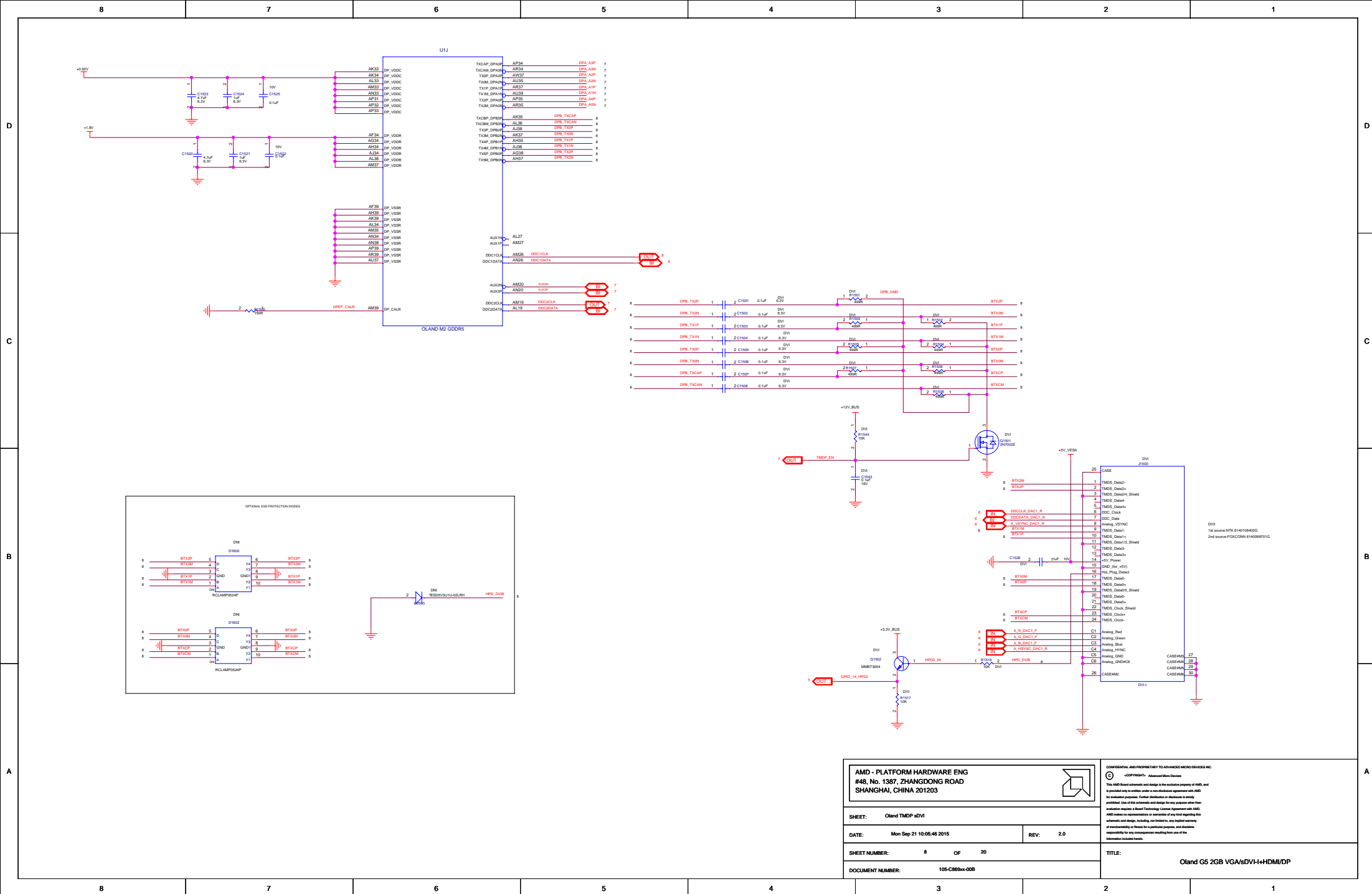
HDMI must only be enabled on systems that are legally entitled to use it.

It is the responsibility of the system designer to ensure that the system is entitled to support this feature.

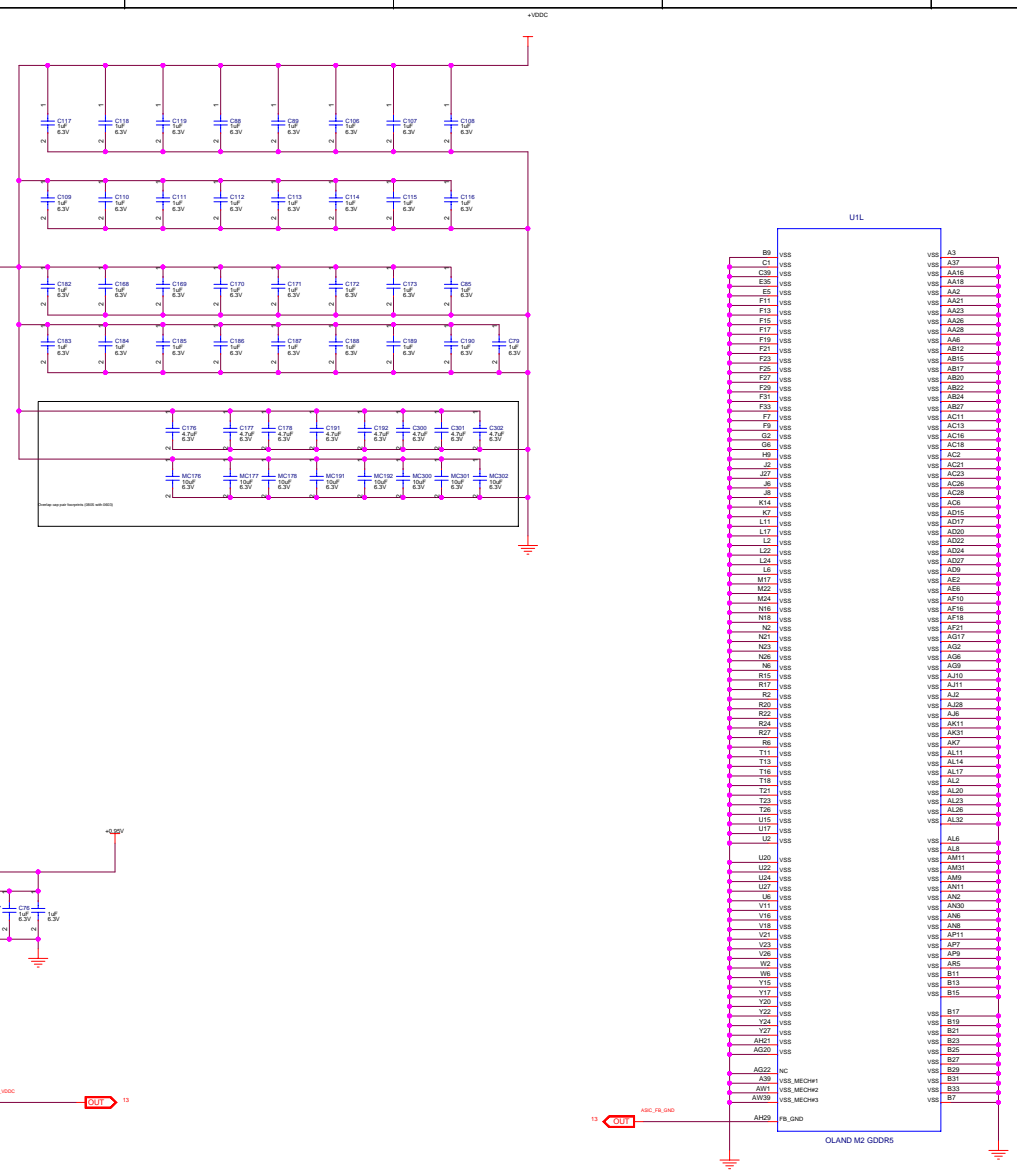
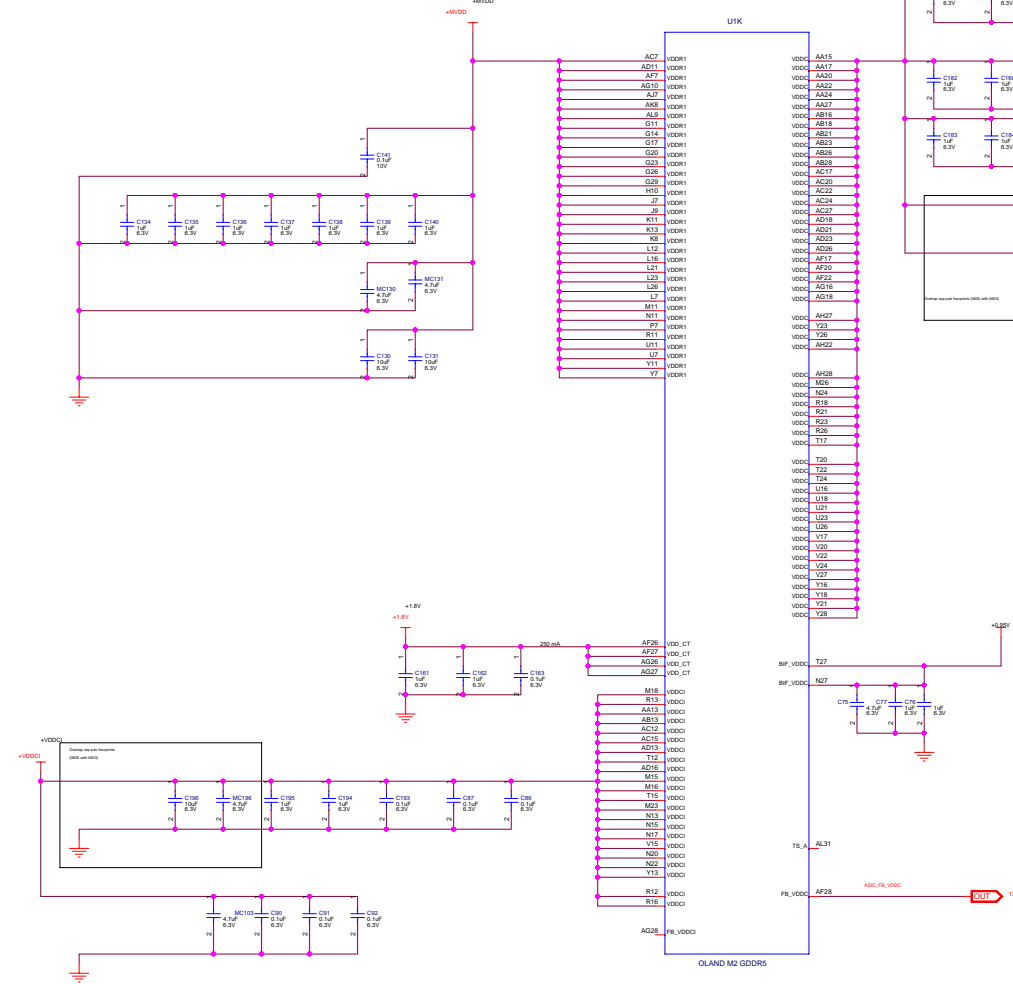


TITLE: Oland G5 2GB VGA/sDVI-I+HDMI/DP

SHEET: Oland Connector HDMI/DP	
DATE: Mon Sep 21 10:05:38 2015	REV: 2.0
SHEET NUMBER: 7 OF 20	
DOCUMENT NUMBER: 105-C869xx-008	



OLAND Power & GND



AMD - PLATFORM HARDWARE ENG
#48, No. 1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203



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SHEET: Oland Power&GND

DATE: Mon Sep 21 10:05:46 2015

REV: 2.0

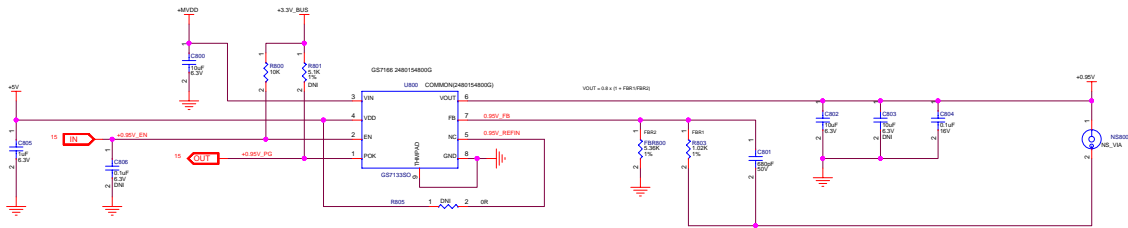
SHEET NUMBER: 9 OF 20

TITLE: Oland G5 2GB VGA/sDVI-I+HDMI/DP

DOCUMENT NUMBER: 105-C869xx-00B

Linear Regulators

LDO #1: $V_{in} = +1.5V \pm 2\%$ $V_{out} = +0.95V \pm 2\%$ $I_{out} = 1.44A (T_{BV}) RMS MAX$
PCB: 50 to 70mm sq. copper area for cooling



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SHEET: 0.95V

DATE: Mon Sep 21 10:06:44 2015

REV: 2.0

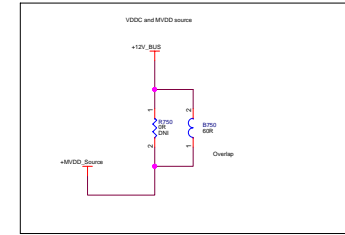
SHEET NUMBER: 10 OF 20


DOCUMENT NUMBER: 105-C869rev-008

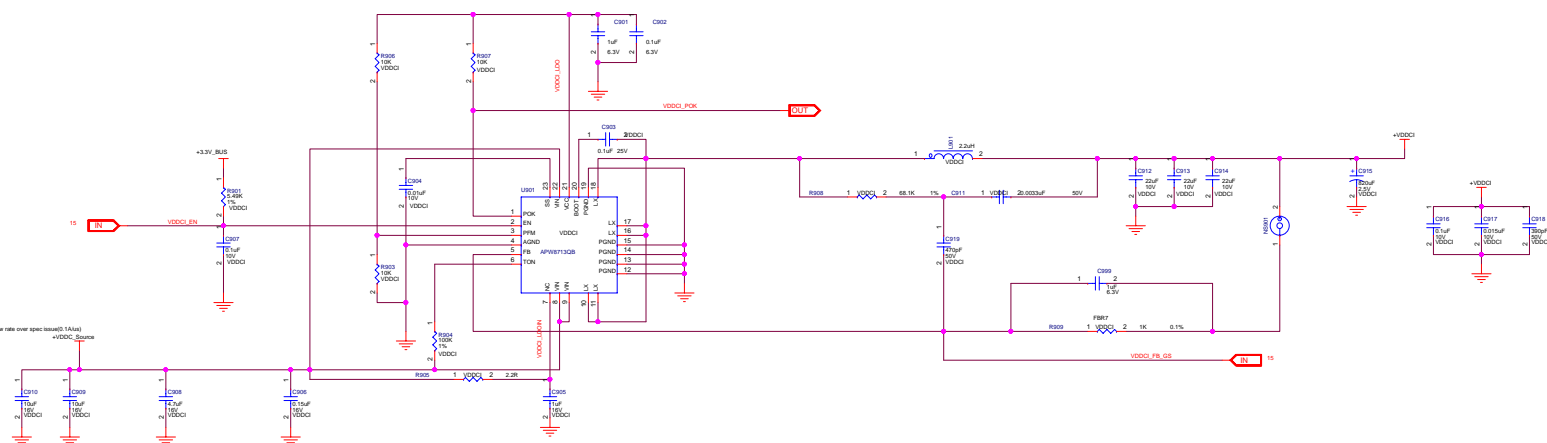
TITLE: Oland G5 2GB VGA/sDV-H+HDMI/DP

Regulator for MVDD

Use +VDDC_Source to share L601 to alternate PCBs 12V current share rate over spec (max0.14ms)
+MVDD_Source



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SHEET: MVDD		REV: 2.0		TITLE: Oland G5 2GB VGA/sDV/H+HDMI/DP	
DATE: Tue Sep 22 06:32:59 2015					
SHEET NUMBER: 11 OF 20					
DOCUMENT NUMBER: 105-C869xx-008					



Tue Sep 22 06:32:59 2015 REV: 2.0

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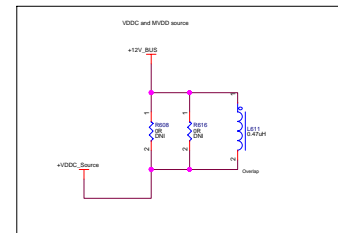
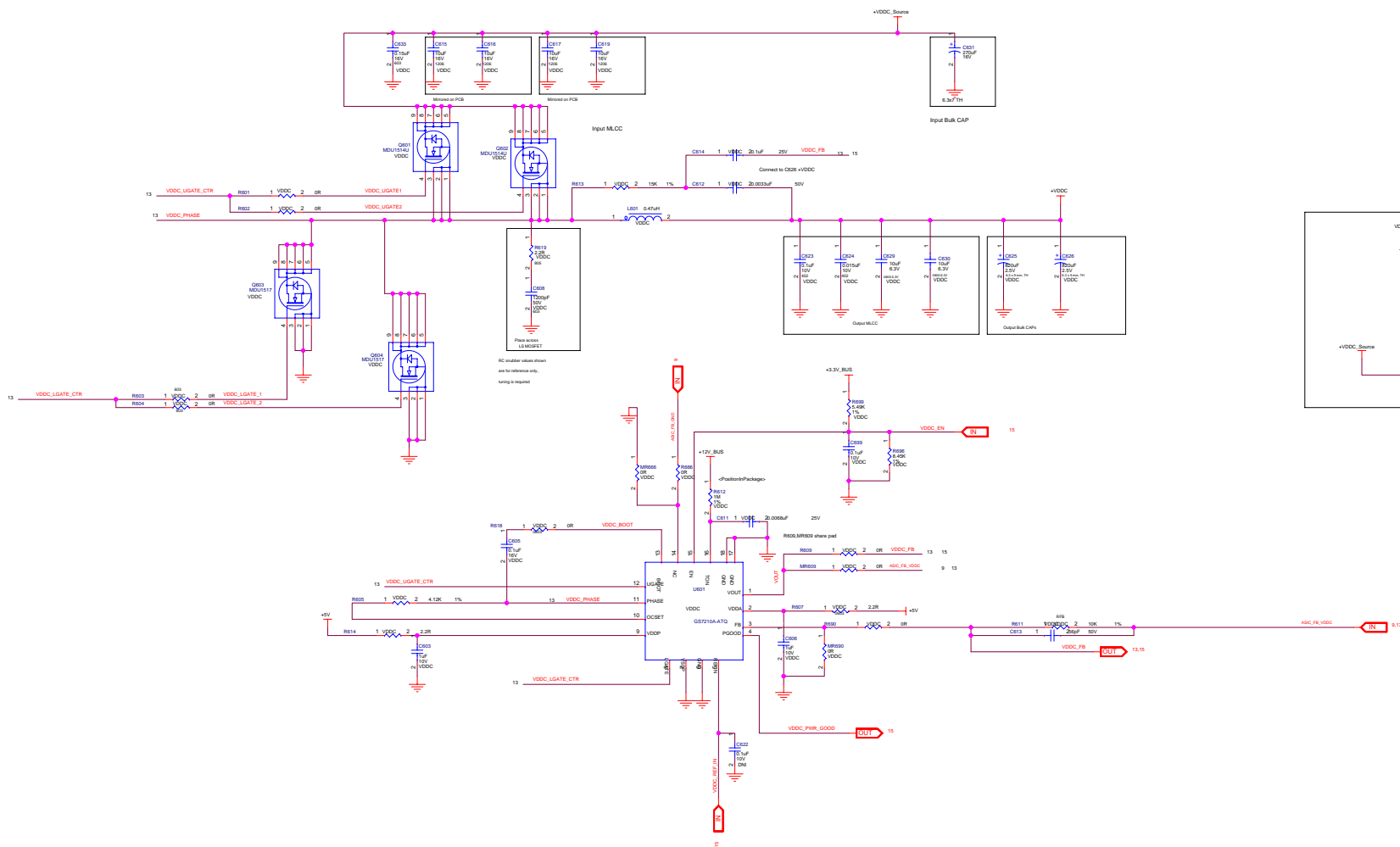
105-C869xx-00B


NOTE

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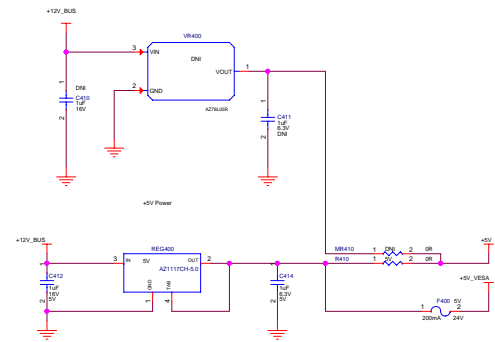
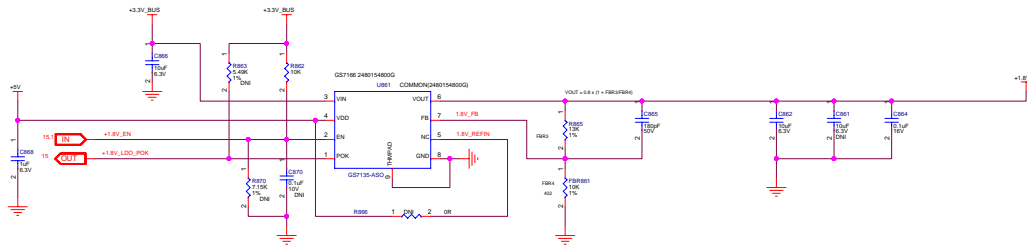
Oland G5 2GB VGA/sDVI-I+HDMI/DP



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SHEET: VDDC					
DATE: Mon Sep 21 10:06:41 2015		REV: 2.0			
SHEET NUMBER: 13 OF 20				TITLE: Oland G5 2GB VGA/sDVI+HDMI/DP	
DOCUMENT NUMBER: 105-C869a-008					

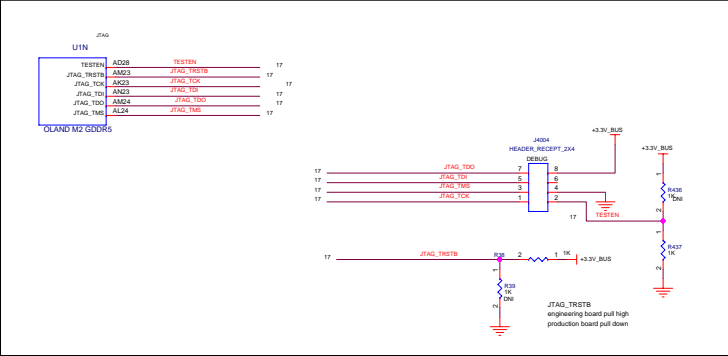
Linear Regulators


LDO K2:	Vin = 3.00V to 3.60V (3.3V +/- 9%)	Vout = +1.8V +/- 2%;	Iout = 1.5A (TBD) RMS MAX
PCB: 50 to 70mm sq. copper area for cooling			



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SHEET: Linear Regulators	
DATE: Mon Sep 21 10:06:44 2015	REV: 2.0
SHEET NUMBER: 14 OF 20 20	
DOCUMENT NUMBER: 105-CM500-008	

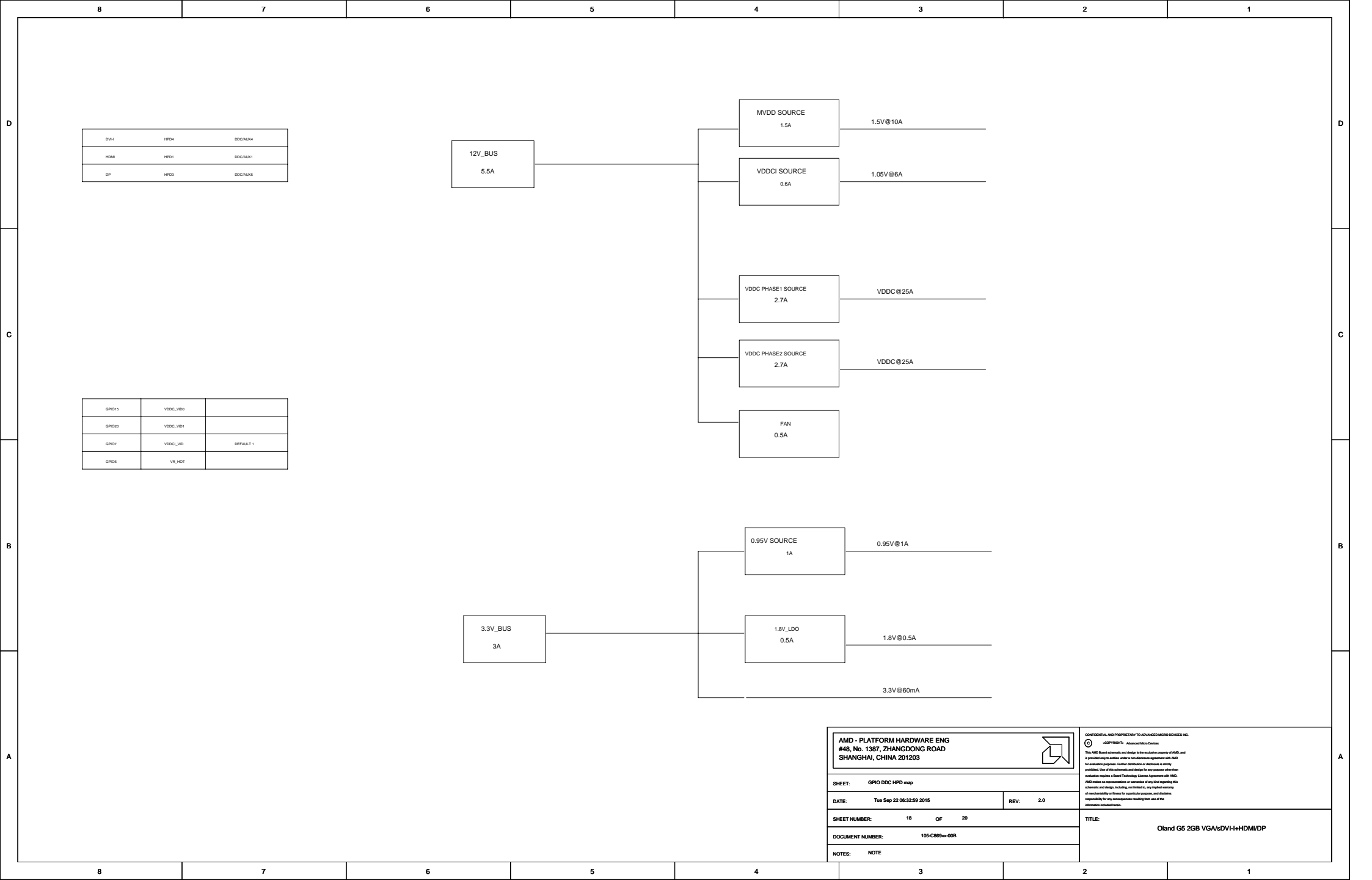
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TITLE: Oland G5 2GB VGA/sDVI+HDMI/DP	

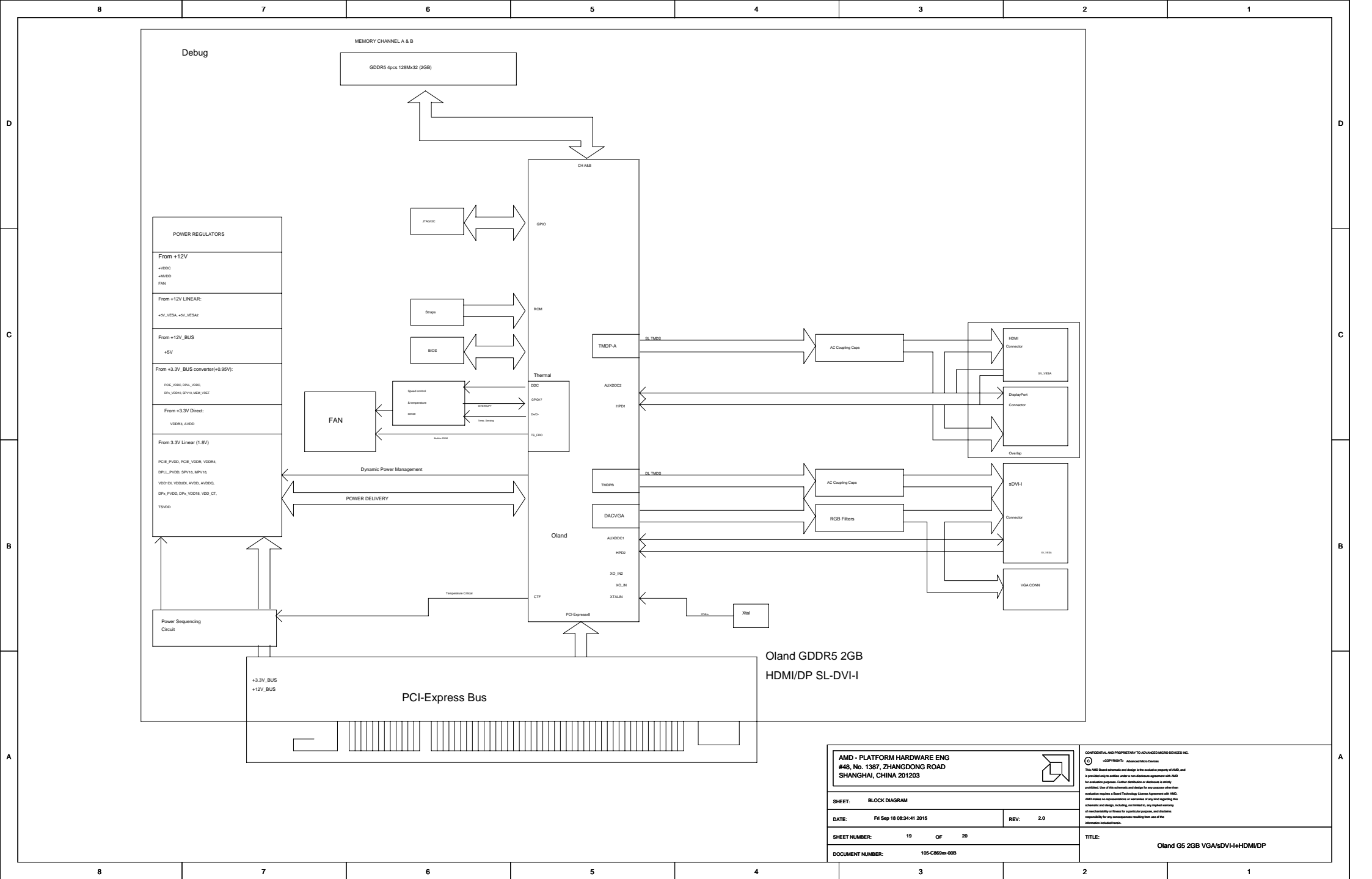


AMD - PLATFORM HARDWARE ENG #4B, No. 1367, ZHANGDONG ROAD SHANGHAI, CHINA 201203		
SHEET: Debug Circuit		
DATE: Mon Sep 21 10:06:47 2015	REV: 2.0	
SHEET NUMBER: 17 OF 20		
DOCUMENT NUMBER: 105-C869xx-008		

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TITLE: Oland G5 2GB VGA/sDV/H+HDMI/DP





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AMD



TITLE:

Oland G5 2GB VGA/sDVI-++HDMI/DP

DOCUMENT NUMBER:

105-C869xx-008

DATE:

Tue Sep 22 02:20:17 2015

SHEET NUMBER:

20 OF 20

REV:

2.0

REVISION HISTORY

ENGINEER:

Tony Yang

NOTES:

NOTE

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SCH Rev

PCB Rev

Date

REVISION DESCRIPTION

1.00

000

04/19/14

1 Initial release

2.00

000

09/22/15

2 Add VGA, etc

8

7

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D

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