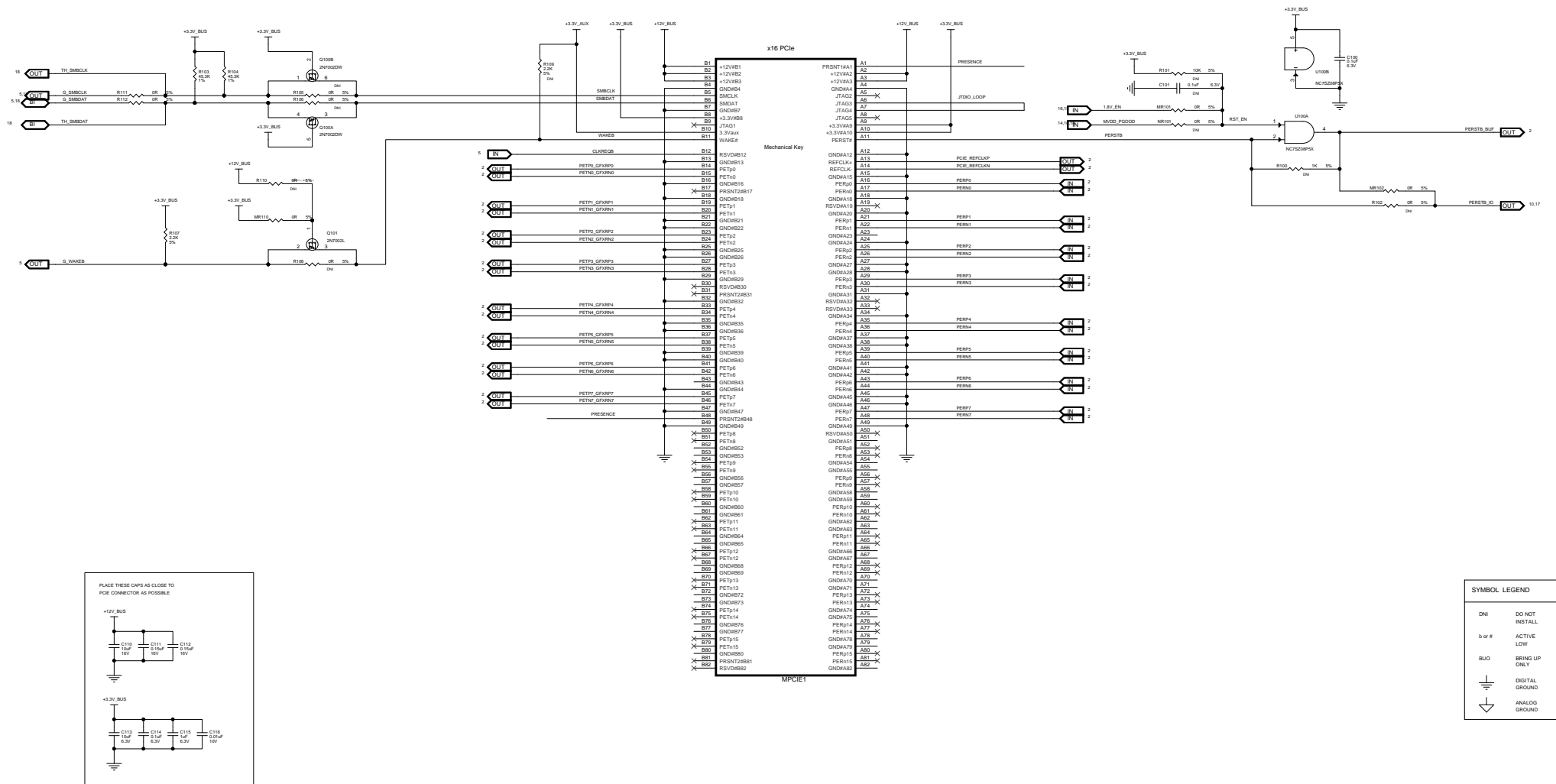
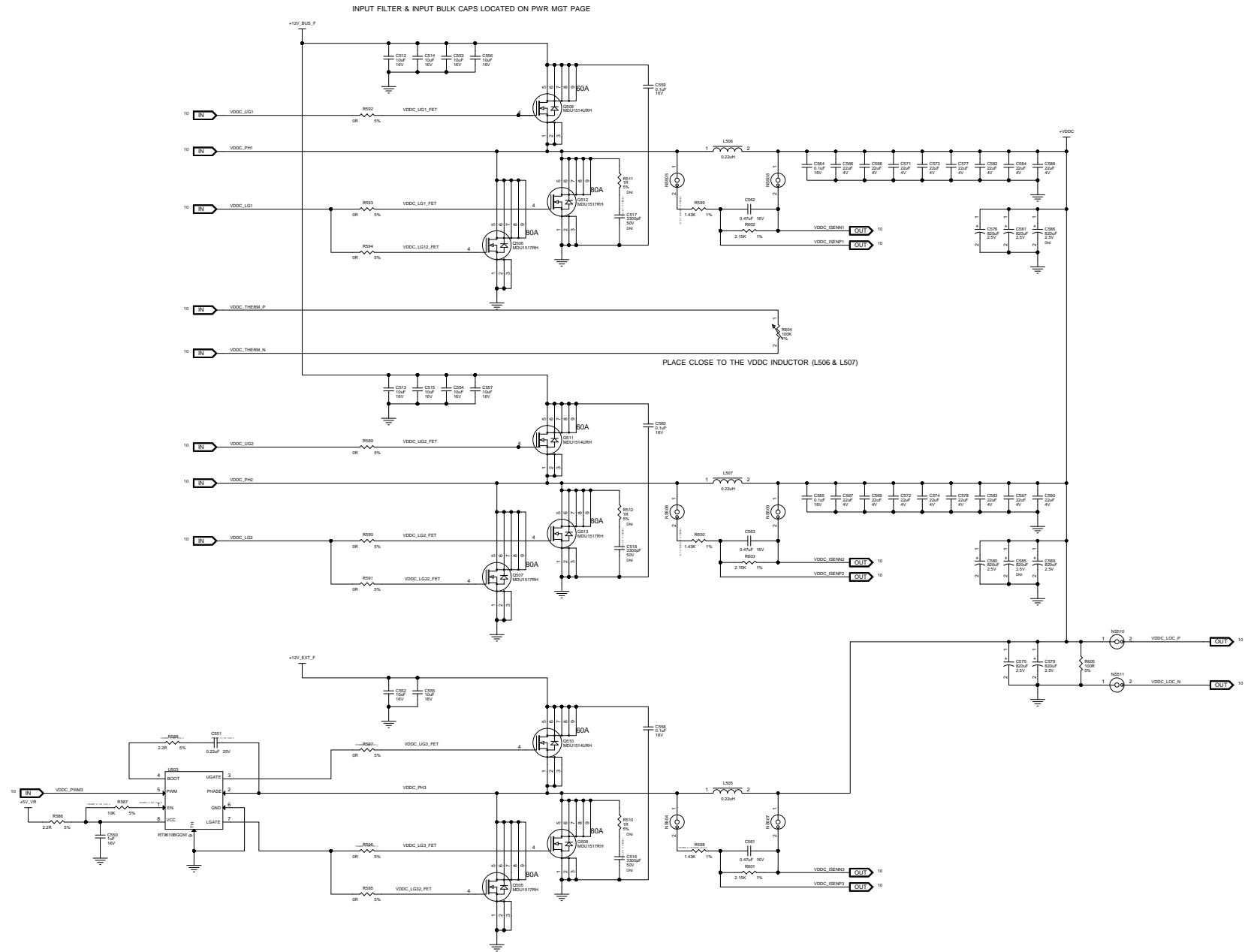


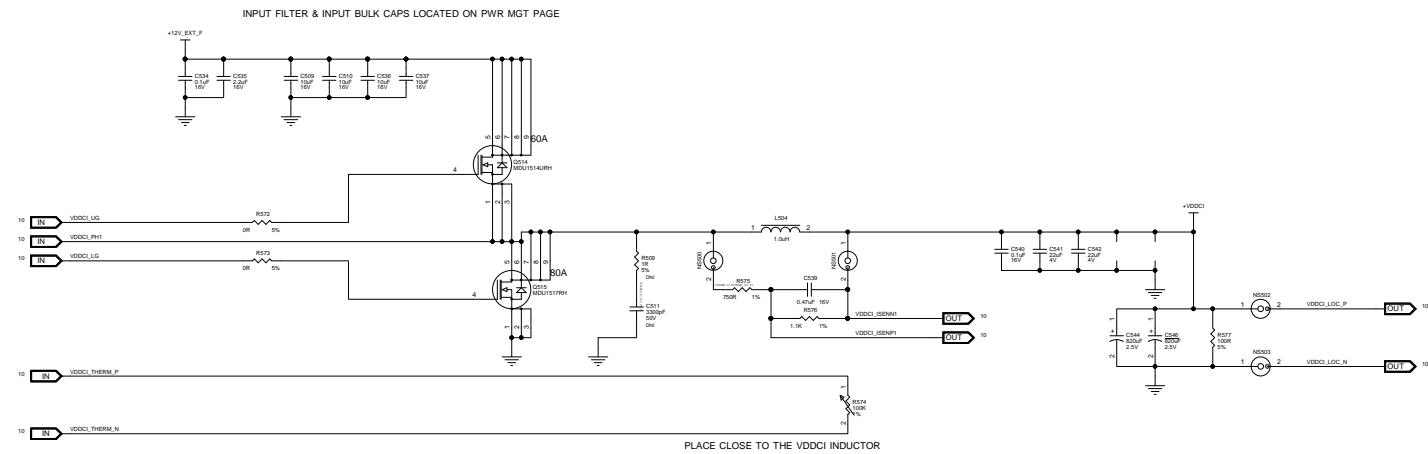
(1) PCIE EDGE CONNECTOR



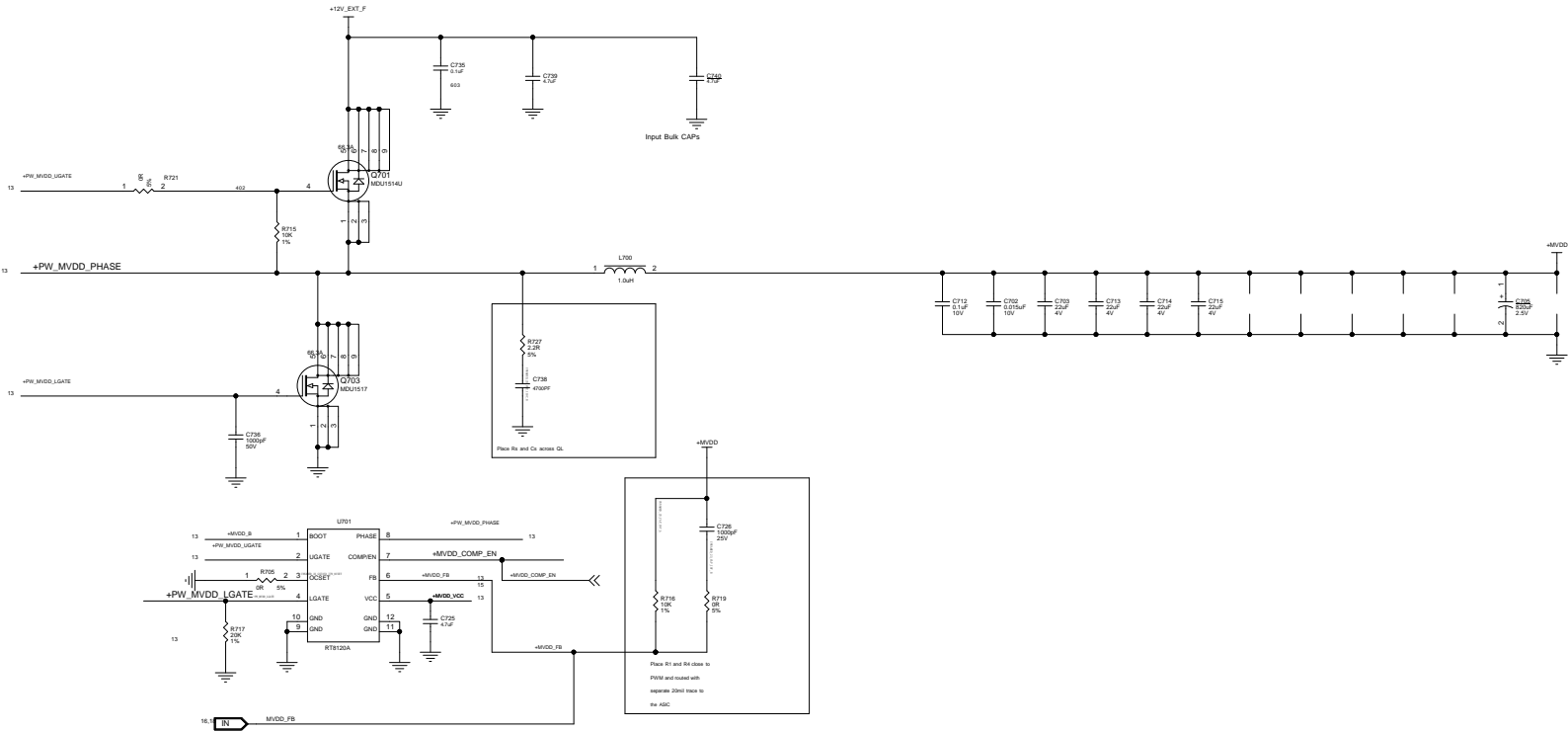
(11) VDDC



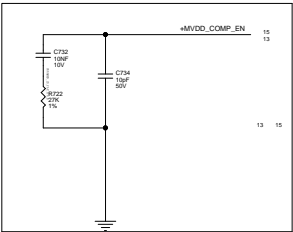
(12) VDDCI



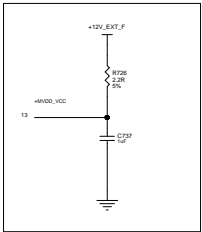
(14) MVDD



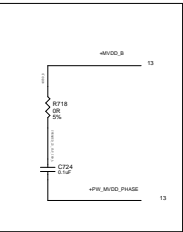
COMPENSATION CIRCUIT



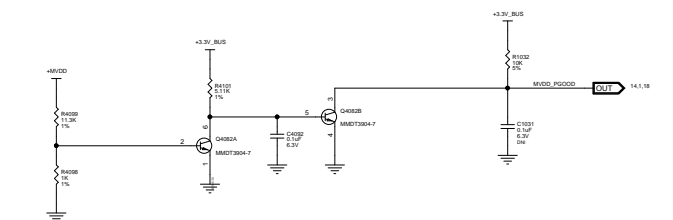
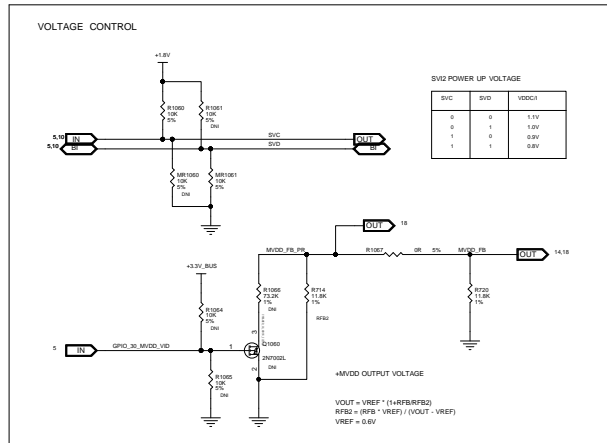
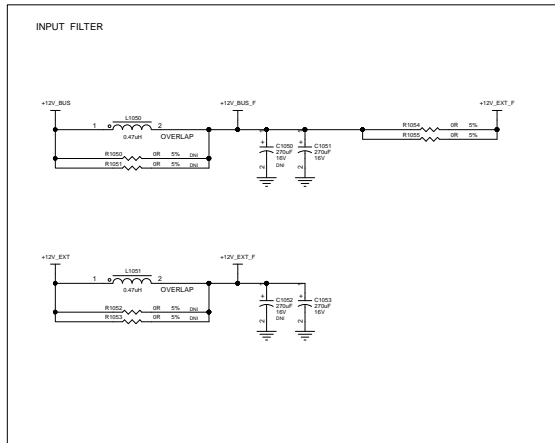
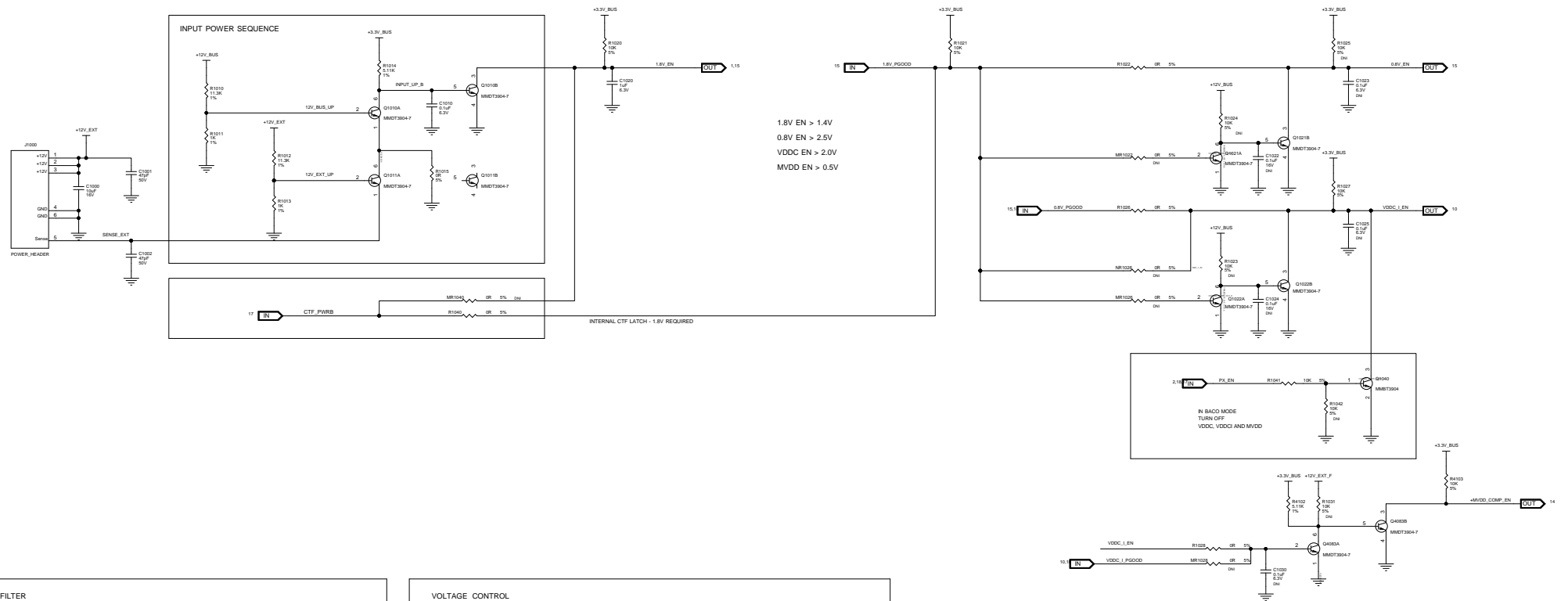
FILTERED Smps VCC



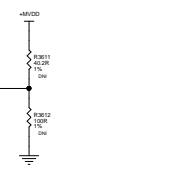
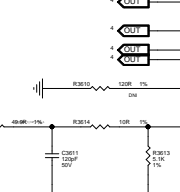
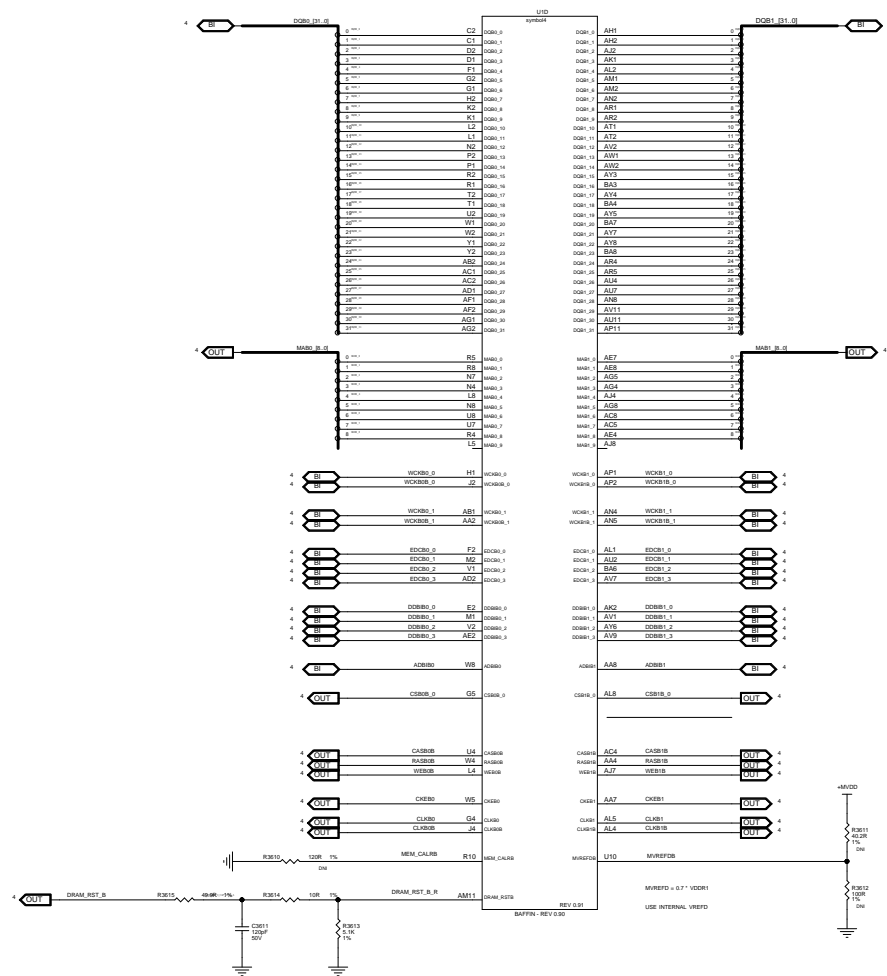
BOOT CIRCUIT



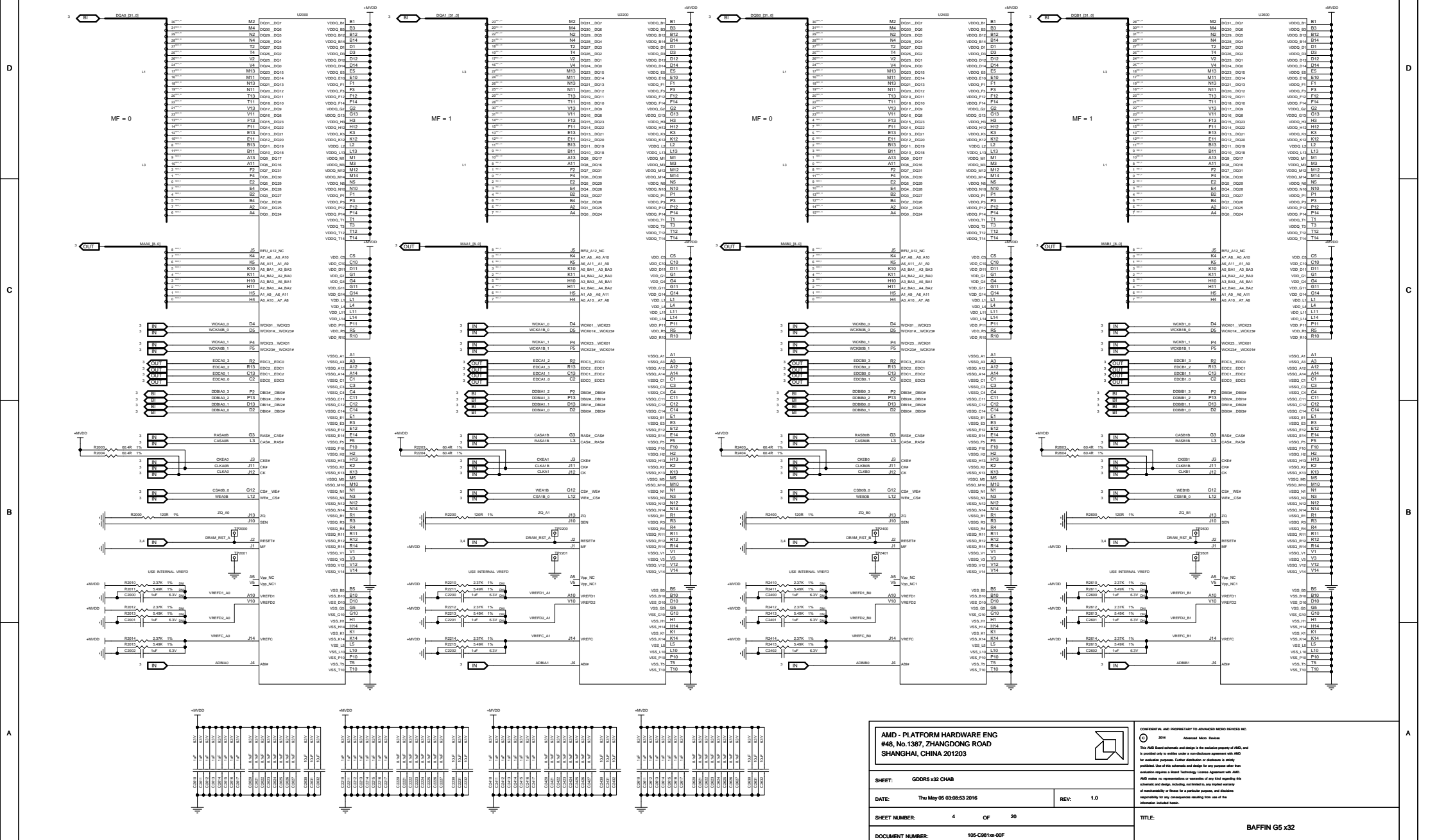
(16) POWER MANAGEMENT



BAFFIN G5 x32



(4) GDDR5 x32 CHAB



AMD - PLATFORM HARDWARE ENGINEERING
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: GDDR5 x32 CHAB

DATE: Thu May 06 03:08:53 2016

SHEET NUMBER: 4 OF

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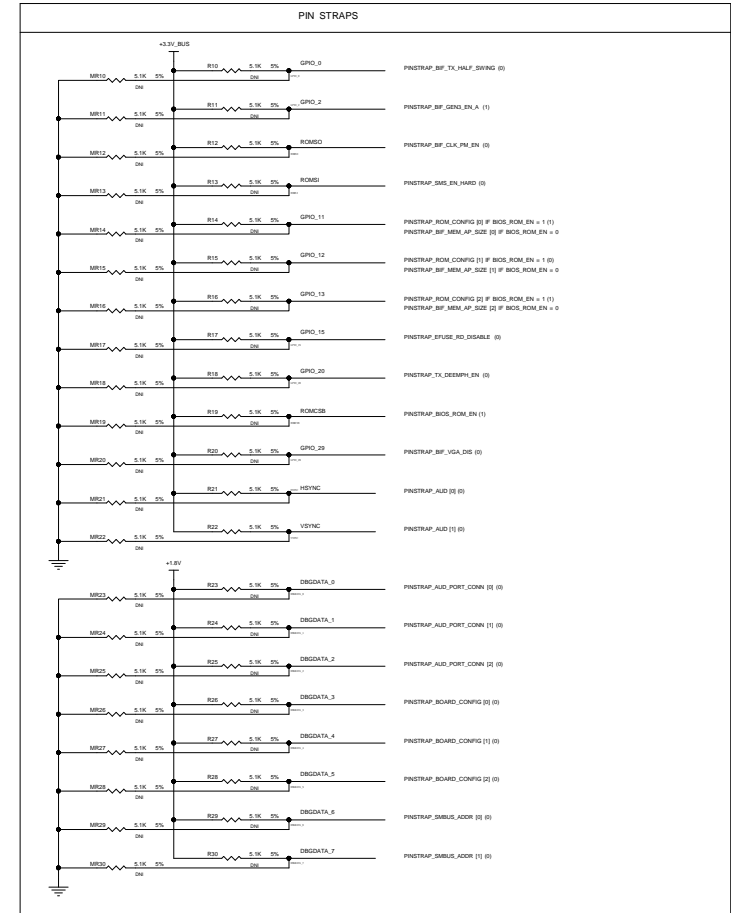
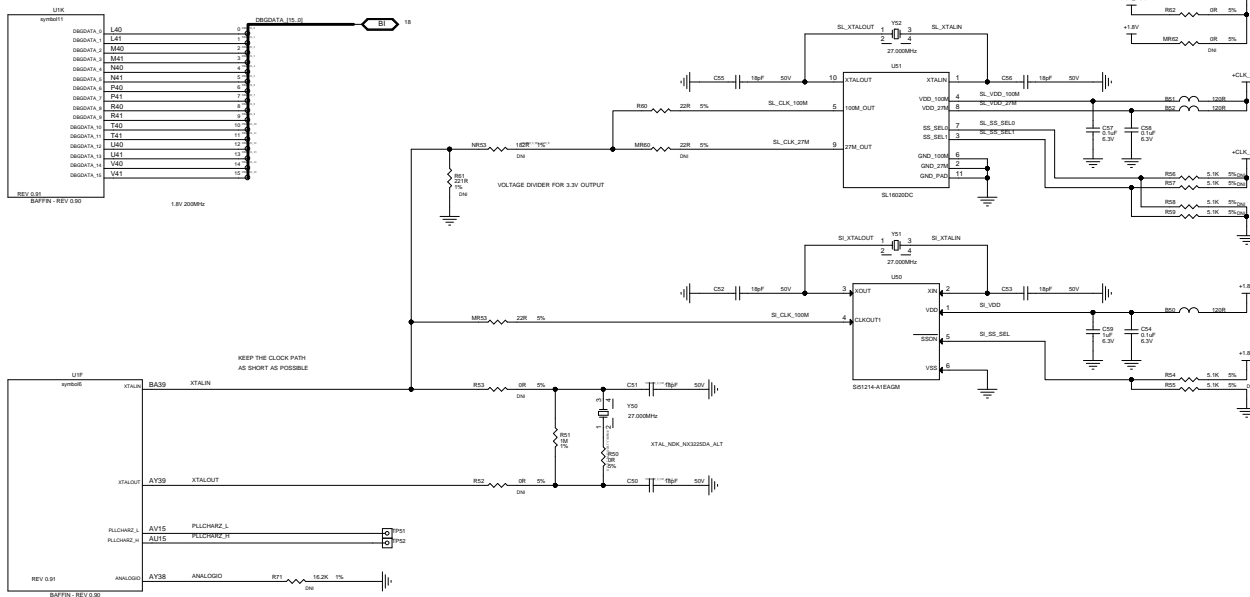
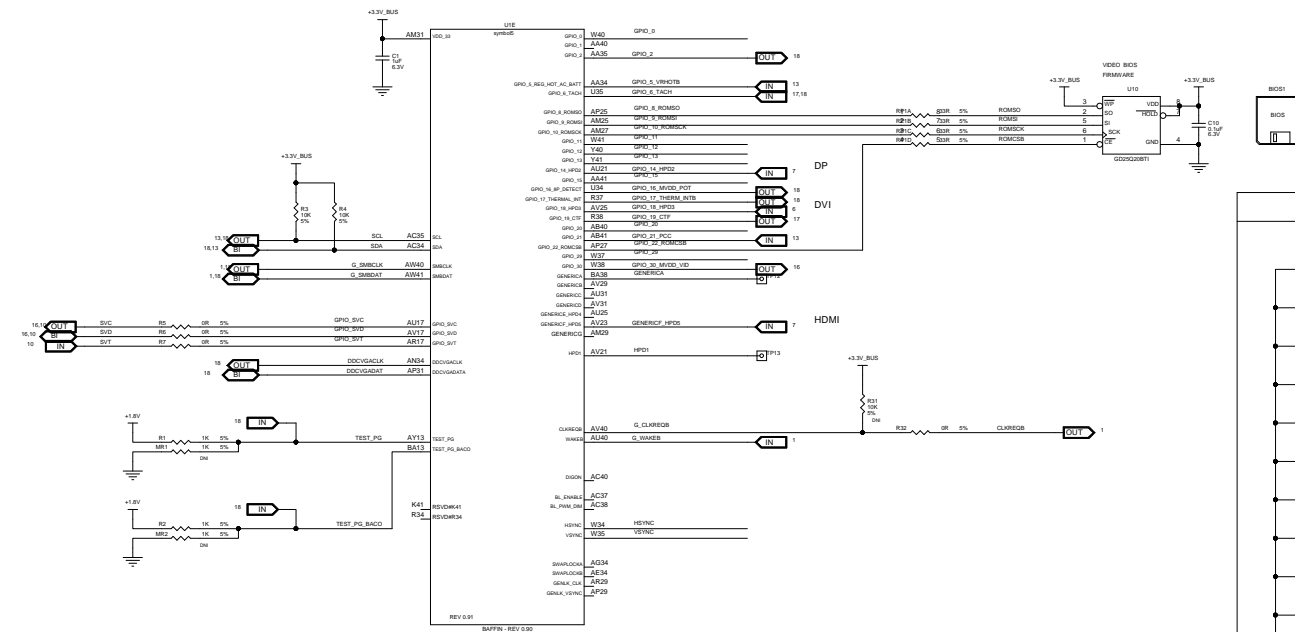
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TITLE: **BAFFIN G5 x32**

(5) BAFFIN GPIO STRAPS CLK

DEVICE	ADDRESS
AD5274	0x5C
RT9403	0x52
VTMM II	

DEVICE	ADDRESS
LM9003	0x28



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SHEET: BAFFIN GPIO STRAPS CLK

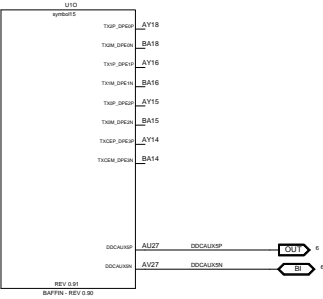
DATE: Thu May 05 03:08:53 2016


REV: 1.0

SHEET NUMBER: 5 OF 20

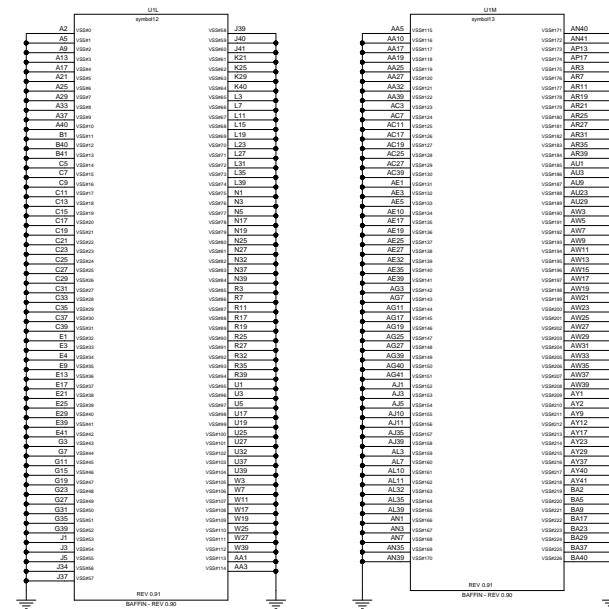
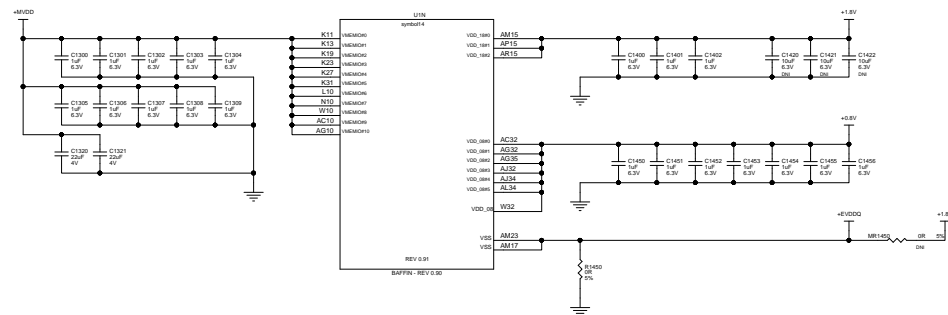
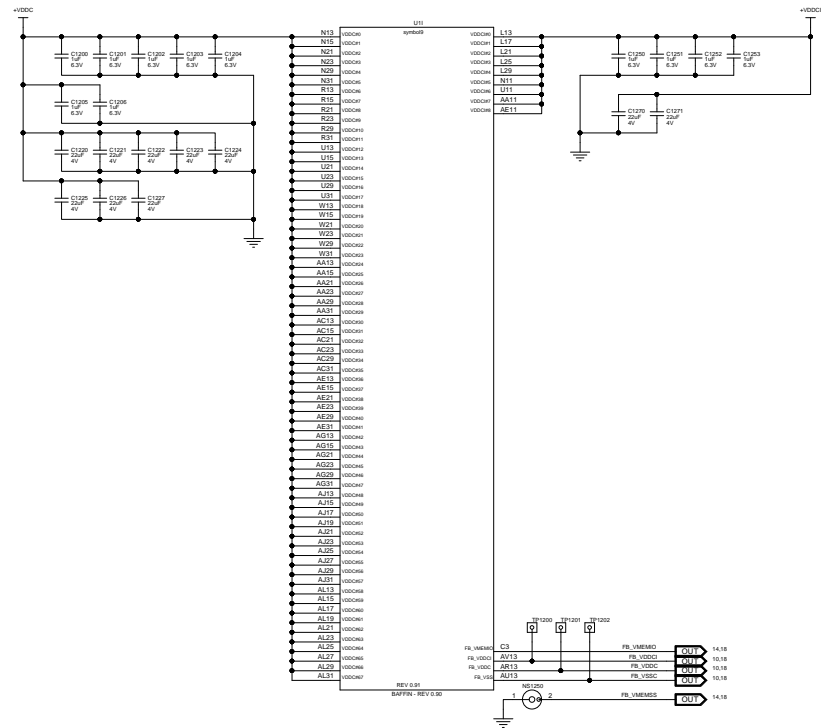
TITLE: **BAFFIN G5 x32**

(8) BAFFIN TMDPE



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SHEET: BAFFIN TMDPE - NC		REV: 1.0	
DATE: Wed Apr 13 08:02:51 2016			
SHEET NUMBER: 8 OF 20		TITLE: BAFFIN G5 x32	
DOCUMENT NUMBER: 105-C081xx-00F			

(9) BAFFIN POWER GND



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SHANGHAI, CHINA 201203



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SHEET: BAFFIN POWER GND

DATE: Wed Apr 13 08:08:28 2016

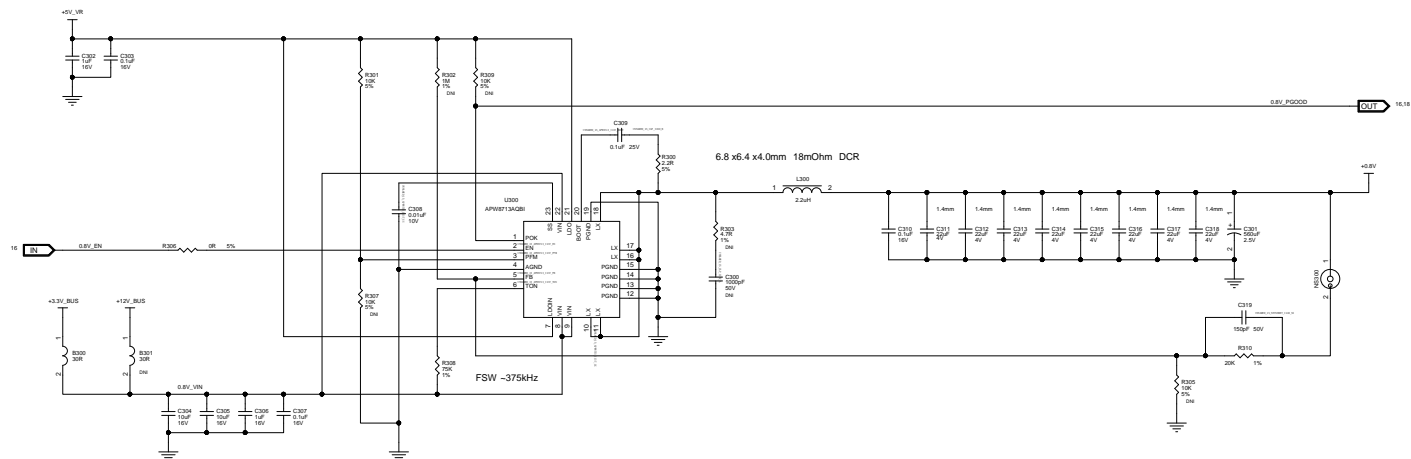
REV: 1.0

SHEET NUMBER:	9	OF	20
DOCUMENT NUMBER:	105-C981xx-00F		

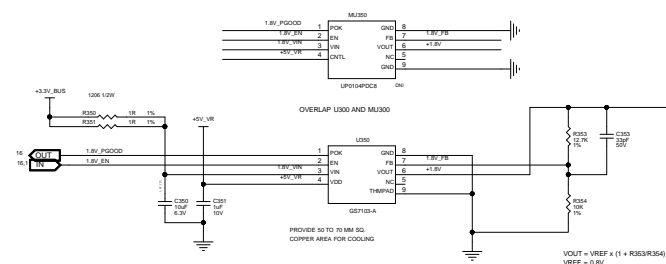
TITLE: **BAFFIN G5 x32**

(15) REG - 0V8 1V8 5V0

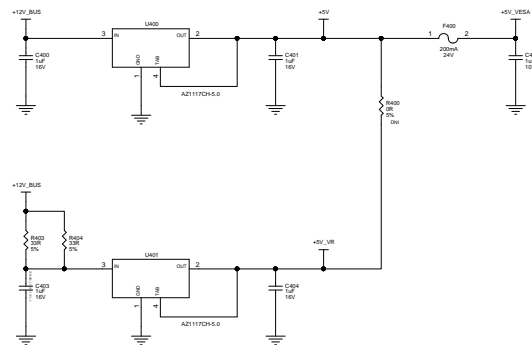
0.8V REGULATOR



1.8V REGULATOR



5V REGULATOR

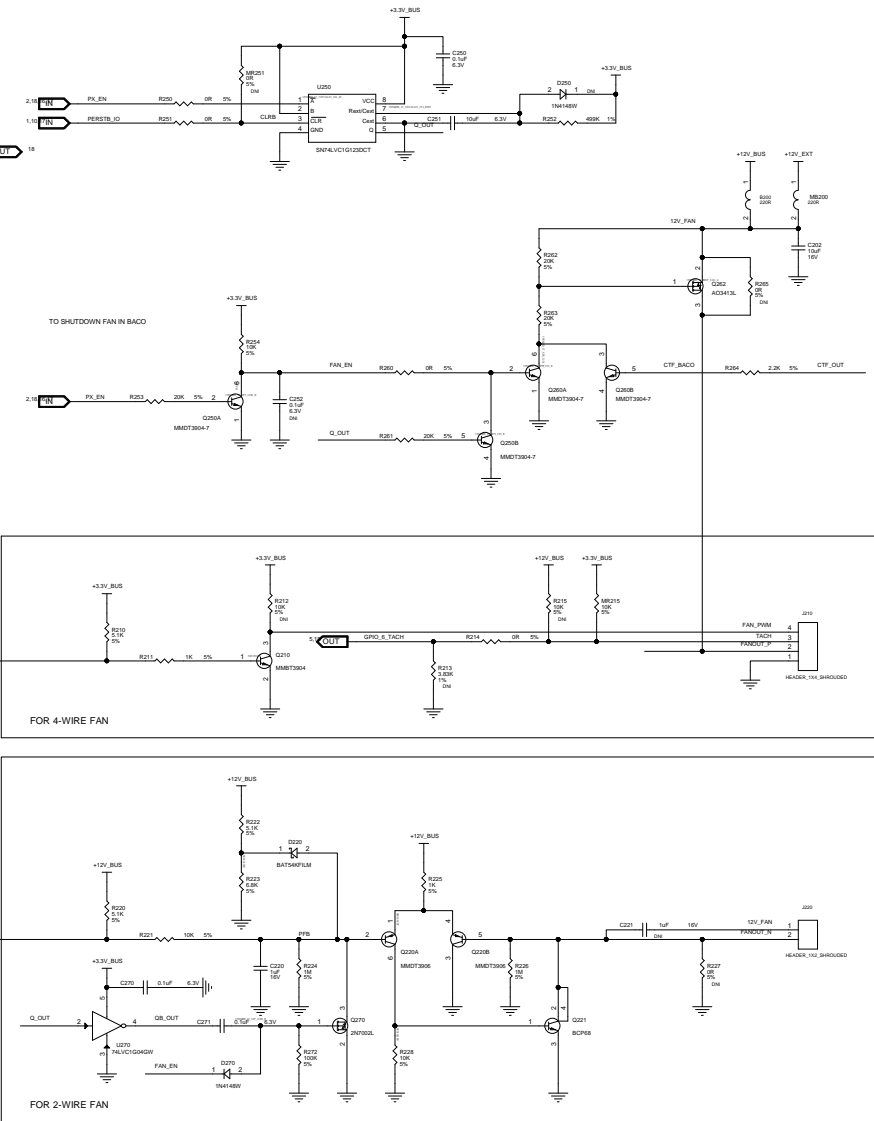
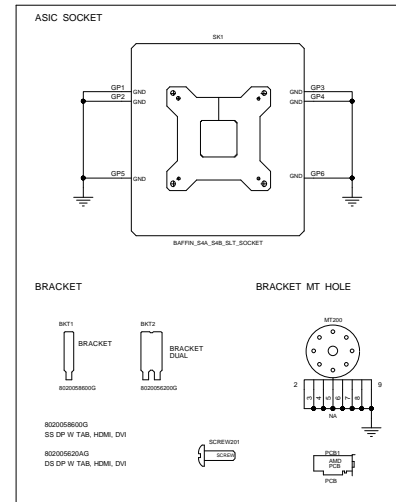
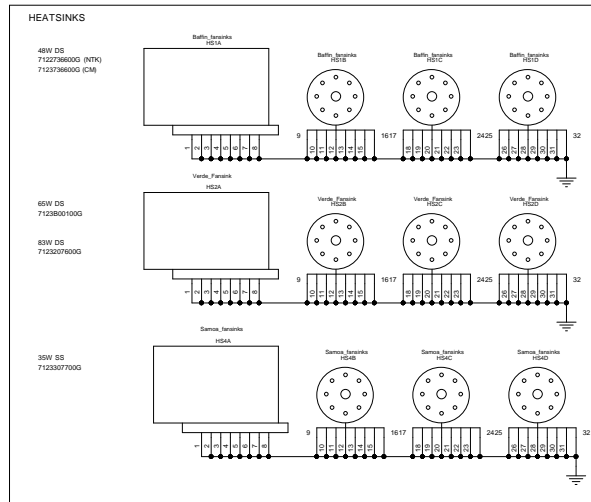
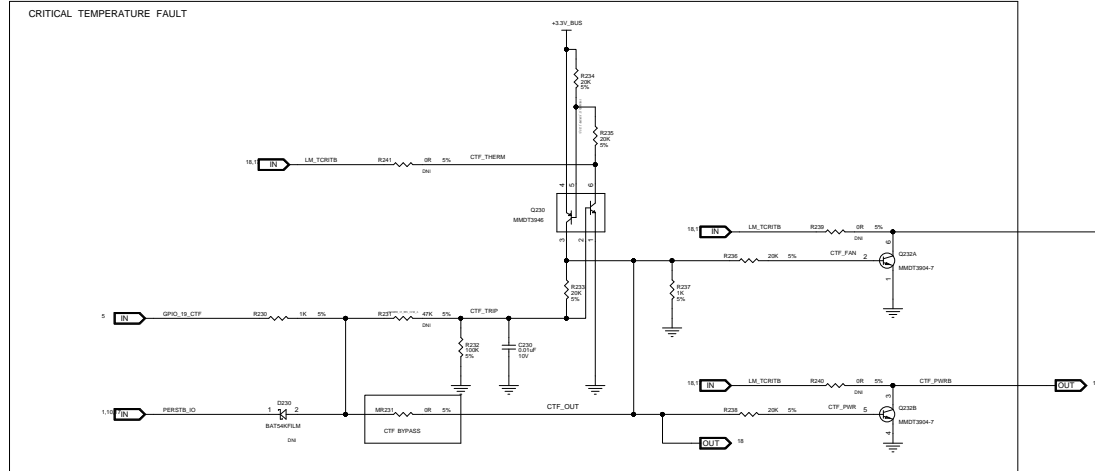
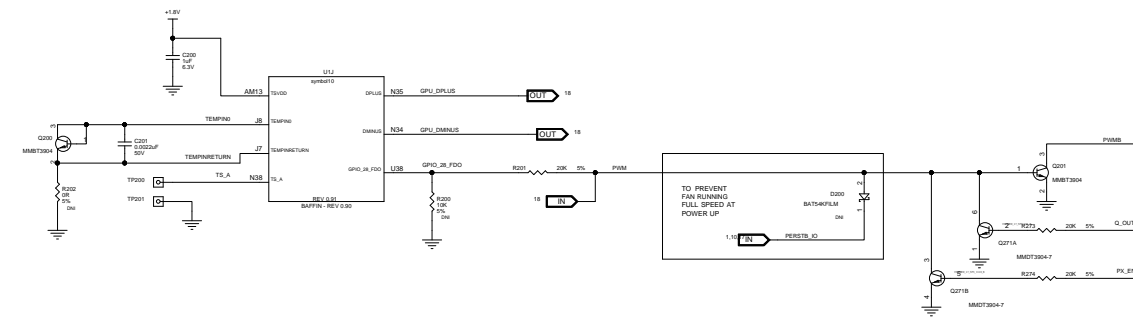



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SHEET: REG - 0V8 1V8 5V0	
DATE: Wed Apr 13 08:08:31 2016	REV: 1.0
SHEET NUMBER: 15 OF 20	
DOCUMENT NUMBER: 105-C081xx-00F	

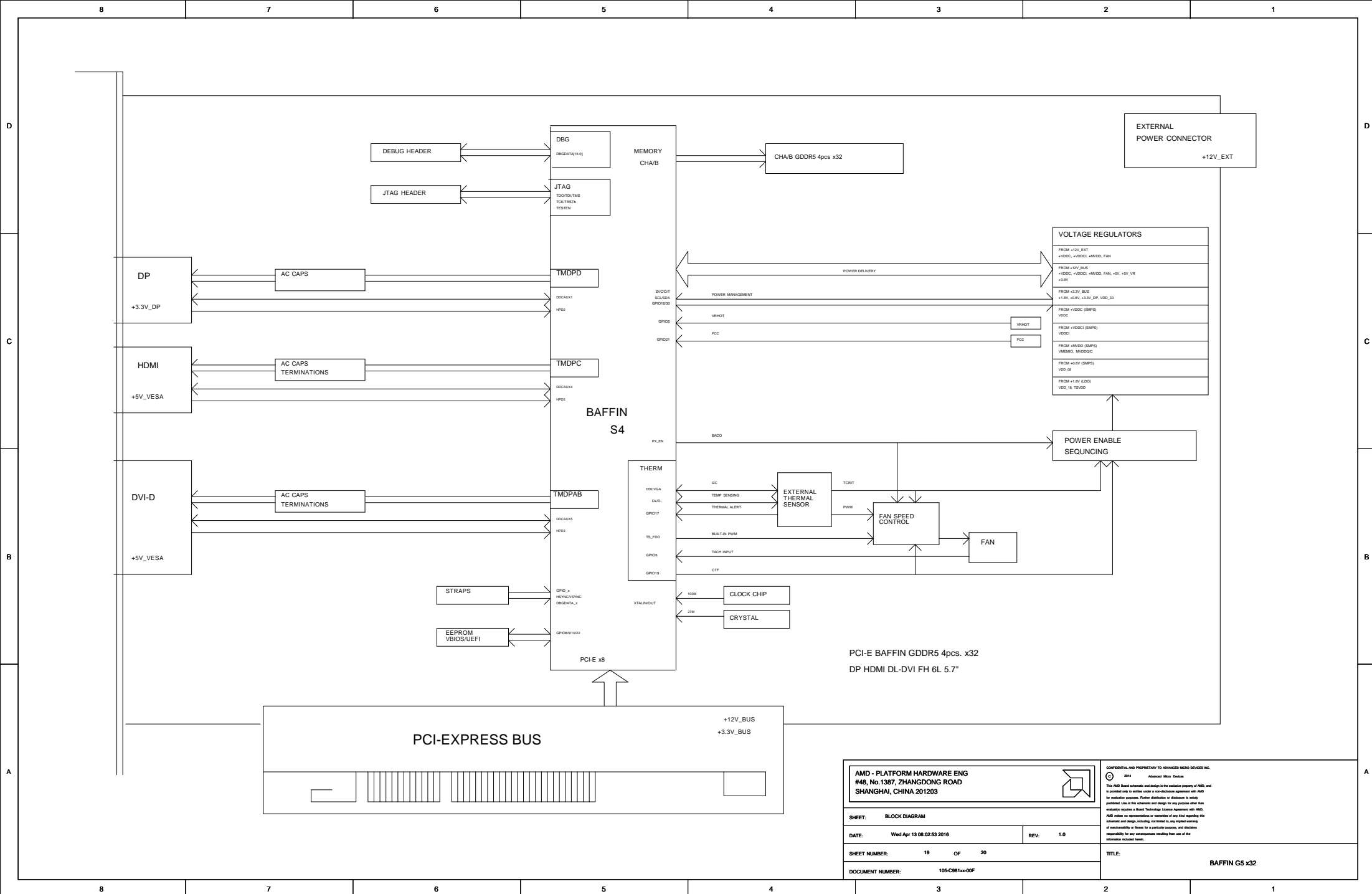
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TITLE: BAFFIN G5 x32

(17) THERMAL MANAGEMENT



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SHEET: THERMAL MANAGEMENT		REV: 1.0		TITLE: BAFFIN G5 x32	
DATE: Wed Apr 13 08:08:32 2016					
SHEET NUMBER: 17 OF 20					
DOCUMENT NUMBER: 105-C86txa-00F					



8		7		6		5		4		3		2		1		
AMD 		TITLE: BAFFIN G5 x32				DOCUMENT NUMBER: 105-C981xx-00F			DATE: Thu May 05 03:08:49 2016			SHEET NUMBER: 20 OF 20		REV: 1.0		
REVISION HISTORY				ENGINEER: XXX		NOTES: NOTE			<div>CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC.</div> <div>This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representation or warranty of any kind regarding this schematic and design, including, not limited to, any explicit warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information included herein.</div> <div>C 2014 Advanced Micro Devices</div>				AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203			
SCH Rev	PCB Rev	Date	REVISION DESCRIPTION													
0	00A	06/05/2016	INITIAL DESIGN													
1	00B	06/09/2016	ADDED C10A, C10B (BANK) REPLACED C706 AND ADDED R70A, R70B, C700 (BANK0) REMOVED C670 (J000) ADDED MR14A0 (J0000) REPLACED L1000A101 (INPUT INDUCTORS) REMOVED MR100, R100A, R100B, C101, C102, C10A, C10B, Q101, Q102 (PMOS COMPONENTS) ADDED SR0A01, SR0A10 (POWER FET SWITCH) ADDED TP0A01, TP0A01, TP0A01, TP0A01, TP0A01, TP0A01, TP0A01, TP0A01													
2	00C	10/07/2016	ADDED MR100A, MR100B, MR100C, Q101, Q102, R100A, R100B, C10A, C10B, C102, C10A (PMOS COMPONENTS) ADDED R100, MR100 (AMPLIFIED PERSETS OPTION) REPLACED SR1 (SR000)													
3	00D	02/09/2016	ADDED MR01A (ALTERNATE PU OPTION FOR TRACK) ADDED CAP1A, CAP1B (EXTRA DECOUPLING OPTION FOR LMR000A100) ADDED C0A (EXTRA DECOUPLING OPTION FOR SR01A100) (DIFFERENTIAL IMPEDANCE CHANGE FOR HDMI LAYOUT)													
4	00E	03/02/2016	(DIFFERENTIAL IMPEDANCE CHANGE FOR HDMI LAYOUT) ADDED R100A-R100B													
5	00F	03/16/2016	ADDED L100A-L100B ADDED R107, R17A, R10B, R10B, R101, R107, R10B ADDED R10, R10 OPTION TO CONNECT P06 SIGNALS TO EXT THERMAL SENSOR ADDED ANOTHER THERMAL SENSOR OPTION													