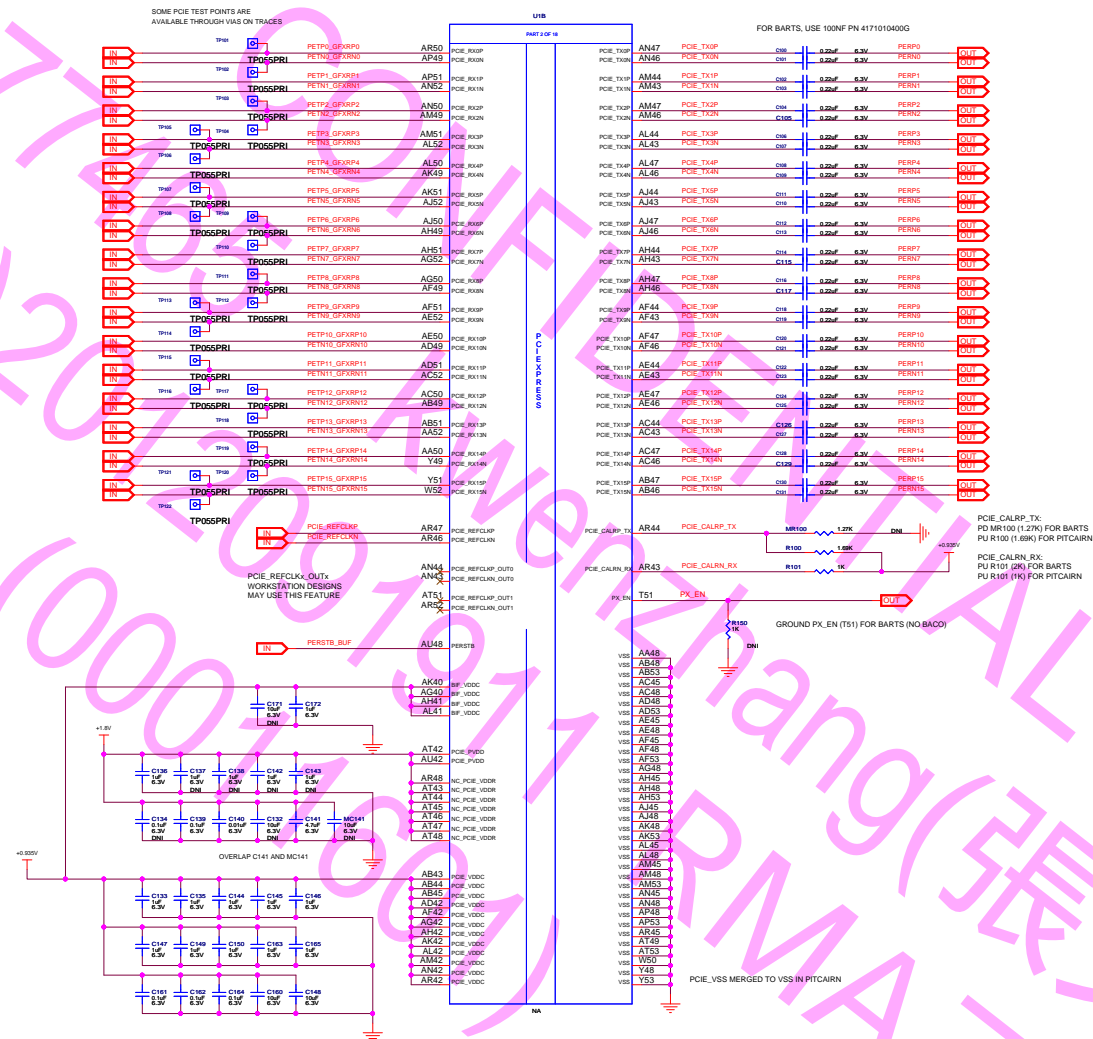
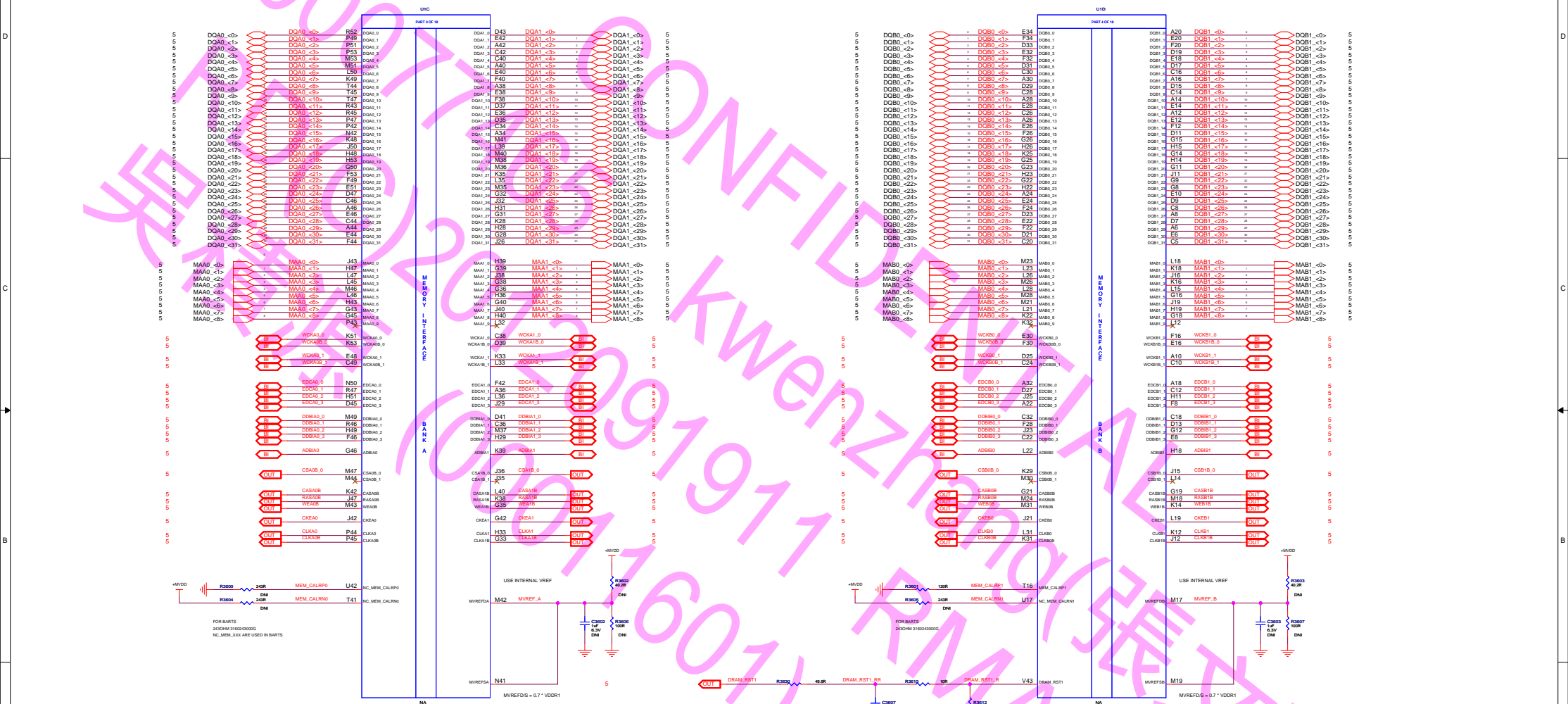


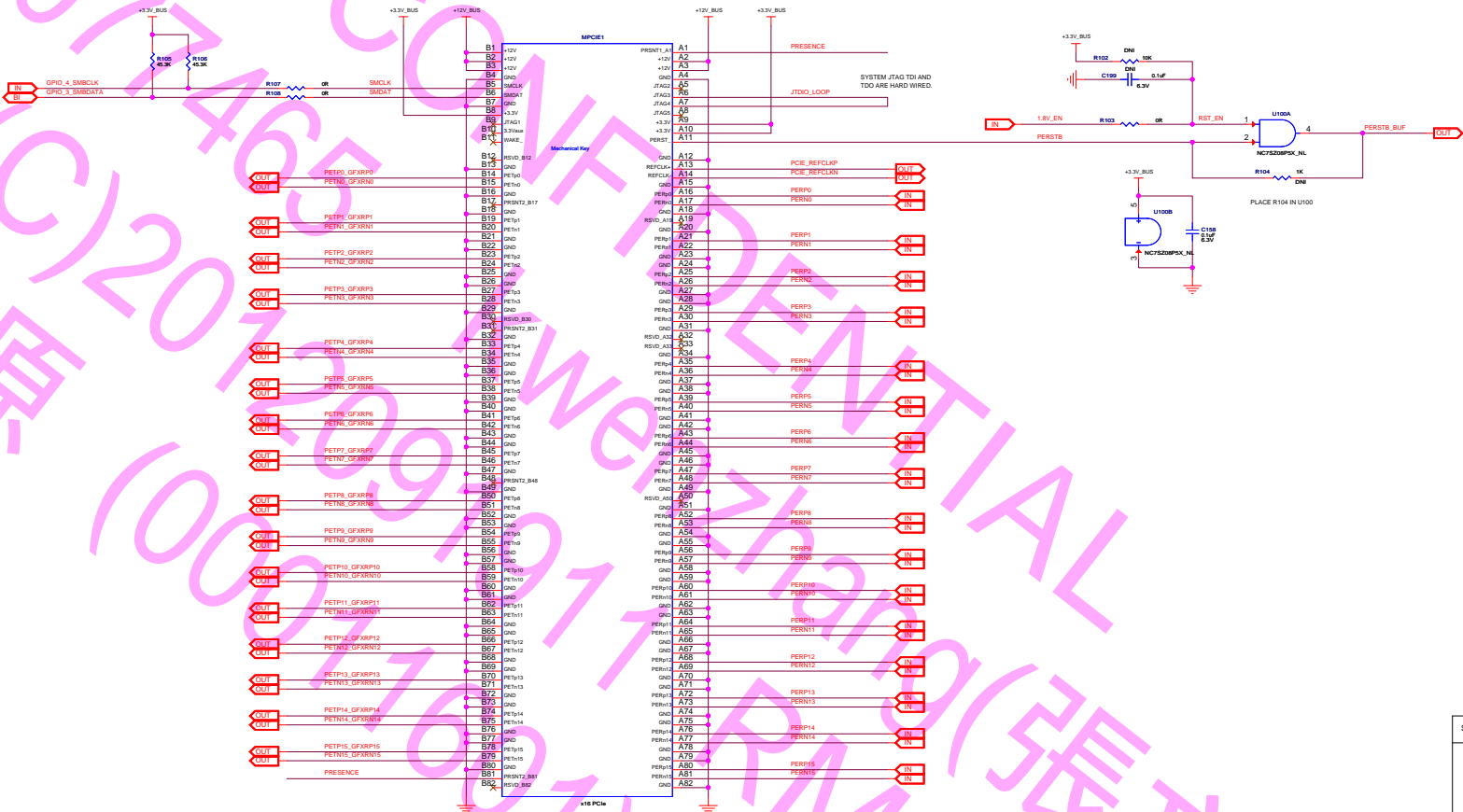
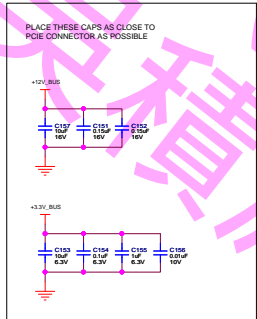
(2) PITCAIRN PCIE INTERFACE



(3) PITCAIRN MEM INTERFACE CH AB



(1) PCI-EXPRESS EDGE CONNECTOR



SYMBOL LEGEND

DNI	DO NOT INSTALL
b or #	ACTIVE LOW
BUG	BRING UP ONLY
	DIGITAL GROUND
	ANALOG GROUND

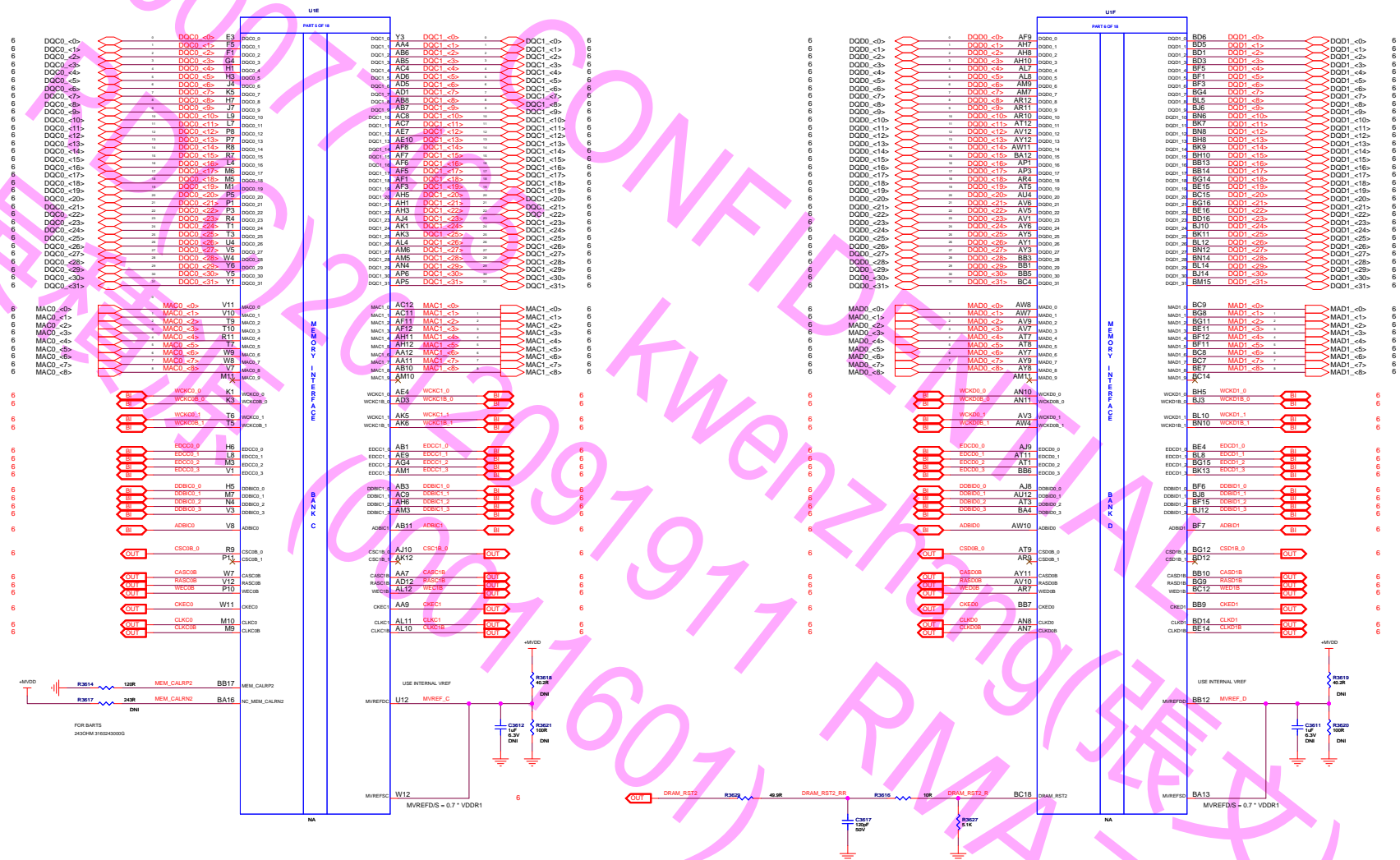


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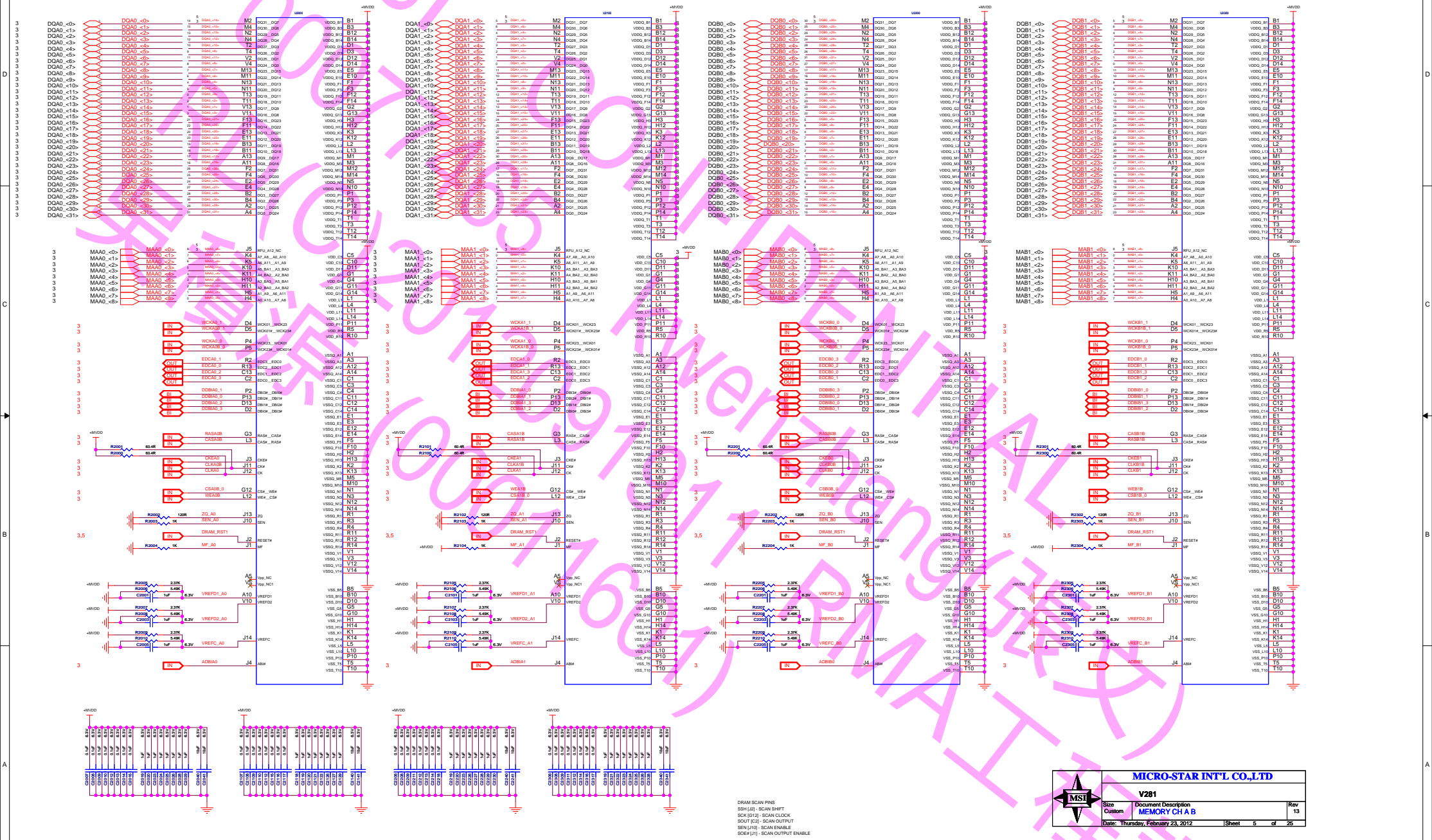
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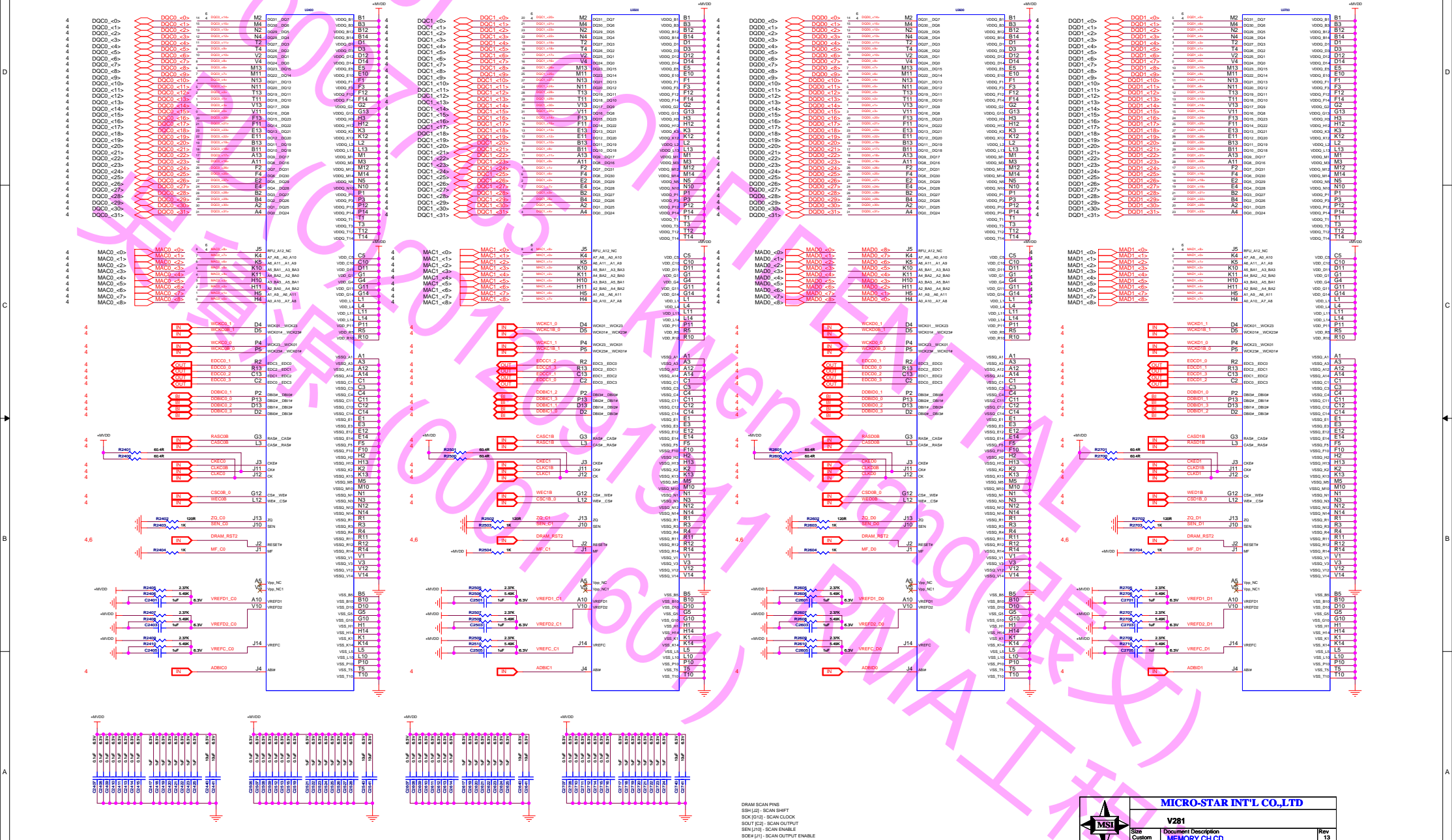
(4) PITCAIRN MEM INTERFACE CH CD



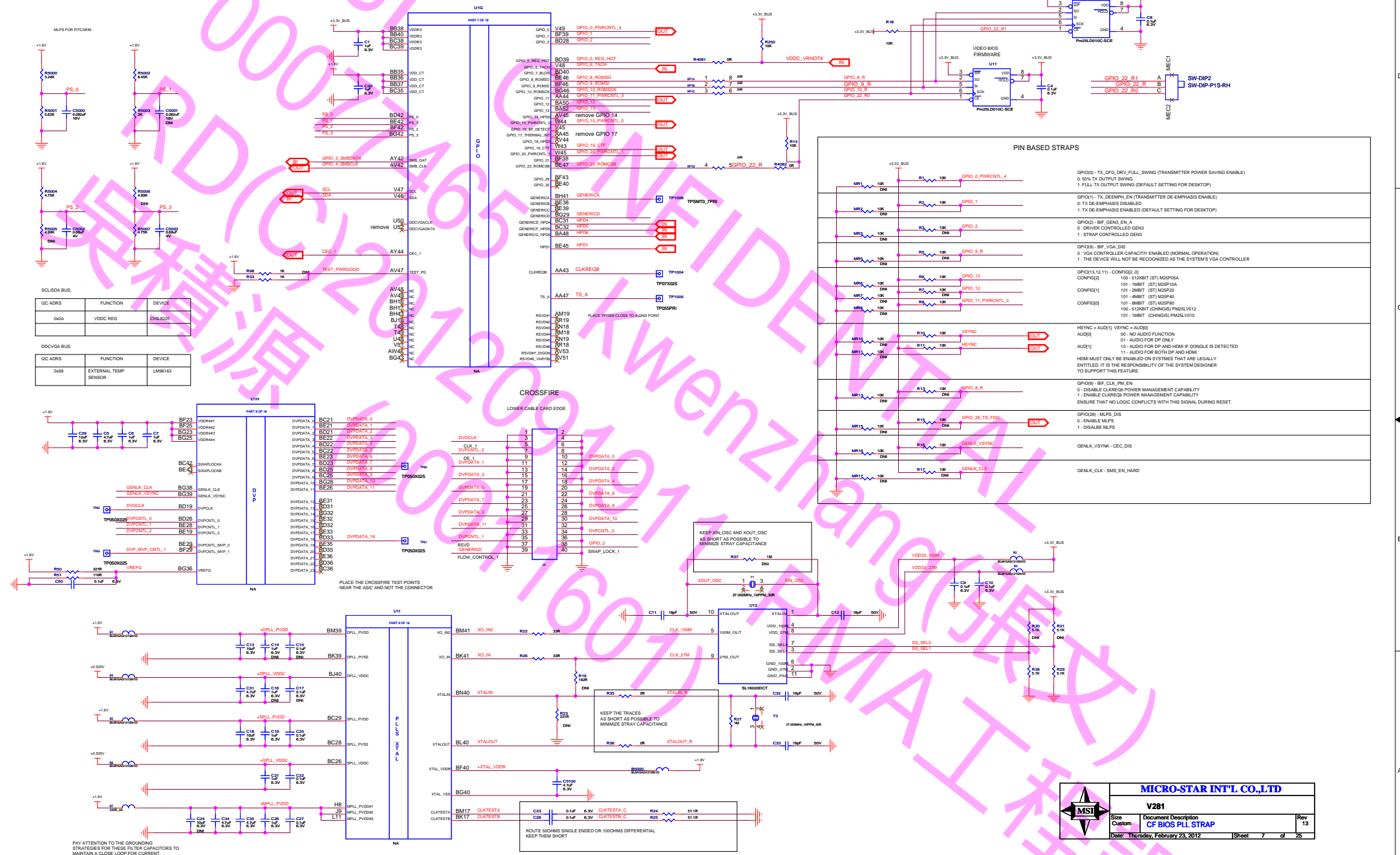
(5) GDDR5 MEMORY CH AB



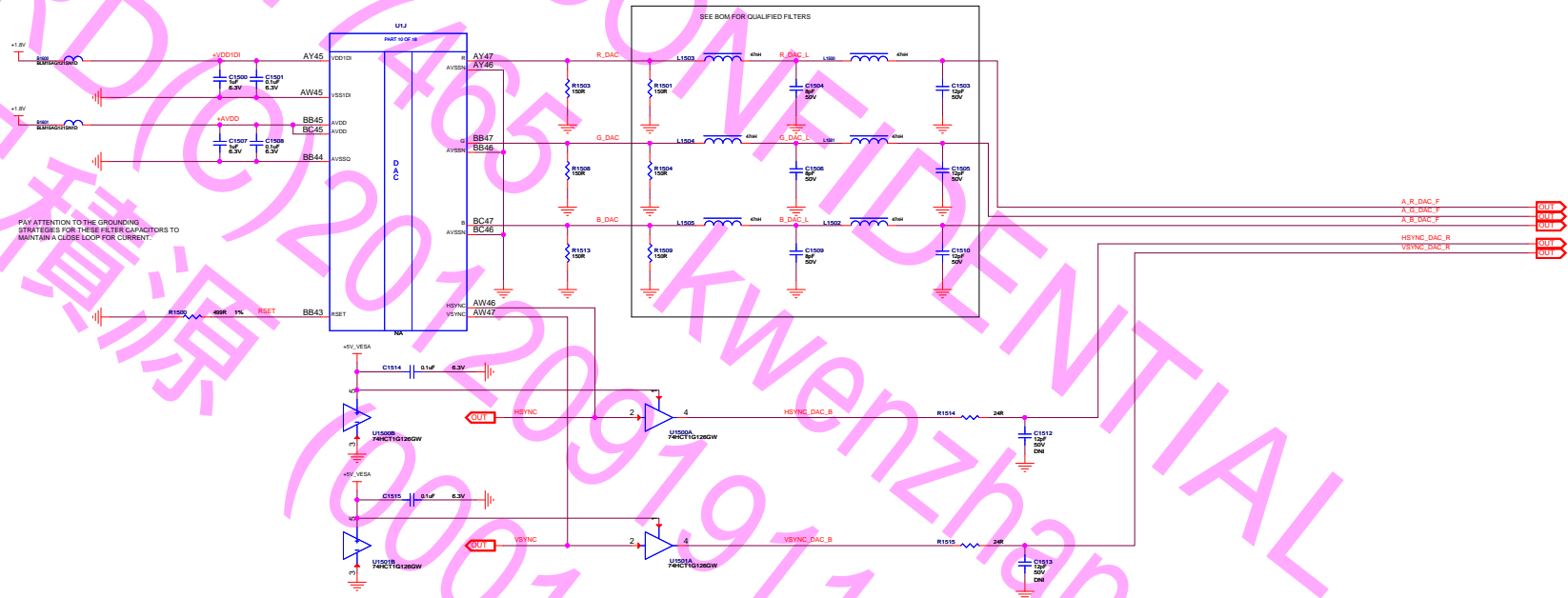
(6) GDDR5 MEMORY CH CD



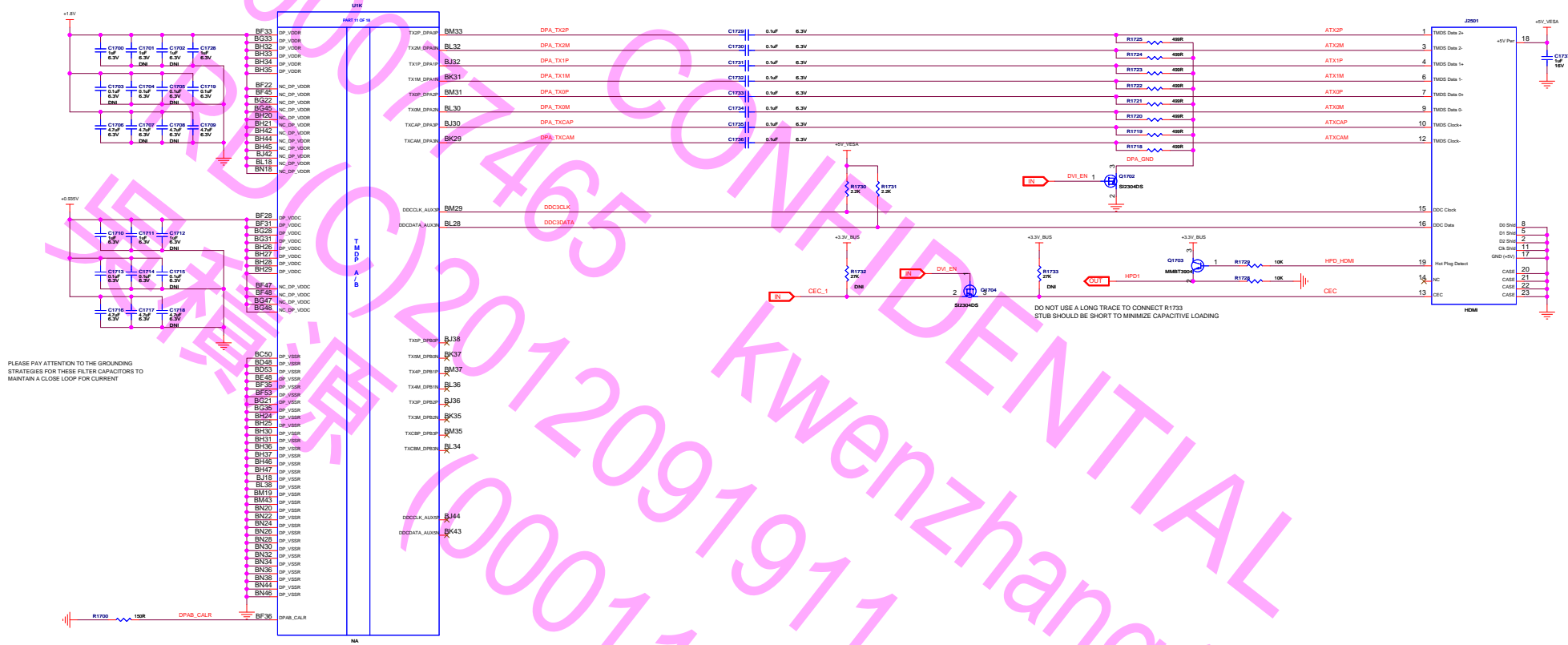
(7) PITCAIRN GPIO STRAP CF PLL XTAL



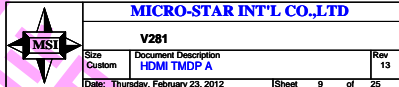
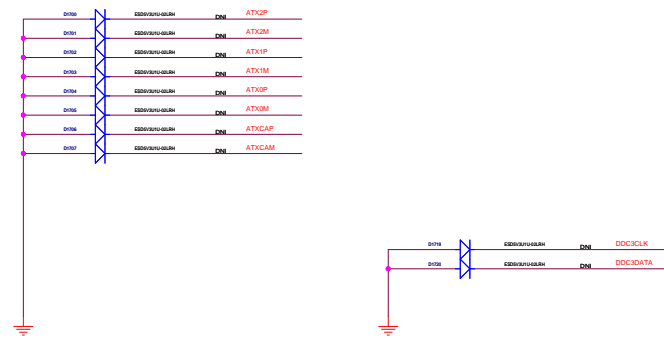
(8) PITCAIRN DAC

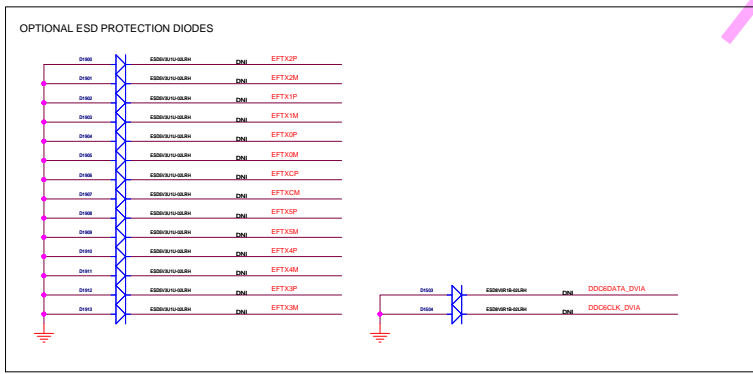


(9) PITCAIRN TMDP AB HDMI sDVI

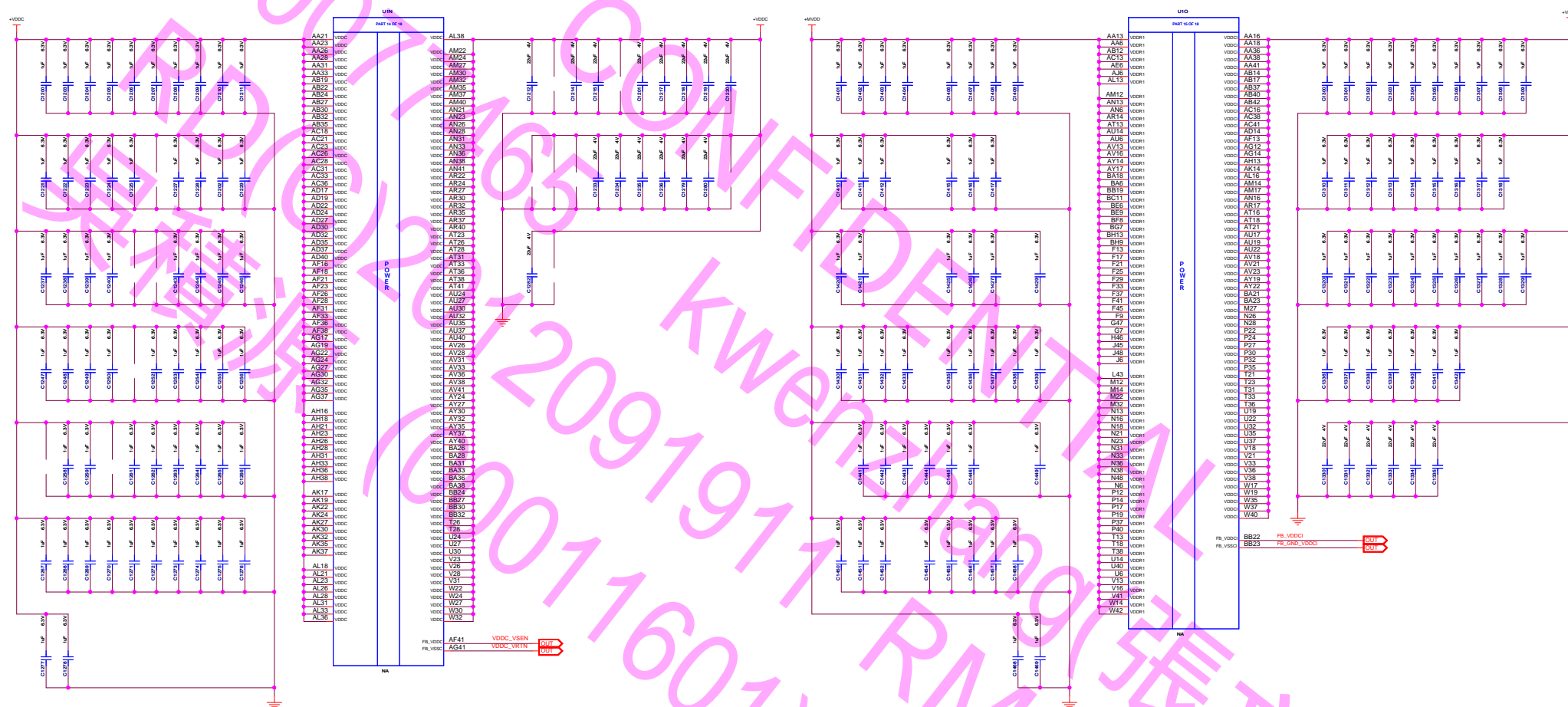


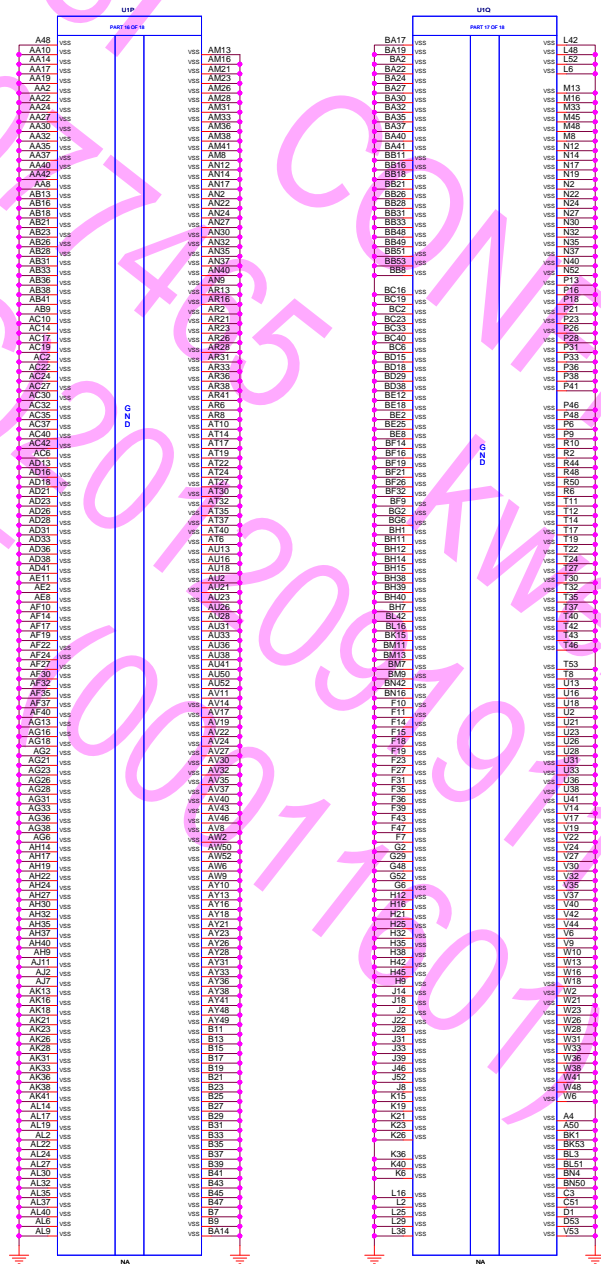
OPTIONAL ESD PROTECTION DIODE





(12) PITCAIRN POWER





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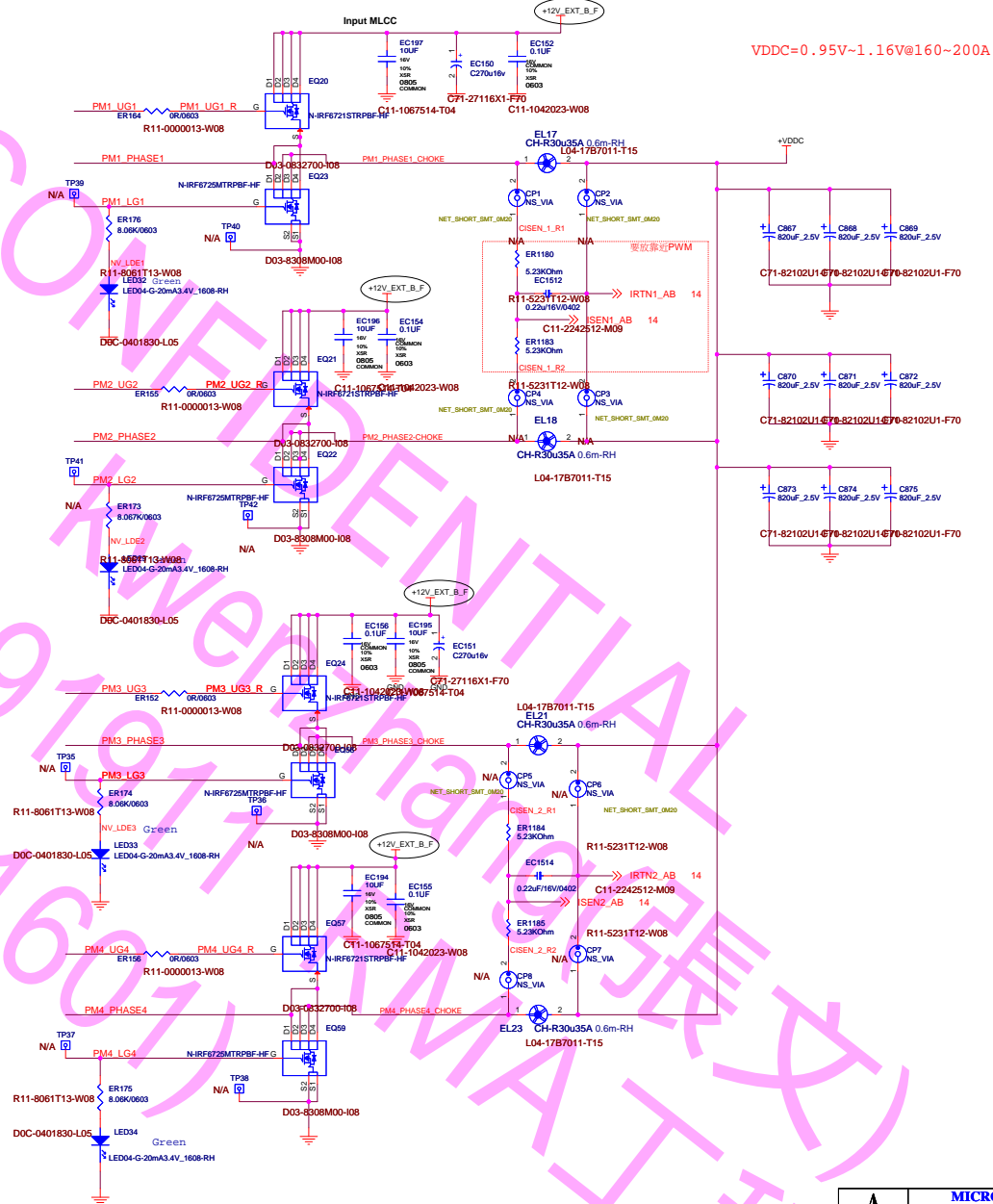
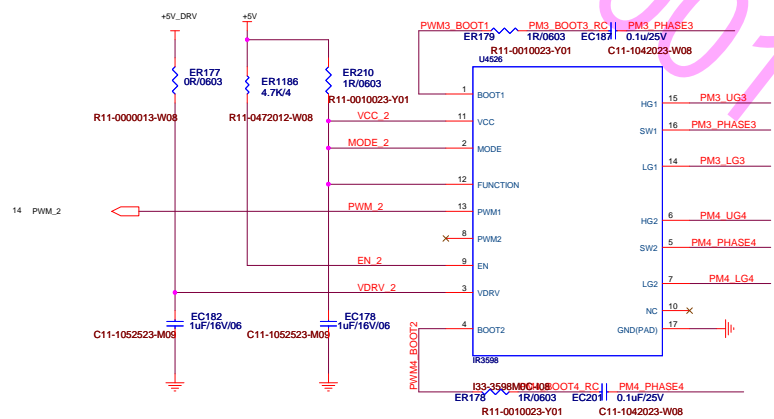
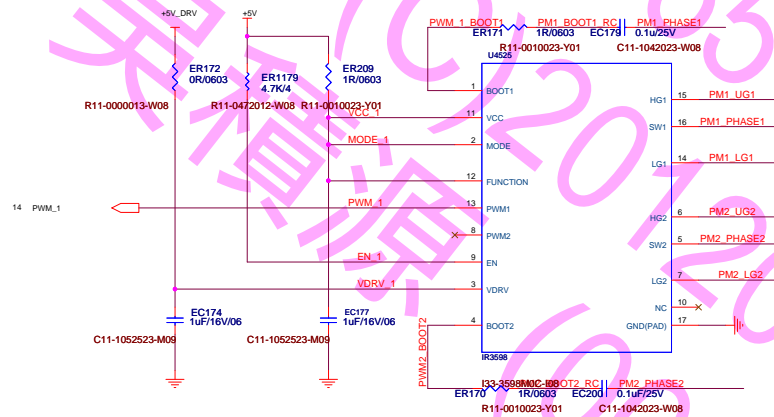
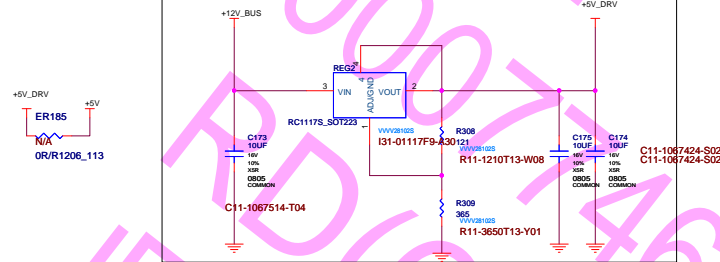
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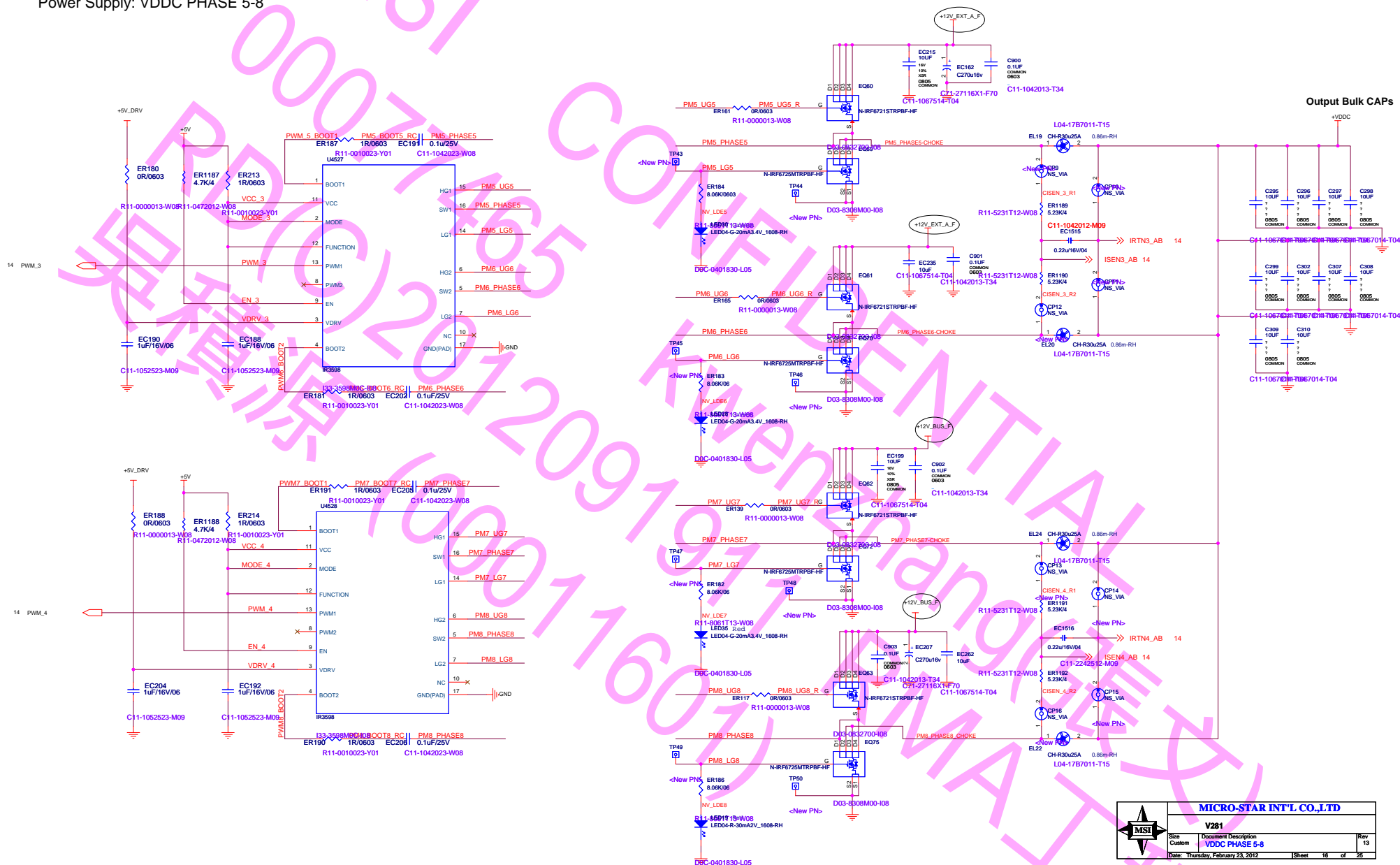
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Power Supply: VDDC PHASE 1,~4



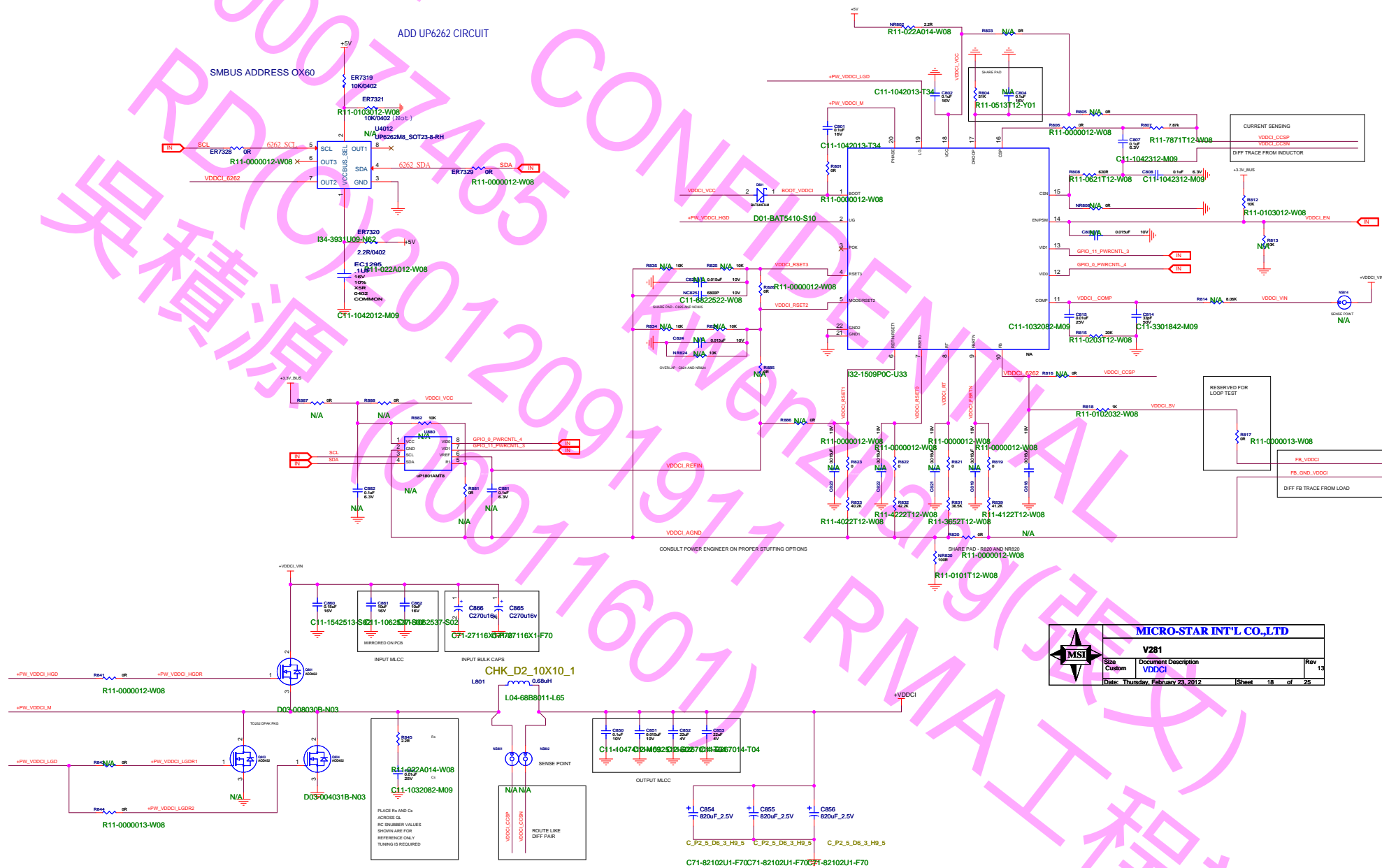
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Power Supply: VDDC PHASE 5-8





(15) VDDCI



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LAYOUT GUIDELINE

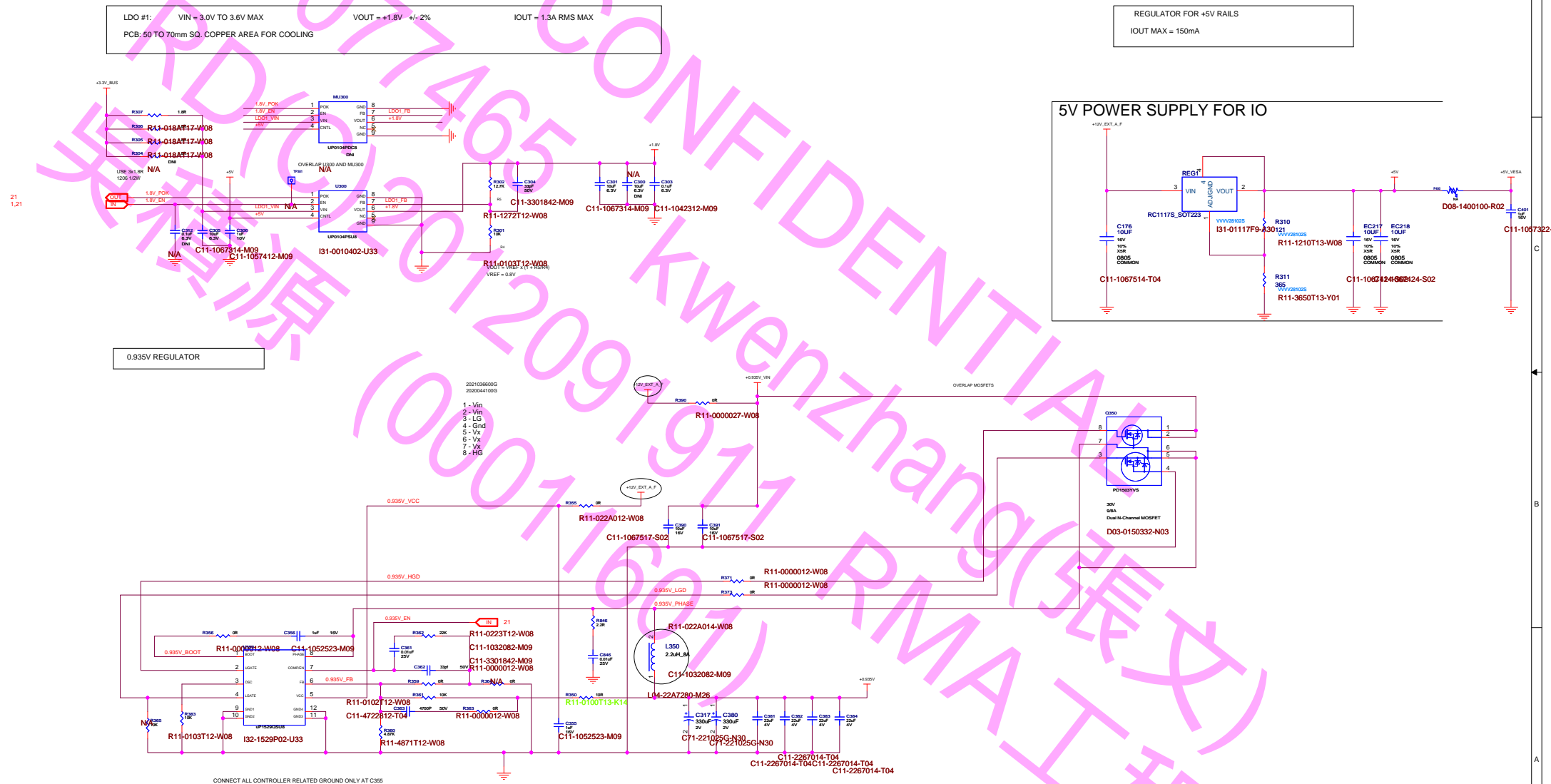
1. POSITION THE CONTROLLER (U703) SUCH THAT GATE (P6M) IS THE CLOSEST TO THE GATE OF THE MOSFETS. YOU CAN PLACE THE GATE RESISTORS R721 AND R722 NEXT TO THE GATE OF THE MOSFETS. MAKE THE GATE DRIVER TRACES (P6V, M6D03, L6D02) AS SHORT AS POSSIBLE TO REDUCE THE TRACE INDUCTANCE.


2. PLACE THE BYPASS CAPACITORS FOR VCC (C703) AS WELL AS BOOST CAPS (C606) AS CLOSE TO THE CONTROLLER AS POSSIBLE.

3. VOLTAGE AMPLIFIER COMPENSATION NETWORK: PLACE R714 CLOSE TO THE PIN 7. PLACE THE REST OF THE COMPENSATION NETWORK (R710, R711, R712, R713, C711, C712 AND C713) CLOSE TO THE PINS 7 AND 6.

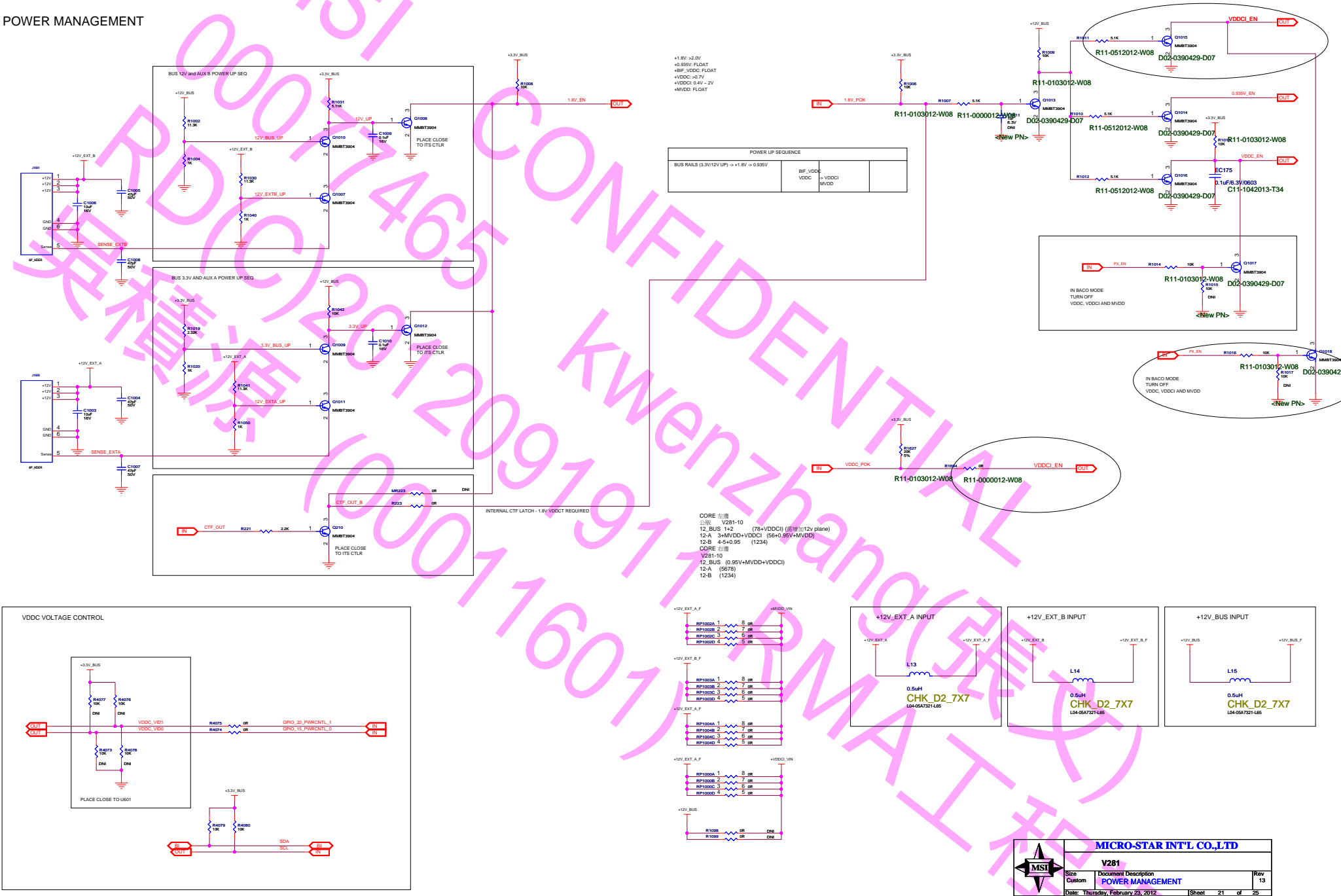
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(17) SMALL RAIL REGULATOR

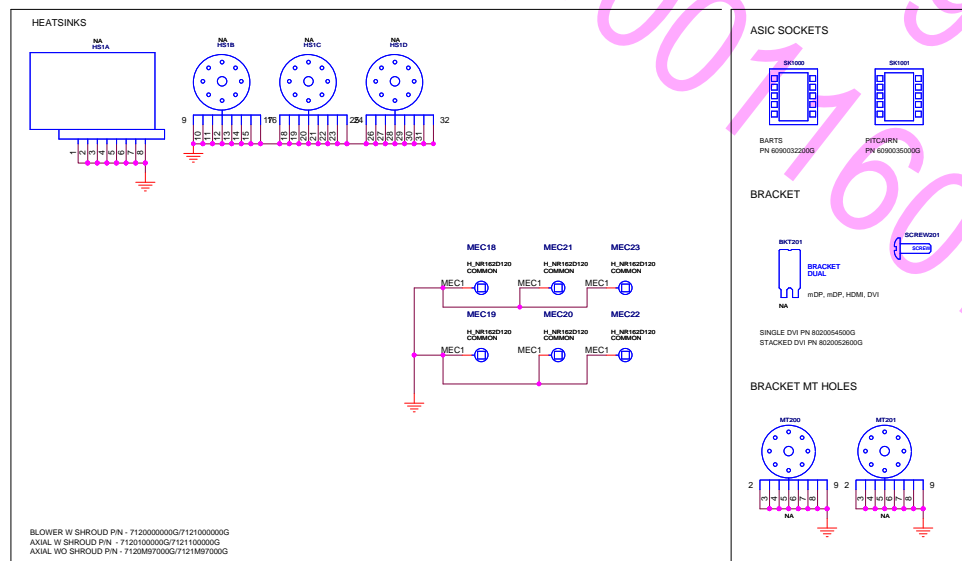
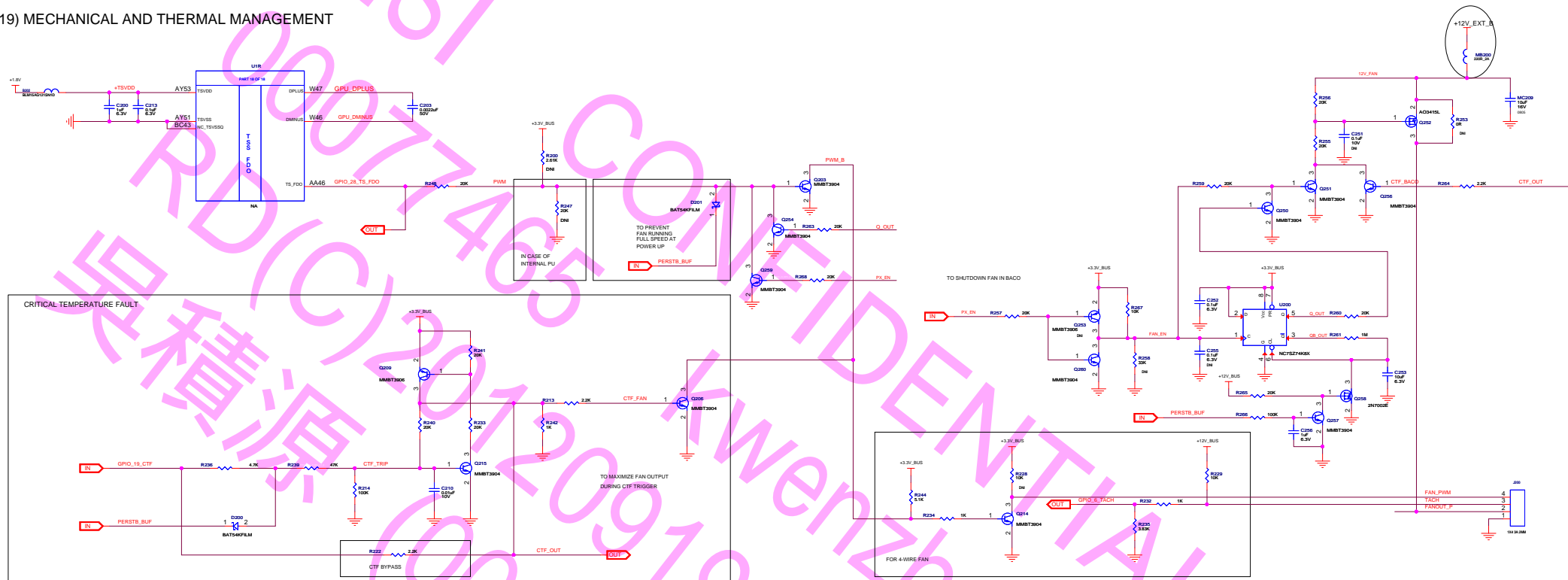


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(18) POWER MANAGEMENT



(19) MECHANICAL AND THERMAL MANAGEMENT



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TITLE:

Pitcalm GDDR5 x32

DOCUMENT NUMBER:

105-C401xx-00

DATE:

Wed Dec 21 19:16:58 2011

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NOTES:

NOTE

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Advanced Micro Devices

AMD - PLATFORM SOLUTIONS ENG.

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Markham, ON L3T 7X6

SCH Rev	PCB Rev	Date	REVISION DESCRIPTION
0	00A	10/26/2011	INITIAL DESIGN BASED ON CH45 FIT-CAMER TRAIL LAYOUT
1	00B	10/26/2011	LAYOUT INCREASE SPACING BETWEEN BGA AND JANE
2	00C	10/11/2011	UPDATE FRAMEWORK CIRCUITRY LAYOUT IMPROVE SIGNAL ROUTING
3	00	10/10/2011	ADD 750K OHM SERVO UNBOUNDED INPUT OPTION FOR 0.980V BIAS ADD 0.01uF 0.01uF, 0.01uF, 0.01uF

REV 1.1
1. 修正VDDCI EN 接脚
REV 1.3
移除1.1 板内阻金手指测试

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V2B1			
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(20) DEBUG CIRCUIT

