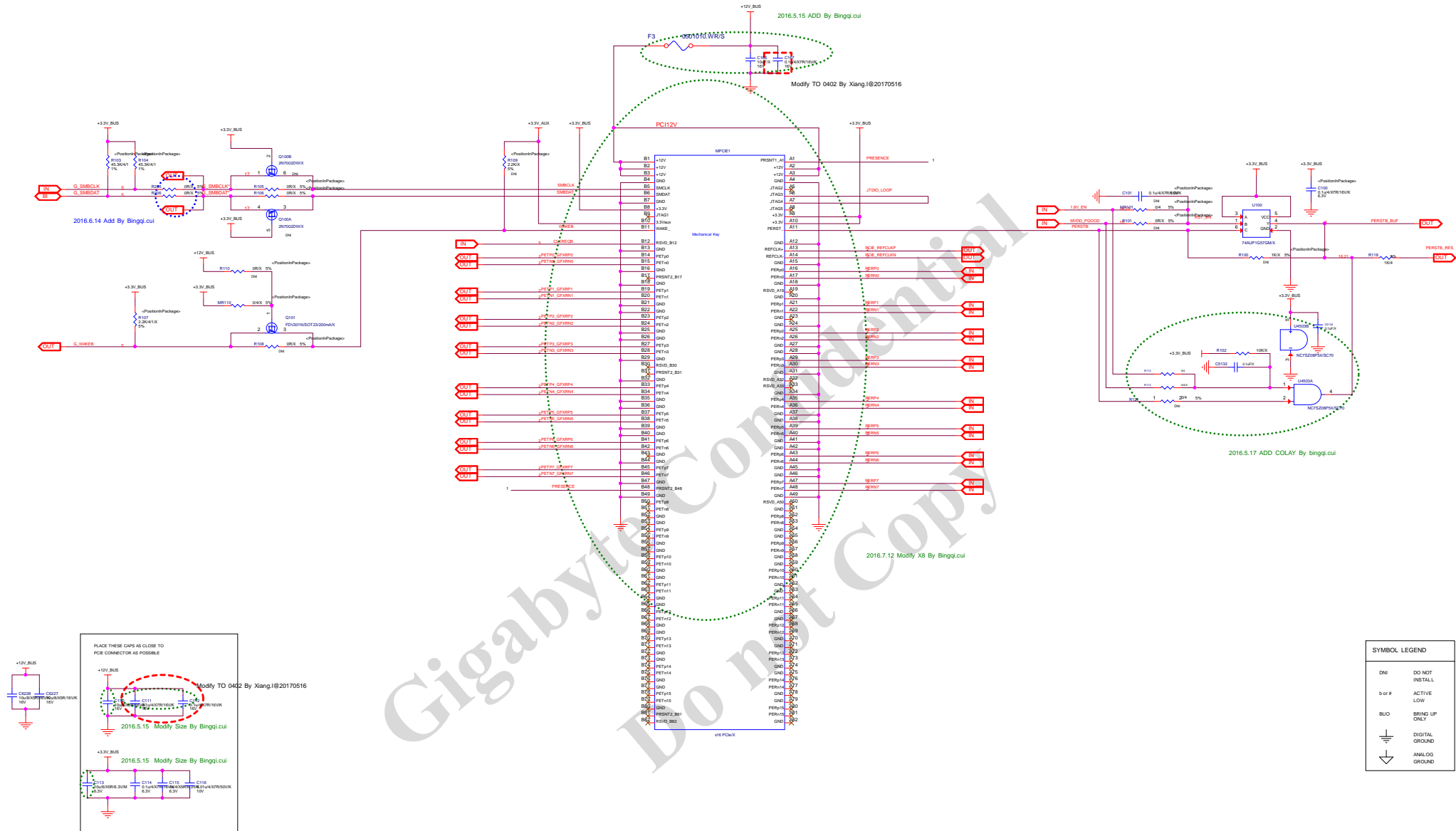
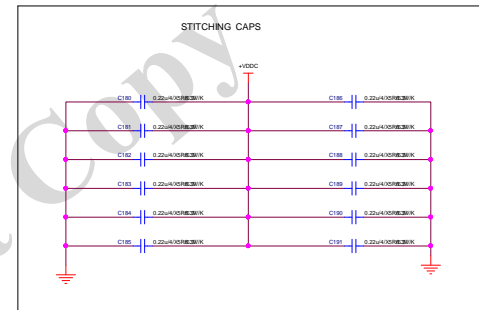
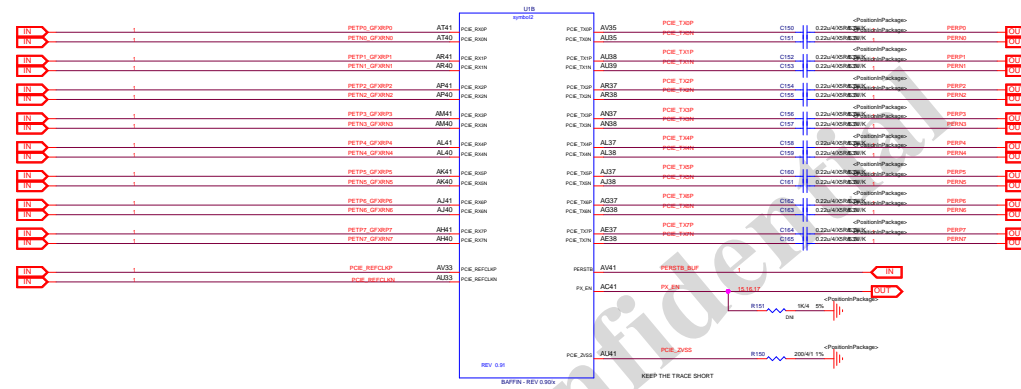


(1) PCIE EDGE CONNECTOR

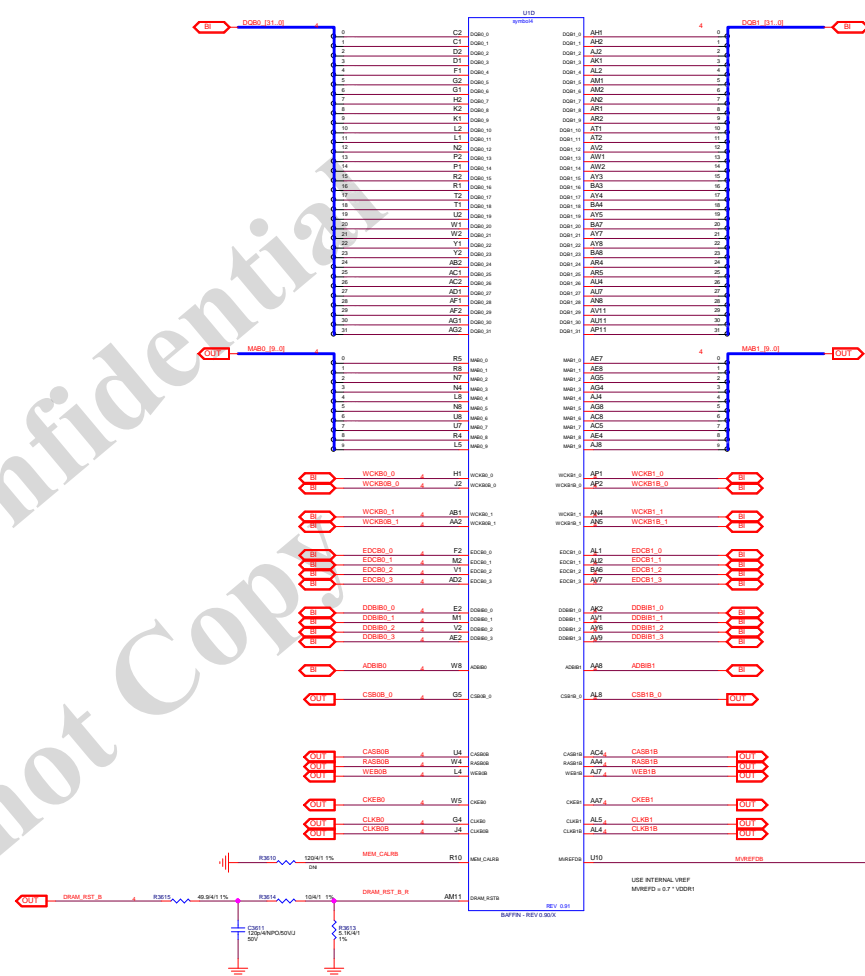
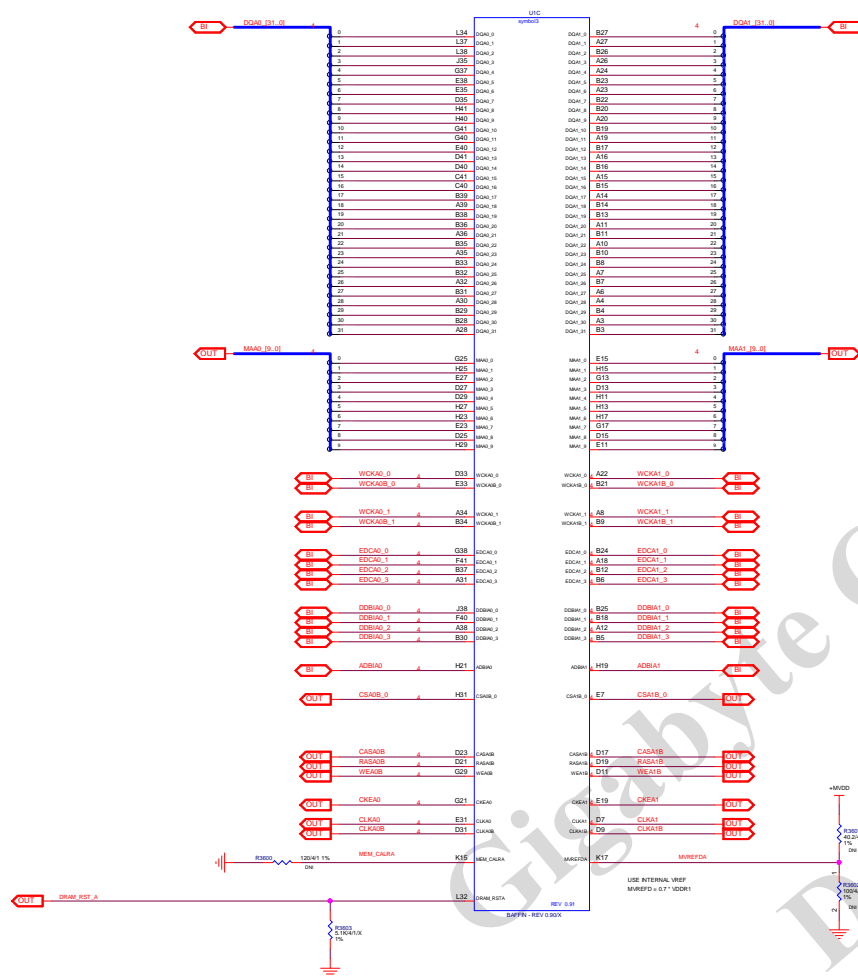


File	GV-RX860C-2GD	Rev	1.0
Size	Custom	Document Number	PCIE EDGE CONNECTOR
Date	Sunday, June 25, 2017	Sheet	1 of 22

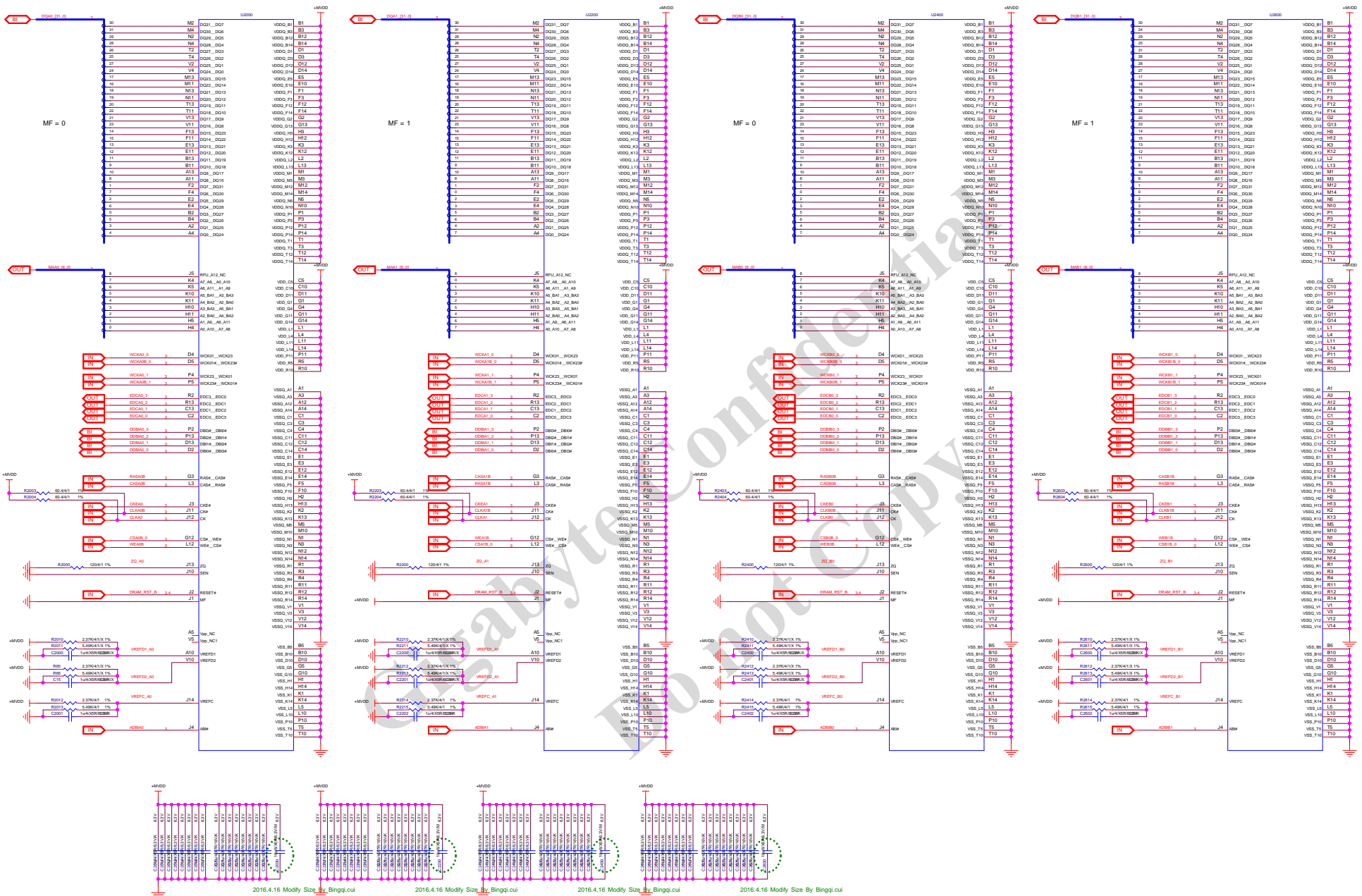
(2) BAFFIN PCIE



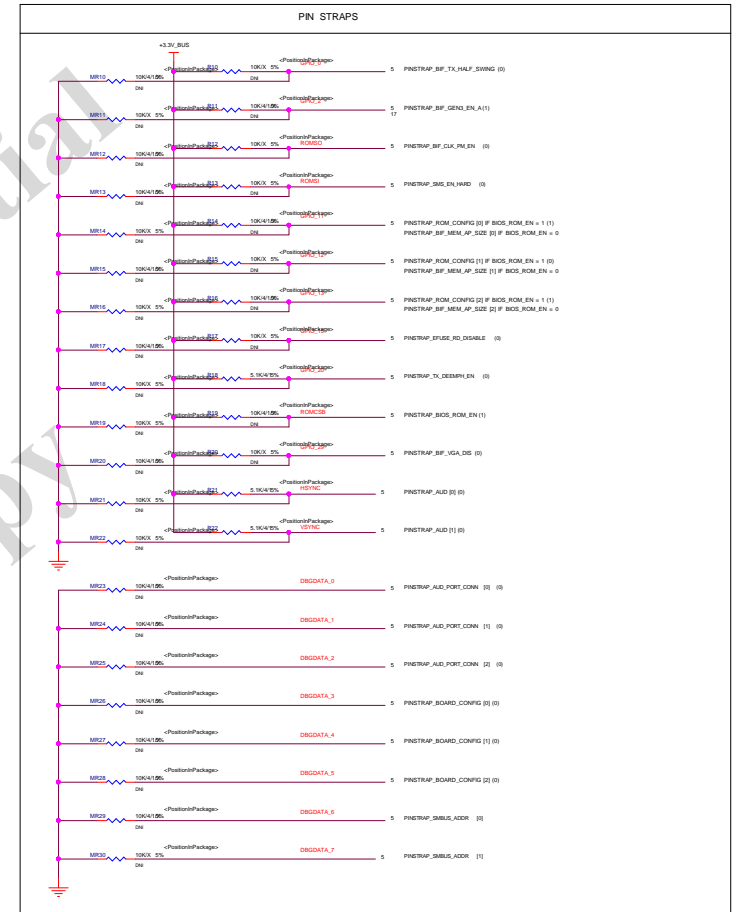
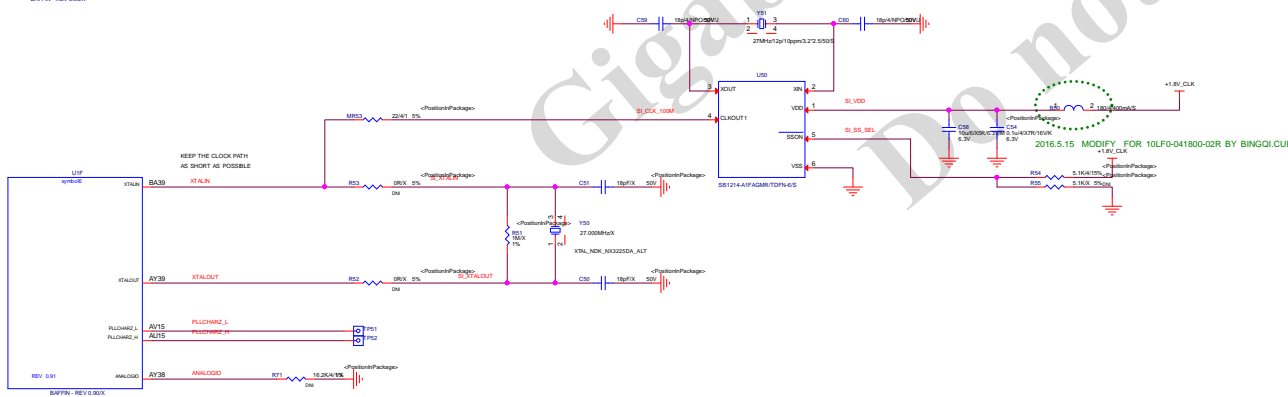
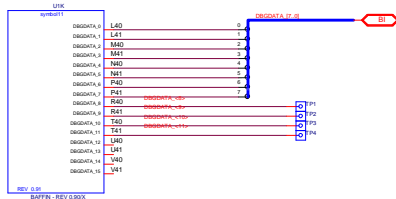
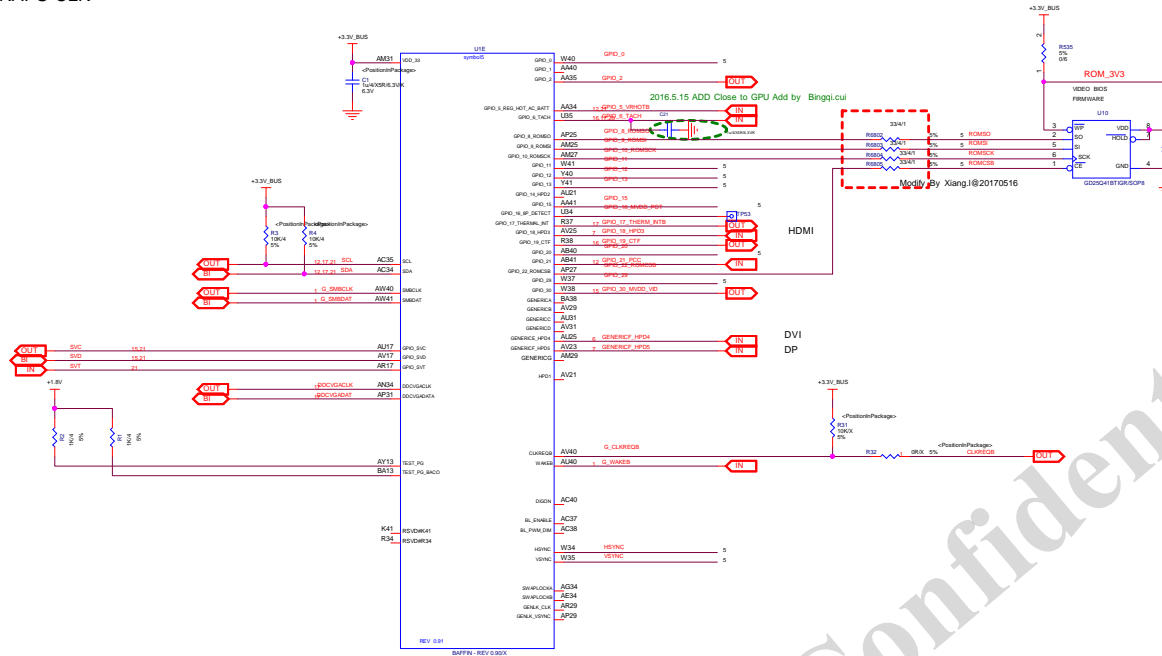
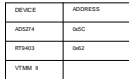
### (3) RACERX MEMORY INTERFACE

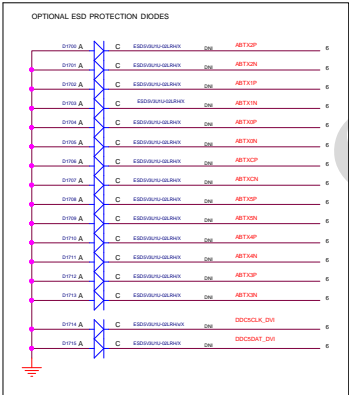


(4) GDDR5 CHAB x32

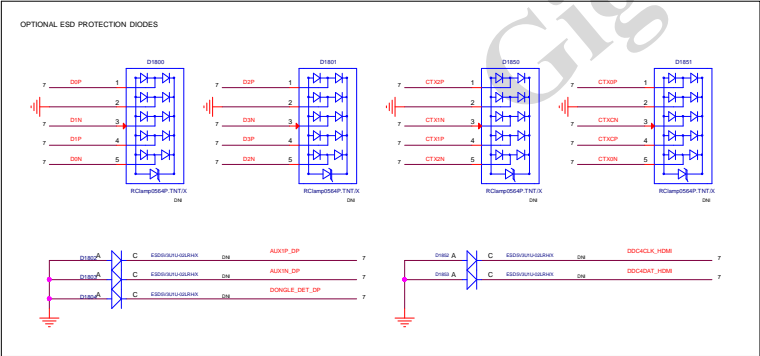
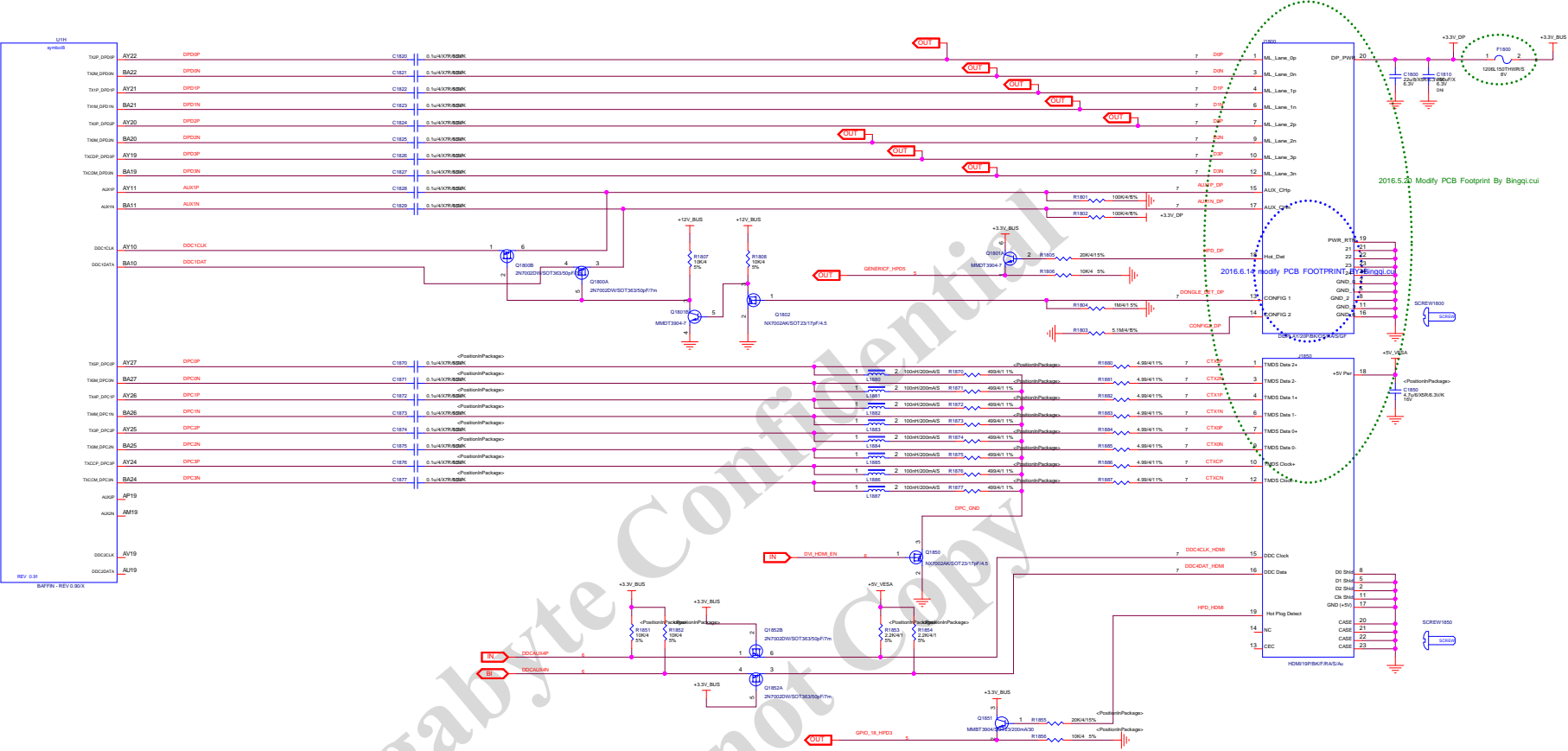


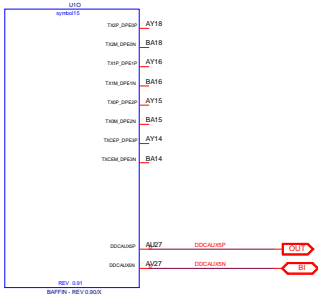
### (5) BAFFIN GPIO STRAPS CLK





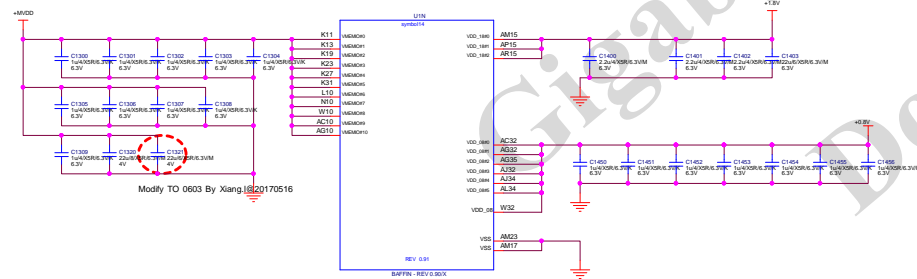
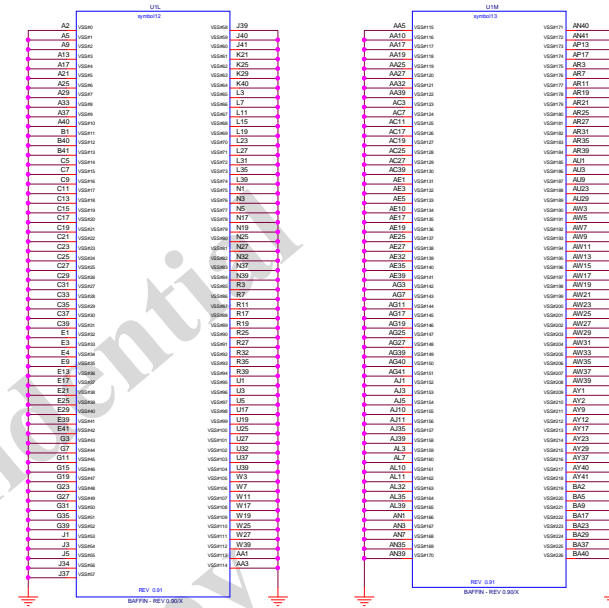
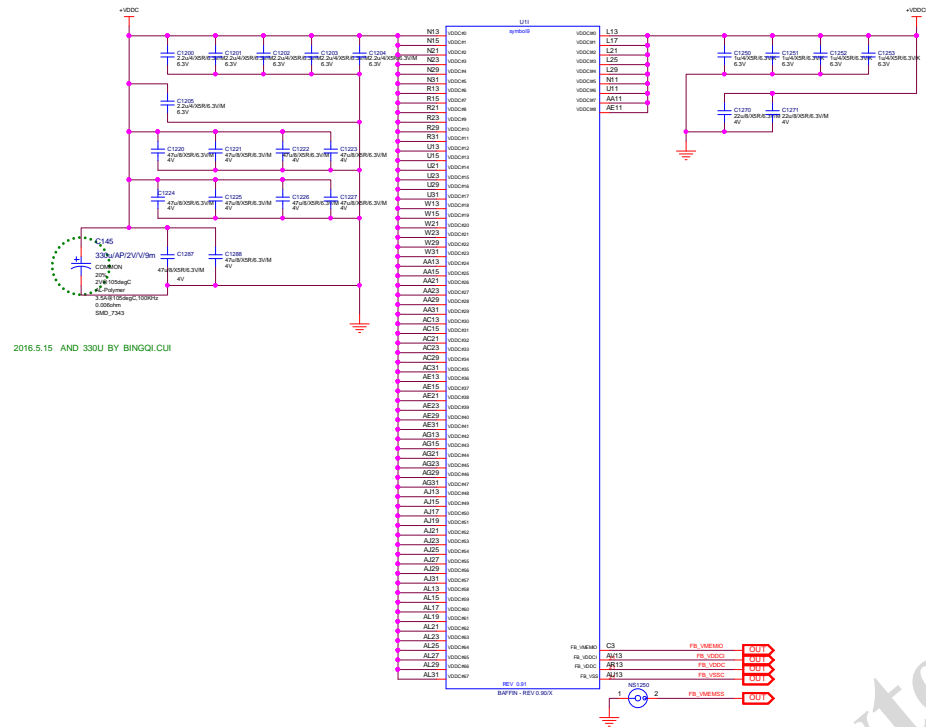
Title									
GV-RX580-OC-2GD									
Size		Document Number							Rev
Custom		BAFFIN TMDPAB - dIVI							1.0
Date		Sunday, June 25, 2017			Sheet		6		of 22



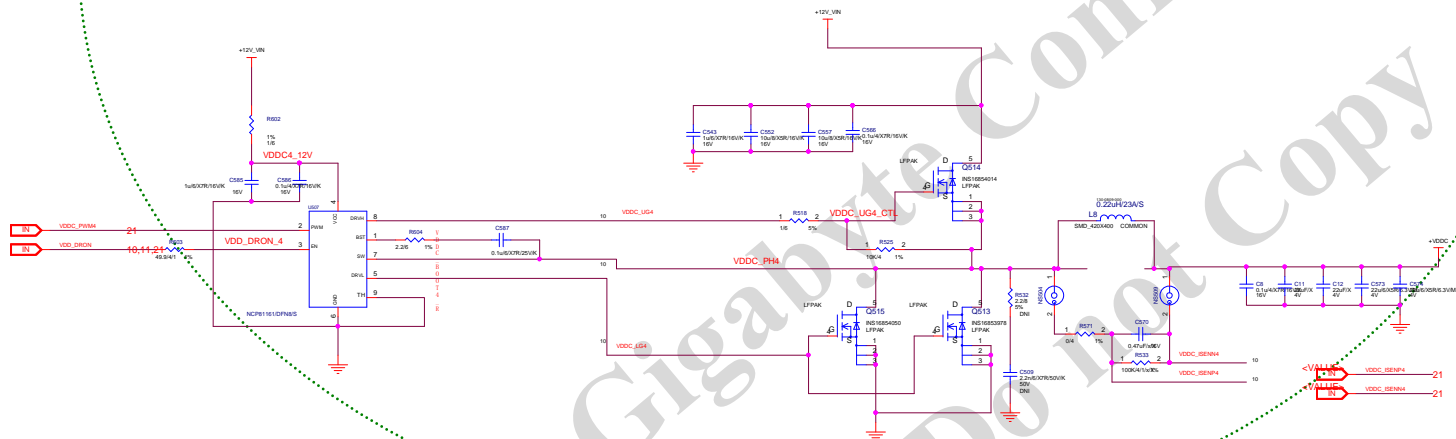
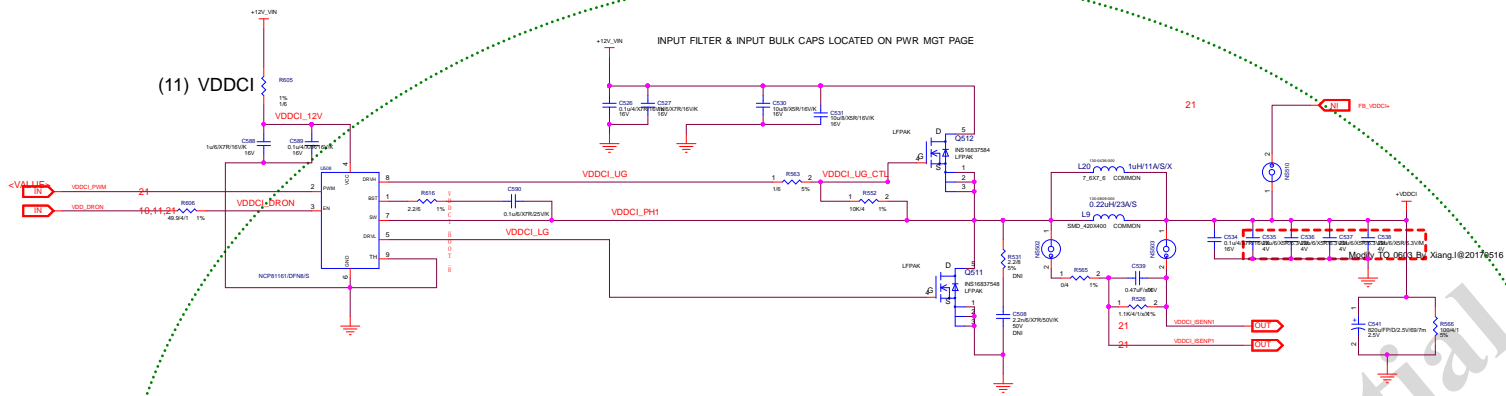




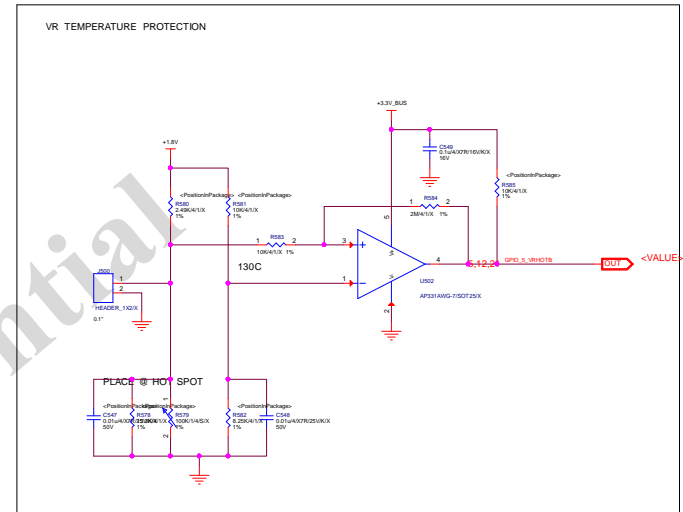
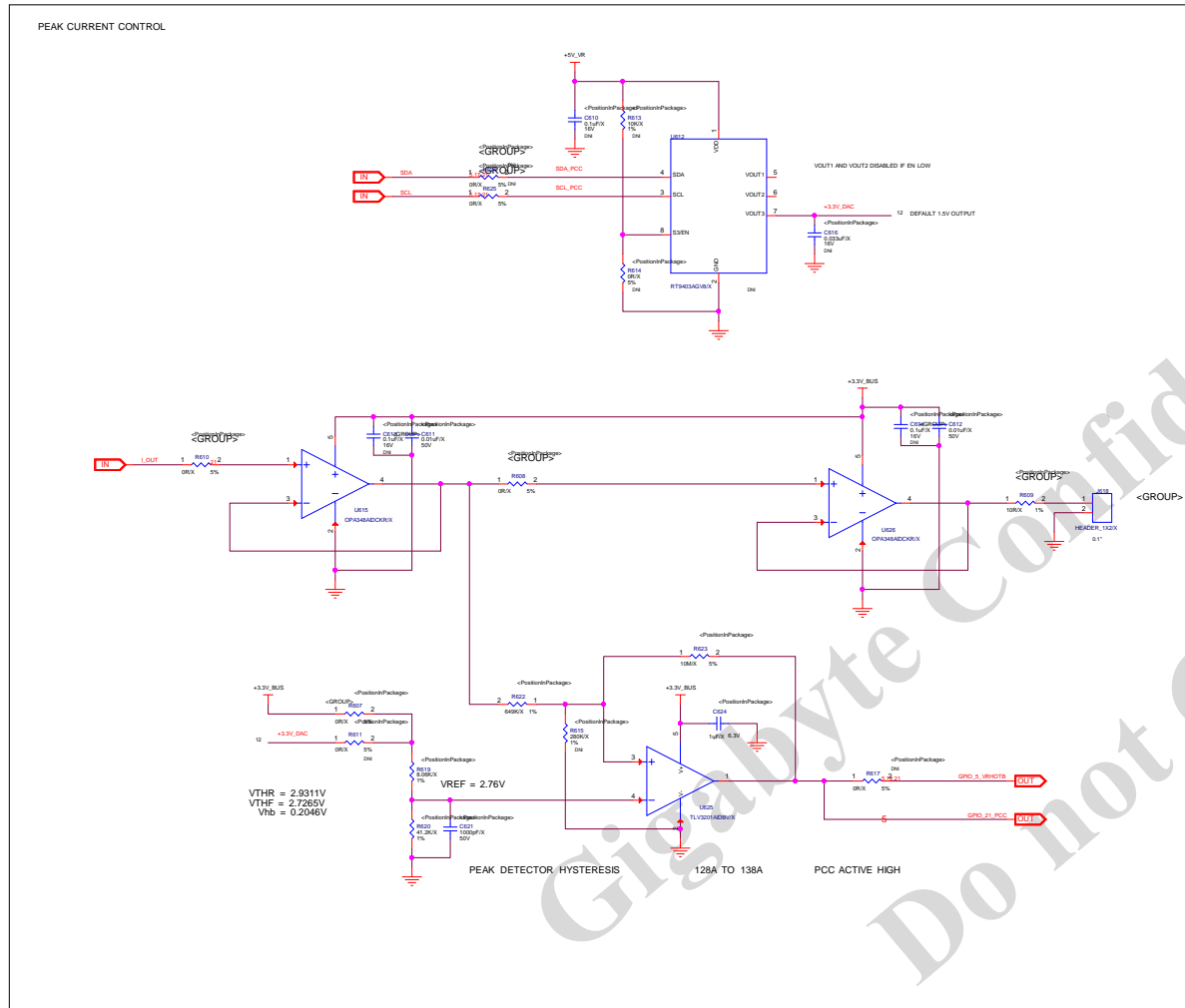
(9) BAFFIN POWER GND



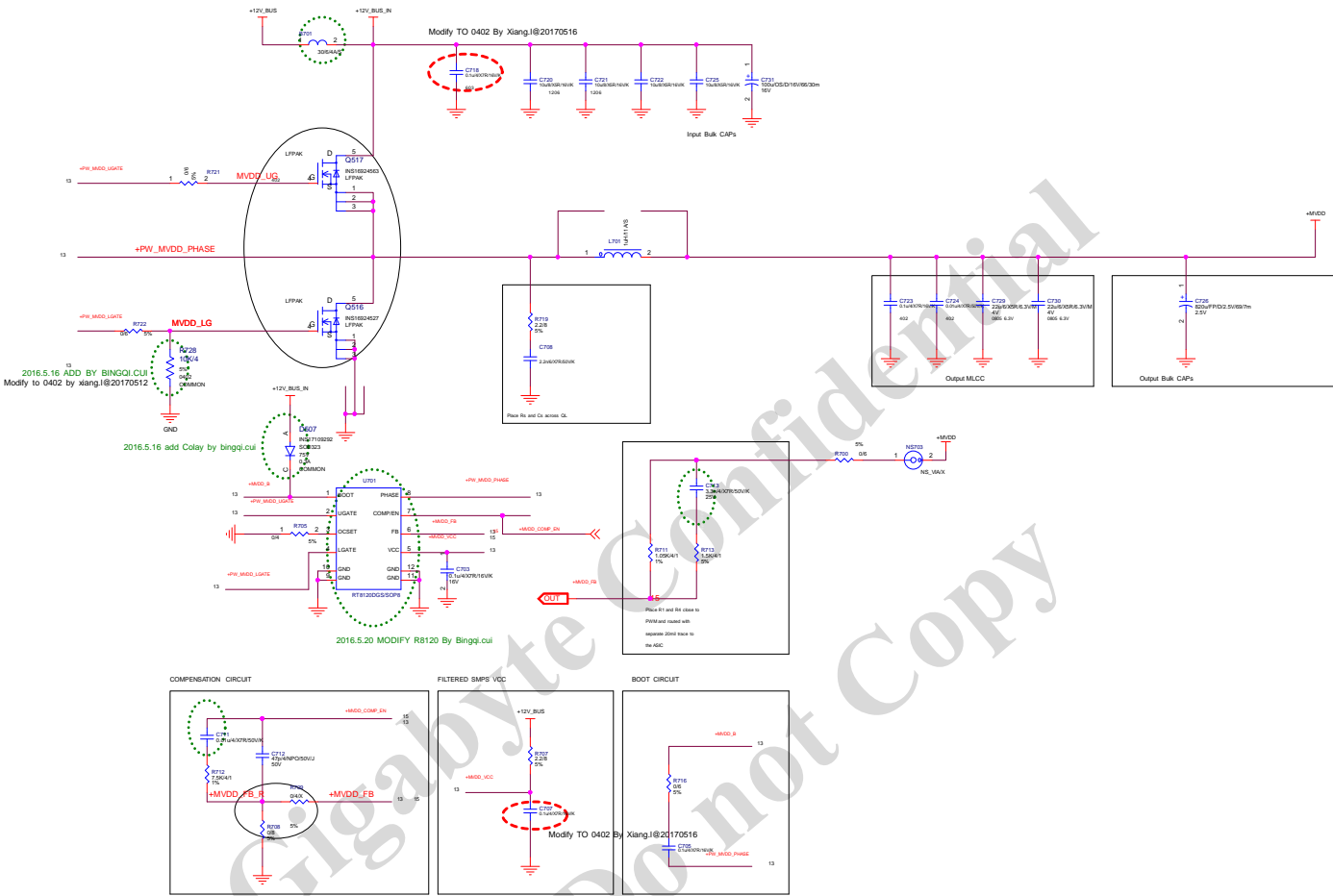




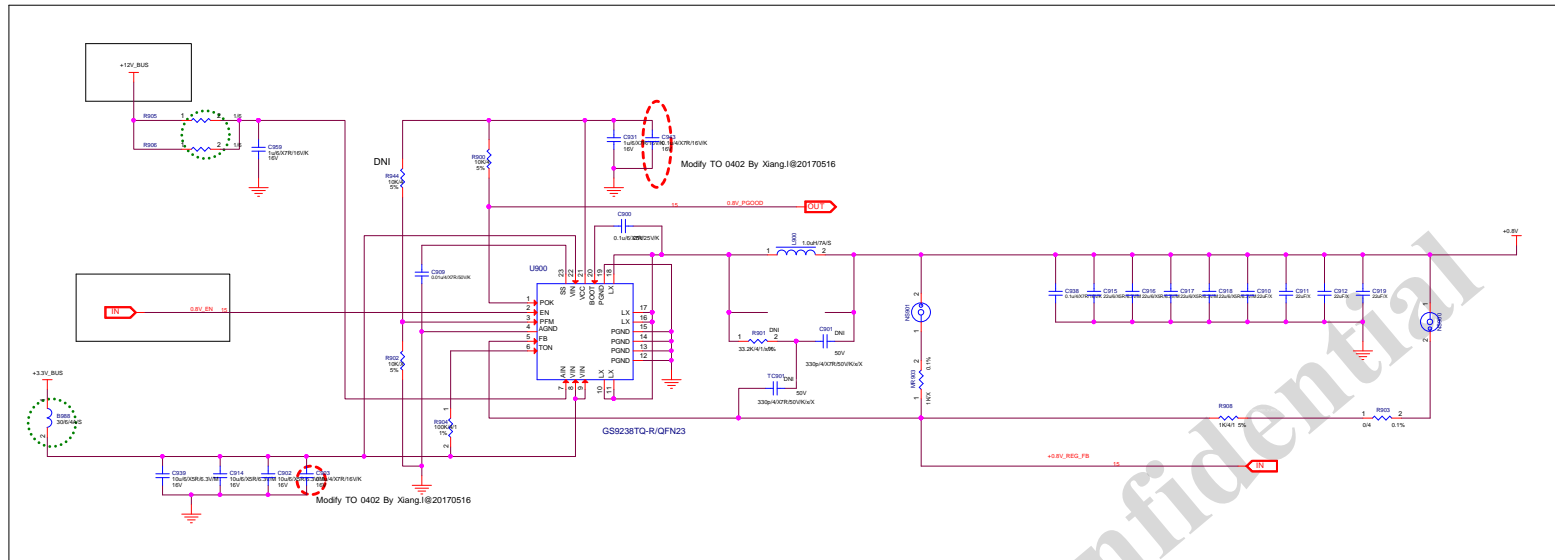
File			
GV-RX560C-2GD			
Size	Document Number	Rev	
Custom	VDDCI	1.0	
Date: Sunday, June 25, 2017		Sheet	11 of 22



Title			
GV-RX560C-2GD			
Size	Document Number	Rev	
Custom	REG - PCC VRHOT	1.0	
Date:	Sunday, June 25, 2017	Sheet	12 of 22

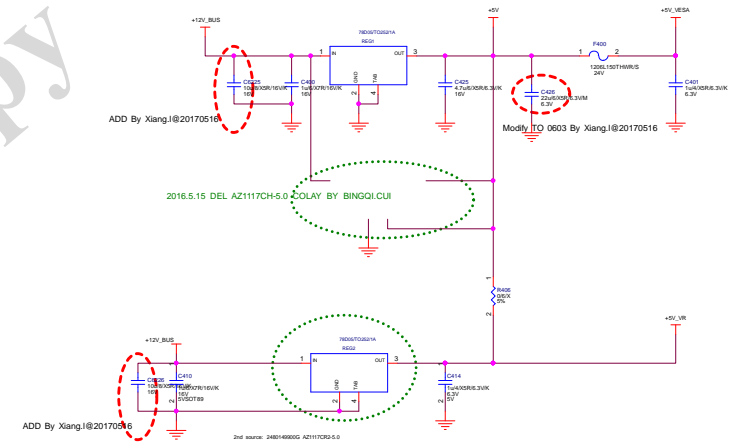
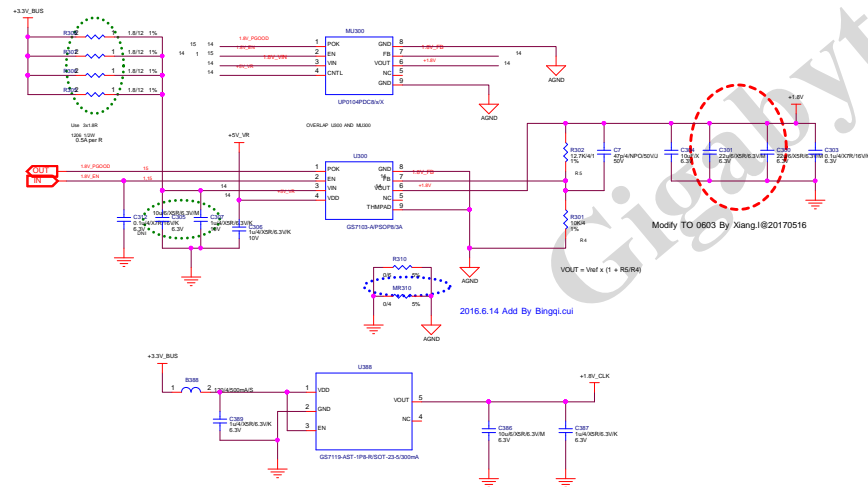


(19) 0.8V



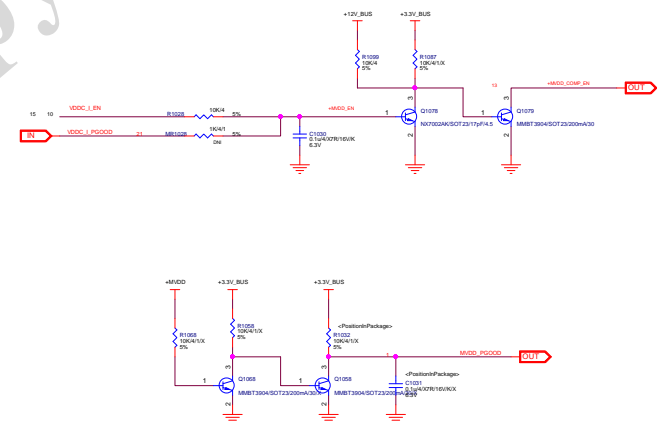
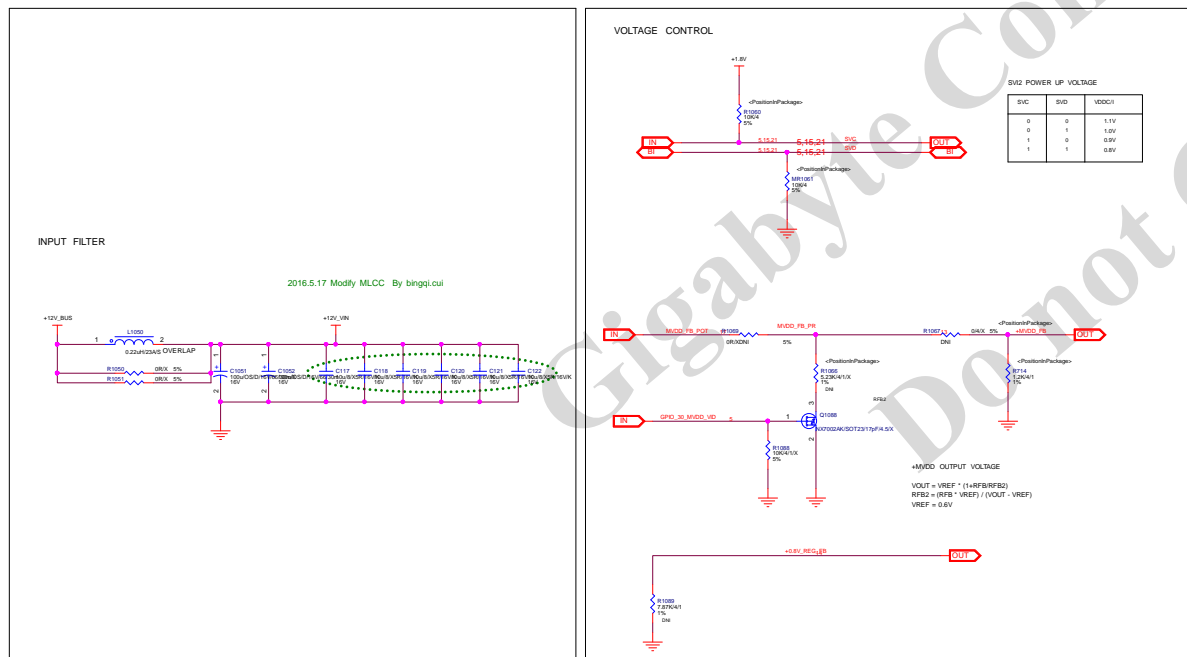
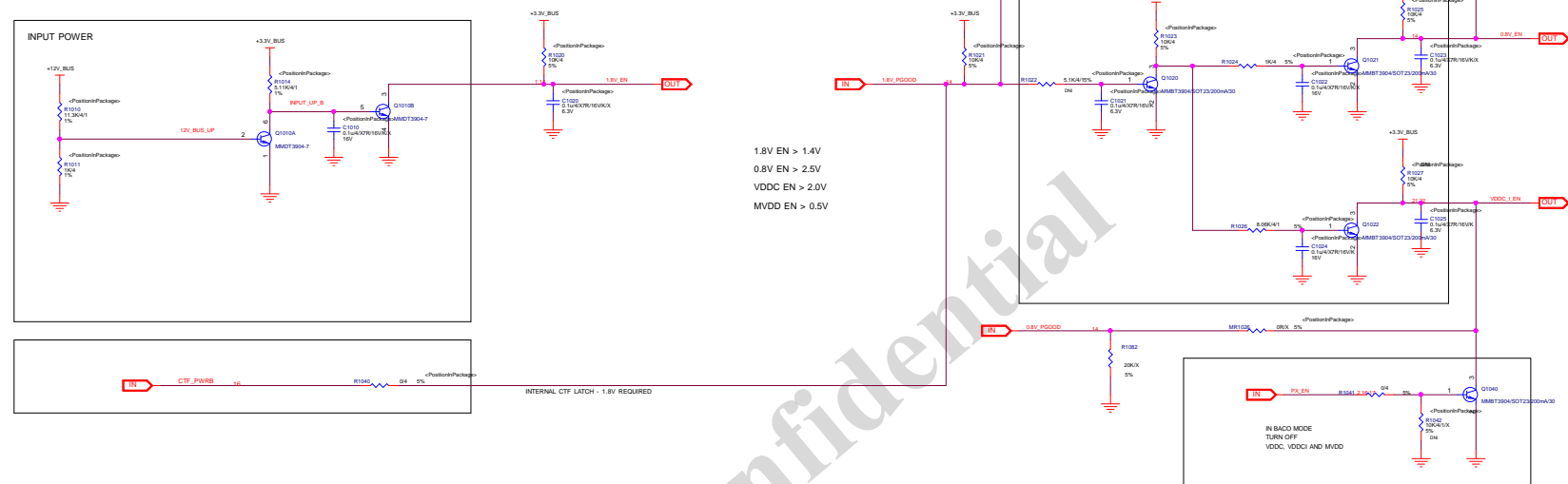
REGULATOR FOR +5V RAILS  
IOUT MAX = 150mA

LDO #1: VIN = 3.0V TO 3.6V MAX VOUT = +1.8V +/- 2% IOUT = 1.3A RMS MAX  
PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING

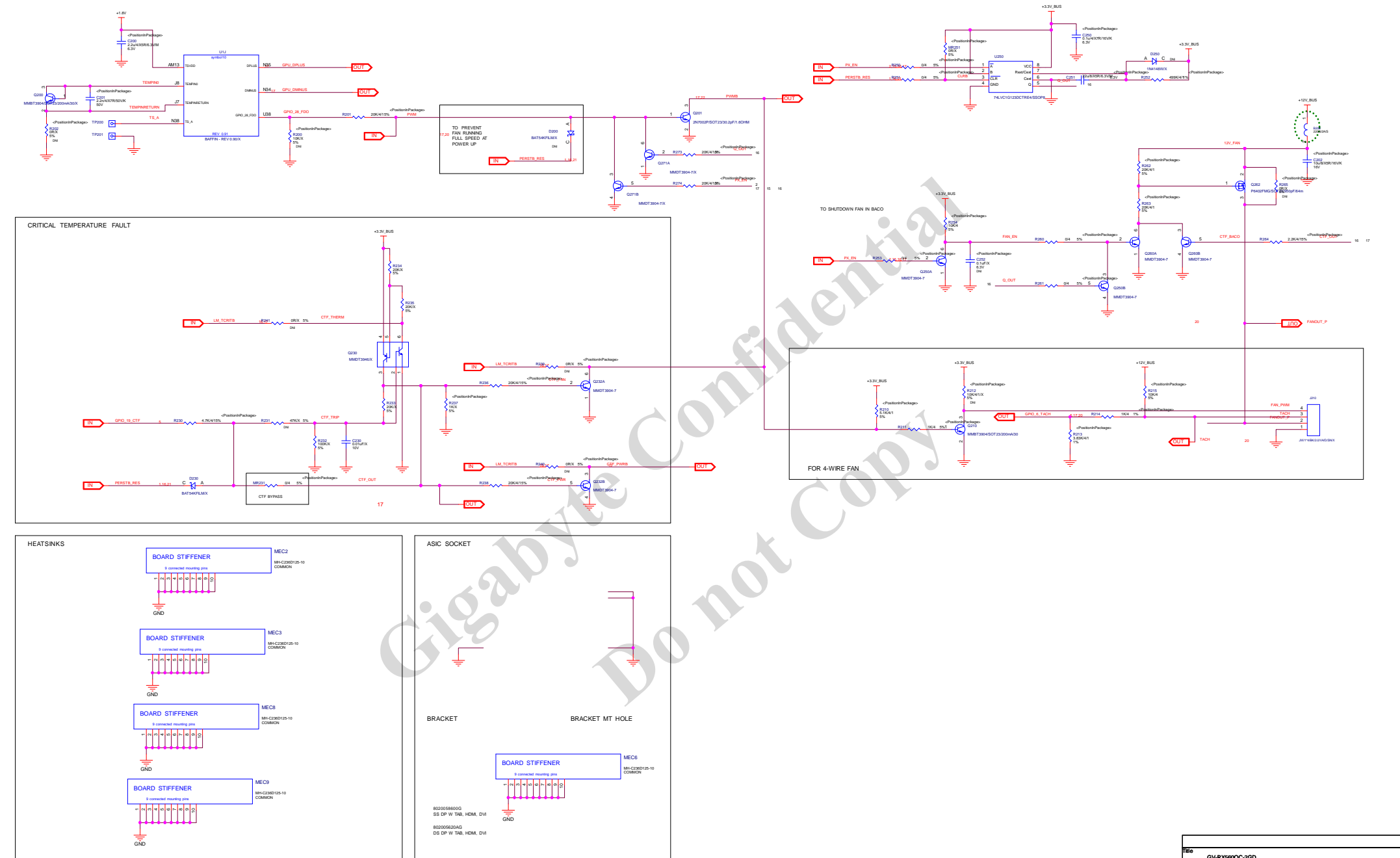


File			GV-RX560C-2GD
Size			Document Number
Custm			0.8V REG
Date:			Sunday, June 25, 2017
Sheet			14 of 22
Rev			1.0

## (16) POWER MANAGEMENT



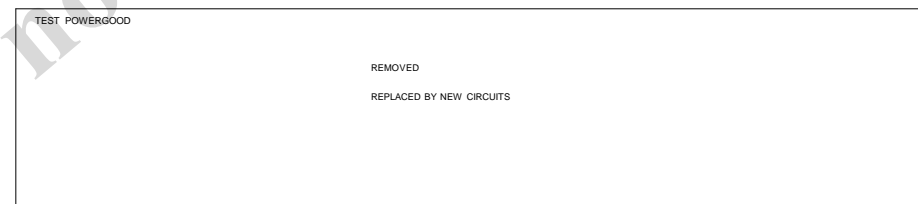
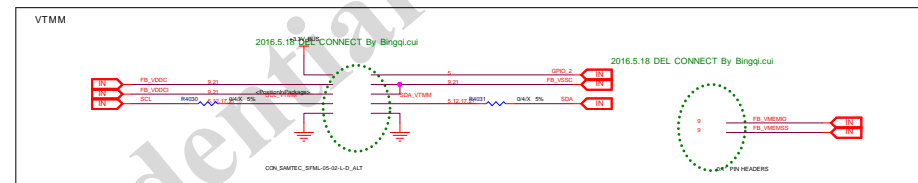
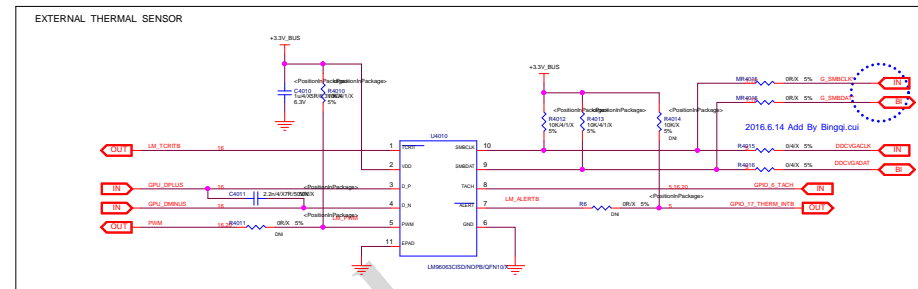
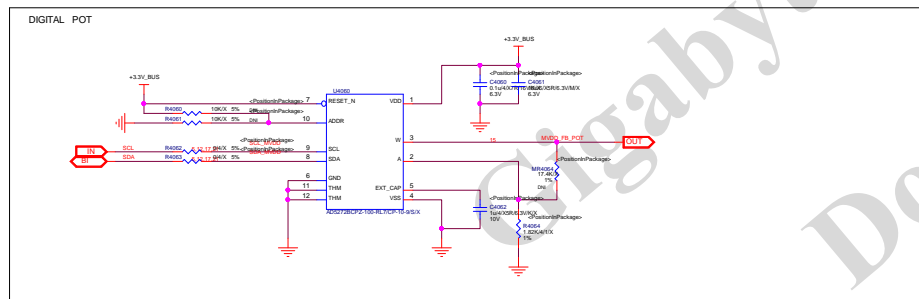
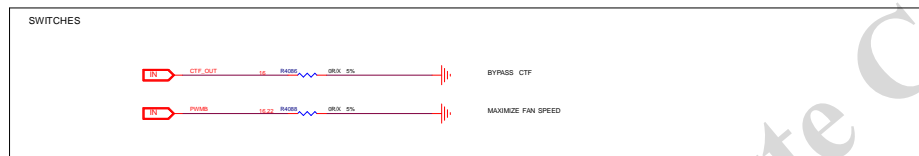
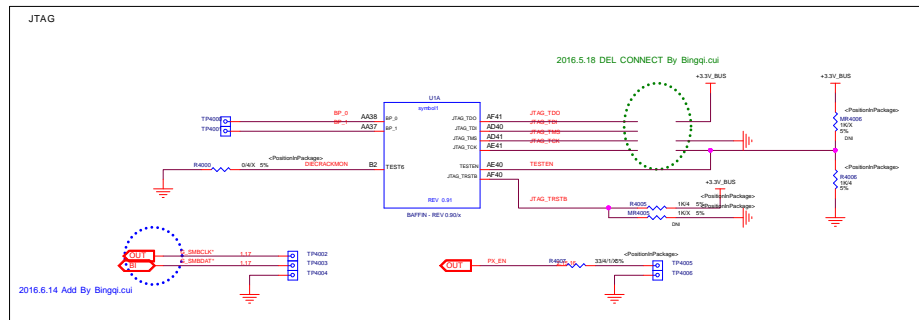
(17) THERMAL MANAGEMENT



Title			
GV-RX560OC-2GD			
Size	Document Number		Rev
Custom	THERMAL MANAGEMENT		1.0
Date:	Sunday, June 25, 2017	Sheet	16 of 22

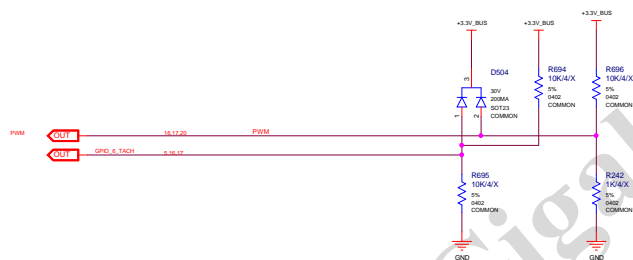
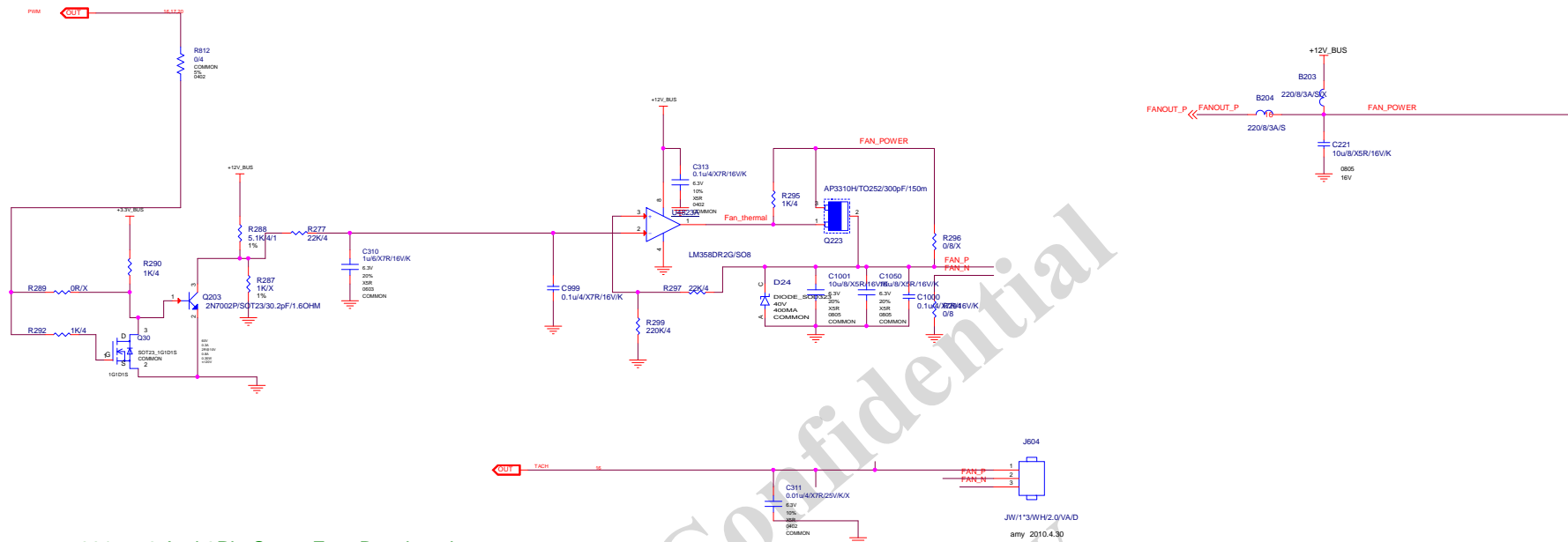


## (18) DEBUG CIRCUITS



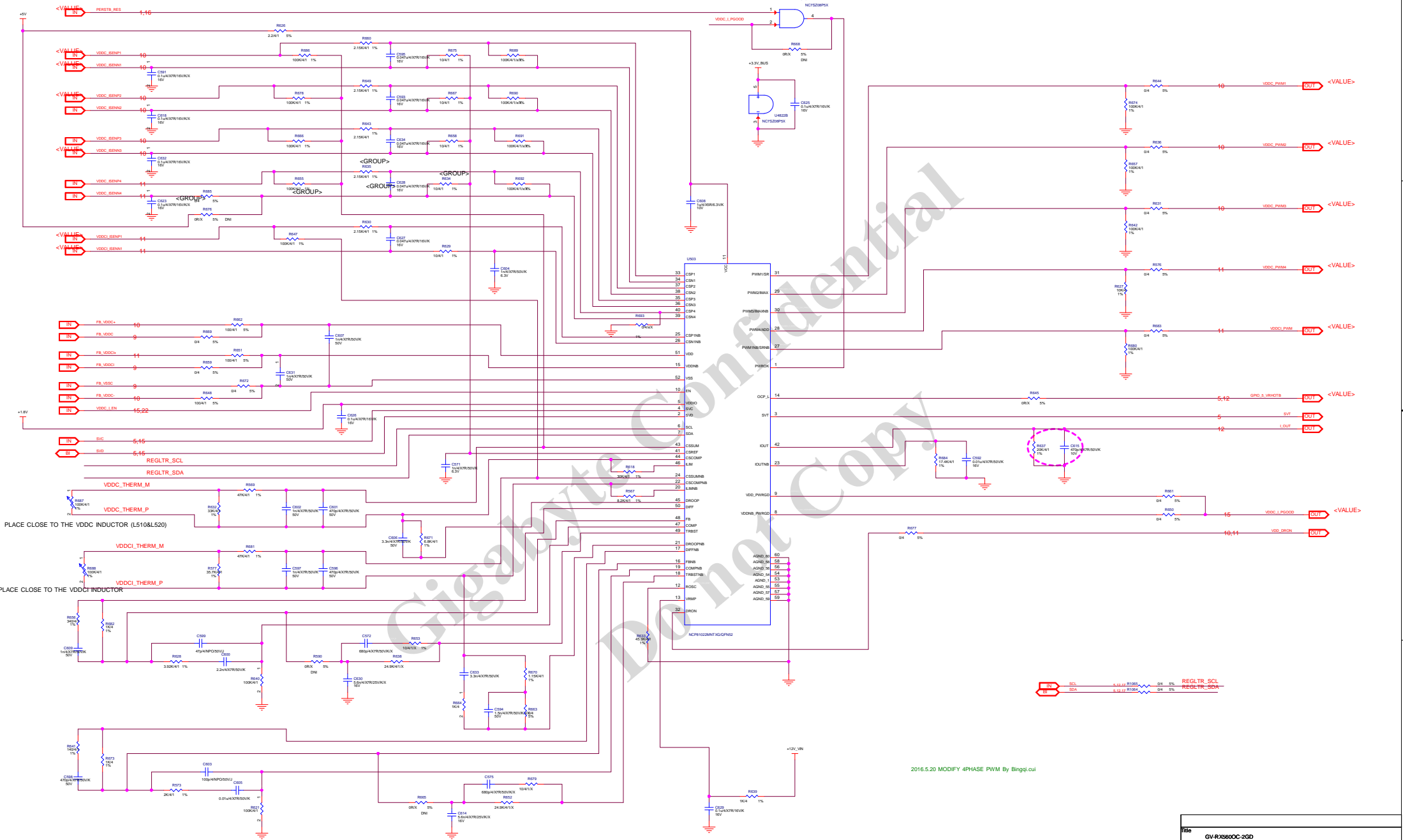


Gigabyte Confidential  
Do not Copy

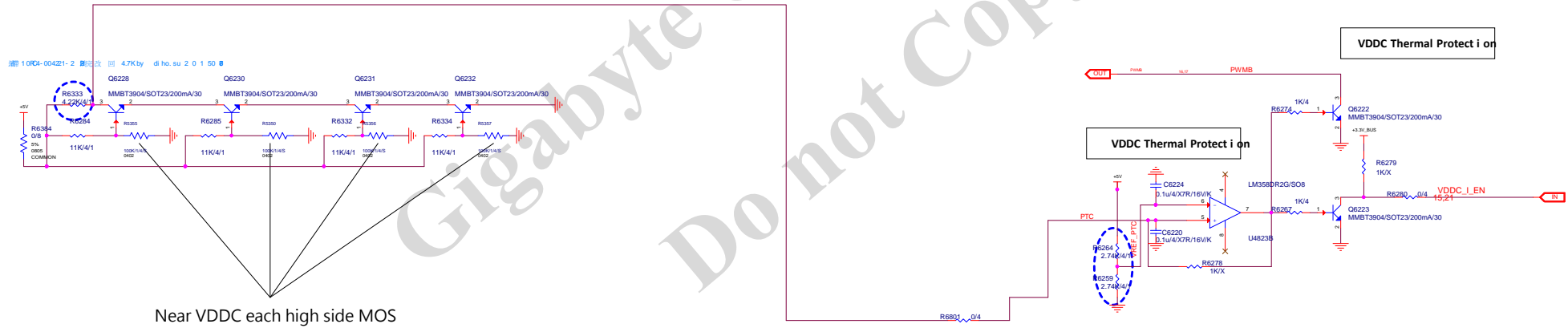


File			GV-RX560C-2GD
Size	Document Number	Rev	
Cuspm	SHEET TITLE	1.0	
Date:	Sunday, June 25, 2017	Sheet	20 of 22

(14) VDDGFX/VDDC CNTL



2016.5.15 Add MOS VRHOT CIRCUIT BY BINGQI.CUI



Near VDDC each high side MOS

Title			GV-RX860C-2GD
Size	Document	Number	Rev
Custm	MOS		1.0
Date			Sunday, June 25, 2017
Sheet			22 of 22