

PG410 A01

GP106 8GB/4GB GDDR5, 256b, 256Mx32/128MX32
Tall DVI-D + DP + DP + HDMI/DP + DP

TABLE OF CONTENTS

Page	Description
1	Table of Contents
2	Block Diagram
3	PCI Express
4	GPU FB_AB
5	MEMORY: FBA[31:0]
6	MEMORY: FBA[63:32]
7	MEMORY: FBB[31:0]
8	MEMORY: FBB[63:32]
9	MEM FB_AB PWR
10	GPU FB_CD
11	MEMORY: FBC[31:0]
12	MEMORY: FBC[63:32]
13	MEMORY: FBD[31:0]
14	MEMORY: FBD[63:32]
15	MEM FB_CD PWR
16	GPU PWR & GND
17	GPU Decoupling
18	IFPAB DVI-D-DL
19	IFPE DP
20	IFPF DP
21	IFPC HDMI/DP
22	IFPD DP
23	MIOA/B Interface and Frame Lock
24	MISC1: Fan, Thermal, JTAG, GPIO, STEREO
25	MISC2: ROM, XTAL, STRAPS

Page	Description
26	PS: 1V8_PLL, 1V8_AON
27	PS: 5V_PEX_VDD
28	PS: NV3V3, NV12V
29	PS: FBVDD
30	PS: NVVDD Controller
31	PS: NVVDD Phase 1,2 & 3
32	PS: NVVDD Rail Balance Login & Dynamic Phase
33	PS: Input, Filtering, and Monitoring
34	PS: 12V Power Steering, PSI Control & LED
35	PS: Shut Down and Sequencing
36	PS: G06 MISC
37	MECH

NVIDIA CORPORATION

©2017 NVIDIA CORPORATION

NOT FOR SALE OR REUSE

MI-776

800-1G410-BASE-100

PS-001

PS-002

PS-003

PS-004

PS-005

PS-006

PS-007

PS-008

PS-009

PS-010

PS-011

PS-012

PS-013

PS-014

PS-015

PS-016

PS-017

PS-018

PS-019

PS-020

PS-021

PS-022

PS-023

PS-024

PS-025

PS-026

PS-027

PS-028

PS-029

PS-030

PS-031

PS-032

PS-033

PS-034

PS-035

PS-036

PS-037

PS-038

PS-039

PS-040

PS-041

PS-042

PS-043

PS-044

PS-045

PS-046

PS-047

PS-048

PS-049

PS-050

PS-051

PS-052

PS-053

PS-054

PS-055

PS-056

PS-057

PS-058

PS-059

PS-060

PS-061

PS-062

PS-063

PS-064

PS-065

PS-066

PS-067

PS-068

PS-069

PS-070

PS-071

PS-072

PS-073

PS-074

PS-075

PS-076

PS-077

PS-078

PS-079

PS-080

PS-081

PS-082

PS-083

PS-084

PS-085

PS-086

PS-087

PS-088

PS-089

PS-090

PS-091

PS-092

PS-093

PS-094

PS-095

PS-096

PS-097

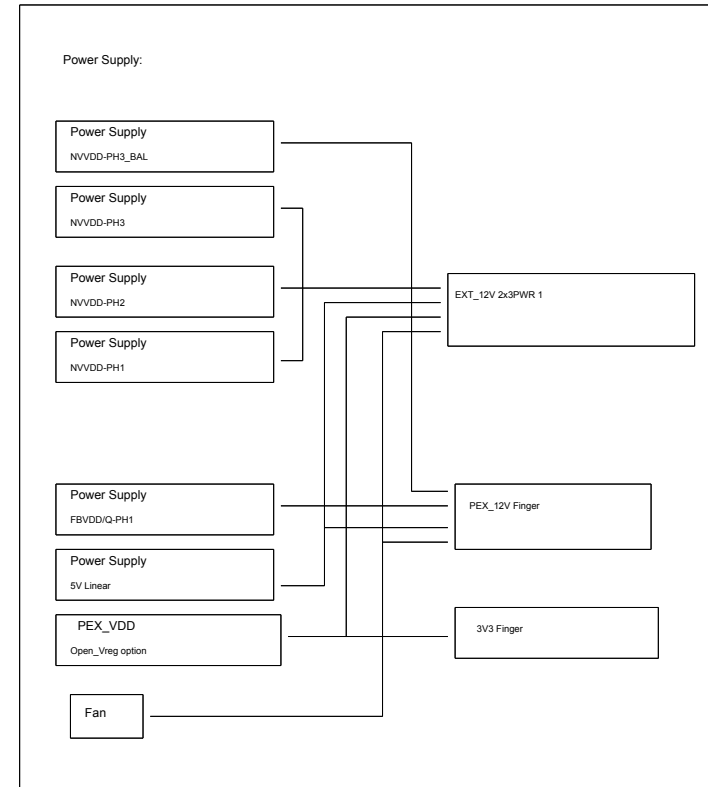
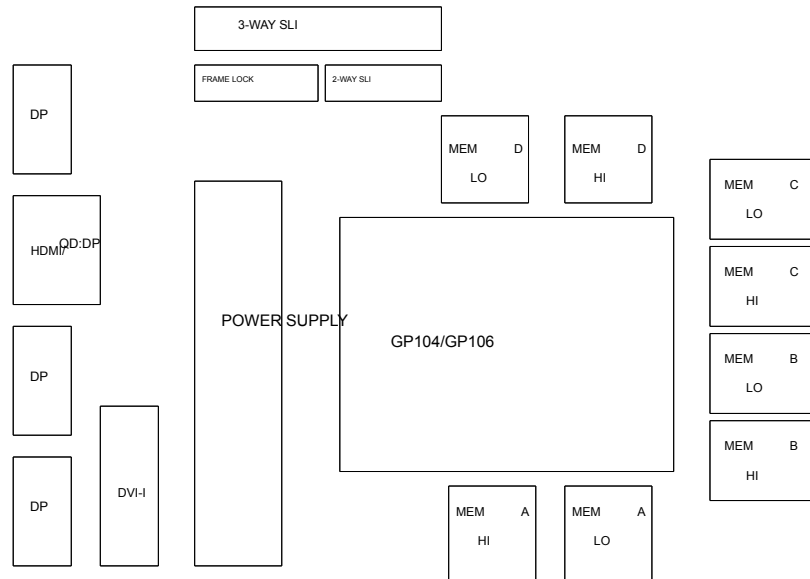
PS-098

PS-099

PS-100



ALL NVIDIA DESIGN SPECIFICATIONS, PERFORMANCE SPECIFICATIONS, REFERENCE WORKS, FILES, DRAWINGS, EXHIBITS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY "MATERIAL") ARE HEREBY PROVIDED TO YOU BY NVIDIA CORPORATION AND ITS AFFILIATES ("NVIDIA") FOR YOUR INFORMATION ONLY. NVIDIA MAKES NO REPRESENTATION OR WARRANTY, EXPRESS OR IMPLIED, REGARDING THE ACCURACY, COMPLETENESS, OR TIMELINESS OF THE MATERIAL. NVIDIA SHALL NOT BE LIABLE FOR ANY DAMAGES, INCLUDING BUT NOT LIMITED TO, DIRECT, INDIRECT, INCIDENTAL, OR CONSEQUENTIAL DAMAGES, ARISING FROM OR OUT OF THE USE OF THE MATERIAL, EVEN IF SUCH DAMAGES ARE CAUSED BY NVIDIA'S NEGLIGENCE. THE MATERIAL IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY KIND, INCLUDING THE WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

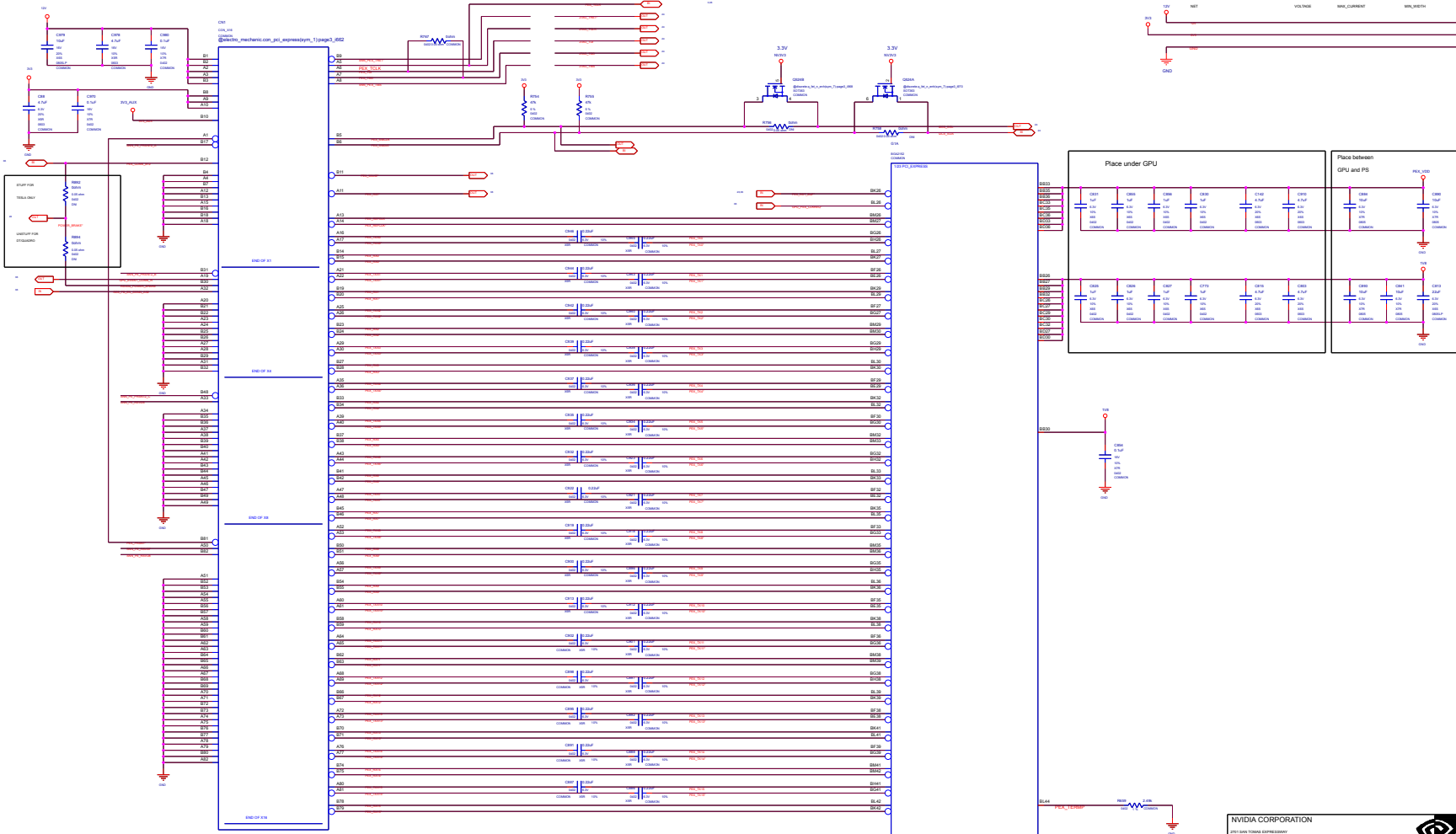


ALL NVIDIA DESIGN SPECIFICATIONS, PERFORMANCE SPECIFICATIONS, REFERENCE DESIGNS, FILES, DOCUMENTS, EVALUATION CODES AND OTHER DOCUMENTS OR INFORMATION TOGETHER AND SEPARATELY, ARE HEREBY PROVIDED AS IS. THE USER'S USE OF SUCH INFORMATION AND ANY RESULTS THEREOF IS AT THE USER'S SOLE RISK. NVIDIA MAKES NO REPRESENTATION OR WARRANTY, EXPRESS OR IMPLIED, REGARDING THE ACCURACY, COMPLETENESS, OR TIMELINESS OF SUCH INFORMATION. NVIDIA MAKES NO REPRESENTATION OR WARRANTY, EXPRESS OR IMPLIED, REGARDING THE ACCURACY, COMPLETENESS, OR TIMELINESS OF SUCH INFORMATION. NVIDIA MAKES NO REPRESENTATION OR WARRANTY, EXPRESS OR IMPLIED, REGARDING THE ACCURACY, COMPLETENESS, OR TIMELINESS OF SUCH INFORMATION.

PROPERTY	PROPERTY DESCRIPTION
POWER SUPPLY	Power Supply


NVIDIA CORPORATION			
2001 LAND VISTA DRIVE			
SAN JOSE, CALIFORNIA 95134, USA			
REV. 001	000-10410-BASE-100	1001	2010-01-01
PCB REV.	PCB REV.	1001	2010-01-01
REV. 001	000-10410-BASE-100	1001	2010-01-01

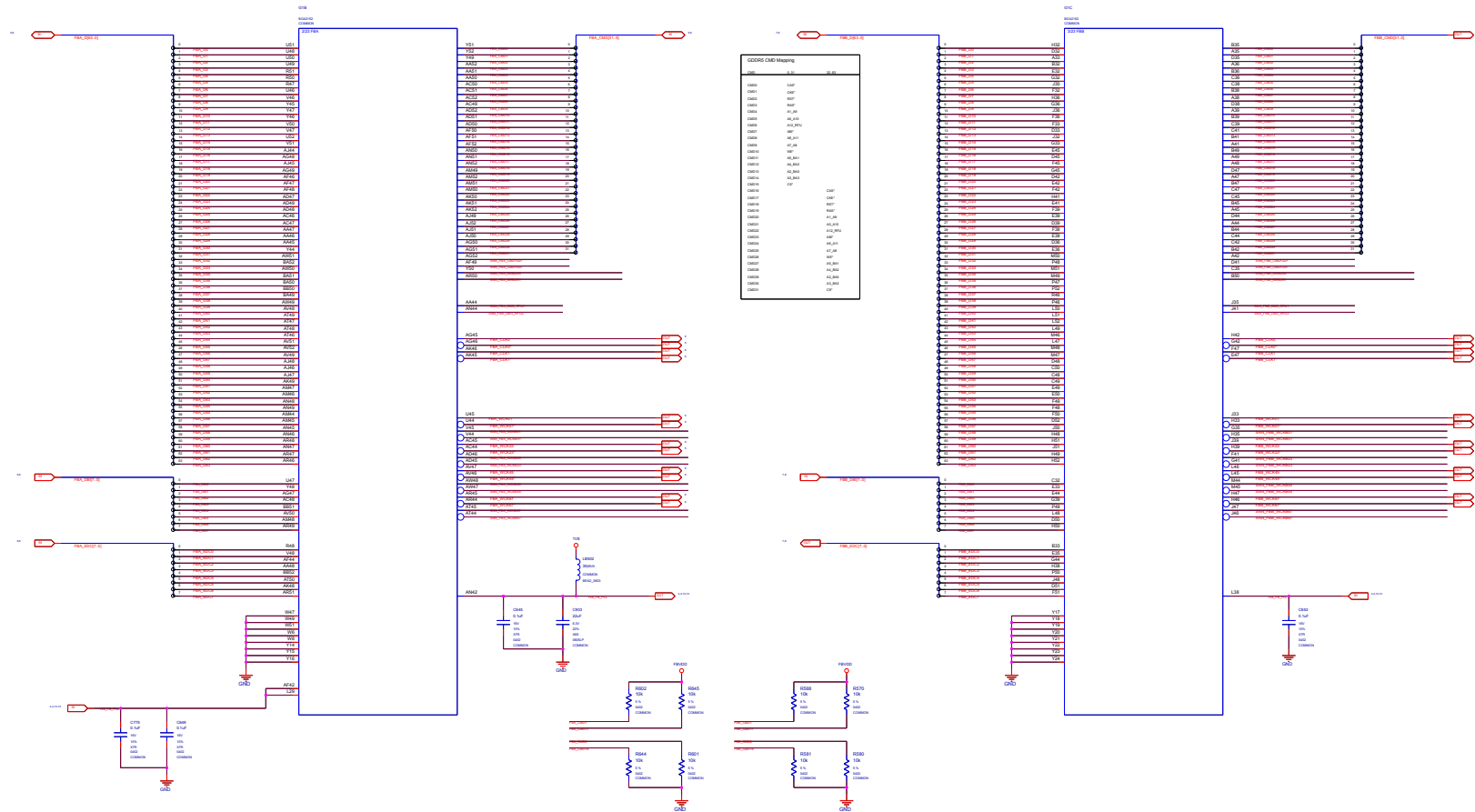




ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGRAMS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL

ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PCI Express

NVIDIA CORPORATION			
3701 SAN TONINO EXPRESSWAY			
SANTA CLARA, CA 95051, USA			
NV_PN		600-1G410-BASE-100	
PCB REV	Part to Add	PAGE	3 OF 3
ISSUE DATE	1/8	ISSUE	7/1/2006 (C001)



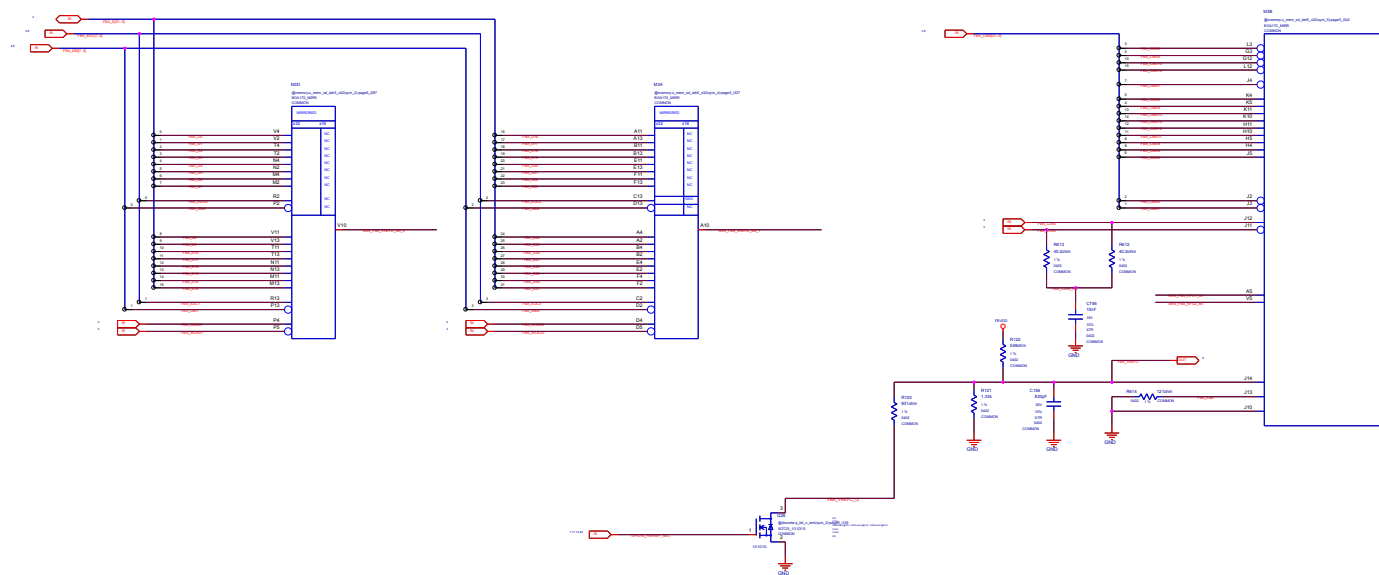
All NVIDIA design specifications, reference specifications, block diagrams, drawings, data sheets, and other documents are incorporated by reference into this document. NVIDIA and its subsidiaries, affiliates, licensors, and other contributors make no representations, warranties, or guarantees, expressed or implied, regarding the accuracy, completeness, or reliability of the information contained herein. NVIDIA and its subsidiaries, affiliates, licensors, and other contributors make no representations, warranties, or guarantees, expressed or implied, regarding the accuracy, completeness, or reliability of the information contained herein. NVIDIA and its subsidiaries, affiliates, licensors, and other contributors make no representations, warranties, or guarantees, expressed or implied, regarding the accuracy, completeness, or reliability of the information contained herein.

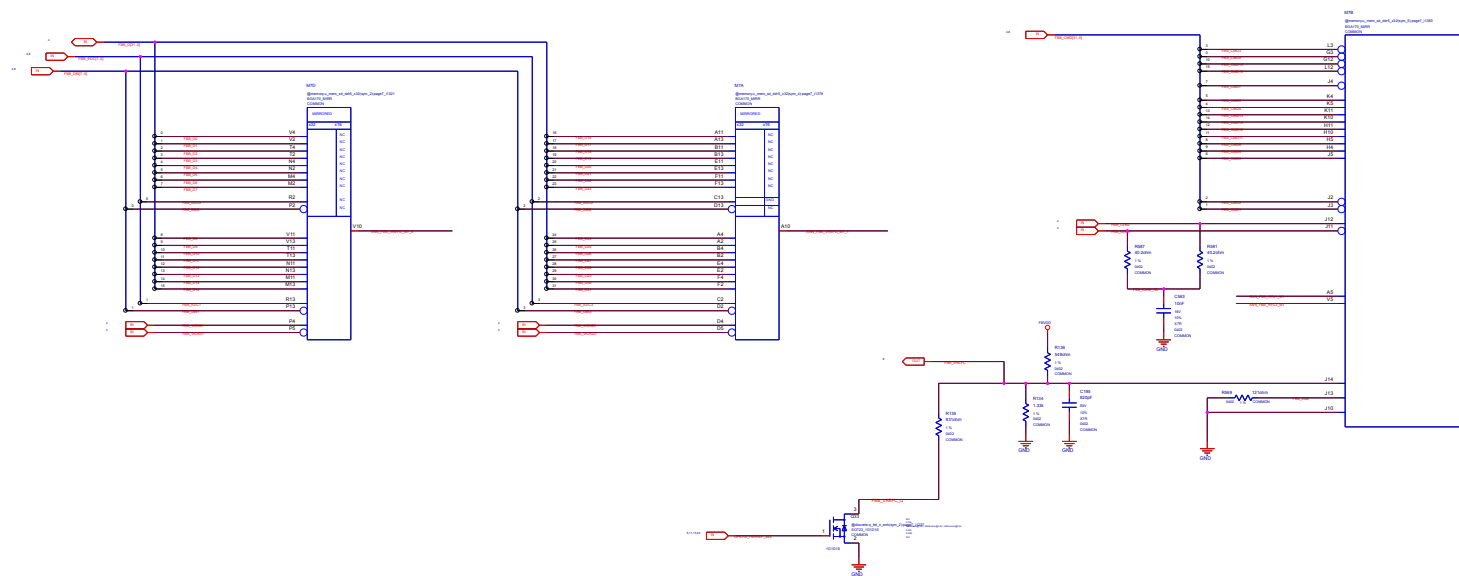
REVISION: 1.0
DATE: 01/10/2020

NVIDIA CORPORATION
9700 BAY FERRY EXPRESSWAY
SANTA CLARA, CA 95051, USA


W_P/N: 800-1G410-BASE-100
P/N: 800-1G410-BASE-100
REV: 1.0

© 2020 NVIDIA CORPORATION

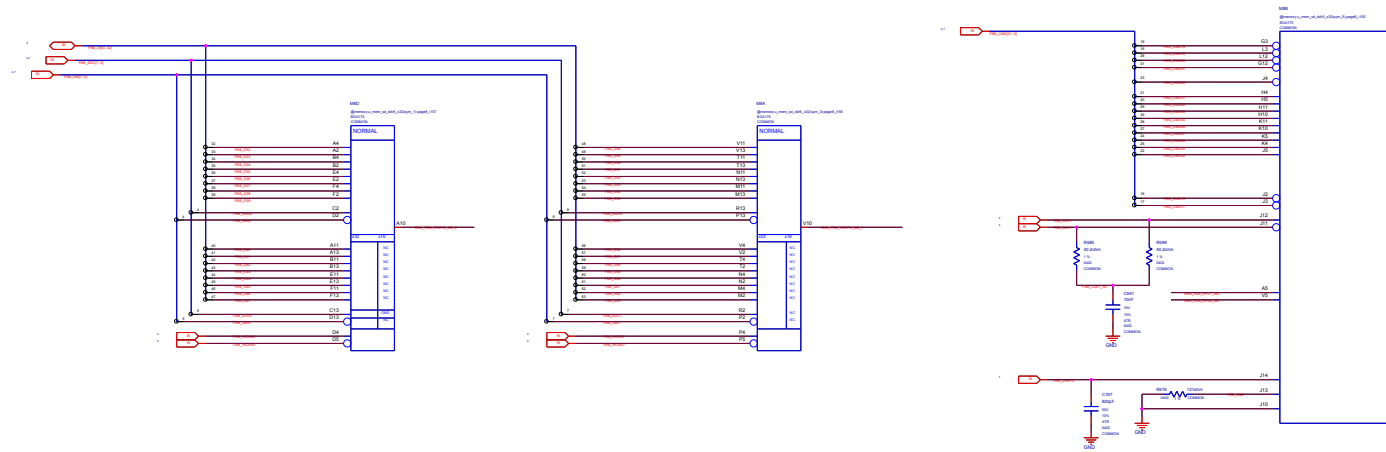




ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	MEMORY PAGE(S)

NVIDIA CORPORATION				
2701 SAN TOMAS EXPRESSWAY				
SANTA CLARA, CA 95050, USA				
NV_PN		600-1G410-BASE-100		
PCB REV	PG410-A21	PAGE	7 OF 20	
BOM REV	A	DATE	21-MAR-2015	

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED TO YOU BY THE MATERIALS BEING PROVIDED TO YOU. THE MATERIALS BEING PROVIDED TO YOU MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DERIVATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL WARRANTIES INCLUDING, WITHOUT LIMITATION, THE MERCHANTABILITY OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.



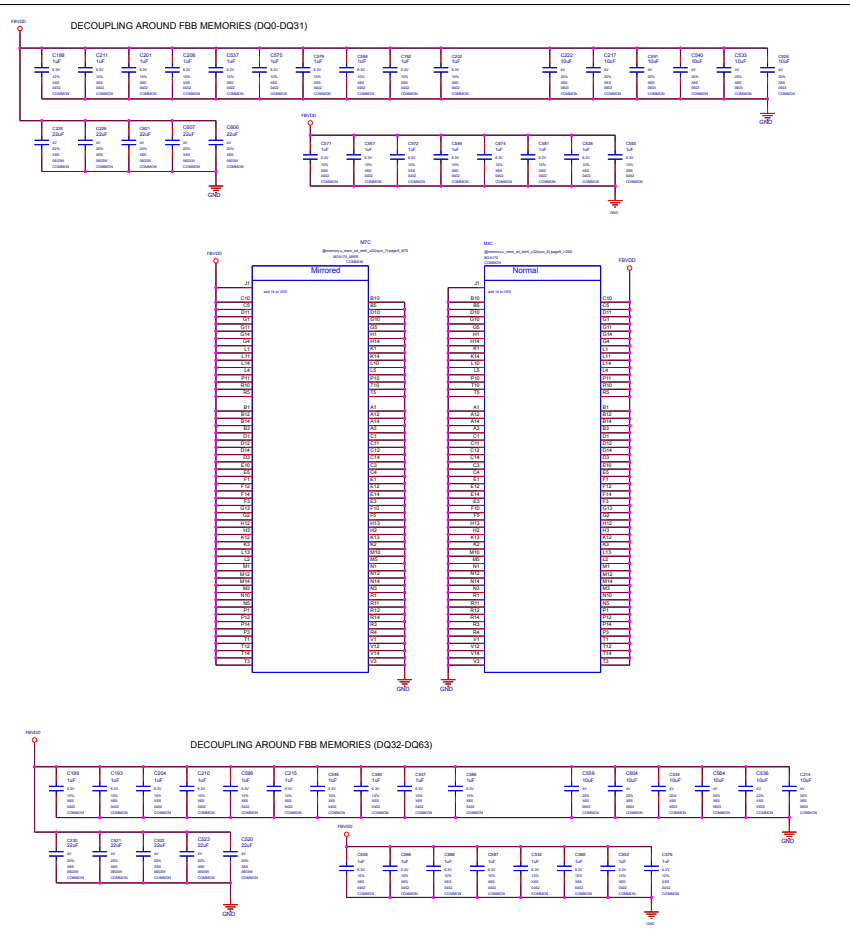
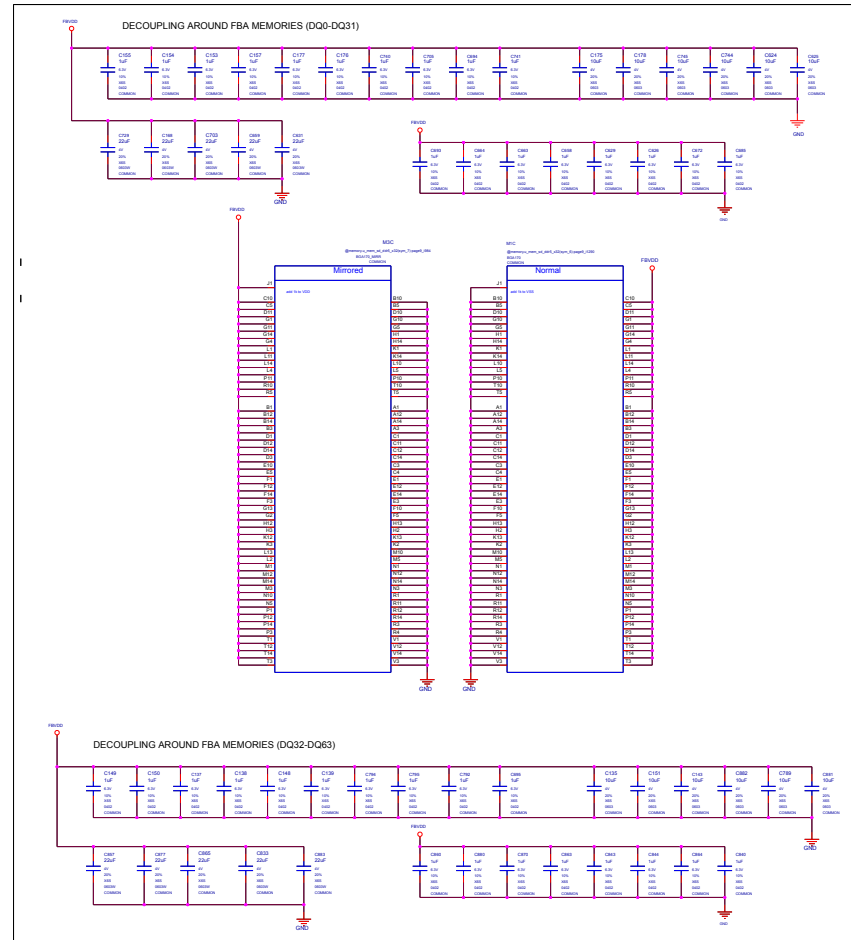
ASSEMBLY	CASSEMBLY DESCRIPTION
PAGE ON TAG	MEMORY PAGE(S)

ALL MEDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE IMAGES, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED TO YOU. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INTELLECTUAL PROPERTY AND SPECIFICATIONS. WEAKNESS AND INFRINGEMENTS, EXPRESSED, IMPLIED, TENTATIVE OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, ARISING FROM A COURSE OF DEALING, TRADE PRACTICE, OR INTELLECTUAL PROPERTY.

NVIDIA CORPORATION



IV_PN	600-1G410-BASE-100		
FOR REV	PCRT0-021	PAGE	8 OF 27
BOM REV	A	DATE	21-03AN-2016

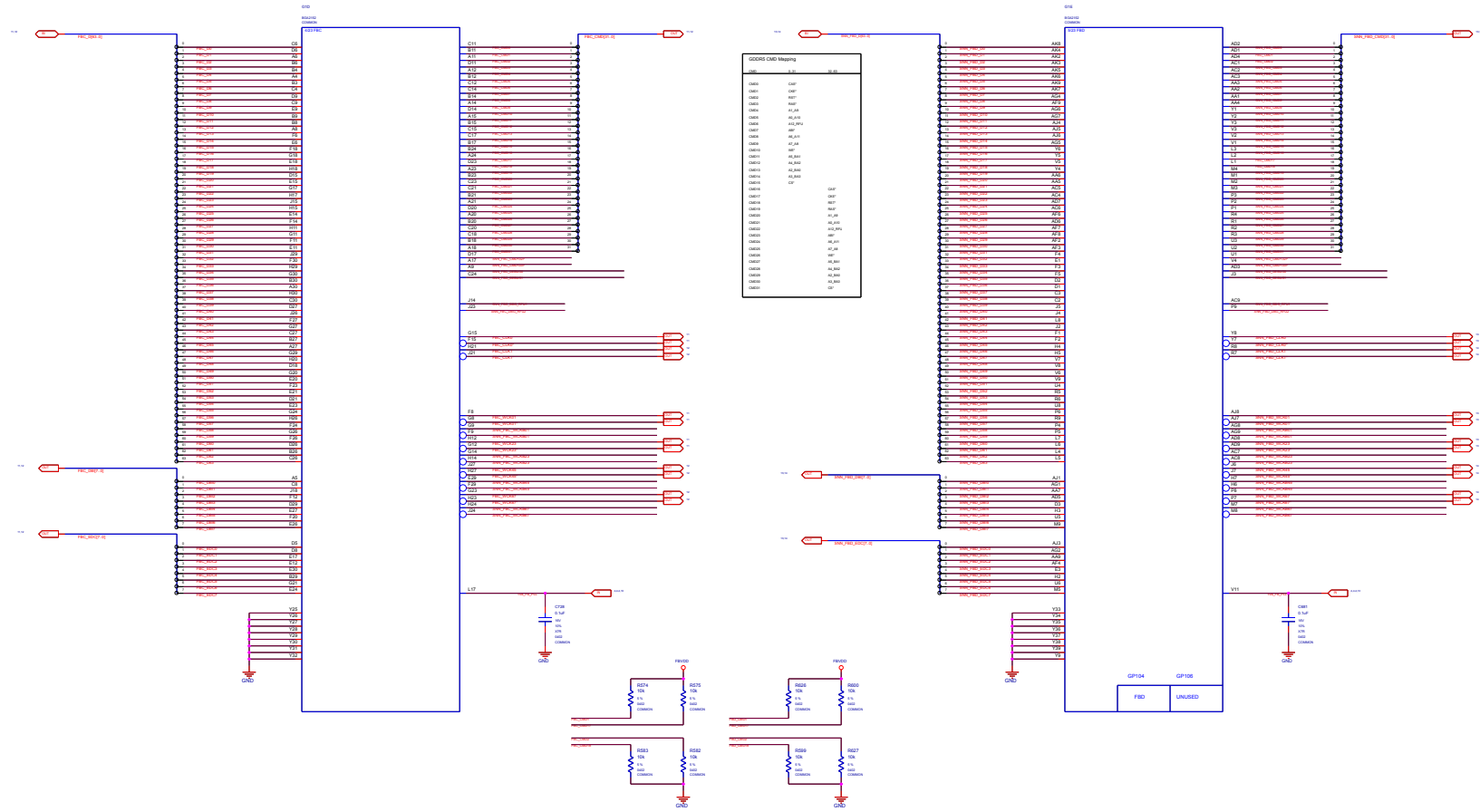


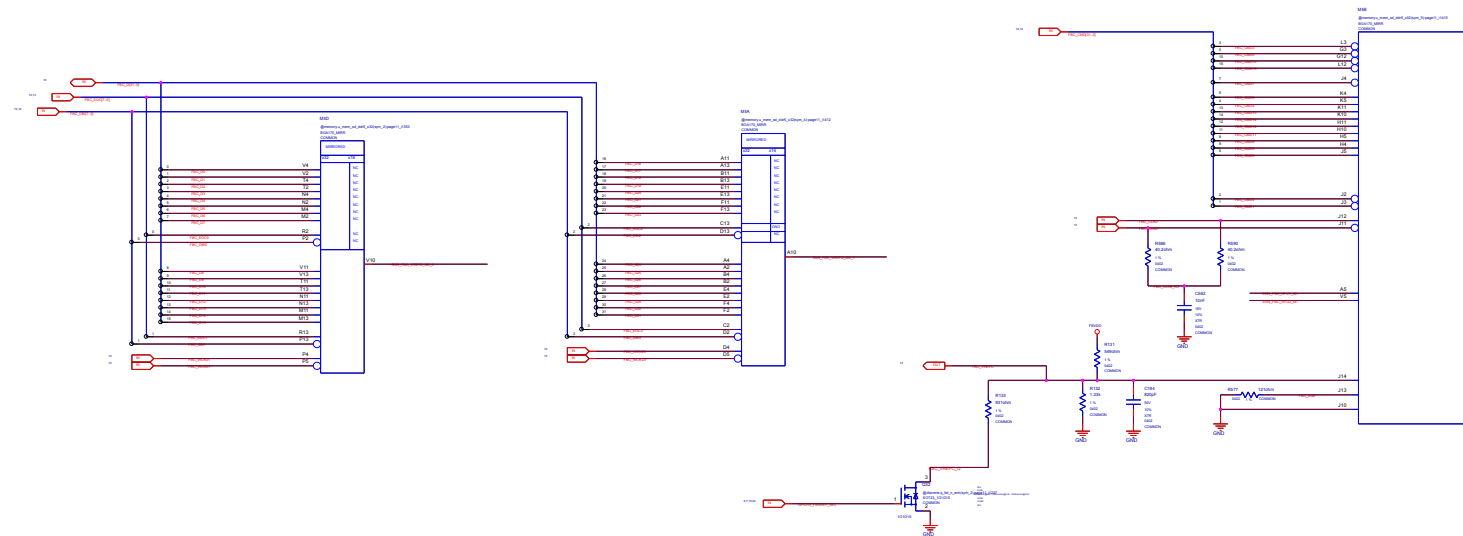
ALL NVIDIA DESIGN SPECIFICATIONS, RECOMMENDED REFERENCE VALUES, DIMENSIONS, ASSEMBLY, AND OTHER DOCUMENTS OR INFORMATION (COLLECTIVELY AND SEPARATELY, "MATERIALS") ARE HEREBY PROVIDED TO YOU BY THE MATERIALS ONLY. NVIDIA MAKES NO REPRESENTATION OR WARRANTY OF ANY KIND, INCLUDING MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT, REGARDLESS OF THE TYPE OF TRANSACTION OR THE TYPE OF TRANSACTION. NVIDIA MAKES NO REPRESENTATION OR WARRANTY OF ANY KIND, INCLUDING MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT, REGARDLESS OF THE TYPE OF TRANSACTION OR THE TYPE OF TRANSACTION. NVIDIA MAKES NO REPRESENTATION OR WARRANTY OF ANY KIND, INCLUDING MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT, REGARDLESS OF THE TYPE OF TRANSACTION OR THE TYPE OF TRANSACTION.

REVISION	DESCRIPTION
1.0	Initial Release

NVIDIA CORPORATION	
1000 AVENUE OF THE ARTS	
SANTA CLARA, CA 95050	
REV. 1.0	600-10410-BASE-100
DATE	11/05/2010







ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE APPLICATIONS, REFERENCE DESIGNS, FILES, DRAWINGS, DIAGRAMS, LISTS AND OTHER DOCUMENTS OR INFORMATION PROVIDED ARE HEREBY INCORPORATED BY REFERENCE INTO THIS DOCUMENT. NVIDIA MAKES NO REPRESENTATION OR WARRANTY WITH RESPECT TO THE MATERIALS OR INFORMATION, AND EXPRESSLY DISCLAIMS ALL WARRANTIES, INCLUDING WITHOUT LIMITATION, THE IMPLIED WARRANTIES OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY CUSTOMS.

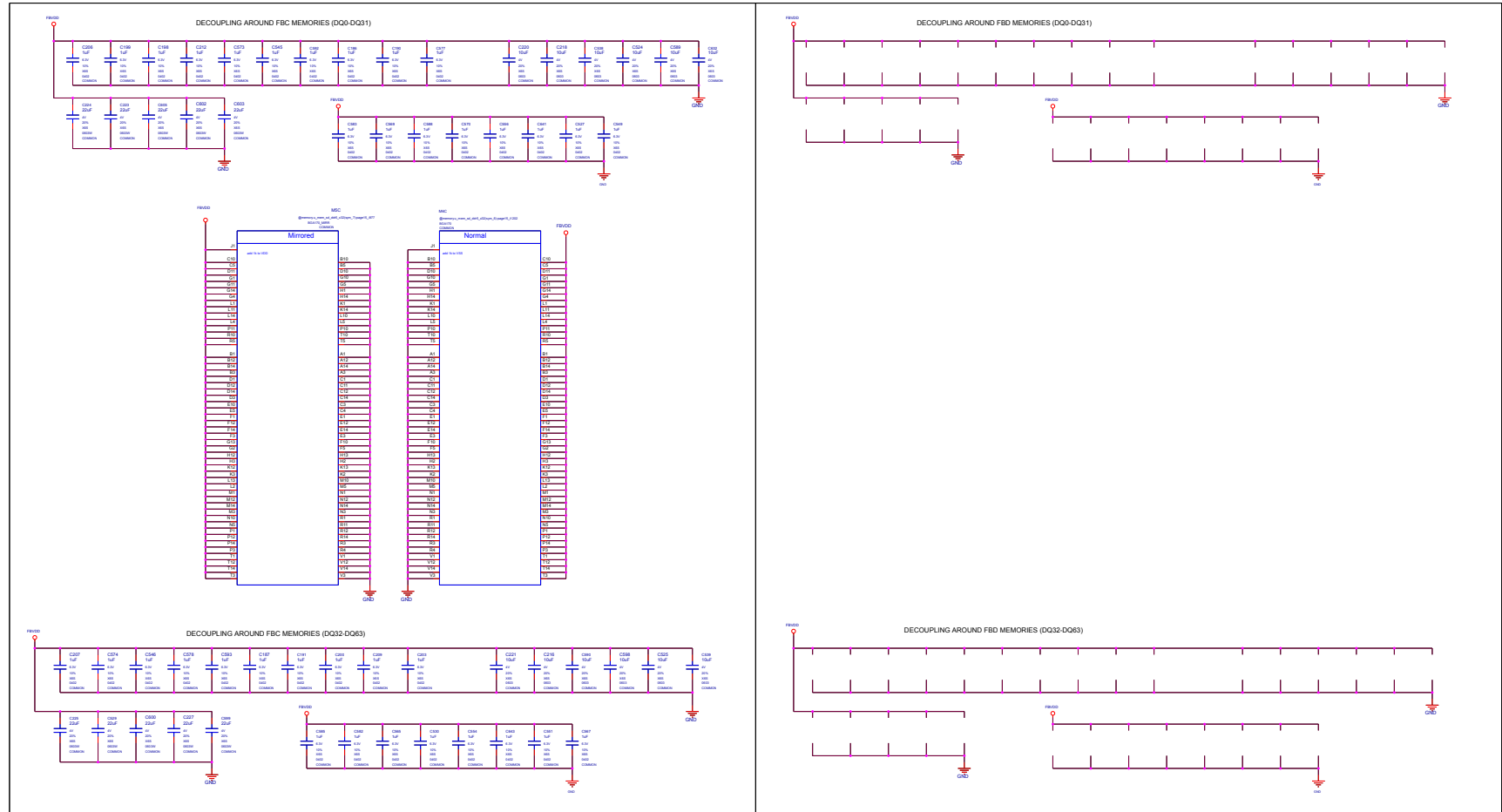
REVISION: 1.0
DATE: 10/27/11

NVIDIA CORPORATION

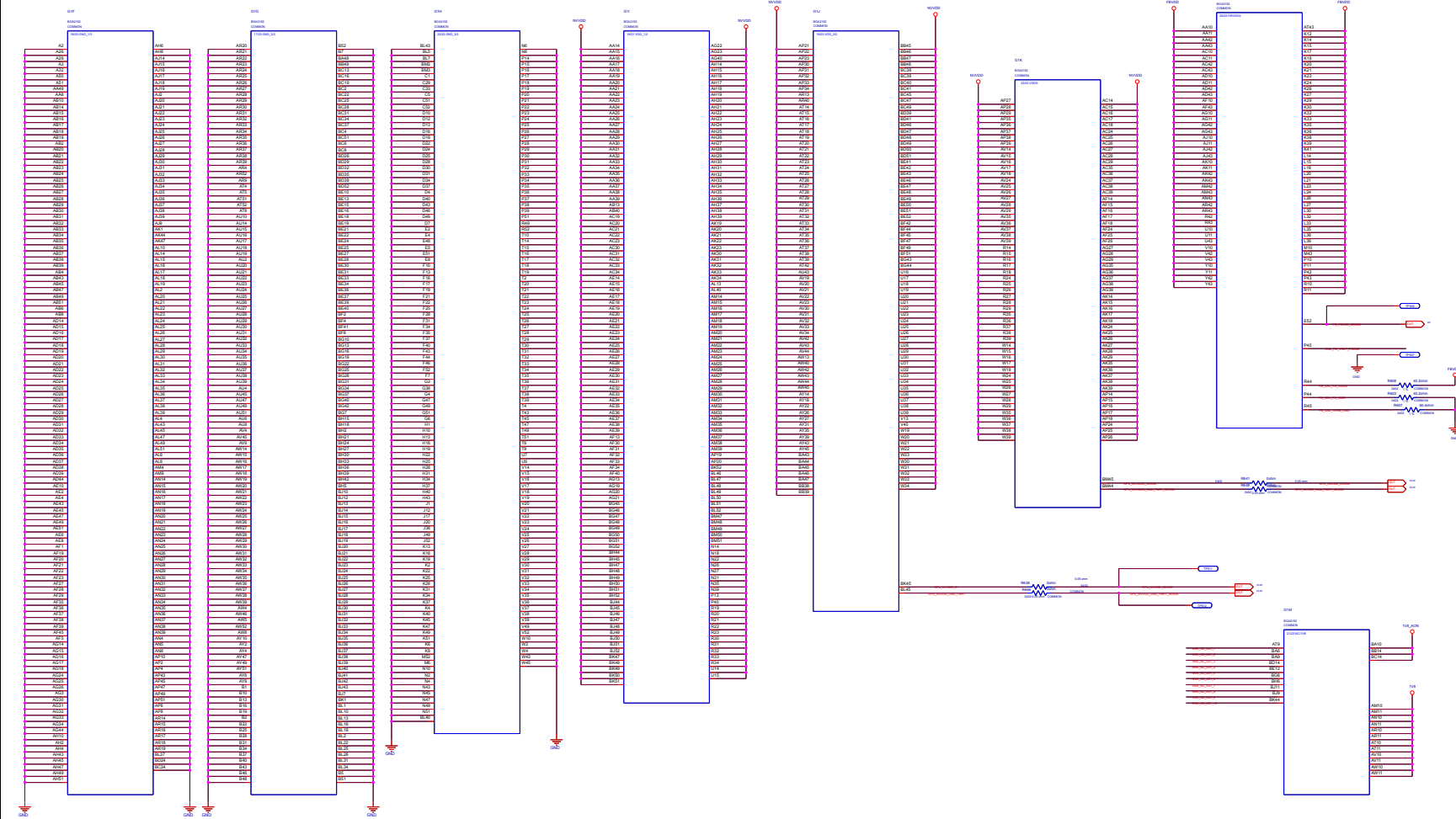
3840 SAN TOME AVE. SANTA CLARA, CA 95051

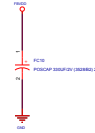



REV	DESCRIPTION	DATE	BY
1.0	Initial Release	10/27/11	11/27/11



Page16: GPU PWR and GND

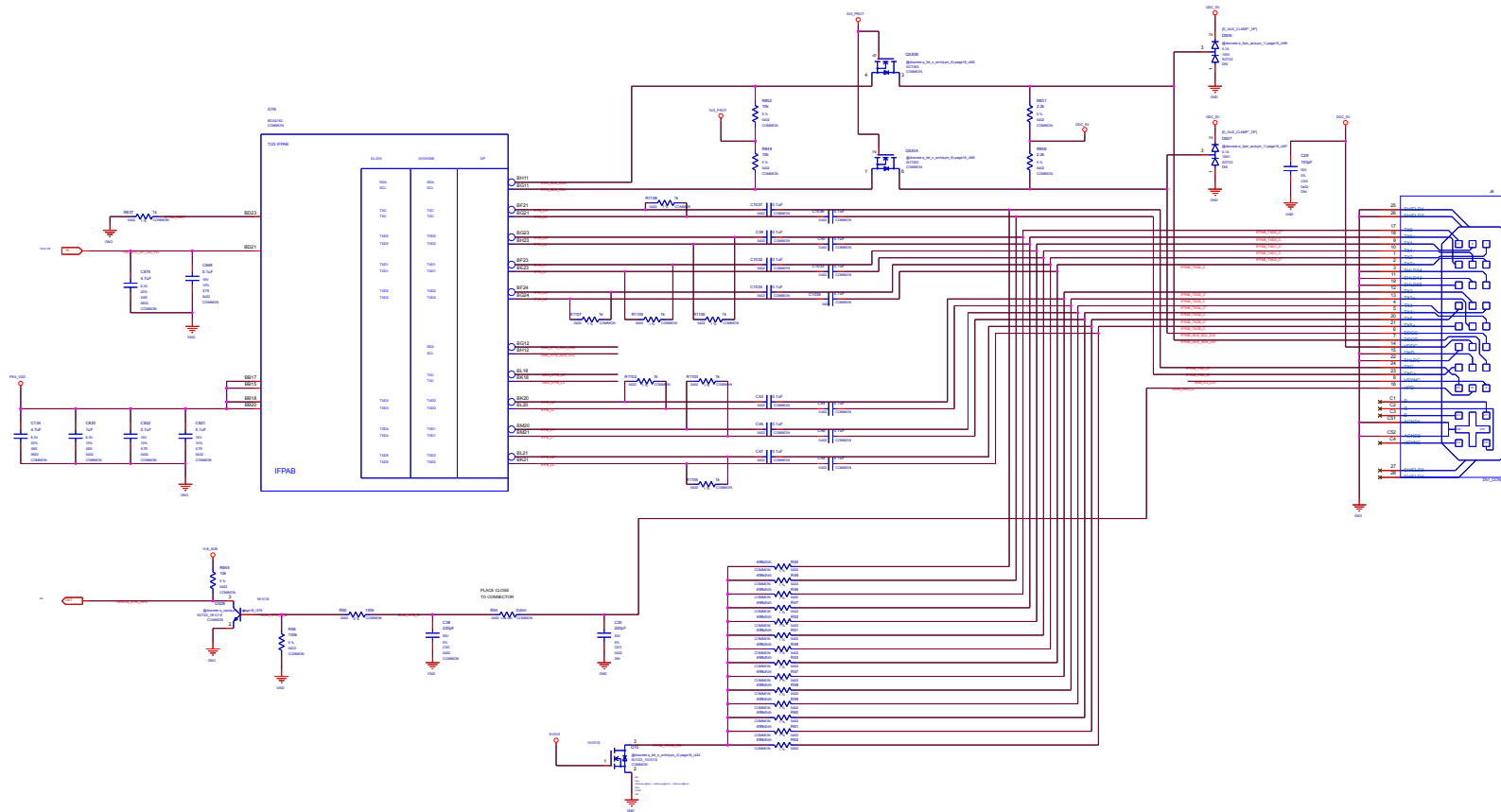




NVIDIA CORPORATION			
3701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95051, USA			
NV_PN		600-1G410-BASE-100	
FCB REV	PG40-AD1	PAGE	17 OF 37
BOM REV	A	DATE	21-MAR-2010



ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGRAMS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VULNERABILITIES OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESS, IMPLIED, STATUTORY OR OTHERWISE WITH REGARD TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.



ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PPR# ENVD-CL

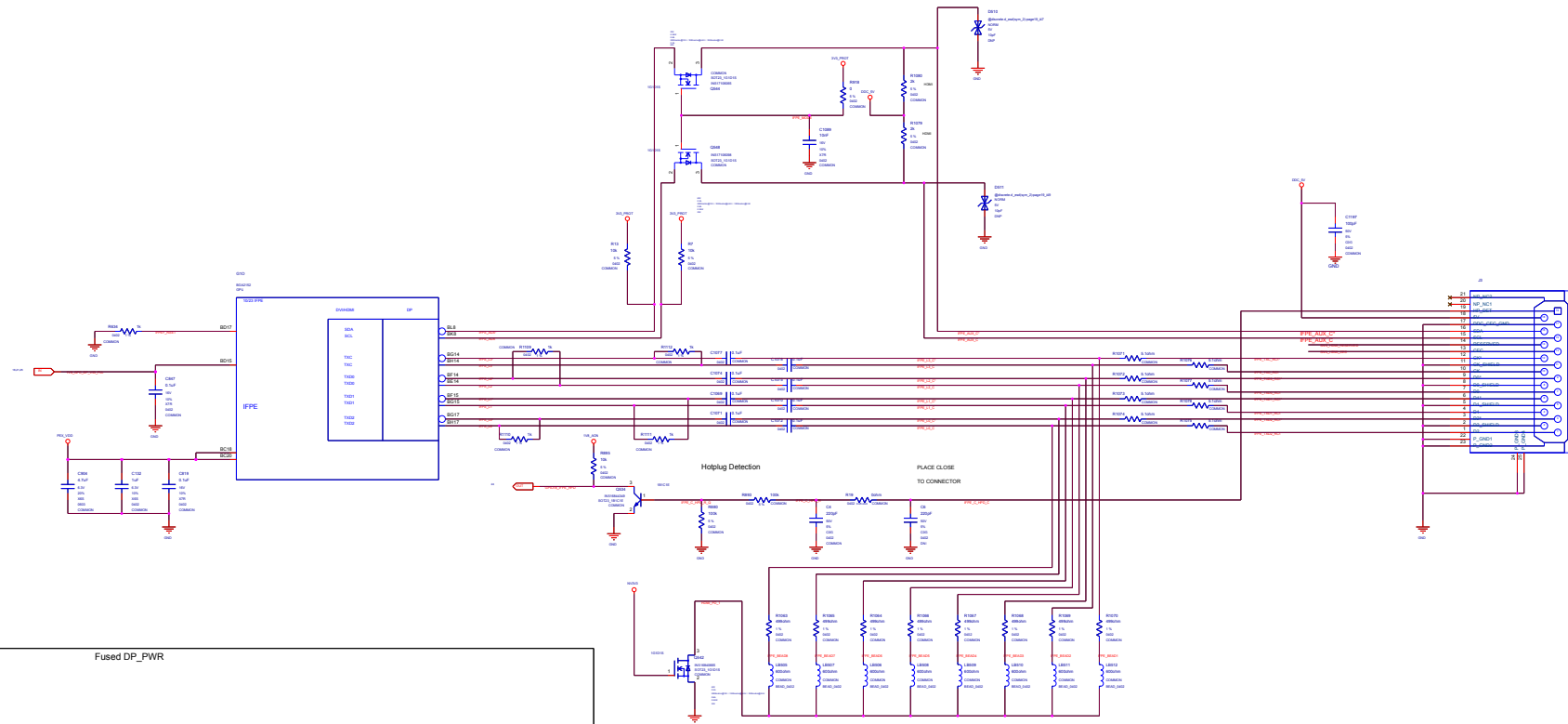
NVIDIA CORPORATION



NV_PN	600-1G410-BASE-100
-------	--------------------

PCB REV	PURVIS-A20	PAGE	18 OF 37
SCM REV	A	DATE	21-MAR-2016

1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through



Fused DP_PWR

Place Bulk CNP close to connector.

ASSEMBLY	ASSEMBLY DESCRIPTION
PAGE TOTAL	OFFER

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOMs, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY "MATERIALS") ARE BEING PROVIDED "AS IS" BY NVIDIA WITHOUT WARRANTY OF ANY KIND, INCLUDING WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

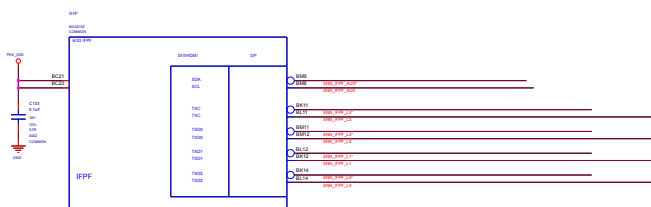
NVIDIA CORPORATION

2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA



NV_PN	600-1G410-BASE-100		
PCB REV	PG410-AD1	PAGE	19 OF 27
BCM REV	A	DATE	21-0001-2016

1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through



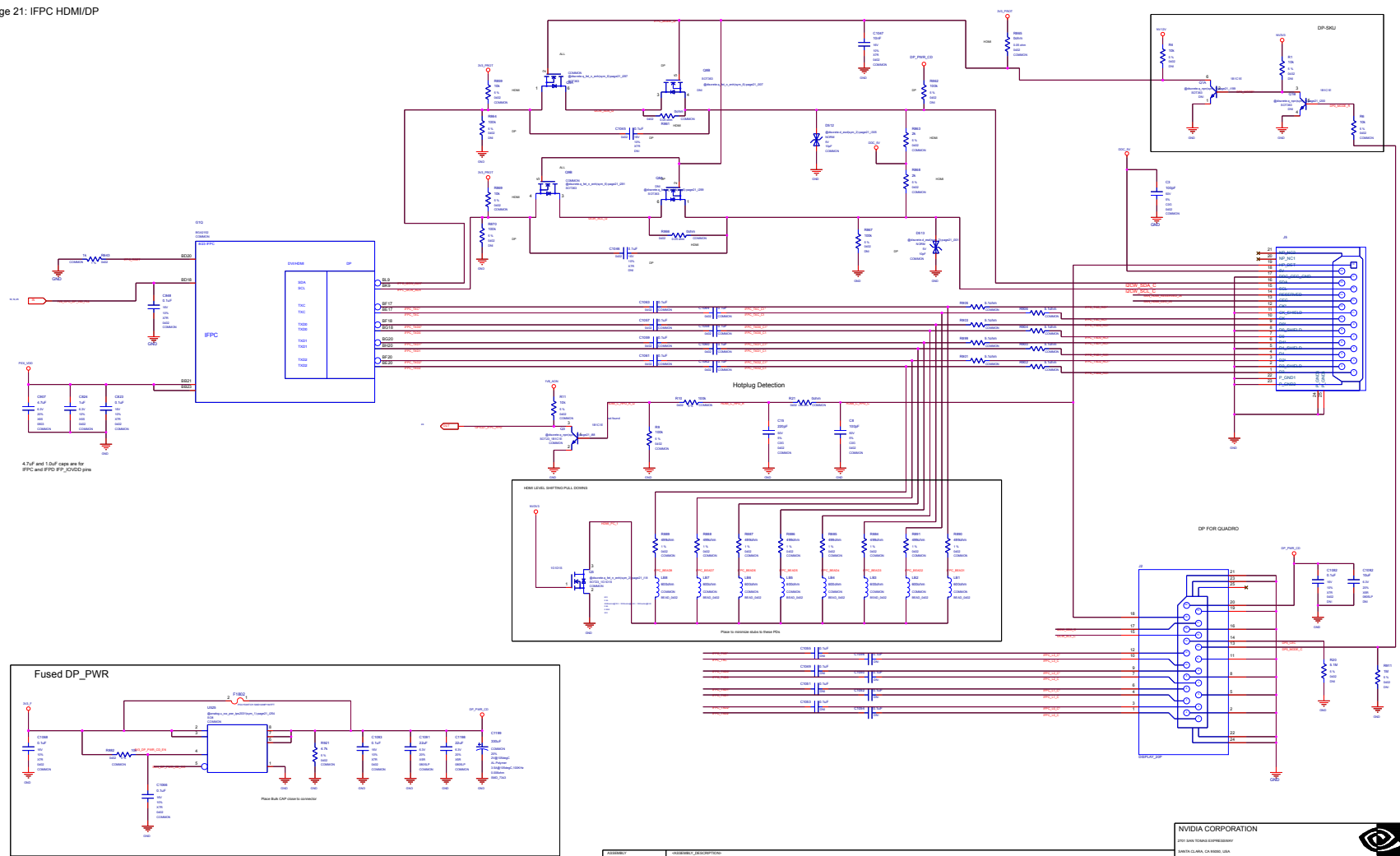
ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	IFT (P)

NVIDIA CORPORATION

2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA




NV_PN	600-1G410-BASE-100		
PCB REV	PGW10-A21	PAGE	20 OF 2
BOM REV	A	DATE	21-MAY-2016

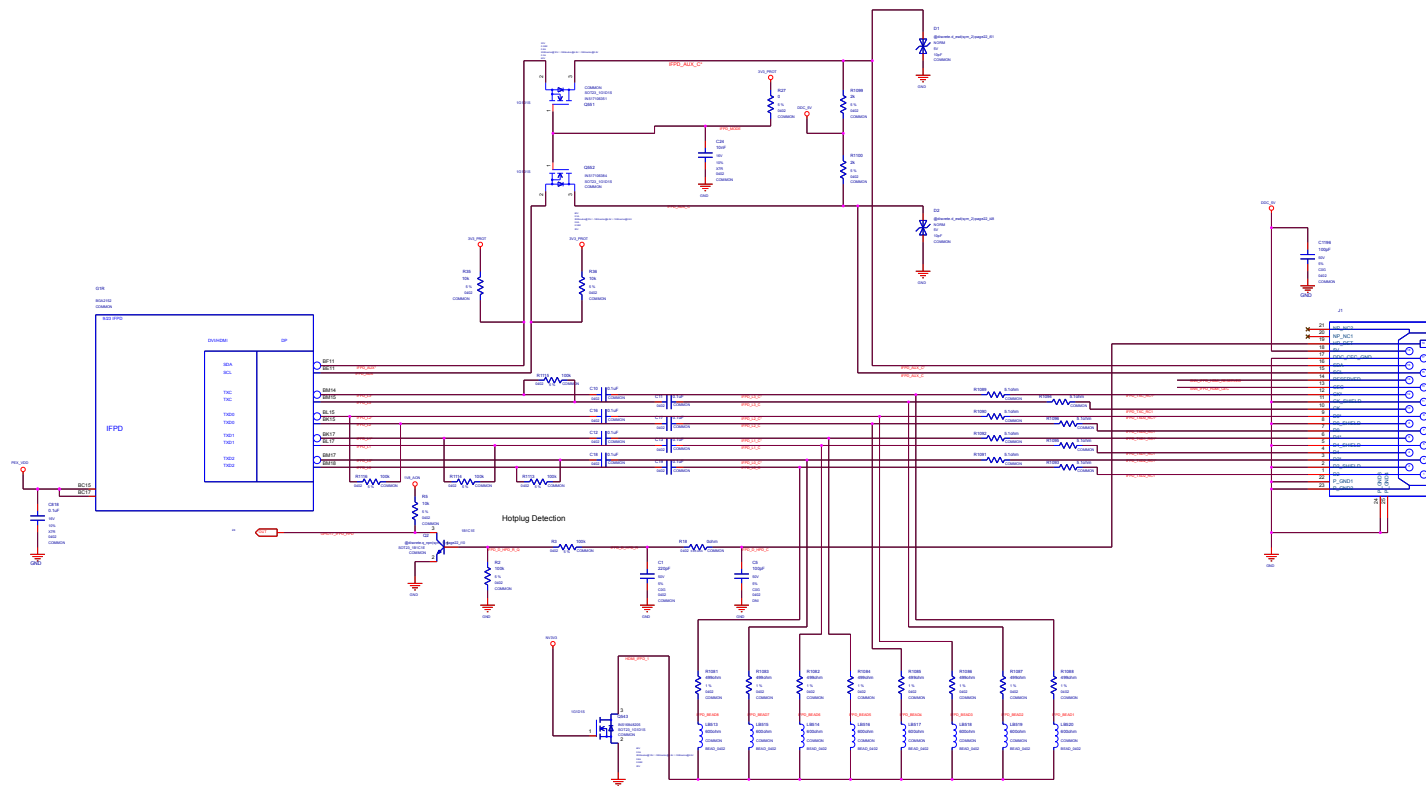


ALL NIVIDA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE SCHEMES, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NIVIDA MAKES NO WARRANTIES, EXPRESS, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	ASSEMBLY_DESCRIPTOR
PAGE DETAIL	BPC_HEADER


NVIDIA CORPORATION			
3701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95051, USA			
NV_PN	800-1G410-BASE-100		
FCB REV	PG410-AD1	PAGE	21 OF 27
BCR REV	A	DATE	21-MAR-2010

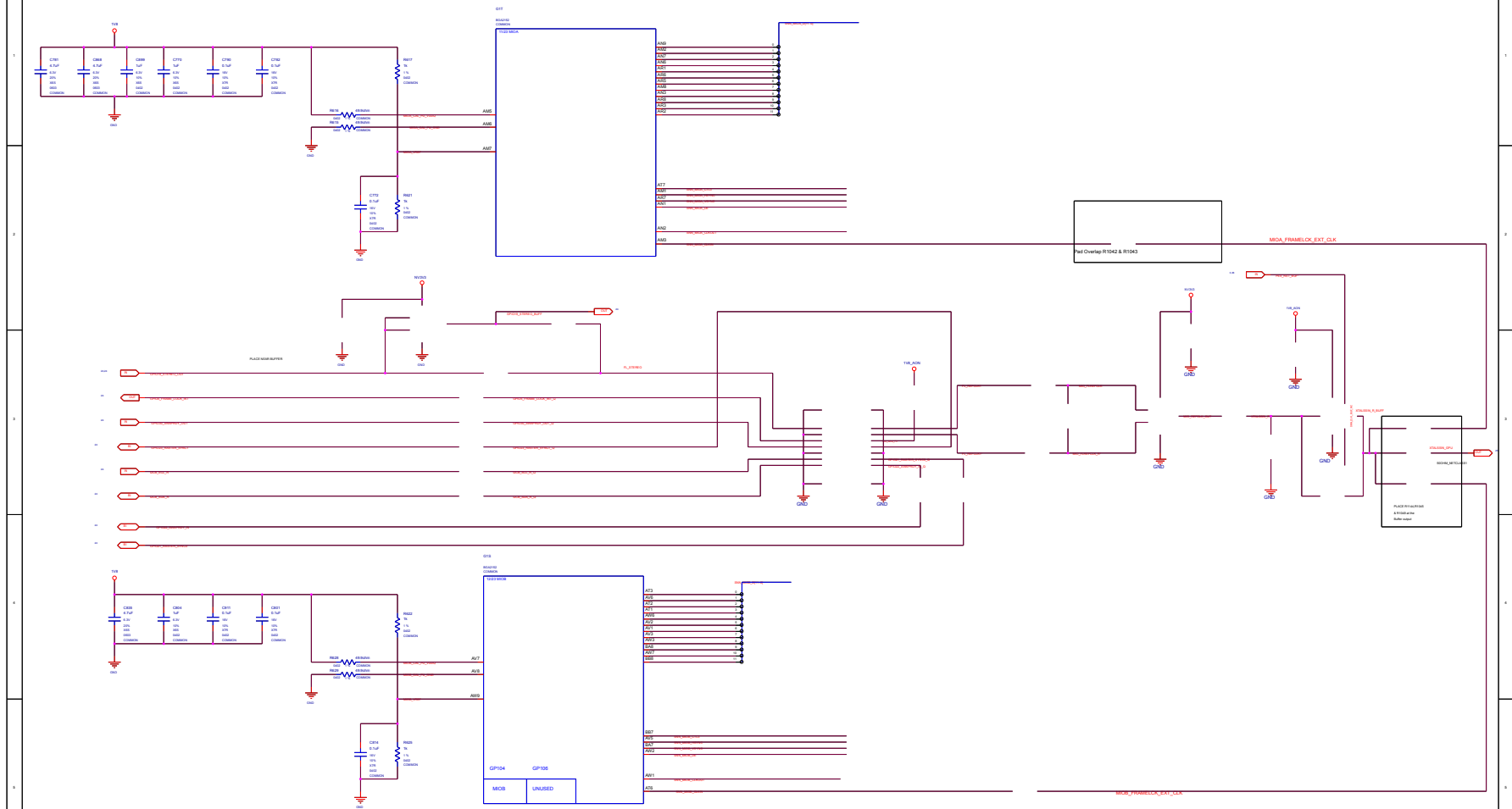
1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through




ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PFD EP

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS". THE MATERIALS MAY CONTAIN ERRORS AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INTELLECTUAL PROPERTY AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.


NVIDIA CORPORATION			
3701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95050, USA			
NV_PN		600-1G410-BASE-100	
FOR REV	PGT0-001	FNCL	22 OF 27
DOM REV	A	DATE	21-MAR-2016



ASSEMBLY	ASSEMBLY DESCRIPTION
PAGE DETAIL	MECHANICAL AND FRAME LAYOUT

NVIDIA CORPORATION			
3701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95050, USA			
NV_PN		600-1G410-BASE-100	
PCB REV	HW/REV A01	FRS#	23 OF 37
BCM REV	A	DATE	21-SEAN-2016

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE SAMPLES, FILES, DRAWINGS, DIAGNOSTICS, LISTS, AND OTHER DOCUMENTS OR INFORMATION, TOGETHER WITH AND SEPARATELY, MATERIALS ARE PROVIDED "AS IS." THE MATERIALS MAY CONTAIN DESIGN AND/OR FUNCTION VIOLATIONS OR VIOLATIONS OF INTELLECTUAL PROPERTY STANDARDS AND NVIDIA MAKES NO WARRANTIES, EXPRESS, IMPLIED, STATUTORY OR OTHERWISE, WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ANY IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA			
NV_PN	600-1G410-BASE-100		
PCB REV	PG410-AJ21	PROD	24 SEP '07
ROM REV	A	URDN	21-MAR-2010



STRAP2	STRAP1	STRAP0	RAMCFQ[4:0]
L	L	L	00000
L	L	H	00001
L	H	L	00010
L	H	H	00011
H	H	L	00110
H	H	H	00111
L	L	M	01000

ROM_IO	ROM_BI	ROM_BCLK	SOR_EXPOSED [2]	1 ENABLE 0 DISABLE
L	L	L	1111 DEFAULT	SORD123 ENABLE
L	L	H	1110	
L	H	L	1101	
L	H	H	1100	
H	L	L	1011	
H	L	H	1010	
H	H	L	1001	
H	H	H	1000	
L	L	M	0111	
L	M	L	0110	
L	M	H	0101	
L	H	M	0100	
H	L	M	0011	
H	M	L	0010	
H	M	H	0001	
H	H	M	0000	

STRAP5	STRAP4	STRAP3	SBM_ALT_ADDR	DEVID_SEL	PCI_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	Y DEFAULT
L	L	L	0	0	0	0

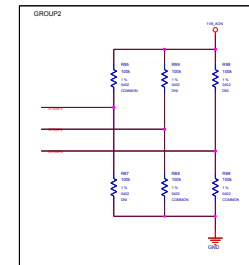
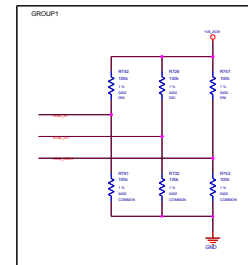
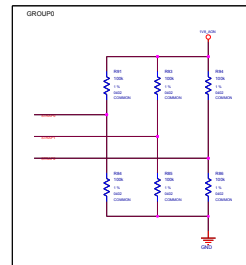
H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V

```
1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE

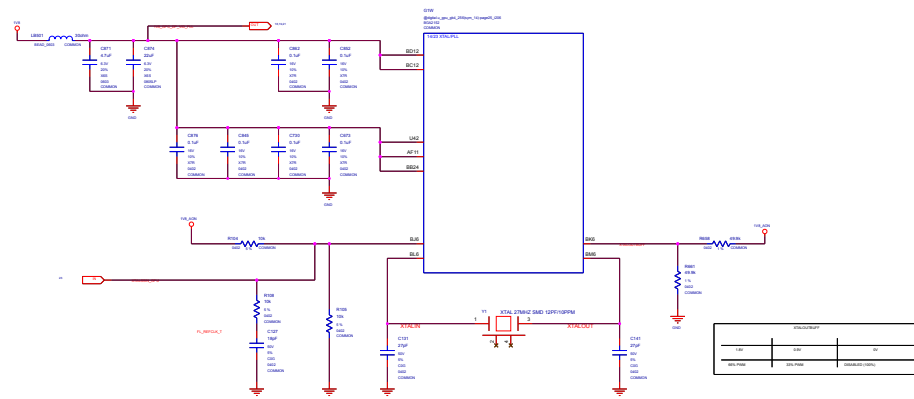
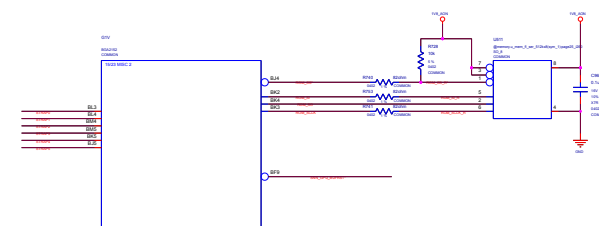
1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGINAL
```

```
1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER

1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE
```



```
CFGQ3[0] Config Width Vendor
0000 256Mb/32 256-bit Samsung
0001 256Mb/32 256-bit Micron
0010 256Mb/32 256-bit Hynix
0011 Reserved
0100 Reserved
0101 Reserved
0110 Reserved
0111 Reserved
```



NVIDIA CORPORATION
2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA



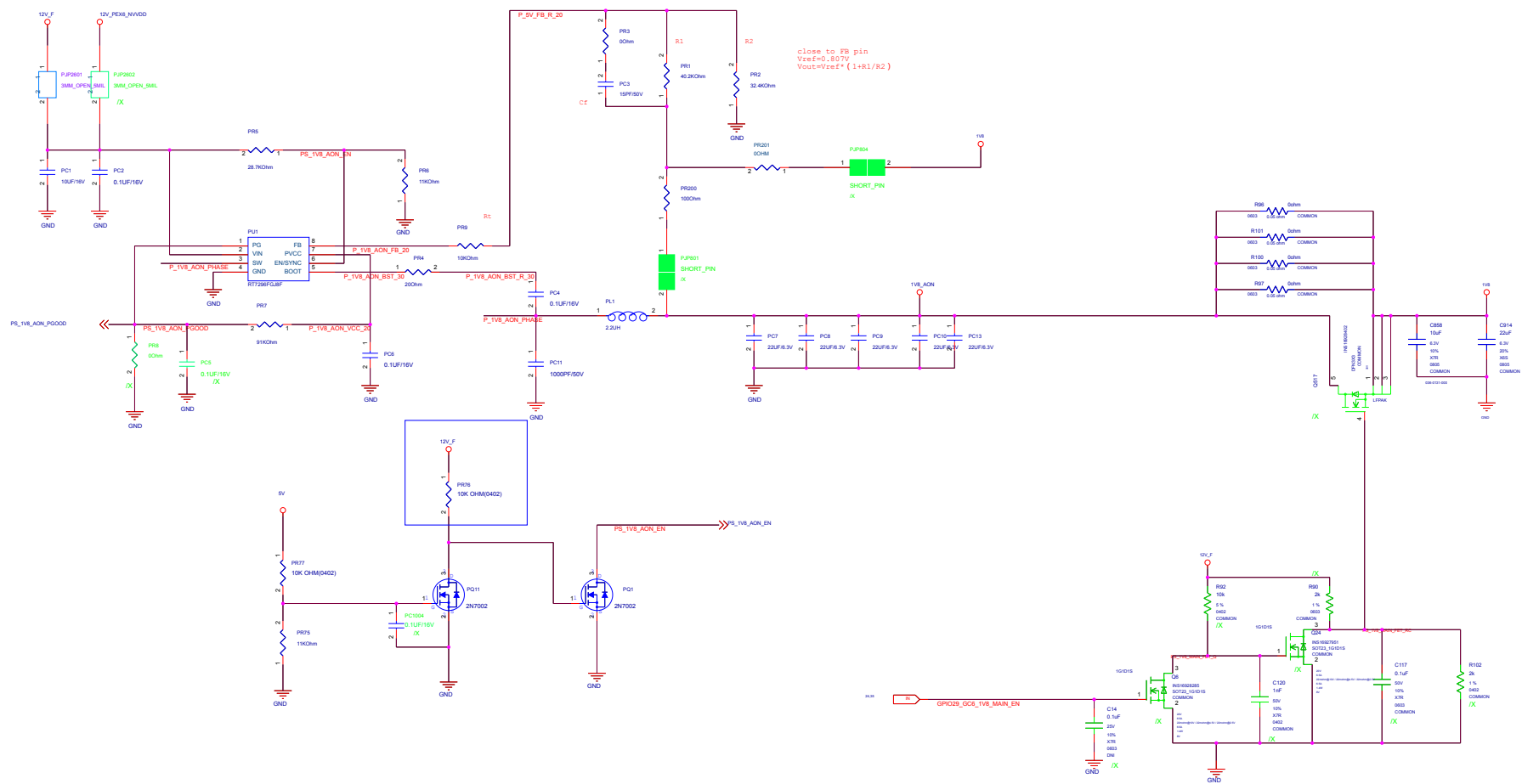
NV_PN	600-1G410-BASE-100
-------	--------------------

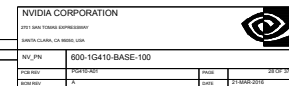
PCB REV	PCB15-AD1
---------	-----------

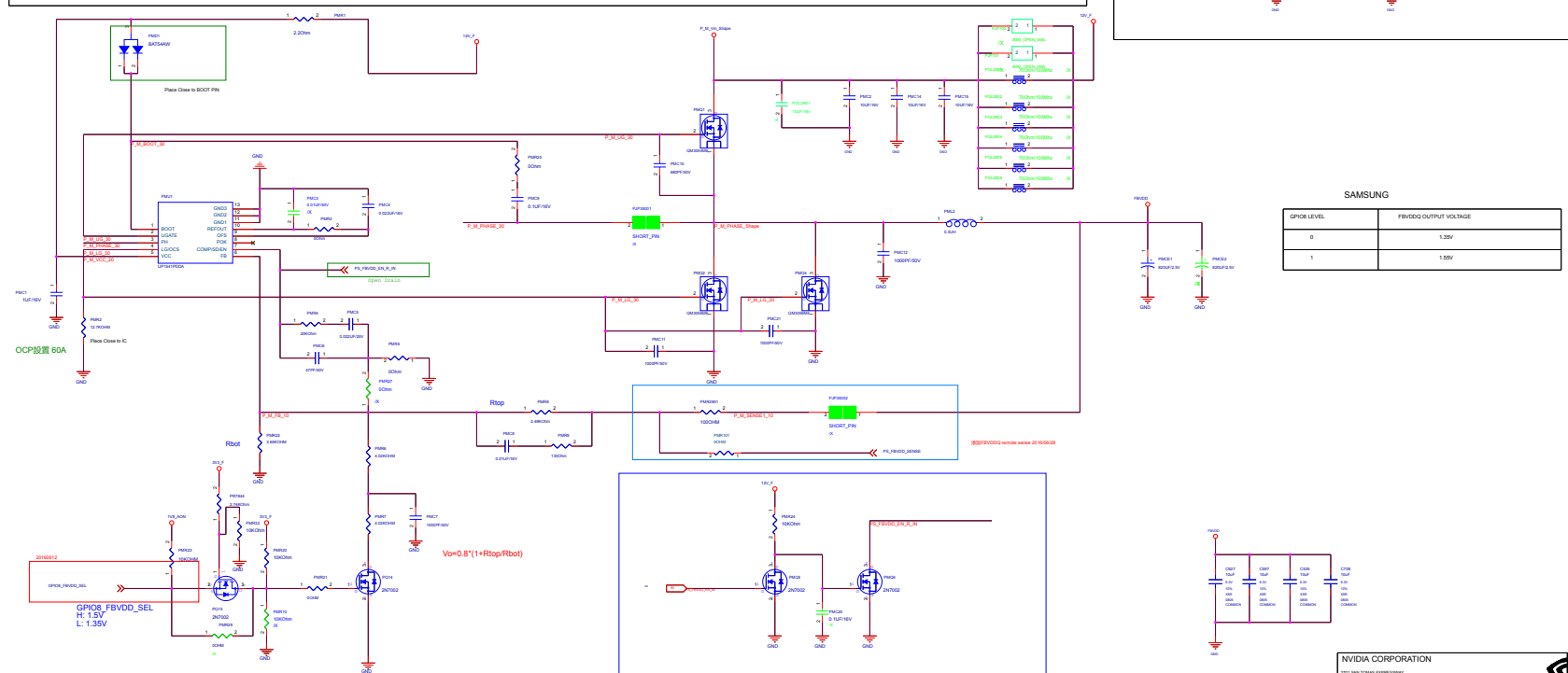
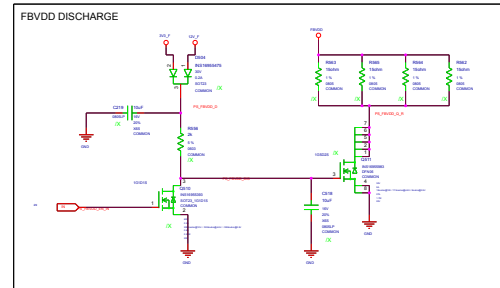
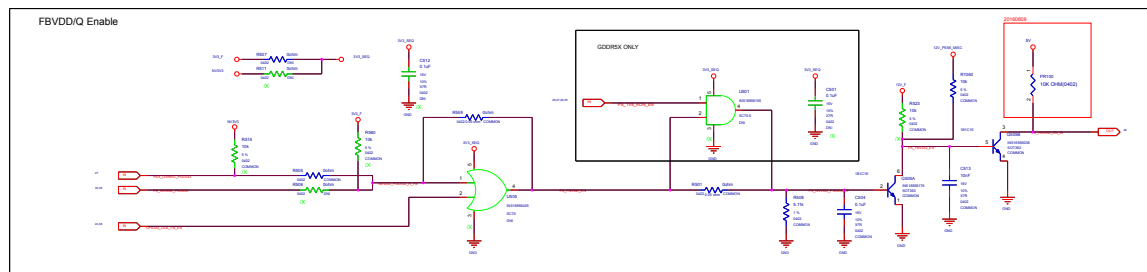
BOC REV	A
---------	---

--	--

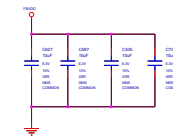
--	--

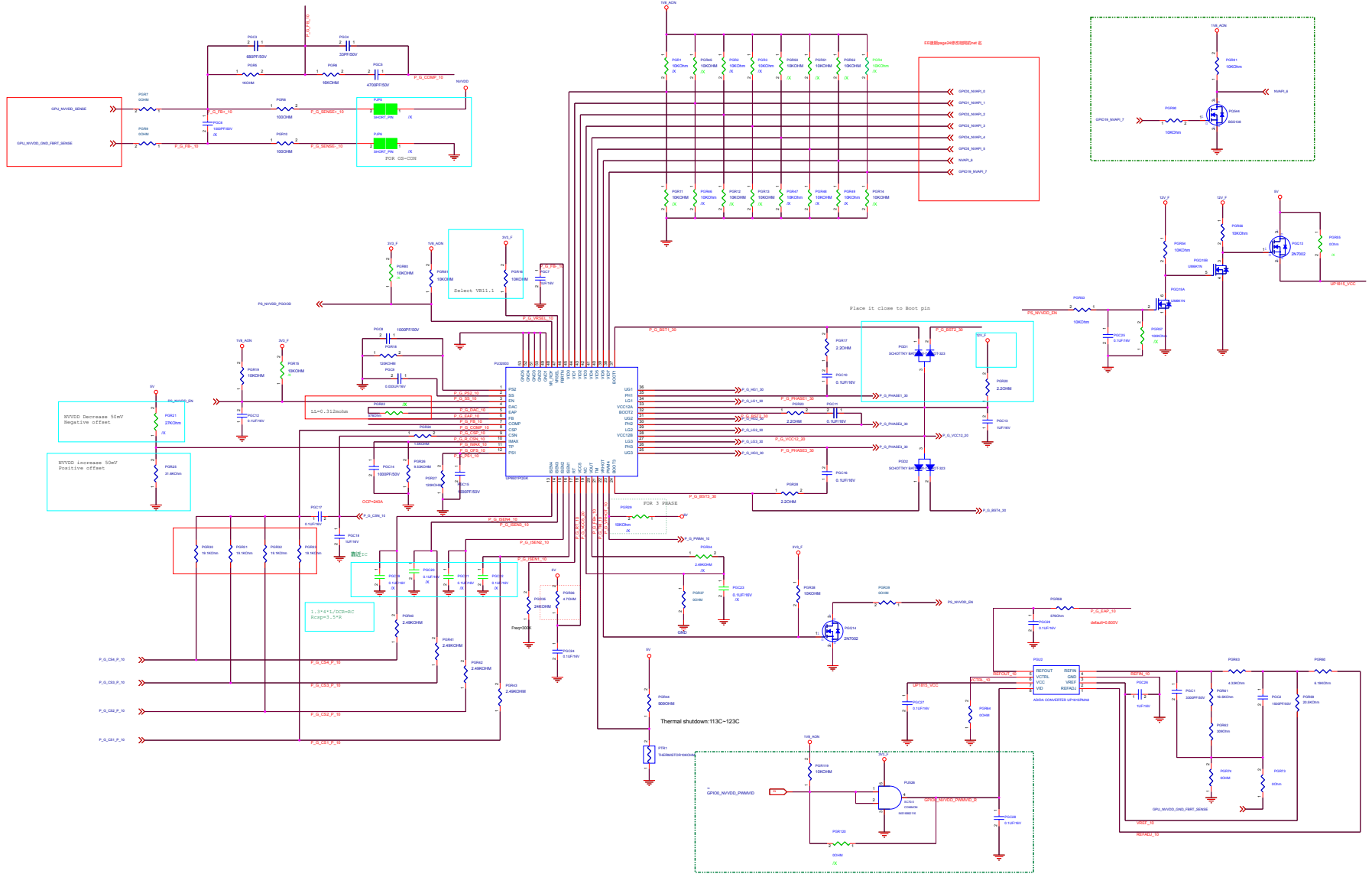


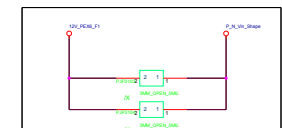
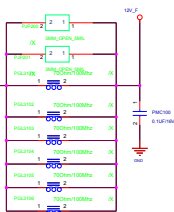
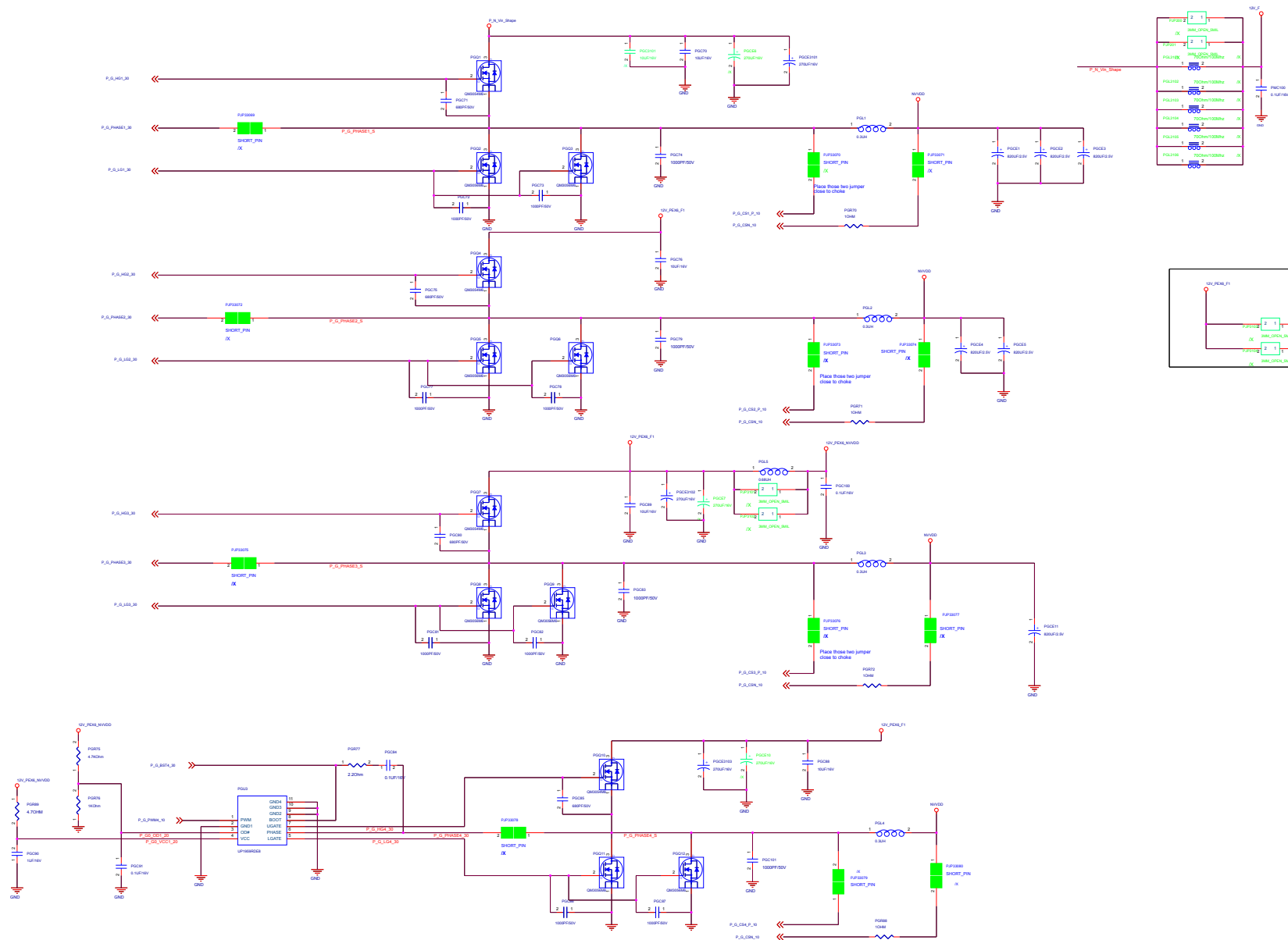




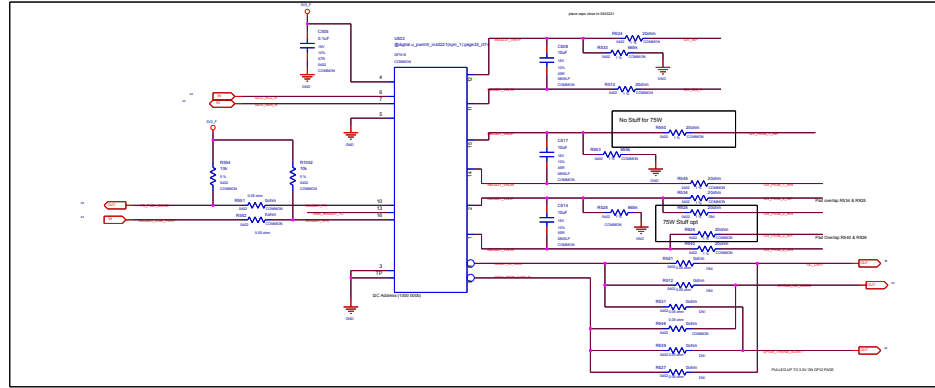
GPIO LEVEL	FBVDDQ OUTPUT VOLTAGE
0	1.35V
1	1.55V



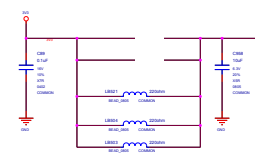




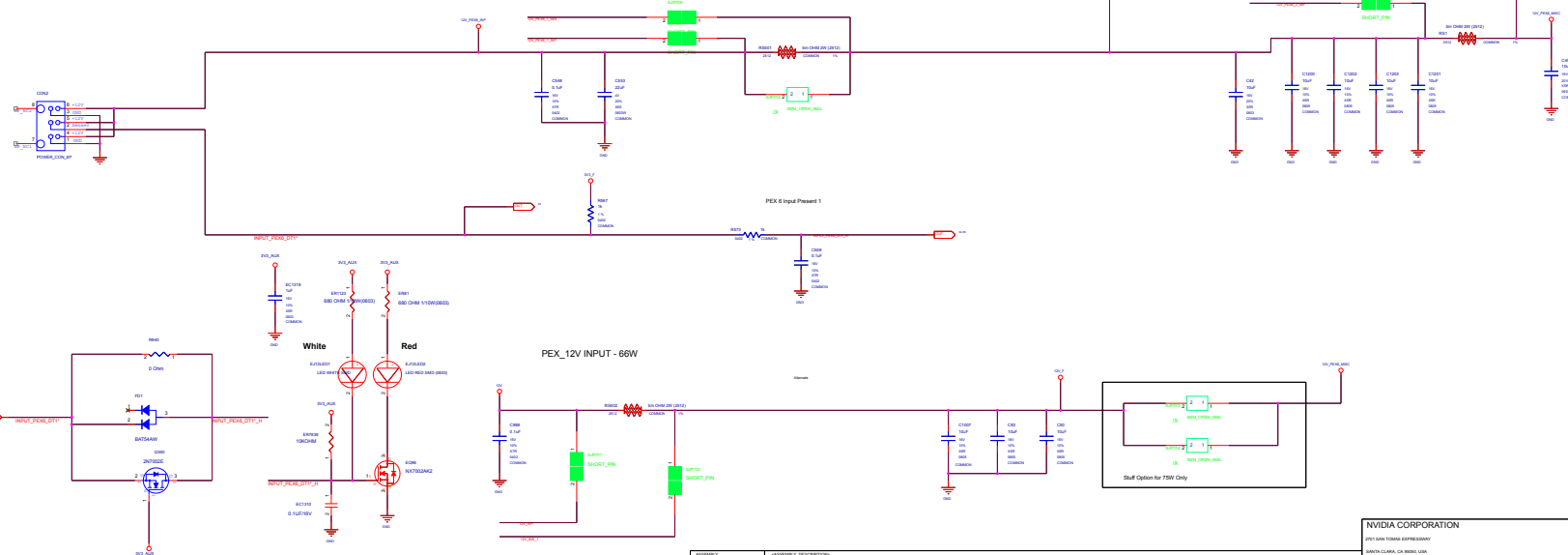
20160704



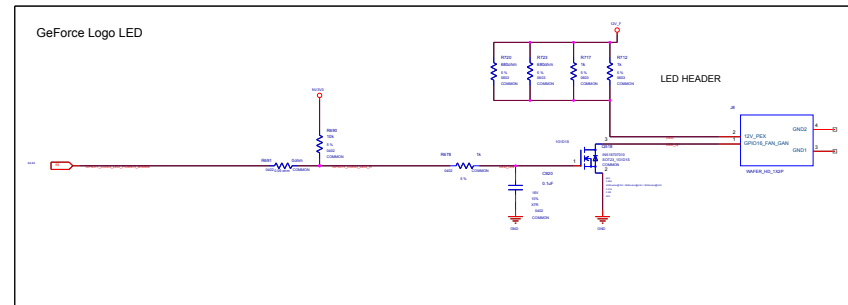
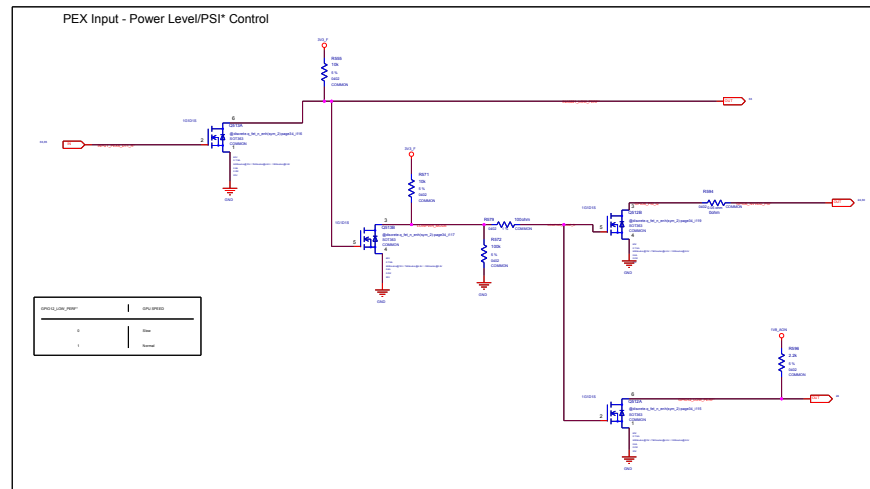
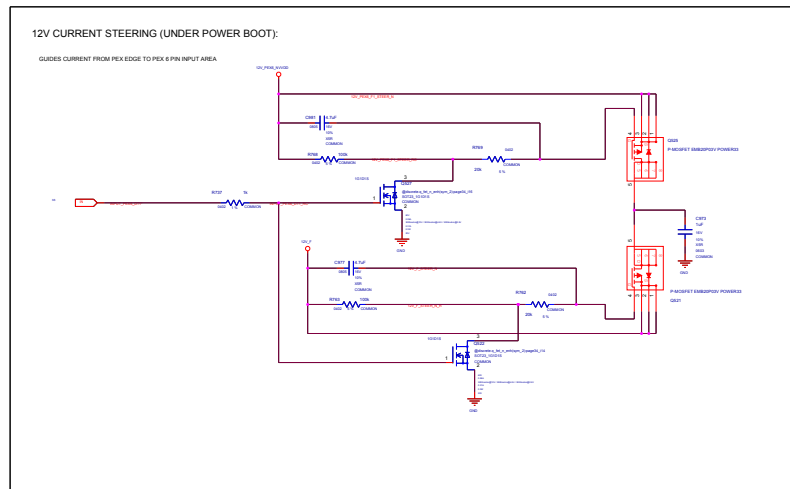
PEX 3V3 INPUT - 10W




PEX6 INPUT 1 - 2x3 PCIE CON 75W



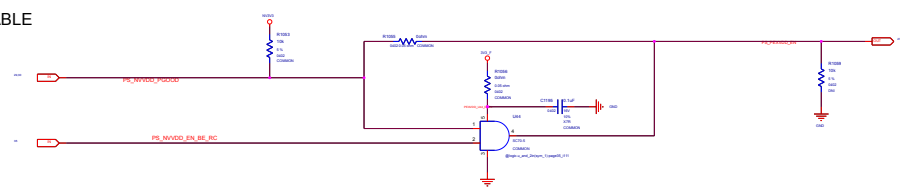
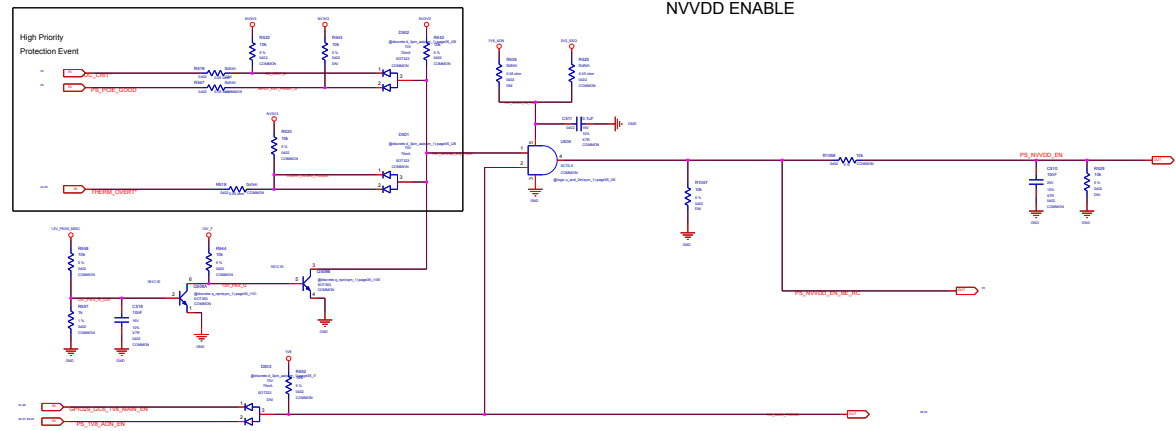
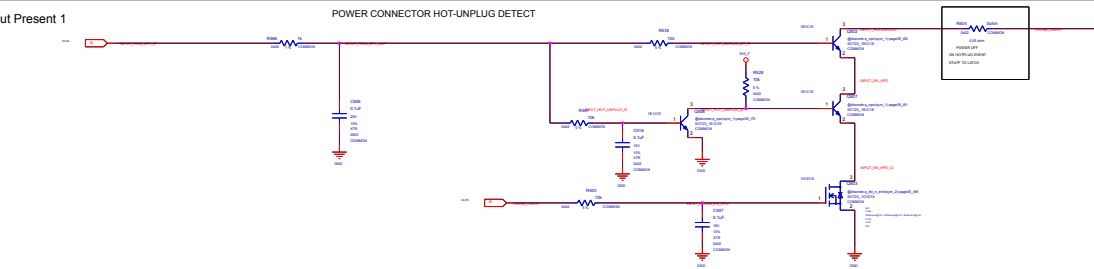
ASSEMBLY	600-1G410-0100	600-1G410-BASE-100	33 OF 38
MODEL DATA	PL Spun Fiberglass and Mounting		



ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE TOTAL	P0: 12V Power Steering P00 Control & L

NVIDIA CORPORATION 3701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95051, USA			
NV_PN	600-1G410-BASE-100		
PCB REV	PCB01-A01	PAGE	DE 03-21
PCB MFR	4	DATE	10-08-04 09:00



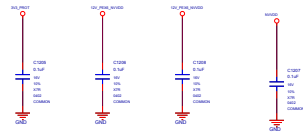


NVIDIA CORPORATION
2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA



NV_PN	600-1G410-BASE-100
-------	--------------------

PCB REV	PG10-001	PAGE	25 OF 27
SCM REV	A	DATE	21-MAR-2018



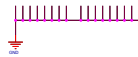
Brackets:



MOUNTING HOLES FOR HS DS:



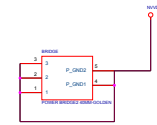
MOUNTING HOLES FOR HS SS:



GPU Stiffener:



Power Plate



NVIDIA CORPORATION			
4701 SAN TOME EXPRESSWAY			
SANTA CLARA, CA 95051-1000			
REV	800-1G415-BASE-100	DATE	07 SEP 17
DESIGN	800-1G415-BASE-100	DATE	07 SEP 17
DESIGN	800-1G415-BASE-100	DATE	07 SEP 17



ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCED SPECIFICATIONS, REFERENCE MATERIAL, TRADE DRESSINGS, LOGOS AND OTHER DOCUMENTS OR INFORMATION TOGETHER AND SEPARATELY, MATERIALS, AND BRANDS PROVIDED TO IT, THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN DEFECTS OR DEFICIENCIES OF QUALITY, PERFORMANCE AND SPECIFICATIONS. NVIDIA DISCLAIMS ANY WARRANTIES, EXPRESS, IMPLIED, STATUTORY OR OTHERWISE, WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF PERFORMANCE, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICES, OR QUALITY STANDARDS.