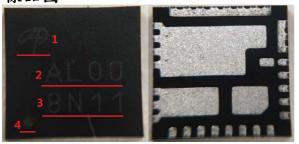
料號

10IFD-505332-00R

品名規格

DR MOS AOZ5332QI 50A AOS SMD QFN-31L 5\*5

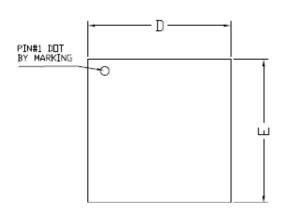
零件樣品圖:



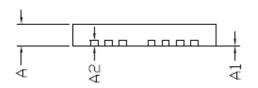
# 外觀文字面檢驗重點:

- 1. AOS LOGO (不可變)
- 2. AL00:Part Number Code(不可變)
- 3. 8N11: Data Code & Assmbly Lot Code(可變)
- 4. PIN1點(不可變)

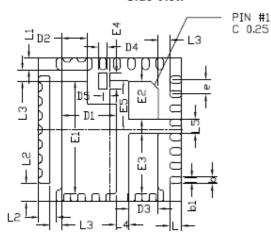
## 機構尺寸工程圖:



**Top View** 



Side View



**Bottom View** 

	DIME	INOISNE	MM	DIMENSION IN INCHES					
SYMBOLS	MIN	NOM	MAX	MIN	MOM	MAX			
A	0.700	0.750	0.800	0.028	0.030	0.031			
A1	0.000	•	0.050	0.000	•	0.002			
A2		0.2REF		0.008REF					
D	4.900	5.000	5.100	0.193	0.197	0.201			
E	4.900	5.000	5.100	0.193	0.197	0.201			
D1	1.870	1.920	1.970	0.074	0.076	0.078			
D2	0.850	0.850 0.900		0.033	0.035	0.037			
D3	0.990 1.040		1.090	0.039	0.041	0.043			
D4	0.250 0.300		0.350	0.010	0.012	0.014			
D5	0.200 0.250		0.300	800.0	0.010	0.012			
E1	3.875 3.925		3.975	0.153	0.155	0.156			
E2	1.270	1.320	1.370	0.050	0.052	0.054			
E3	2.050	2.100	2.150	0.081	0.083	0.085			
E4	0.500	0.550	0.600	0.020	0.022	0.024			
E5	1.661	1.711	1.761	0.065	0.067	0.069			
L	0.350	0.400	0.450	0.014	0.016	0.018			
L1	0.350	0.400	0.450	0.014	0.016	0.018			
L2	0.575	0.625	0.675	0.023	0.025	0.027			
L3	0.350	0.400	0.450	0.014	0.016	0.018			
L4	0.400	0.450	0.500	0.016	0.018	0.020			
L5	0.450	0.500	0.550	0.018	0.020	0.022			
b	0.200	0.250	0.300	800.0	0.010	0.012			
b1	0.130	0,180	0.230	0.005	0.007	0.009			
e		0.50BSC		0.02BSC					
		THE PARTY SET TO STORY		-	A PART A PART A				

# IQC電氣、機構檢驗重點:

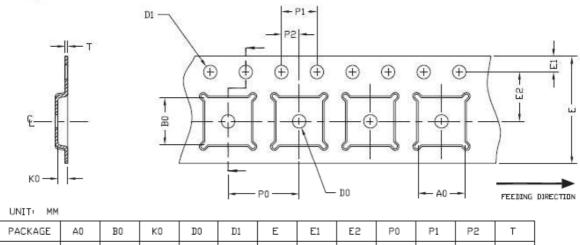
- 1. 量測各部尺寸須與規格值相符.
- 2. 檢視產品外觀,表面無異物,且不得有任何破損及變形.
- 3. 電氣特性檢驗:

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
V <sub>IN</sub>	Power Stage Power Supply		2.5		25	٧
V <sub>cc</sub>	Low Voltage Bias Supply	PVCC=VCC	4.5		5.5	٧
R <sub>eJC</sub> <sup>(4)</sup>	Thermal Resistance	PCB Temp = 100°C		2.5		°C/W
R <sub>BJA</sub> <sup>(4)</sup>	Thermal Resistance			13.8		°C/W
Input Supply	and UVLO			•		
VCC <sub>ON</sub>	Under-Voltage Lockout	V <sub>CC</sub> Rising		3.5	3.3	٧
V <sub>CC_HYST</sub>	Olider-voltage Lockout	V <sub>CC</sub> HYST		400		mV
I <sub>vec</sub>		DISB# = 0V, V <sub>CC</sub> = 5V (Shutdown)		1		
		SMOD# = High, DISB# = High, V <sub>PWM</sub> = 0V (No Switching)		720		μΑ
	Control Circuit Bias Current	SMOD# = Low, DISB# = High, V <sub>PWM</sub> = 0V (No Switching)		590		
		SMOD# = Low, DISB# = High, V <sub>PWM</sub> =1.65V (Tri-State, No Switching)		500		
I <sub>PVCC</sub>	Drive Circuit Operating Current	DISB# = High, V <sub>PWM</sub> = 400kHz, 20% Duty Cycle		20		mA
	Drive Oricuit Operating Current	DISB# = High, V <sub>PWM</sub> = 1MHz, 20% Duty Cycle		50		μА

### 4. 包裝方式如下,詳細請查閱data sheet.

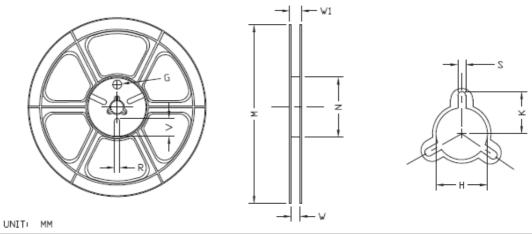
# Tape and Reel Dimensions, QFN5x5A\_31L\_EP3\_S

#### Carrier Tape



PACKAGE	Α0	В0	K0	D0	D1	E	E1	E2	P0	P1	P2	Т
QFN5x5 (12 mm)	5.25 ±0.10	5.25 ±0,10	1.10 ±0,10	1.50 MIN.	1.50 +81 -88	12.0 ±0,3	1.75 ±0,10	5.50 ±0,05	8.00 ±0.10	4.00 ±0.10	2.00 ±0,05	0.30 ±0,05

#### Reel



TAPE SIZE	REEL SIZE	М	N	W	W1	Н	К	S	G	R	V
12 mm	ø330	¢330.0 ±2.0	ø79.0 ±1.0	12.4 +2.0 -0.0	17.0 +2.6 -12	ø13.0 ±0.5	10.5 ±0.2	2.0 ±0.5			

# μ-EDX 有害物質檢測:

 $\mu$ -EDX 需依照HCSR 有害物質管制規範實施檢測,並參照IQC EDX Test Guide 規範。

Date: 2018/10/25 Rev. 00 首次發行