



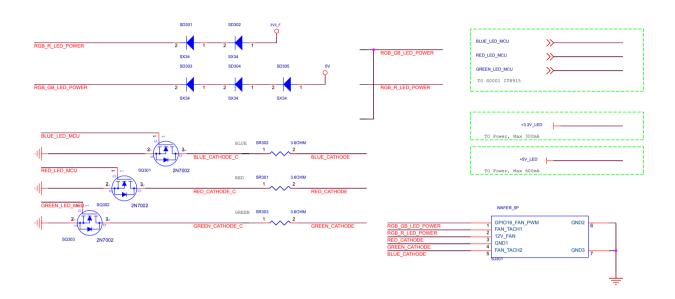


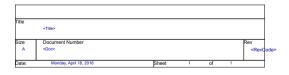


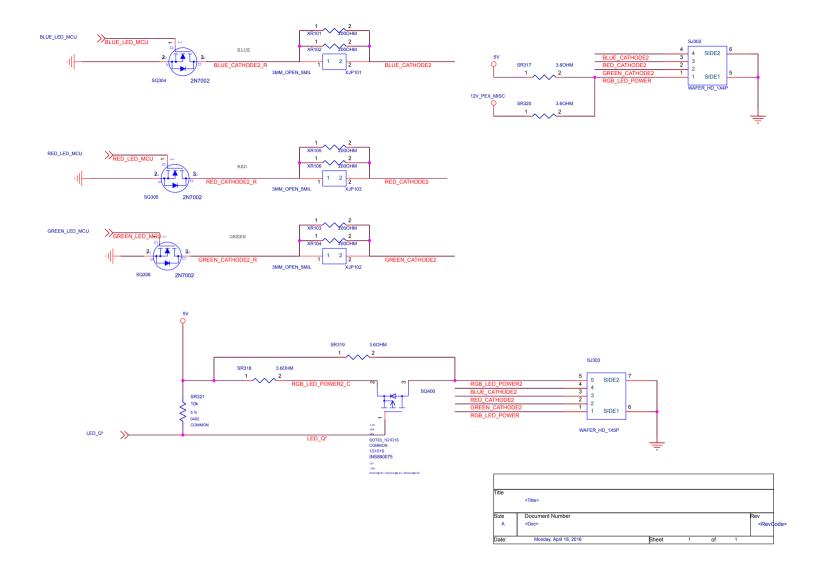
| STRIX_SYSFAN_MCU      | Mode   |
|-----------------------|--------|
| L                     | NORMAL |
| H(Default)            | STRIX  |
| SYSFANTACH_SELECT_MCU | Mode   |

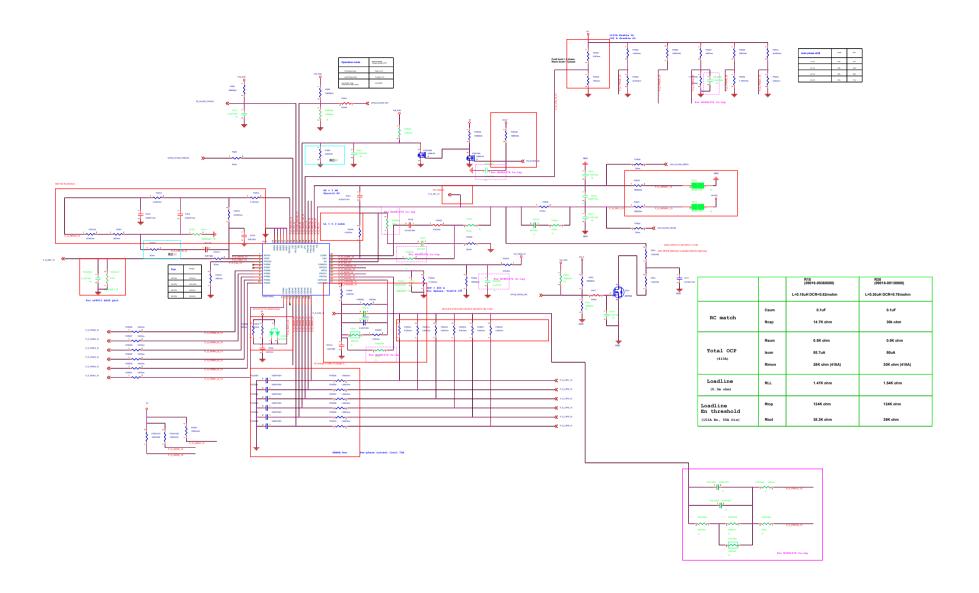
| SYSFANTACH_SELECT_MCU | Mode         |
|-----------------------|--------------|
| L                     | SYSFAN_TACH1 |
| H(Default)            | SYSFAN_TACH2 |

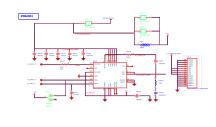


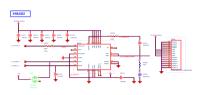


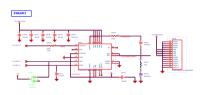


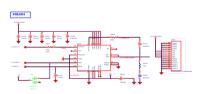


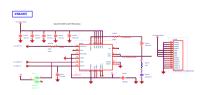




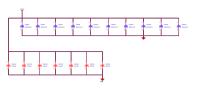


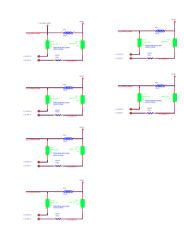




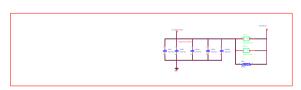


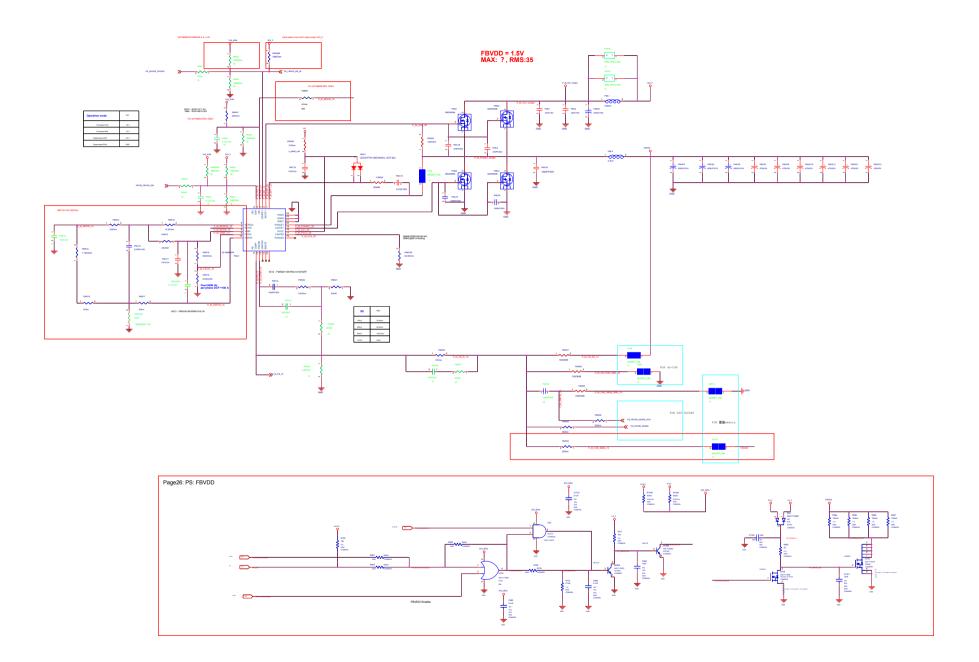


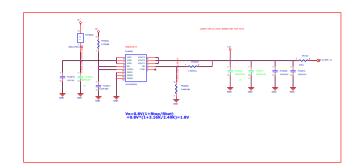


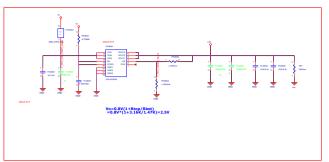


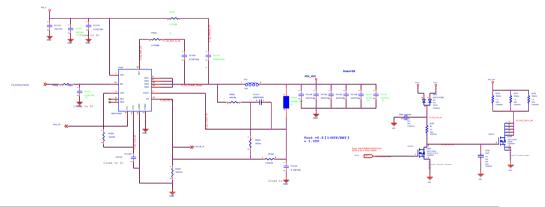


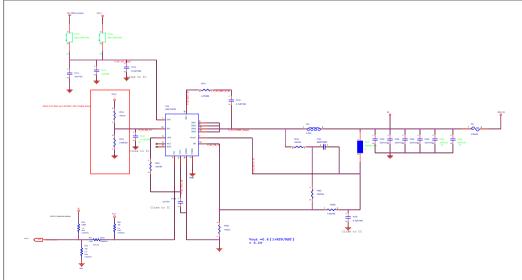


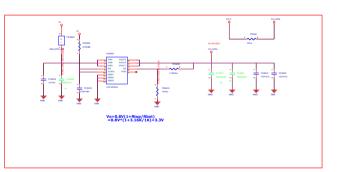


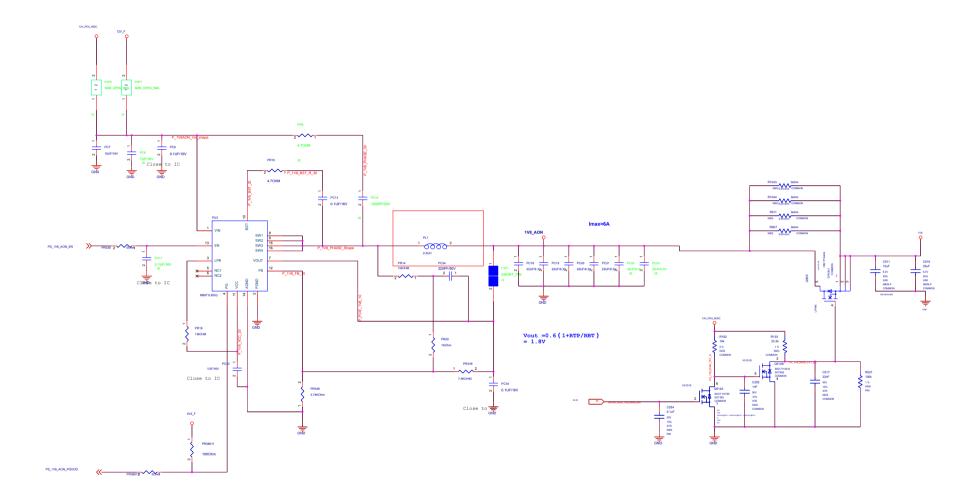


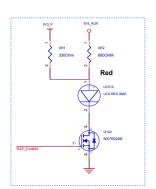


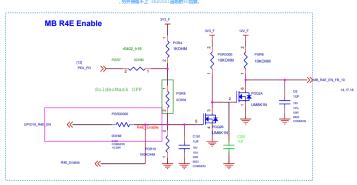














- P-STATE VOLTAGES 1. P0 at 1.05V to 1.15V (depending on VB) 2. P8/P12 at 0.80V
- 1. Ramp 0V to 1.10V in ~2mS 2. Hold at 1.10V for 170uS 3. Read VID 4. VID set to 0.9V during GPIO tri-state VID[5:1]=11000 to set 0.9125V



