0Rev.	Modified Chapters	Modification Description	Issued Dept.	Issued by	Revised Date
1.00		First release	GRTD	Maico_Zhang	Feb.1.2019
1.01		Add new PCBA(YV0CT1-A03)	GRTD	Maico_Zhang	Feb.1.2019
1.02		Add new P/N(YV0CT2-A01)	GRTD	Maico_Zhang	Feb.1.2019
1.03		Add new PCBAYV0CT3-A01)	GRTD	Maico_Zhang	Feb.1.2019
1.04		Add new P/N(YV0CR0-A05)	GRTD	Maico_Zhang	Feb.1.2019
1.05		Add new PCBA(YV0CR1-A06)	GRTD	Maico_Zhang	Feb.1.2019
1.06		Add new P/N(YV0CQ0-A01)	GRTD	Maico_Zhang	Feb.1.2019
1.07		Add new PCBA(YV0CQ1-A01)	GRTD	Maico_Zhang	Feb.1.2019
1.08		Add new PCBA(YV0CQ2-A01)	GRTD	Maico_Zhang	Feb.1.2019
1.09		Add new PCBA(YV0CT4-A01)	GRTD	Maico_Zhang	Mar.7.2019
1.10		Add new PCBA(YV0CT5-A01)	GRTD	Maico_Zhang	Mar.7.2019
1.11		Add new P/N(YV0CU0-A03)	GRTD	Maico_Zhang	Mar.7.2019
1.12		Add new P/N(YV0CU2-A02)	GRTD	Maico_Zhang	Mar.7.2019
1.13		Add new PCBA(YV0CU1-A04)	GRTD	Maico_Zhang	Mar.14.2019
1.14		Add new PCBA(YV0CU3-A03)	GRTD	Maico_Zhang	Mar.14.2019
1.15		Add new PCBA(YV0CR0-A16)	GRTD	Maico_Zhang	Apr.24.2019
1.16		Add new PCBA(YV0CR1-A16)	GRTD	Maico_Zhang	Apr.24.2019
1.17		Add new P/N(YV0CU0-A15)	GRTD	Maico_Zhang	Apr.24.2019
1.18		Add new PCBA(YV0CU1-A15)	GRTD	Maico_Zhang	Apr.24.2019
1.19		Add new P/N(YV0CU2-A15)	GRTD	Maico_Zhang	Apr.24.2019
1.20		Add new PCBA(YV0CU3-A15)	GRTD	Maico_Zhang	Apr.24.2019
1.21		Add new PCBA(YV0CU6-A01)	GRTD	Maico_Zhang	Apr.24.2019
1.22		Add new P/N(YV0CR2-A01)	GRTD	Maico_Zhang	Apr.24.2019
1.23		Add new PCBA(YV0CR3-A01)	GRTD	Maico_Zhang	Apr.24.2019
1.24		Add new PCBA(YV0CR4-A01)	GRTD	Maico_Zhang	Apr.24.2019
1.25		Add new P/N(YV0D10-A01)	GRTD	Maico_Zhang	Apr.24.2019
1.26		Add new P/N(YV0D11-A01)	GRTD	Maico_Zhang	Apr.24.2019
1.27		Add new PCBA(YV0D12-A01)	GRTD	Maico_Zhang	Apr.24.2019
1.28		Add new P/N(YV0D20-A01)	GRTD	Maico_Zhang	Apr.24.2019
1.29		Add new PCBA(YV0D13-A03)	GRTD	Maico_Zhang	May.21.2019
1.30		Add new PCBA(YV0D14-A03)	GRTD	Maico_Zhang	May.21.2019
1.31		Add new PCBA(YV0CU0-B03)	GRTD	Maico_Zhang	May.21.2019
1.32		Add new PCBA(YV0CU6-A11)	GRTD	Maico_Zhang	May.21.2019

Authorize by		**Review by**		**Originator By**	Maico

1.33		Add new PCBA(YV0CT0-B12)	GRTD	Maico_Zhang	May.21.2019
1.34		Add new PCBA(YV0CT1-B12)	GRTD	Maico_Zhang	May.21.2019
1.35		Add new PCBA(YV0CU1-B01)	GRTD	Maico_Zhang	Jun.25.2019
1.36		Add new P/N(YV0D15-A12)	GRTD	Maico_Zhang	Jun.25.2019
1.37		Add new PCBA(YV0D16-A12)	GRTD	Maico_Zhang	Jun.25.2019
1.38		Add new PCBA(YV0D17-A12)	GRTD	Maico_Zhang	Jun.25.2019
1.39		Add new P/N(YV0CU0-A26)	GRTD	Maico_Zhang	Jun.25.2019
1.40		Add new PCBA(YV0CU1-A26)	GRTD	Maico_Zhang	Jun.25.2019
1.41		Add new P/N(YV0CU2-A25)	GRTD	Maico_Zhang	Jun.25.2019
1.42		Add new P/N(YV0D11-A14)	GRTD	Maico_Zhang	Jun.25.2019
1.43		Add new PCBA(YV0D11-A24)	GRTD	Maico_Zhang	Jun.25.2019
1.44		Add new PCBA(YV0D11-A34)	GRTD	Maico_Zhang	Jun.25.2019
1.45		Add new P/N(YV0D13-A14)	GRTD	Maico_Zhang	Jul.4.2019
1.46		Add new PCBA(YV0D13-A24)	GRTD	Maico_Zhang	Jul.4.2019
1.47		Add new PCBA(YV0D13-A34)	GRTD	Maico_Zhang	Jul.4.2019
1.48		Add new P/N(YV0D14-A14)	GRTD	Maico_Zhang	Jul.4.2019
1.49		Add new PCBA(YV0D14-A24)	GRTD	Maico_Zhang	Jul.4.2019
1.50		Add new PCBA(YV0D14-A34)	GRTD	Maico_Zhang	Jul.4.2019
1.51		Add new P/N(YV0CR5-A04)	GRTD	Maico_Zhang	Jul.31.2019
1.52		Add new PCBA(YV0CR6-A04)	GRTD	Maico_Zhang	Jul.31.2019
1.53		Add new PCBA(YV0CR7-A04)	GRTD	Maico_Zhang	Jul.31.2019
1.54		Add new PCBA(YV0D15-A22/A32)	GRTD	Maico_Zhang	Jul.31.2019
1.55		Add new PCBA(YV0D16-A22/A32)	GRTD	Maico_Zhang	Jul.31.2019
1.56		Add new PCBA(YV0D17-A22/A32)	GRTD	Maico_Zhang	Jul.31.2019
1.57		Add new PCBA(YV0D11-A44)	GRTD	Maico_Zhang	Jul.31.2019
1.58		Add new PCBA(YV0CU0-A36)	GRTD	Maico_Zhang	Jul.31.2019
1.59		Add new PCBA(YV0CU1-A36)	GRTD	Maico_Zhang	Jul.31.2019
1.60		Add new PCBA(YV0CU2-A35)	GRTD	Maico_Zhang	Jul.31.2019
1.61		Add new PCBA(YV0CU3-A35)	GRTD	Maico_Zhang	Jul.31.2019
1.62		Add new PCBA(YV0CU6-A21)	GRTD	Maico_Zhang	Jul.31.2019
1.63		Add new PCBA(YV0D13-A44)	GRTD	Maico_Zhang	Aug.22.2019
1.64		Add new PCBA(YV0D14-A44)	GRTD	Maico_Zhang	Aug.22.2019
1.65		Add new PCBA(YV0D15-A42)	GRTD	Maico_Zhang	Aug.22.2019
1.66		Add new PCBA(YV0D16-A42)	GRTD	Maico_Zhang	Aug.22.2019
1.67		Add new PCBA(YV0D17-A42)	GRTD	Maico_Zhang	Aug.22.2019
1.68		Add new PCBA(YV0CT2-B04)	GRTD	Maico_Zhang	Aug.22.2019
1.69		Add new PCBA(YV0CT3-B04)	GRTD	Maico_Zhang	Aug.22.2019
1.70		Add new PCBA(YV0CR2-B02)	GRTD	Maico_Zhang	Sep.12.2019
1.71		Add new PCBA(YV0CR3-B01)	GRTD	Maico_Zhang	Sep.12.2019

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			Date: Dec.12.2019		
			Revision:2.17		

1.72		Add new PCBA(YV0CR4-B02)	GRTD	Maico_Zhang	Sep.12.2019
1.73		Add new PCBA(YV0CU3-A25)	GRTD	Maico_Zhang	Sep.12.2019
1.74		Add new P/N(YV0D10-A24)	GRTD	Maico_Zhang	Sep.30.2019
1.75		Add new PCBA(YV0D12-A24)	GRTD	Maico_Zhang	Sep.30.2019
1.76		Add new P/N(YV0DS0-A03)	GRTD	Maico_Zhang	Oct.8.2019
1.77		Add new PCBA(YV0DS1-A02)	GRTD	Maico_Zhang	Oct.8.2019
1.78		Add new PCBA(YV0DS2-A02)	GRTD	Maico_Zhang	Oct.8.2019
1.79		Add new P/N(YV0DS3-A02)	GRTD	Maico_Zhang	Oct.8.2019
1.80		Add new PCBA(YV0DS4-A03)	GRTD	Maico_Zhang	Oct.8.2019
1.81		Add new PCBA(YV0DS5-A03)	GRTD	Maico_Zhang	Oct.8.2019
1.82		Add new P/N(YV0DS6-A01)	GRTD	Maico_Zhang	Oct.8.2019
1.83		Add new PCBA(YV0DS7-A01)	GRTD	Maico_Zhang	Oct.8.2019
1.84		Add new P/N(YV0DT0-A01)	GRTD	Maico_Zhang	Oct.8.2019
1.85		Add new PCBA(YV0DT1-A01)	GRTD	Maico_Zhang	Oct.8.2019
1.86		Add new PCBA(YV0DT2-A01)	GRTD	Maico_Zhang	Nov.21.2019
1.87		Add new PCBA(YV0DT3-A01)	GRTD	Maico_Zhang	Nov.21.2019
1.88		Add new PCBA(YV0D10-A36)	GRTD	Maico_Zhang	Nov.21.2019
1.89		Add new P/N(YV0DW0-A01)	GRTD	Maico_Zhang	Nov.21.2019
1.90		Add new PCBA(YV0DW1-A01)	GRTD	Maico_Zhang	Nov.21.2019
1.91		Add new PCBA(YV0DW2-A01)	GRTD	Maico_Zhang	Nov.21.2019
1.92		Add new PCBA(YV0DS6-A11)	GRTD	Maico_Zhang	Nov.21.2019
1.93		Add new PCBA(YV0DS7-A11)	GRTD	Maico_Zhang	Nov.21.2019
1.94		Add new PCBA(YV0CU0-A47)	GRTD	Maico_Zhang	Nov.21.2019
1.95		Add new PCBA(YV0CU1-A47)	GRTD	Maico_Zhang	Nov.21.2019
1.96		Add new PCBA(YV0CU2-A47)	GRTD	Maico_Zhang	Nov.21.2019
1.97		Add new PCBA(YV0CU3-A47)	GRTD	Maico_Zhang	Nov.21.2019
1.98		Add new PCBA(YV0DS0-A13)	GRTD	Maico_Zhang	Dec.12.2019
1.99		Add new P/N(YV0DS1-A13)	GRTD	Maico_Zhang	Dec.12.2019
2.00		Add new PCBA(YV0DS2-A13)	GRTD	Maico_Zhang	Dec.12.2019
2.01		Add new PCBA(YV0DS3-A13)	GRTD	Maico_Zhang	Dec.12.2019
2.02		Add new P/N(YV0DS4-A13)	GRTD	Maico_Zhang	Dec.12.2019
2.03		Add new PCBA(YV0DS5-A13)	GRTD	Maico_Zhang	Dec.12.2019
2.04		Add new P/N(YV0DT0-A13)	GRTD	Maico_Zhang	Dec.12.2019
2.05		Add new PCBA(YV0DT1-A13)	GRTD	Maico_Zhang	Dec.12.2019
2.06		Add new PCBA(YV0DT2-A12)	GRTD	Maico_Zhang	Dec.12.2019
2.07		Add new PCBA(YV0DT3-A12)	GRTD	Maico_Zhang	Dec.12.2019
2.08		Add new P/N(YV0DW0-A11)	GRTD	Maico_Zhang	Dec.12.2019
2.09		Add new PCBA(YV0DW1-A11)	GRTD	Maico_Zhang	Dec.12.2019
2.10		Add new PCBA(YV0DW2-A11)	GRTD	Maico_Zhang	Dec.12.2019

2.11		Add new PCBA(YV0CR0-A28)	GRTD	Maico_Zhang	Dec.12.2019
2.12		Add new PCBA(YV0CR1-A28)	GRTD	Maico_Zhang	Dec.12.2019
2.13		Add new P/N(YV0DS6-A27)	GRTD	Maico_Zhang	Dec.12.2019
2.14		Add new PCBA(YV0DS7-A27)	GRTD	Maico_Zhang	Dec.12.2019
2.15		Add new PCBA(YV0D10-A54)	GRTD	Maico_Zhang	Dec.12.2019
2.16		Add new PCBA(YV0D12-A44)	GRTD	Maico_Zhang	Dec.12.2019
2.17		Add new PCBA(YV0D12-A54)	GRTD	Maico_Zhang	Dec.12.2019

1. Objectives

Create test procedure.

2. Scope

Model:GTX1660

PN: YV0CT0(A03/B12)YV0CT1(A03/B12)YV0CT2(A01/B04)YV0CT3(A01/B04)
YV0CR0(A05/A16)YV0CR1(A06/A16)YV0CQ0(A01)YV0CQ1(A01)
YV0CQ2(A01) YV0CT4(A01) YV0CT5(A01) YV0CU0(A03)YV0CU2(A02)
YV0CU1(A04) YV0CU3(A03)YV0CU0(A15) YV0CU1(A15) YV0CU2(A15)

YV0CU3(A15) YV0CU6(A01)YV0CR2(A01) YV0CR3(A01) YV0CR4(A01)
YV0D10(A01/A36) YV0D11(A01)YV0D12(A01) YV0D20(A01) YV0D13(A03)
YV0D14(A03) YV0CU0(B03)YV0CU6(A11) YV0CU1(B01) YV0D15(A12)
YV0D16(A12)YV0D17(A12)YV0CU0(A26/A36/A47)YV0CU1(A26/A36/A47)
YV0CU2(A25/A35/A47)YV0D11(A14/A24/A34/A44) YV0D13(A14/A24/A34/A44)
YV0CU3(A25/A35/A47)YV0D14(A14/A24/A34/A44)YV0CR5(A04)
YV0CR6(A04)YV0CR7(A04) YV0D15(A22/A32/A42) YV0D16(A22/A32/A42)
YV0D17(A22/A32/A42) YV0CU6(A21) YV0CR2(B02) YV0CR3(B01) YV0CR4(B02)
YV0D10(A24) YV0D12(A24)YV0DS0(A03) YV0DS1(A02)YV0DS2(A02)
YV0DS3(A02) YV0DS4(A03) YV0DS5(A03) YV0DS6(A01/A11)YV0DS7(A01/A11)
YV0DT0(A01) YV0DT1(A01) YV0DT2(A01) YV0DT3(A01)YV0DW0(A01)
YV0DW1(A01) YV0DW2(A01)

3. Test Item

3.1 Diagnostic test in Tiny Linux

4. Relevant Document

None.

5. Definition: Please install OS above Tinylinux18.07 ver. to test HDD.

6. Operation Procedures:

6.1 Attention

6.1.1 To avoid damaging devices or M/B, please turn off A/C power then unplug VGA card.

6.1.2 While unplugging the card, do it in upright direction to avoid damaging slot & golden finger.

6.1.3 If any problem happens, please inform test supervisor. Base on ATX power specification, +5V standby voltage is existed while AC power is connecting. For safety

reason, please turn off main power during break time or idling test.

6.1.4 Some test items should be tested under Windows environment. (Please install windows test environment by yourself).

6.2 Fixture connect

6.2.1 Install Intel CPU & CPU Fan.
6.2.2 Install DDR Memory to DIMM1~4.
6.2.3 Plug USB Mouse and Keyboard into MB.
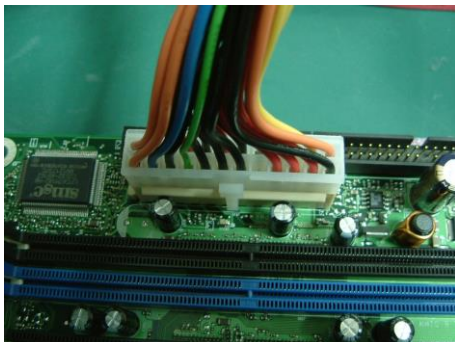6.2.4 Plug VGA Card GTX1660 into PCIEX16.
6.2.5 Connect power con of VGA card & Power supply via bundled power cable.
6.2.6 Connect SATA HDD with SATA CON.

SATA1

6.2.7 Plug 4PIN 12V power cable into CON12V.
6.2.8 Plug ATX Power on Power Connector.

6.3 Diagnostic test in Tiny Linux.
6.3.1Please connect LCD monitor & card's first HDMI CON via a HDMI cable.

HDMI cable
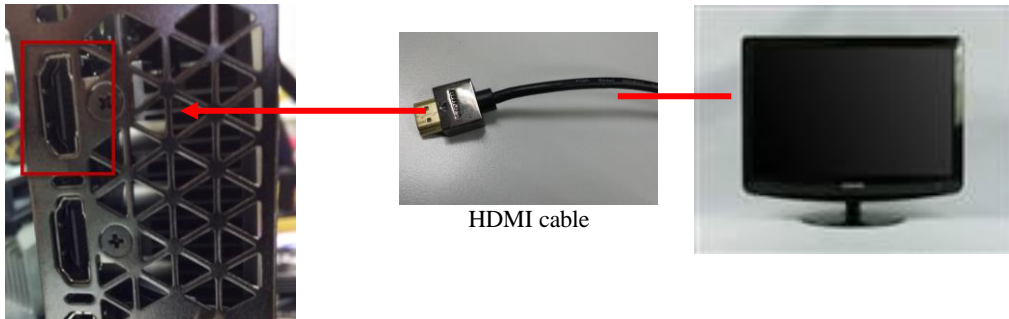
6.3.2 Press Power Switch then turn on power button.

6.3.3 Press "Del" to enter CMOS setup menu while boot screen is displaying.

   EXIT -> Load Setup Defaults.

6.3.4 System will reboot, select Linux to run test program.

   Path:GTX1660\ yv0ct0a0.sh or yv0ct1a0.sh or yv0ct0b1.sh or yv0ct1b1.sh

   Path:GTX16601\ yv0ct2a0.sh or yv0ct3a0.sh or yv0ct4a0.sh or yv0ct5a0.sh
   or yv0ct2b0.sh or yv0ct3b0.sh

   Path:GTX16602\ yv0cr0a0.sh or yv0cr1a0.sh or yv0cr0a1.sh or yv0cr1a1.sh
    or yv0cr0a2.sh or yv0cr1a2.sh

   Path:GTX16603\ yv0cq0a0.sh or yv0cq1a0.sh or yv0cq2a0.sh

   Path:GTX16604\ yv0cu2a0.sh or yv0cu3a0.sh

   Path:GTX16605\ yv0cu0a0.sh or yv0cu1a0.sh or yv0cu0b0.sh or yv0cu1b0.sh

   Path:GTX16606\ yv0cu0a1.sh or yv0cu1a1.sh or yv0cu6a0.sh or yv0cu6a1.sh

   Path:GTX16607\ yv0cu2a1.sh or yv0cu3a1.sh

   Path:GTX16608\ yv0cr2a0.sh or yv0cr3a0.sh or yv0cr4a0.sh or yv0cr2b0.sh
    or yv0cr3b0.sh or yv0cr4b0.sh

   Path:GTX16609\ yv0d10a0.sh or yv0d12a0.sh

   Path:GTX1660a\ yv0d11a0.sh or yv0d13a0.sh or yv0d14a0.sh

Path:GTX1660b\ yv0d10a2.sh or yv0d12a2.sh or yv0d10a3.sh or yv0d10a5.sh
or yv0d12a4.sh or yv0d12a5.sh
Path:GTX1660c\ yv0d15a1.sh or yv0d16a1.sh or yv0d17a1.sh
Path:GTX1660d\ yv0cu0a2.sh or yv0cu1a2.sh or yv0cu0a3.sh or yv0cu1a3.sh
or yv0cu6a2.sh or yv0cu0a4.sh or yv0cu1a4.sh
Path:GTX1660e\ yv0cu2a2.sh or yv0cu3a2.sh or yv0cu2a3.sh or yv0cu3a3.sh
or yv0cu2a4.sh or yv0cu3a4.sh
Path:GTX1660f\ yv0d11a1.sh or yv0d11a2.sh or yv0d11a3.sh or yv0d11a4.sh
or yv0d15a2.sh or yv0d15a3.sh or yv0d15a4.sh
Path:GTX1660g\ yv0d13a1.sh or yv0d13a2.sh or yv0d13a3.sh or yv0d14a1.sh
or yv0d14a2.sh or yv0d14a3.sh or yv0d16a2.sh or yv0d16a3.sh or yv0d17a2.sh
or yv0d17a3.sh or yv0d13a4.sh or yv0d14a4.sh or yv0d16a4.sh or yv0d17a4.sh
Path:GTX1660h\ yv0cr5a0.sh or yv0cr6a0.sh or yv0cr7a0.sh
Path:GTX1660i\ yv0dt0a0.sh or yv0dt1a0.sh or yv0dt2a0.sh or yv0dt3a0.sh
or yv0dt0a1.sh or yv0dt1a1.sh or yv0dt2a1.sh or yv0dt3a1.sh
Path:GTX1660j\ yv0ds0a0.sh or yv0ds1a0.sh or yv0ds2a0.sh
Path:GTX1660k\ yv0ds3a0.sh or yv0ds4a0.sh or yv0ds5a0.sh
Path:GTX1660l\ yv0ds6a0.sh or yv0ds7a0.sh or yv0ds6a1.sh or yv0ds7a1.sh
Path:GTX1660m\ yv0dw0a0.sh or yv0dw1a0.sh or yv0dw2a0.sh
Path:GTX1660o\ yv0ds0a1.sh or yv0ds1a1.sh or yv0ds2a1.sh
Path:GTX1660p\ yv0ds3a1.sh or yv0ds4a1.sh or yv0ds5a1.sh
Path:GTX1660q\ yv0ds6a2.sh or yv0ds7a2.sh
Path:GTX1660r\ yv0dt0a1.sh or yv0dt1a1.sh
Path:GTX1660s\ yv0dw0a1.sh or yv0dw1a1.sh or yv0dw2a1.sh

Run the test program refer to the prompt.

6.3.5 BIOS flash
If you want to check Vbios info, Please run biosinfo
Please use the following command to flash bios and notice the size of character.
Path: Path:GTX1660\
Command: nvflash xxx.rom -6 -A
Notice:
If you can not flash bios because of the bios locked, please use nvflash.exe /r or
nvflash.exe –r to unlock bios. And then, please use nvflash.exe /w or nvflash.exe –w
to lock bios after updating latest bios.

6.4 Result upload to server (need connect to internet)

6.4.1 Download and copy RMA upload tool to diag folder by model (It don't need copy after 2018.5 uploaded model).

The tool of RMA upload tool include 3 files:

Check_SN   RMA_Config   RMAInfo

6.4.2 Edit **RMA_Config** file:

First line confirms the address of local sever, second line is Tester ID.

The **RMA_Config** only need modify one time.


```
CN          Server Select
1150231          Test ID




--------------------------------------
# First  Line :  Server Select.
# Second Line :  Test ID

Server Select :

 1. AS  --> https://as.eservice.asus.com
 2. CN  --> https://cn.eservice.asus.com
 3. AM  --> https://am.eservice.asus.com
 4. AU  --> https://au.eservice.asus.com
 5. EU  --> https://eu.eservice.asus.com

Test ID : RMA Tester  ID
```
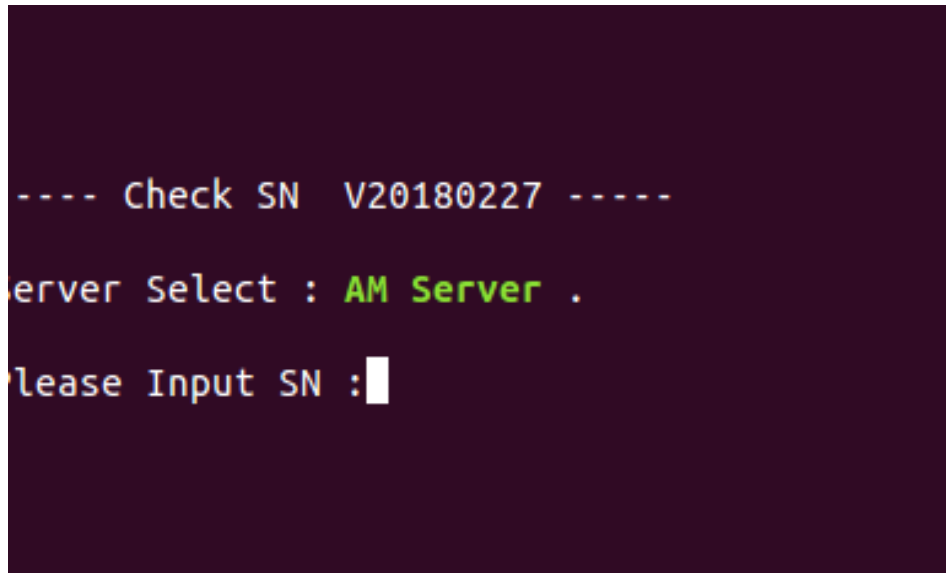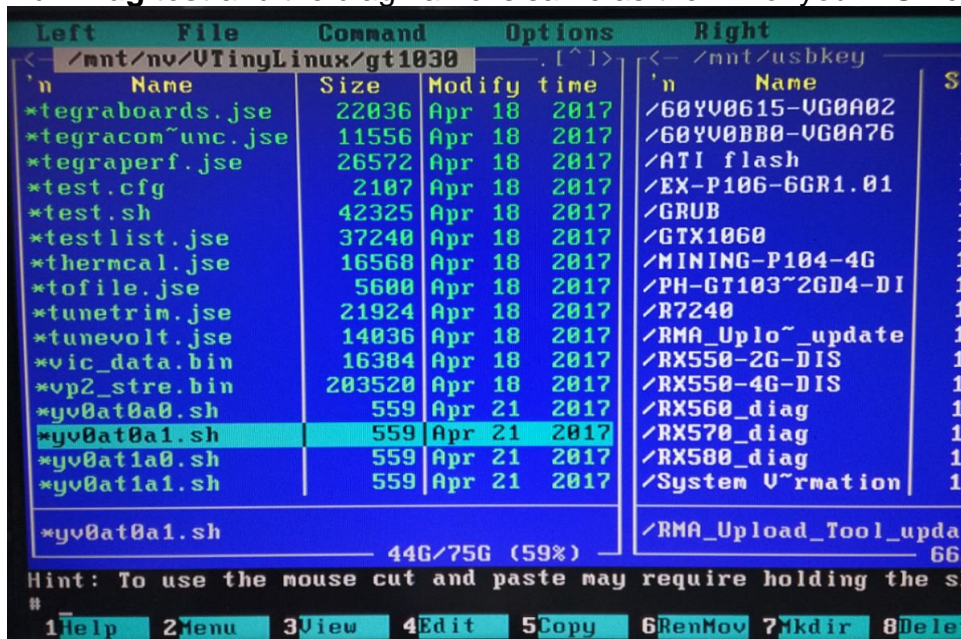
6.4.3 Run **Check_SN** and type in your VGA SN which need to be testing. The SN must be in repair status.

```
---- Check SN  V20180227 -----

erver Select : AM Server .

lease Input SN :
```

6.4.4 Run **Diag test** and the diag name is same as the PN of your VGA card.

```
 Left        File       Command      Options      Right
<-- /mnt/nv/VTinyLinux/gt1030 -----.[^]> <-- /mnt/usbkey ----
'n     Name        Size  | Modify time  'n      Name          S
*tegraboards.jse    22036 Apr 18   2017  /60YV0615-VG0A02       1
*tegracom~unc.jse   11556 Apr 18   2017  /60YV0BB0-VG0A76       1
*tegraperf.jse      26572 Apr 18   2017 /ATI flash             1
*test.cfg            2107 Apr 18   2017 /EX-P106-6GR1.01        1
*test.sh            42325 Apr 18   2017 /GRUB                   1
*testlist.jse       37240 Apr 18   2017 /GTX1060               1
*thermcal.jse       16568 Apr 18   2017 /MINING-P104-4G        1
*tofile.jse          5600 Apr 18   2017 /PH-GT103~2GD4-DI      1
*tunetrim.jse       21924 Apr 18   2017 /R7240                 1
*tunevolt.jse       14036 Apr 18   2017 /RMA_Uplo~_update      1
*vic_data.bin       16384 Apr 18   2017 /RX550-2G-DIS          1
*vp2_stre.bin      203520 Apr 18   2017 /RX550-4G-DIS          1
*yv0at0a0.sh          559 Apr 21   2017 /RX560_diag            1
*yv0at0a1.sh          559 Apr 21   2017 /RX570_diag            1
*yv0at1a0.sh          559 Apr 21   2017 /RX580_diag            1
*yv0at1a1.sh          559 Apr 21   2017 /System V~rmation      1

*yv0at0a1.sh                             /RMA_Upload_Tool_upda
                     -- 44G/75G (59%) --                    66
Hint: To use the mouse cut and paste may require holding the s
#
 1Help   2Menu   3View   4Edit   5Copy   6RenMov 7Mkdir  8Dele
```

6.4.5 After Diag test is finished, run **RMAInfo** and the test result will be sent to server.

ASUSTeK COMPUTER INC	GTX1660 Standard Operation Procedure	Doc. No:
		Date: Dec.12.2019
		Revision:2.17


```
================================================
=        ASUS VGA RMA Infomation          =
=        Rev2018.02.27                     =
================================================

Tester ID = 11502YQ
Addr = AM Server .
Problem Code : MNTF01
NV Test Result = PASS
NV Error Code  = 000000000000

  Update Data To ASUS VGA Server......

    Update Data:  HAC0YZ268879 PASS 06/03/2018 08:51:59  11502YQ

  Please Wait For Ther Return Status:

Result:

    SUCCESS

..............................................
```

6.5 If test OK, please NTF test.

7. Appendix

NO#	Equipment Description	ASUS Part#	Supplier	Quantity	Remarks
1	**LCD Monitor with HDMI/Display Port 4096 x 2304 @ 60 Hz**		**local**	**1**	
2	**USB MOUSE**		**local**	**1**	
3	**KEYBOARD**		**local**	**1**	
4	**ATX POWER SUPPLY (a minimum power of 600W)**		**local**	**1**	
5	**Intel CPU Intel(R) I5 7500 CPU**		**local**	**1**	
6	**Heat sink + FAN**		**local**	**1**	

7	**DDR4 2400 4G**		**local**	**2**	
8	**TEST HDD**		**local**	**1**	
9	**SATA HDD Cable**		**local**	**1**	
11	**HDMI cable** **(Buying a local HDMI cable by yourself)**		**local**	**1**	
12	**ASUS Prime B250-plus MB**		**local**	**1**	
13	**Display port cable** **(Buying a local Display port cable by yourself)**		**local**	**1**	