75W/80W/100W GK107/GK106/GM107 128-BIT GDDR5 in x32/x16 MODE DVI-I-DL + DVI-D-DL/HDMI + mHDMI/DP TABLE OF CONTENTS Page Description Table of Contents PCI Express GPU Frame Buffer Partition A/B FBA Partition [31..0] FBA Partition [63..32] FBB Partition [31..0] FBB Partition [63..32] GPU FB Decoupling DACA VGA 10 IFPAB TMDS 11 IFPC mHDMI 12 IFPC DP, IFPD UNUSED, IFPG 13 IFPEF TMDS IFPE Long HDMI 14 GPU Core Decoupling, GPU VDD_3V3 16 Misc Interfaces Straps, FAN Control 18 Power Supply I: Misc Power/PEXVDD LDO (Option) Power supply II:PEX_VDD(Open Vreg) 19 20 Power Supply III: FBVDDQ Power Supply IV:NVVDD CTRL 21 NVVDD MOSFET 22 12V EXT / 12V PEX Power Select Circuit 23 Thermal, Mechanical NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY BASE LEVEL GENERIC SCHEMATIC ONLY Table of Contents SANTA CLARA CA 95050 LISA NV_PN 600-12010-BASE-QS1 ALL WICE DESIGN SECPLEATIONS, REFERENCE SPECPLEATIONS, REFERENCE SPECPLEATIONS, REFERENCE BOARDS, FLES, DRAWINGS, DIAGNOSTICS, LETS AND OTHER DOCUMENTS OR INFORMATION (TOCE) FER AND SEPANATELY, NATERALES) AND EBRING PROVIDED AS IS: THE MIL TERALES MAY CONTAIN MORNING HOUSE AND SECRET AT THE MILITIAN OF THE MILITIAN O PCB REV P2010-B01

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