

V077-0A DESIGN -- G73, 512 MB DDR2, Dual DVI-I, HDTV,HDCP

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REVISION HISTORY:

SCH VER	PCB VER	DATE	Revision Description
0A	0A	August 3,2006	Base on P900-A00 Page10:D-Sub change to slim type Page12:Add single link DVI-I Page20:Change FBVDDQ MOSFET Q8,Q9 from SO8 to TO252 Change 4.7uI C533,C534,C535,C536,C762 from 0603 to 0805
0A	0A	August 4,2006	Page4,5,7,8,add memory clk termination Page11,12,add TMDS EMI solution Page19,20,add H_GATE damping resistor
0A	0A	August 10,2006	Page 9,10,add diode D7,D8,D9,D10 Page 12,remove C832,C833

Base on P900(V077-0A)modify

Page 4,5,7,8 add FBACLK/FBCCLK up and down 電阻
Page 10 D-SUB Change SLIM D-SUB
Page 11,12 New Add EMI Bridge 電阻
Page 12 New Add DVI 線路
Page 18 New A3V3 PASS TMDS_IOVDD
Page 19,20 New Add PWM 與 HG MOS 之間加電阻
Page 9,10 Add Diode D7,D8,D9.d10

REV	VARIANT	NPVN	ASSEMBLY
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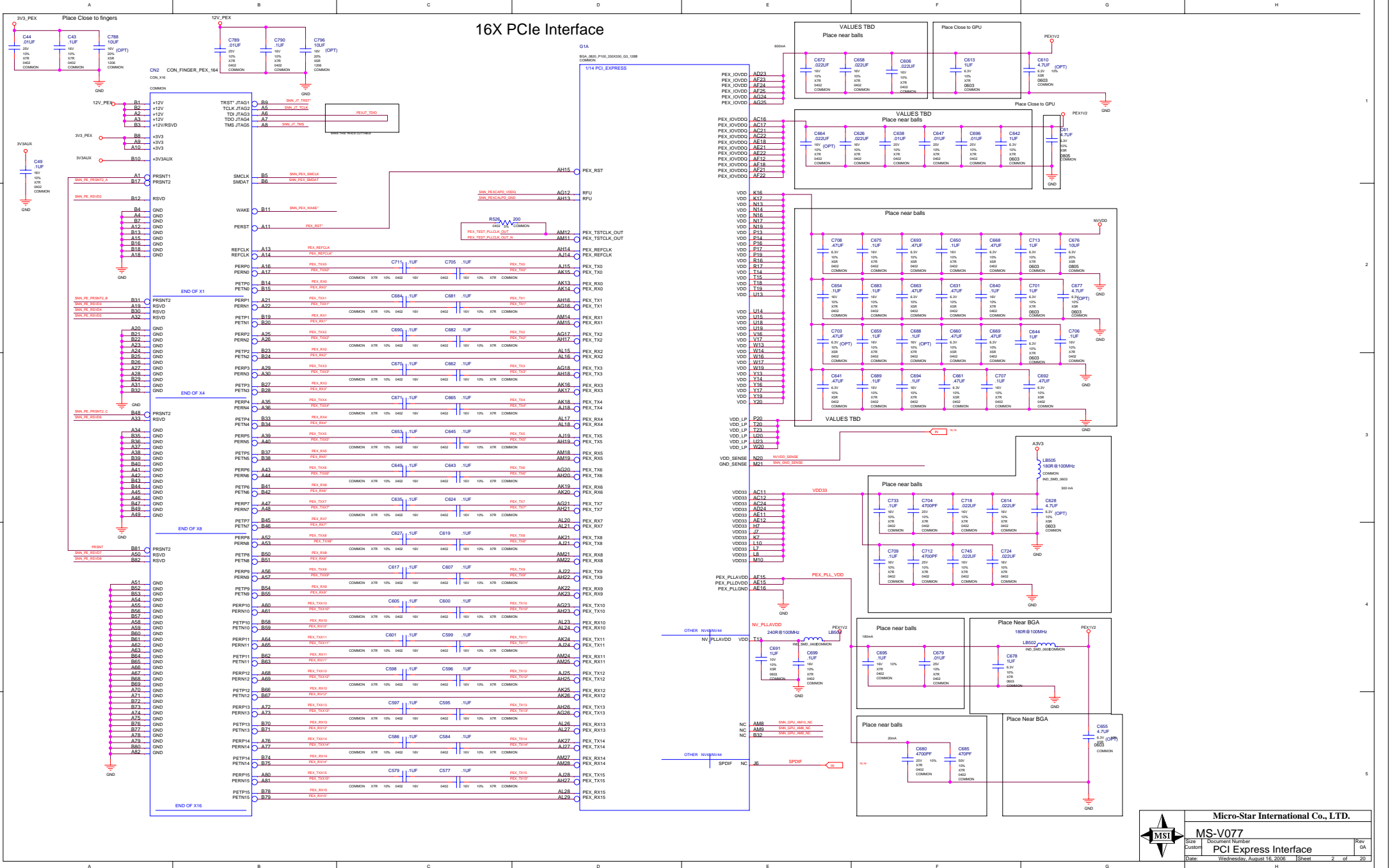


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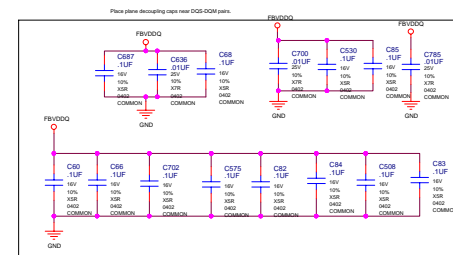
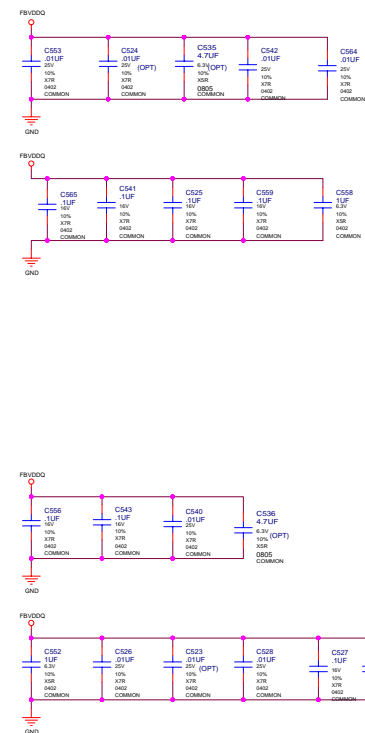
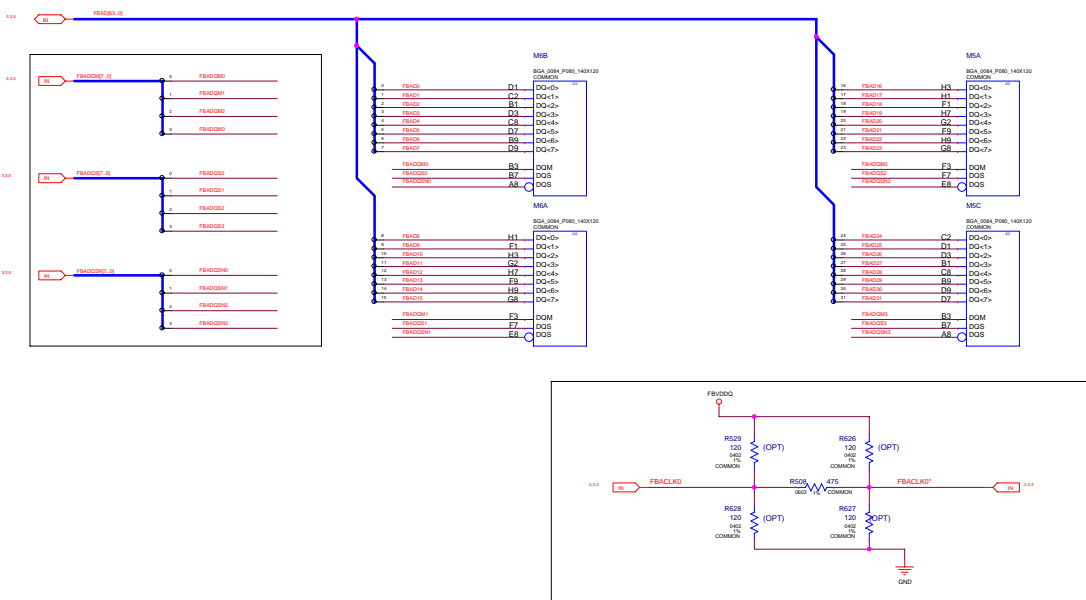
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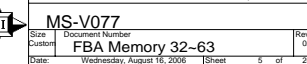
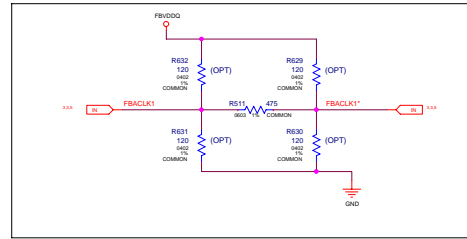
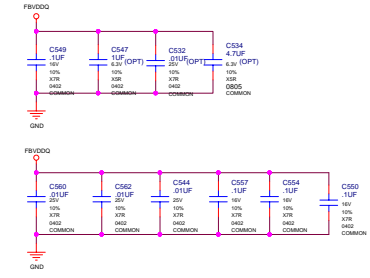
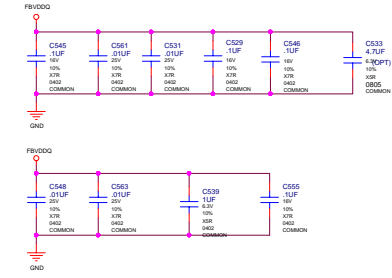
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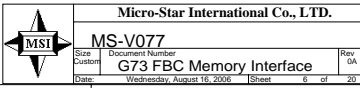
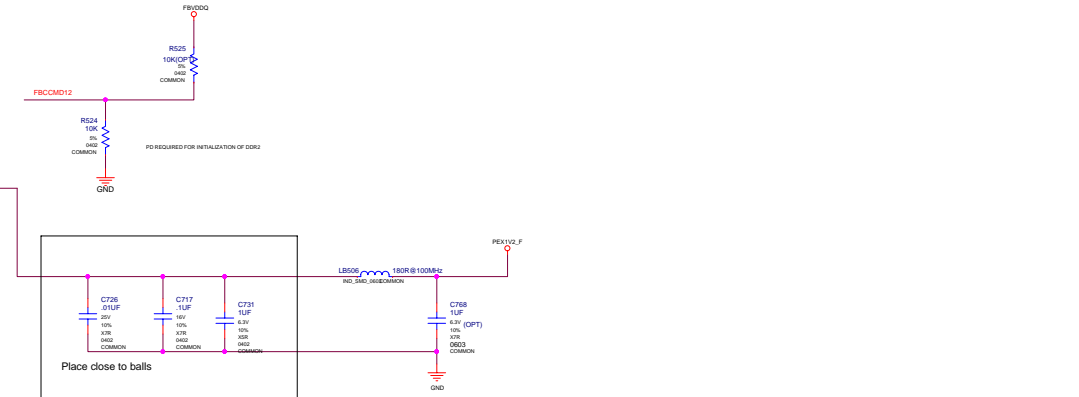


PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY

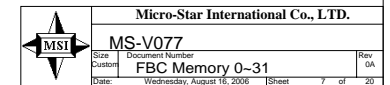


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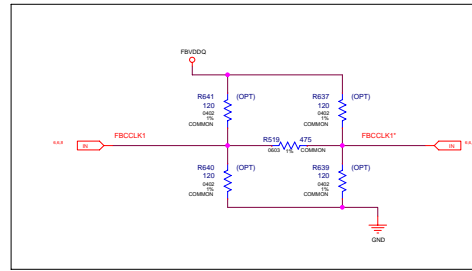
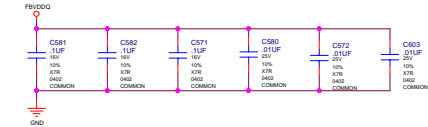




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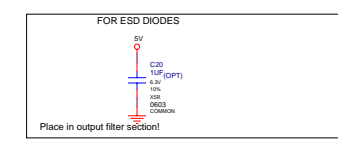
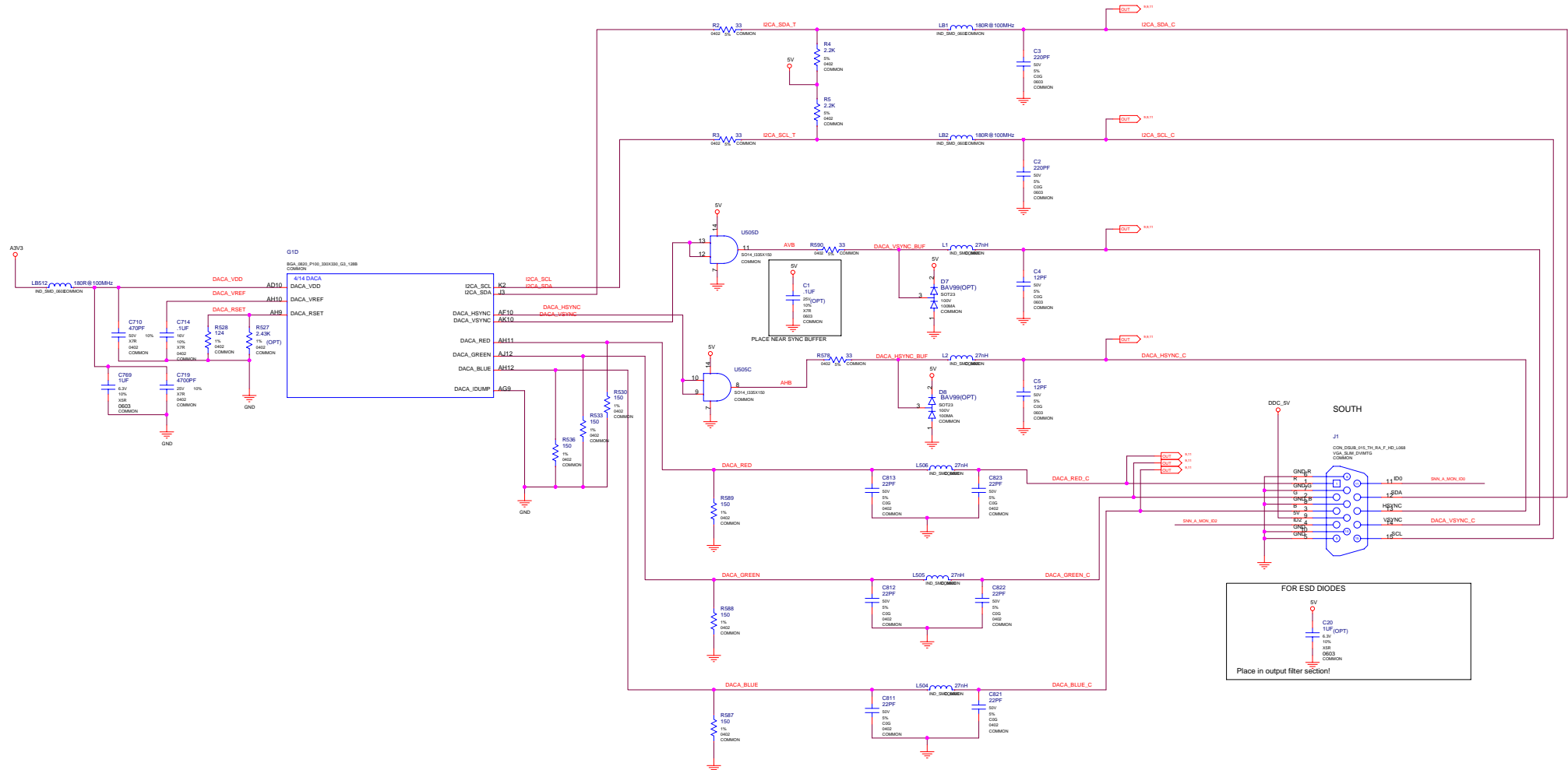


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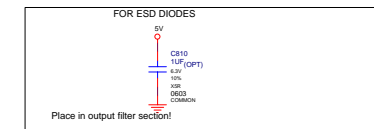
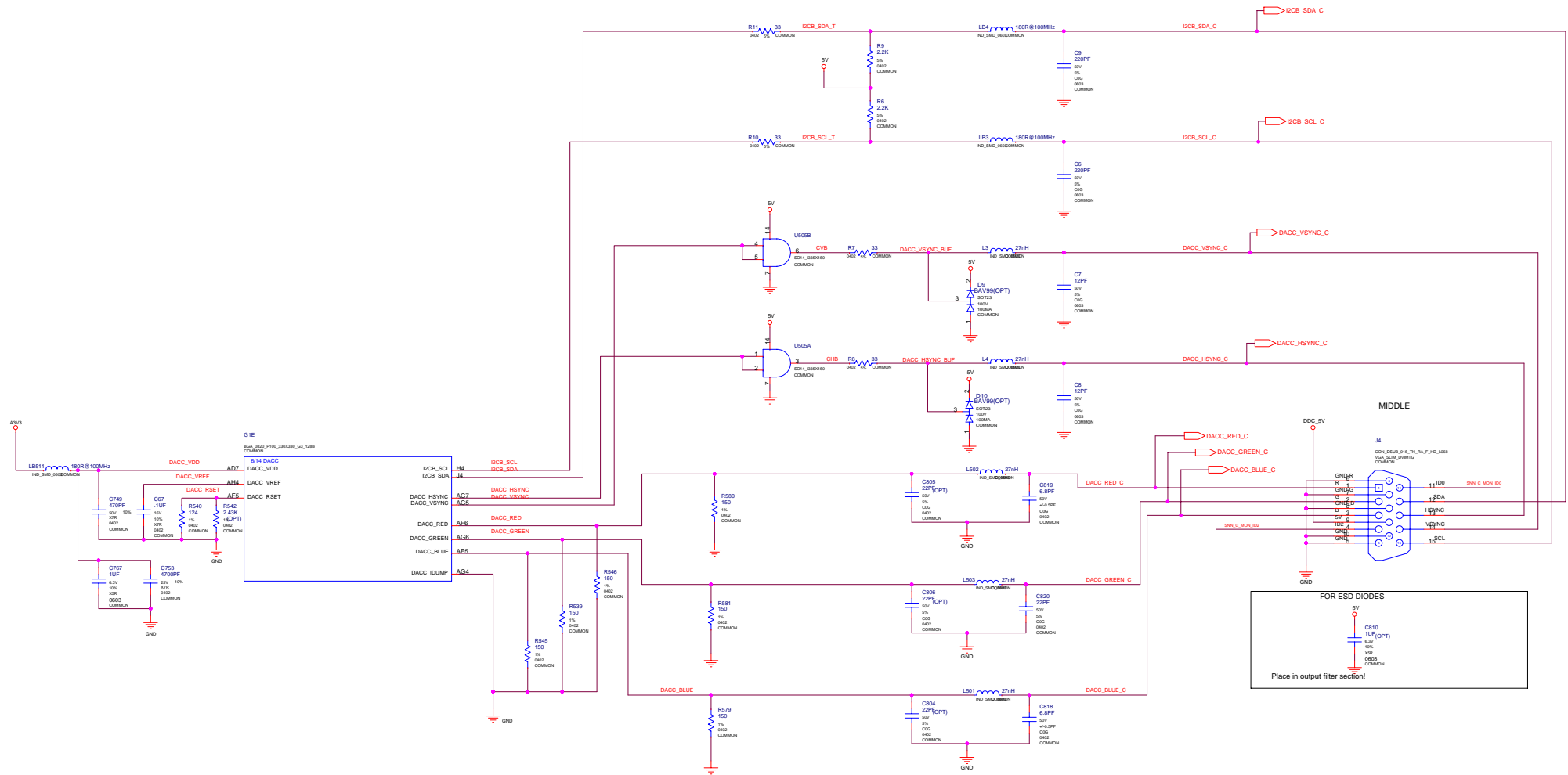
Primary Display (DACA), Slim DB15

DACA RGB-FILTER

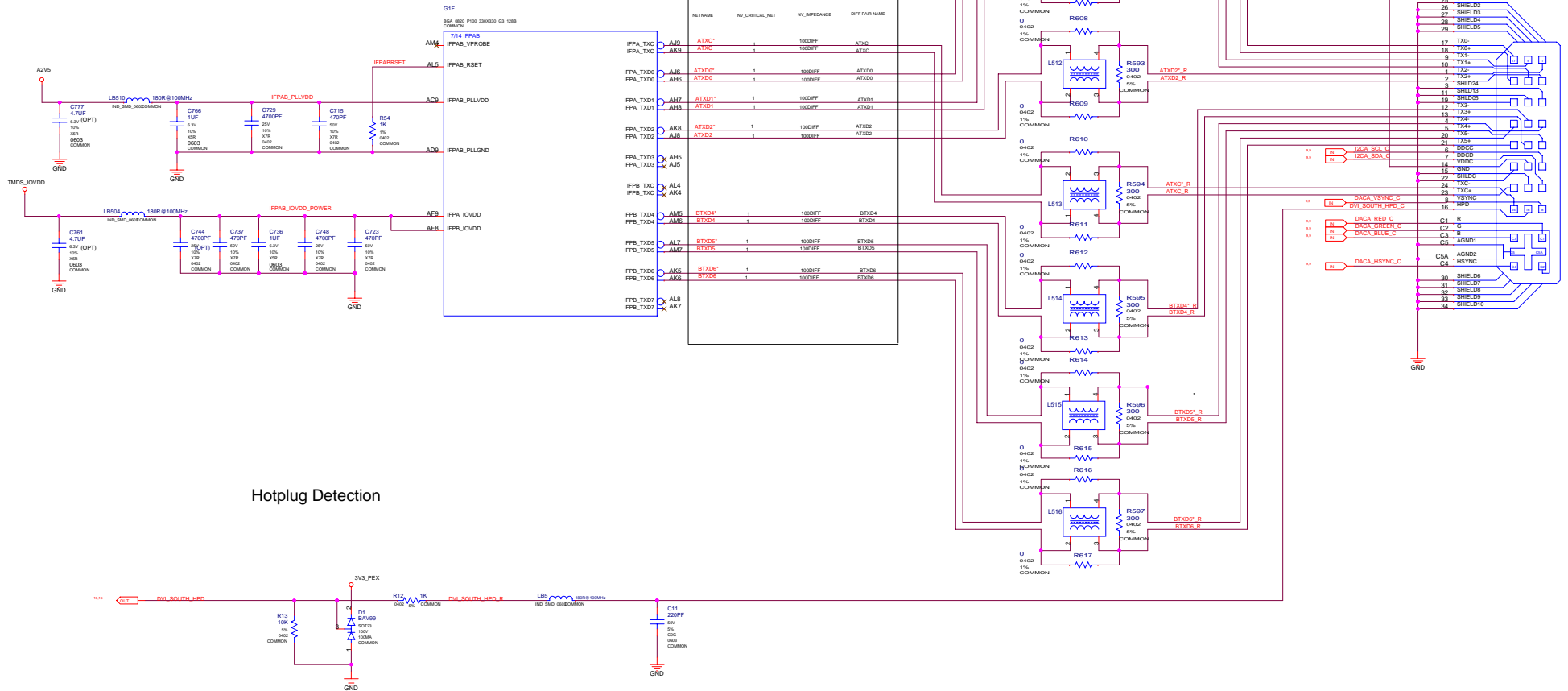


Secondary Display (DACC), DB15

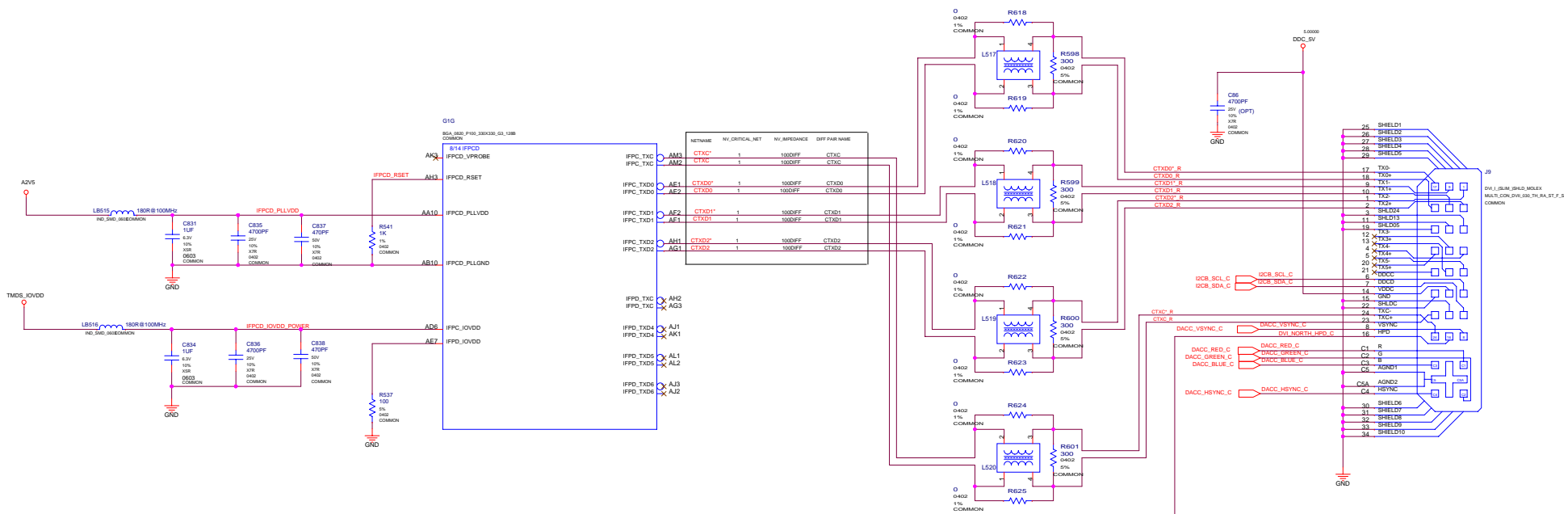
DACC RGB-FILTER



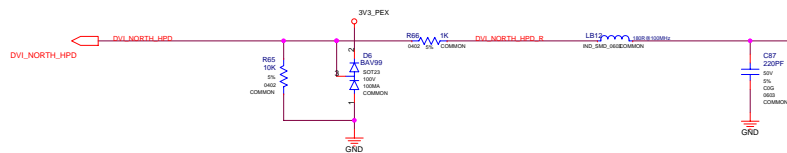
INTERNAL TMDS .. LINK A & B



INTERNAL TMDS .. LINK C&D



Hotplug Detection



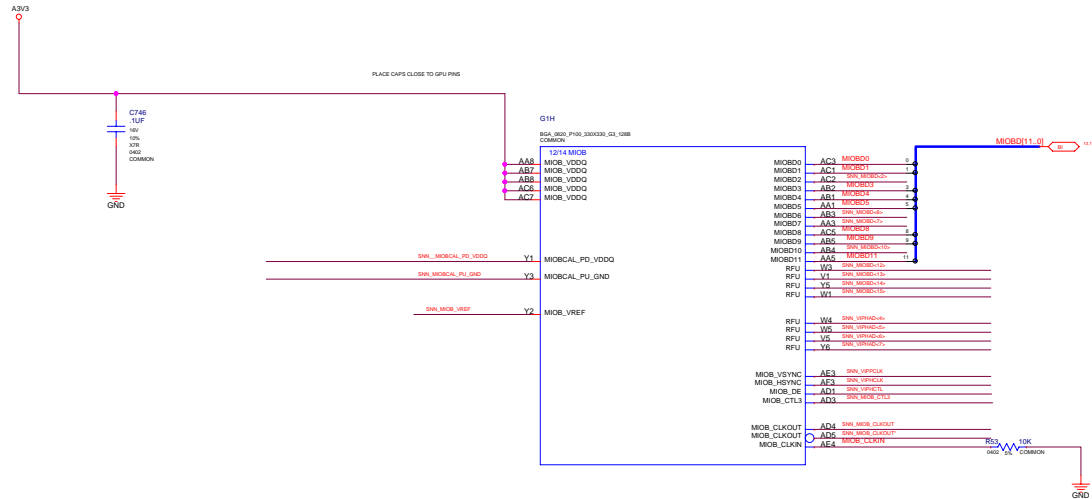
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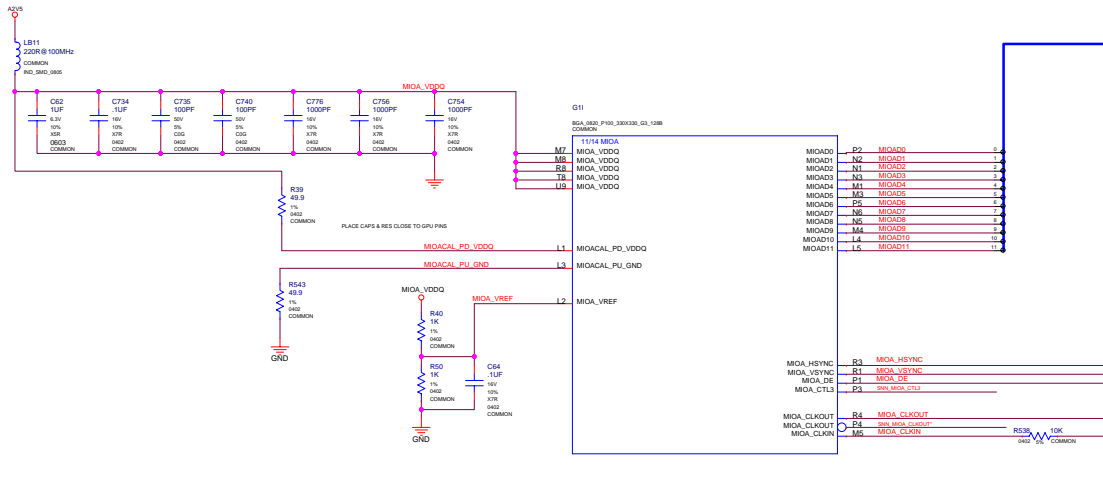
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Internal TMD5 LINK C,D

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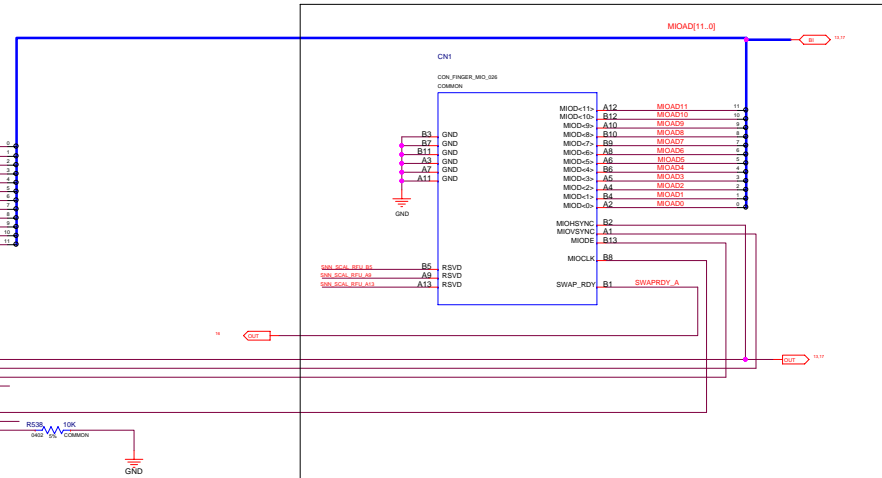
G3 VIP/MIOB



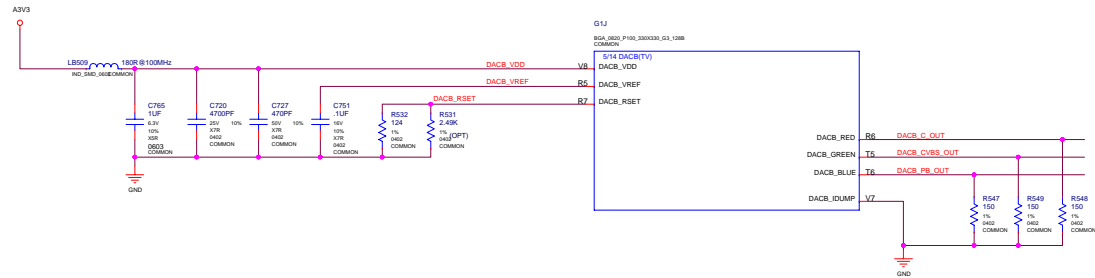
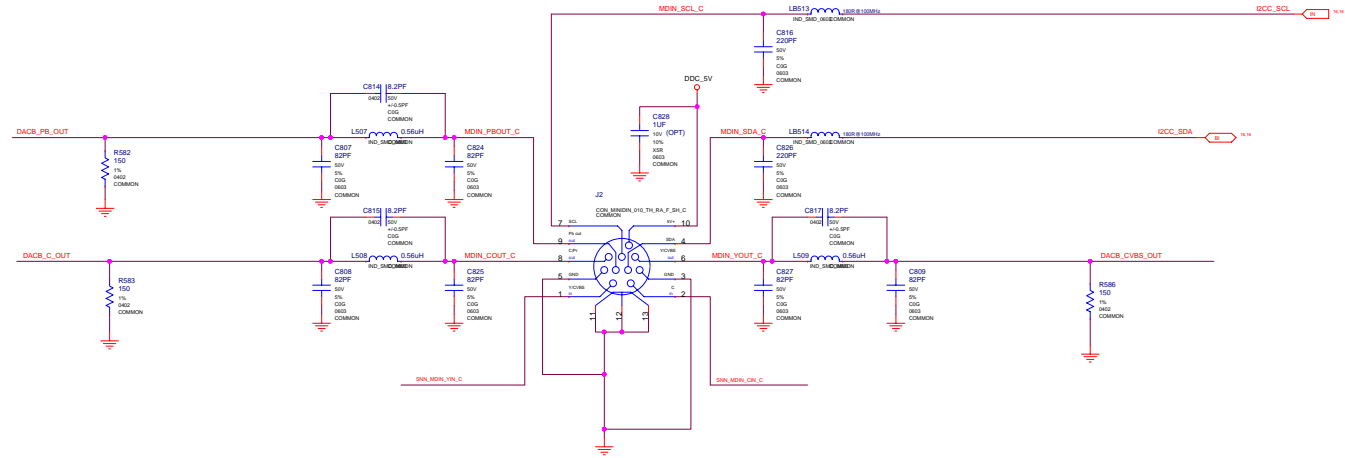
G3 MIOA

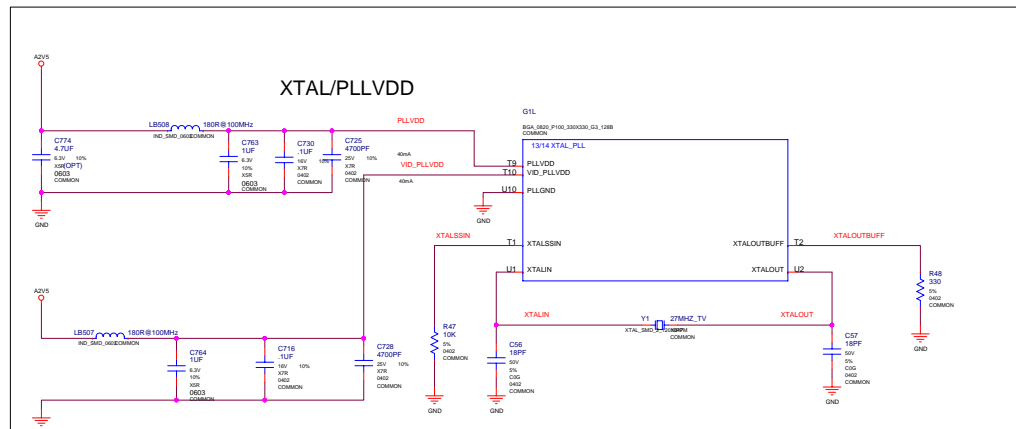


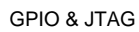
SLI Connector



DACB .. MiniDIN VIDEO OUT CONNECTOR



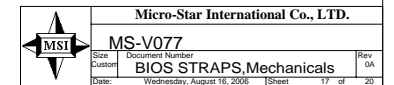
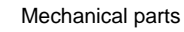




GPIO Assignment Table	
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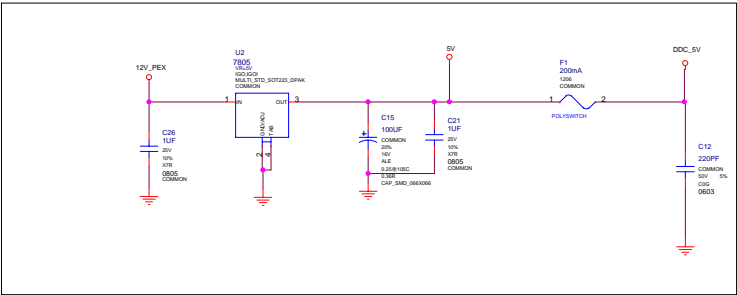


Assembly: BIOS

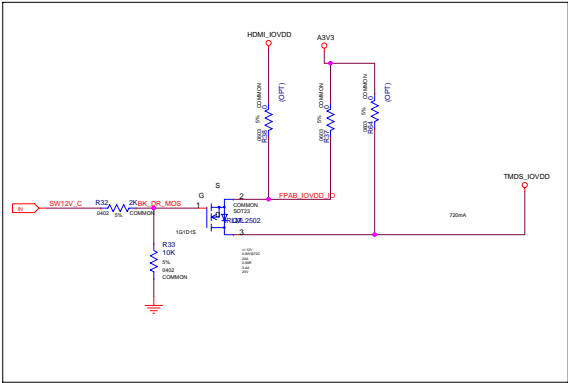


Power Supply:TMDS_IOVDD A3V3 5V

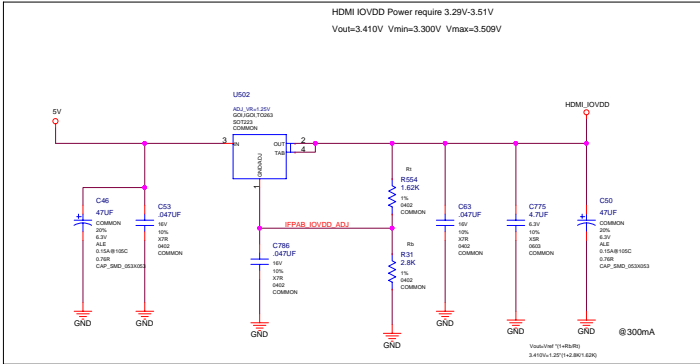
DDC 5V



TMDS IO SUPPLY WITH BACKDRIVE PROTECTION



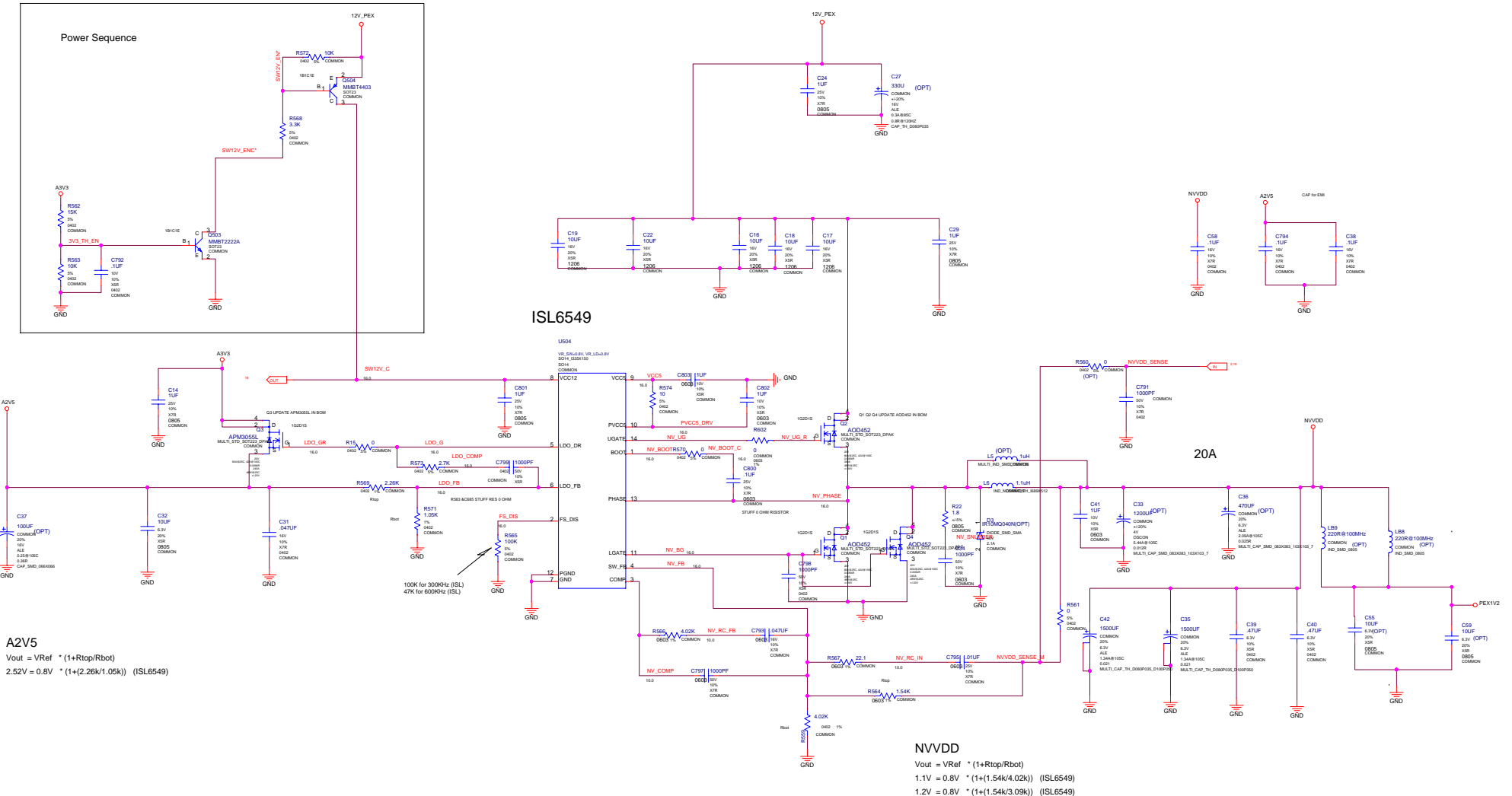
HDMI IO SUPPLY



A3V3 Power Supply



PowerSupply1: NVVDD, A2V5



PowerSupplyIII: FBVDDQ, PEX1V2

