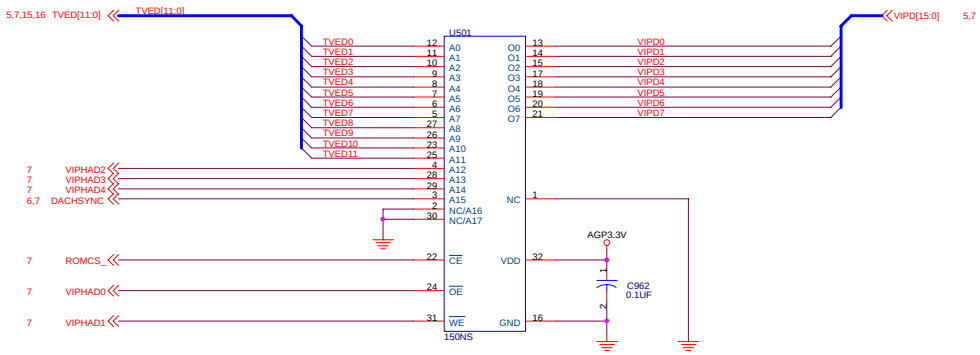

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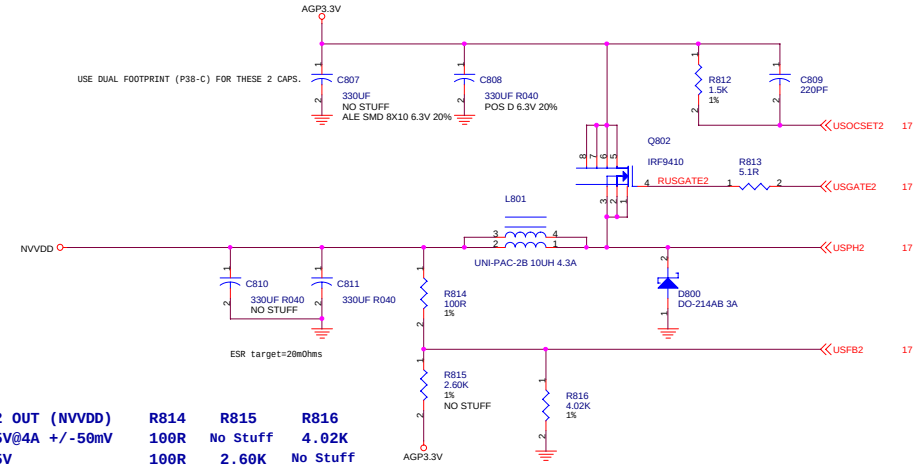

	SWR2 OUT (NVVDD)	R814	R815	R816
DEFAULT-->	2.05V@4A +/-50mV	100R	No Stuff	4.02K
	1.95V	100R	2.60K	No Stuff

R816=100/((2.05V/2)-1)

R815=130/(2-1.95v)

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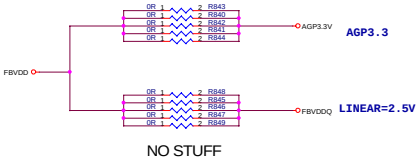
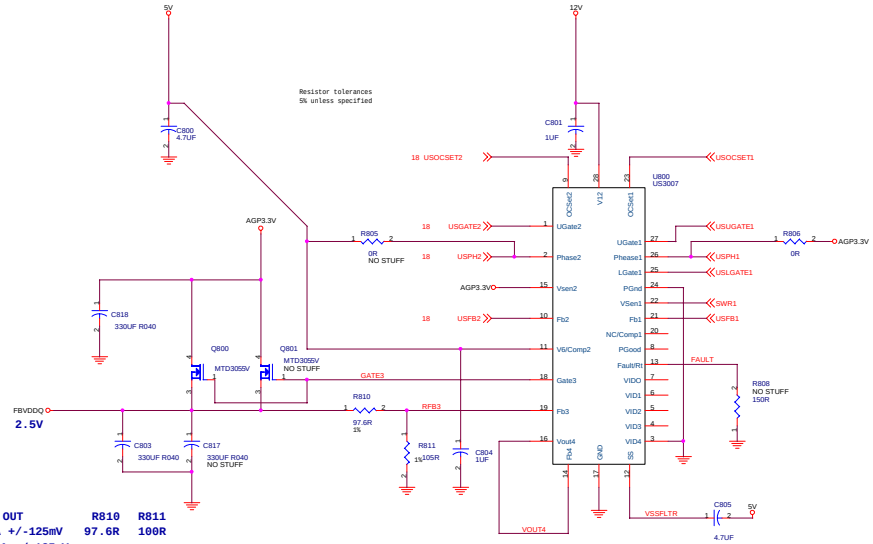
NVIDIA



NO STUFF



LINEAR OUT	R810	R811
2.50V@1.5A +/-125mV	97.6R	100R
2.378V@1.5A +/-125mV	97.6R	110R
2.431V@1.5A +/-125mV	97.6R	105R

R810=((LINEAR OUT/1.26)-1)*(R811)

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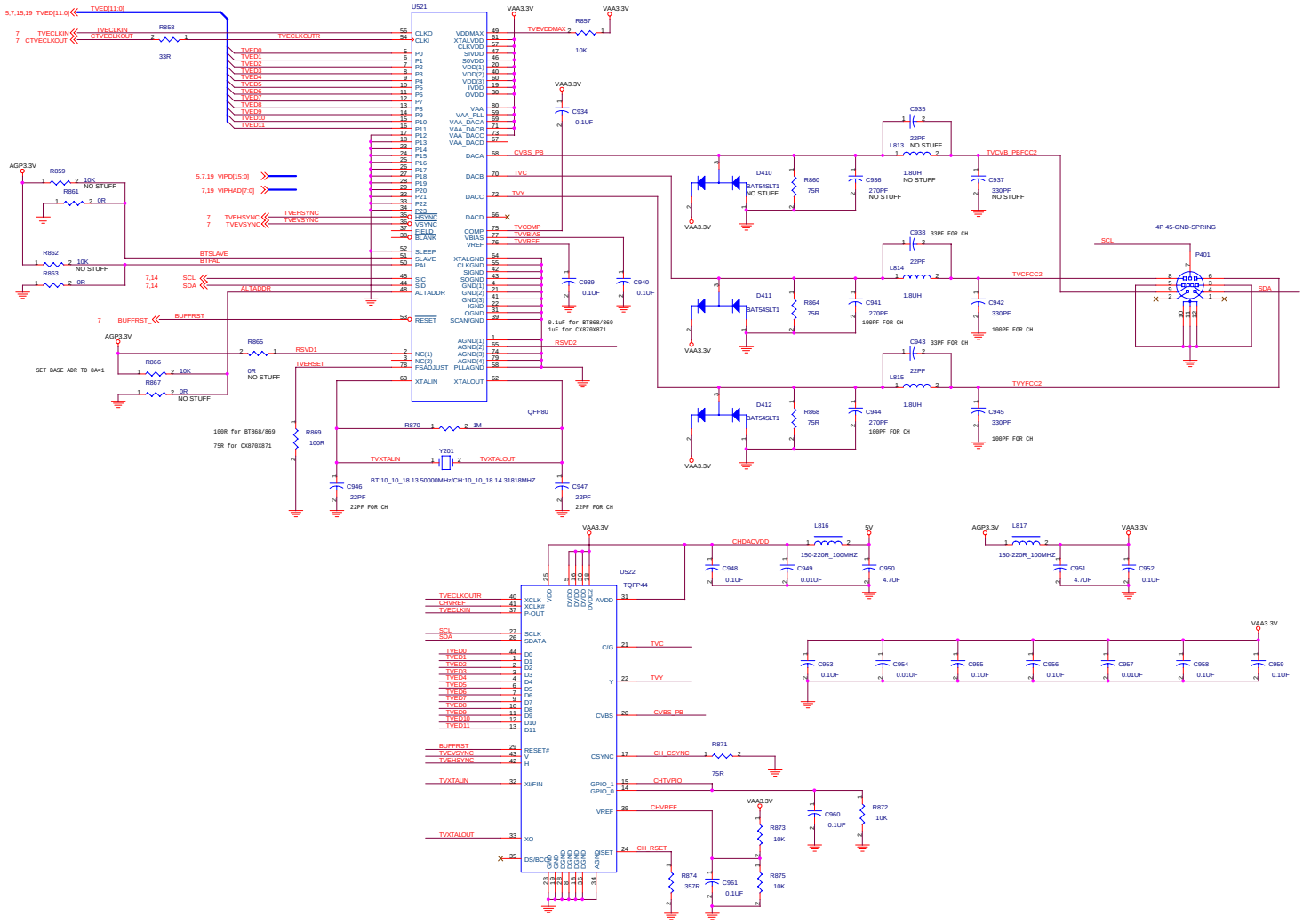

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Size: C	Document Number: TV ENCODERS
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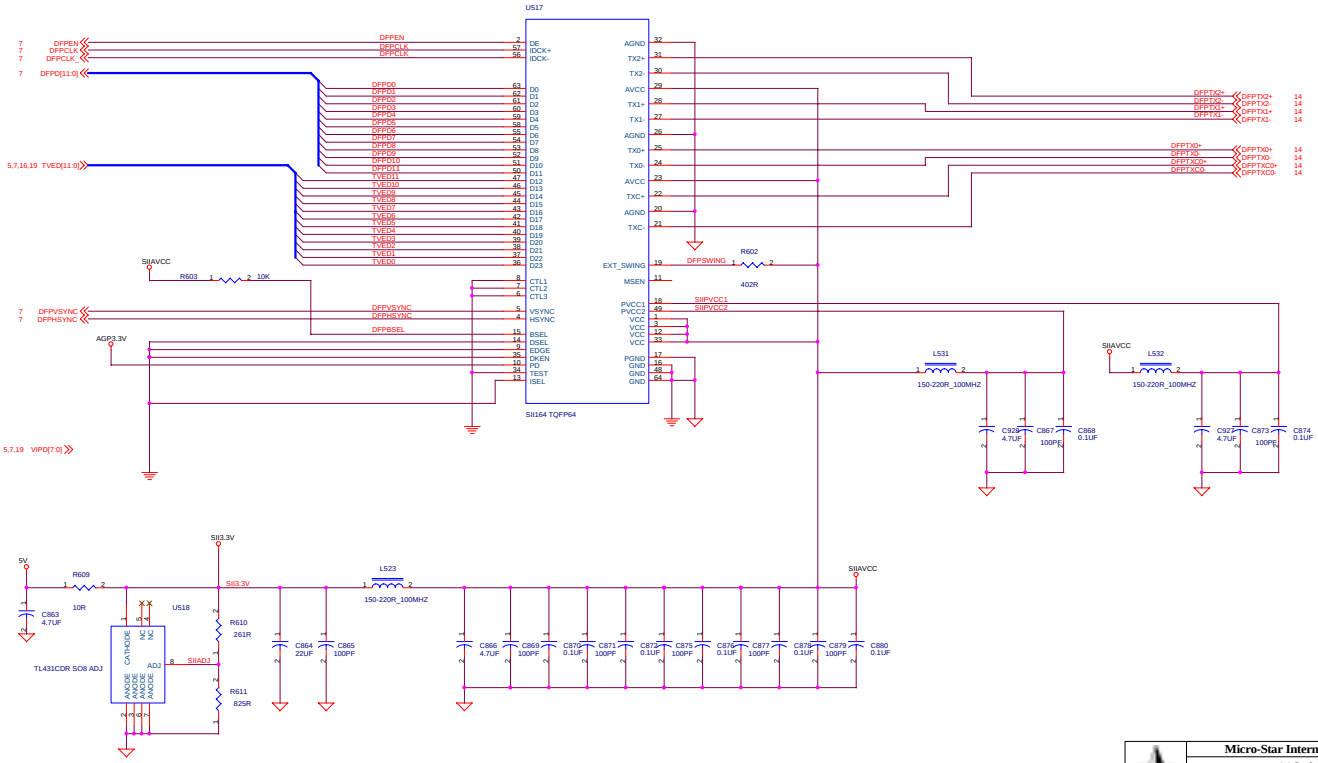

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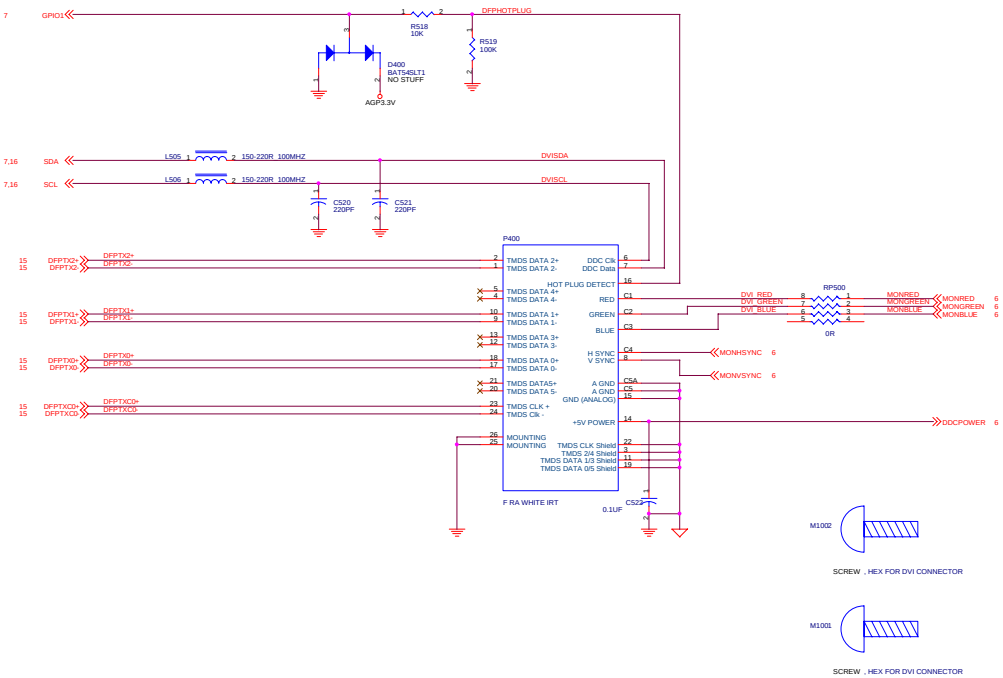
TVED[11:0] 5,7,15,16,19
ROMCS_ 7,19

DFPCLK_ 7,15
DFPD[11:0] 7,15
DFPVSYNC 7,15



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MS-8820			
Size: C	Document Number: DVI-I I/O		Rev: 1.0
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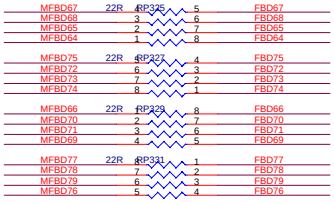
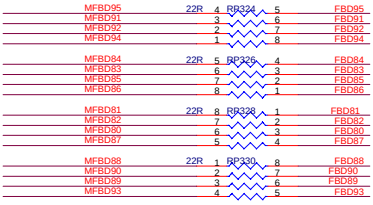
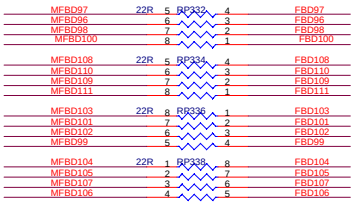
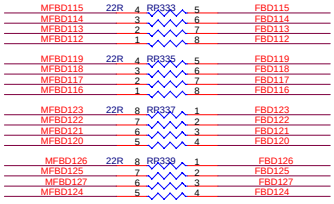




MFBD[127:0] 10,11,12
MFBDQM[15:8] 12

FBD[127:0] 8,11
FBDQM[15:8] 8





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MS-8820		
Size: B	Document Number: FBB DDR 2Mx32 SDRAM TERM	Rev: 2.0
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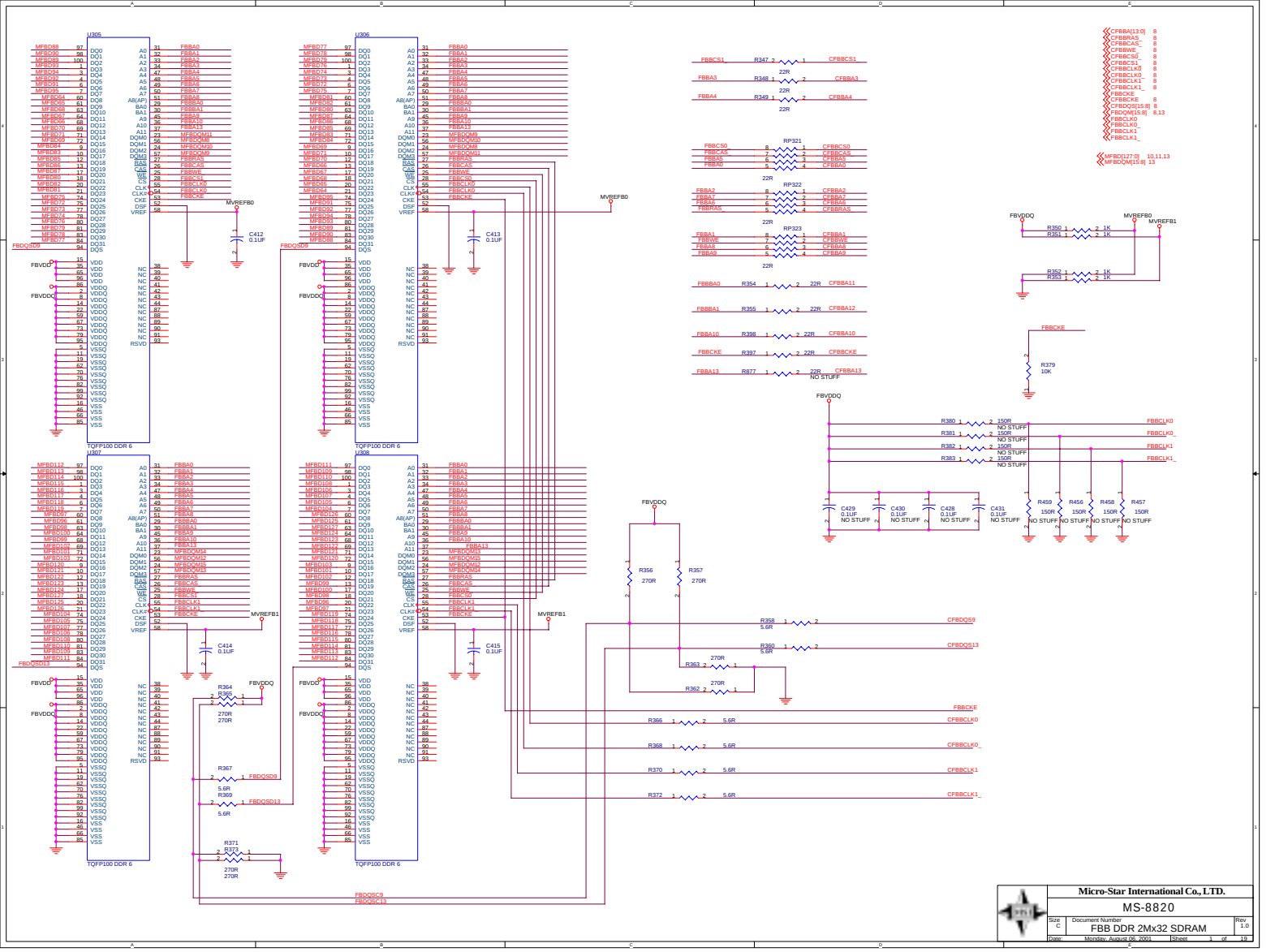
NVIDIA



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MS-8820			
Size: C	Document Number: FBB DDR 2Mx32 SDRAM		Rev: 1.0
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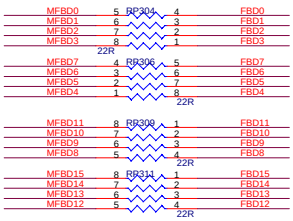
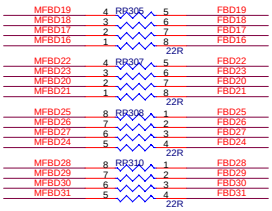
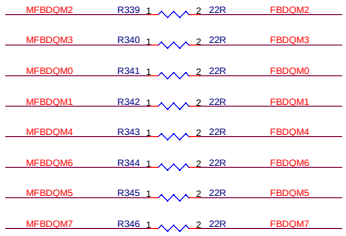
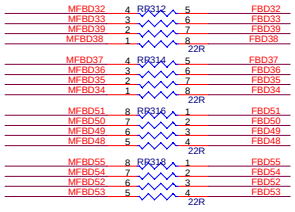


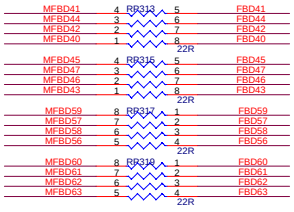


MFBD[127:0] 10,12,13
MFBDQM[7:0] 10

FBD[127:0] 8,13
FBDQM[7:0] 8







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Size B	Document Number: FBA DDR 2Mx32 SDRAM TERM	Rev 1.0
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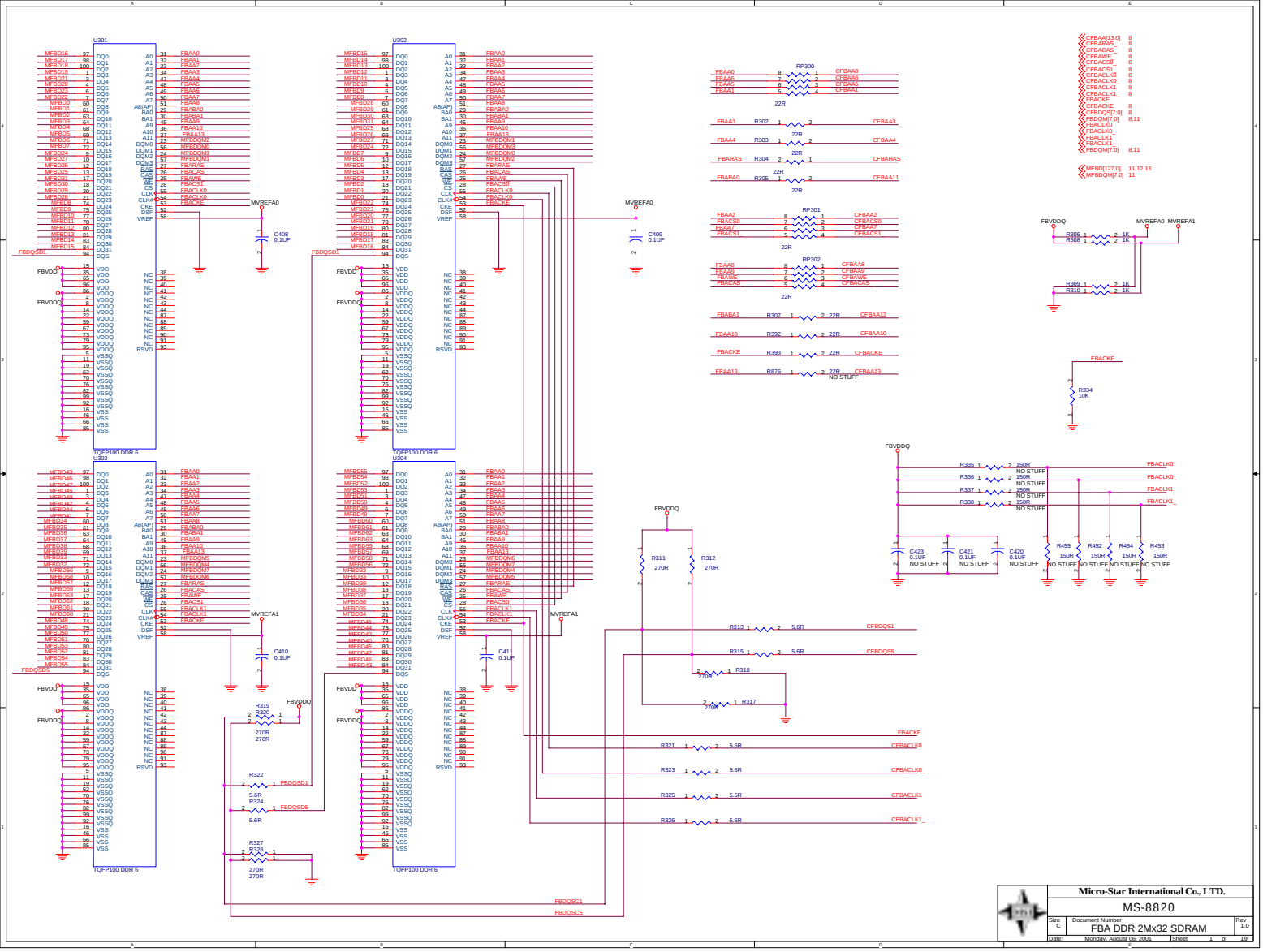

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Size: C	Document Number: FBA DDR 2Mx32 SDRAM
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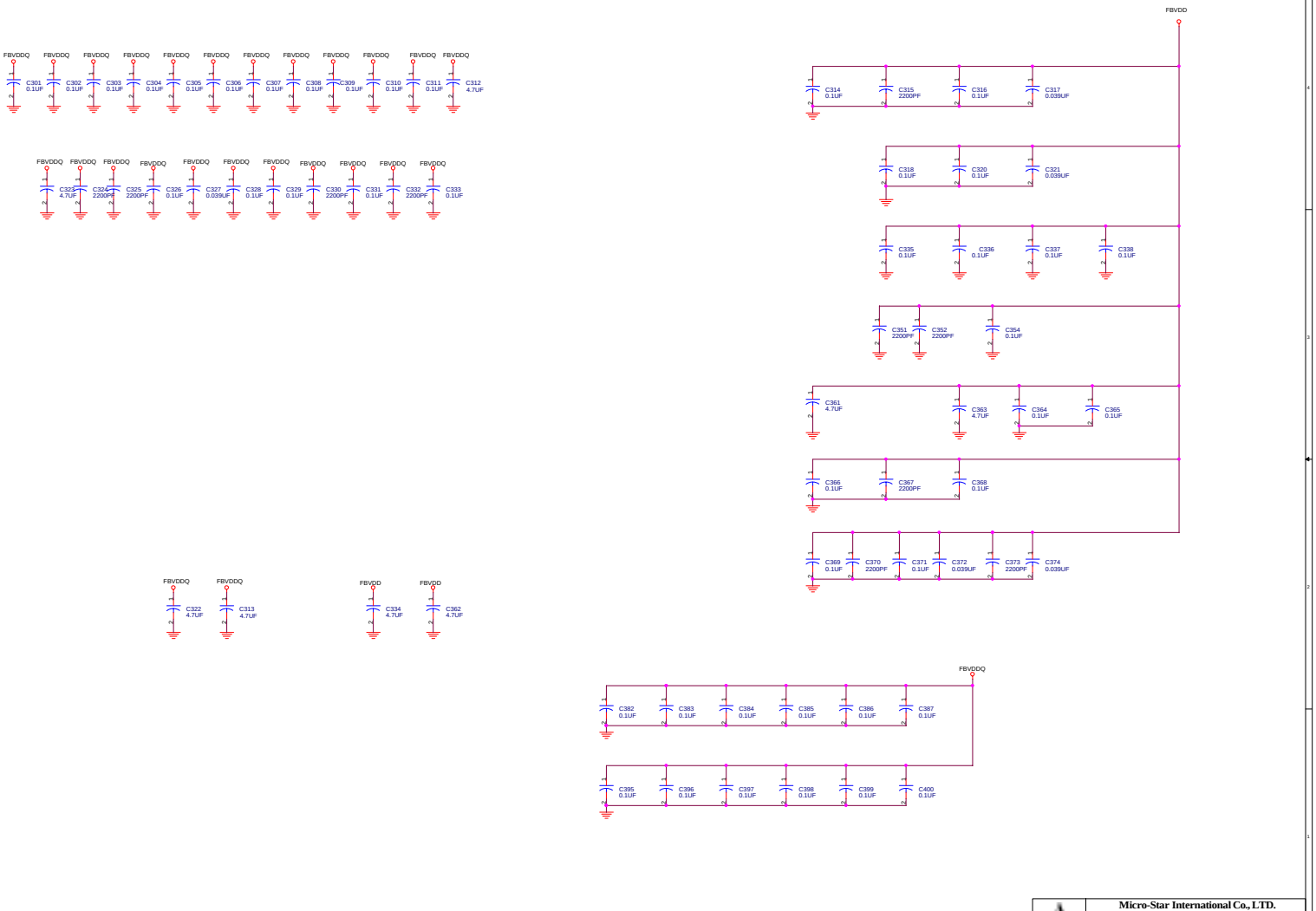

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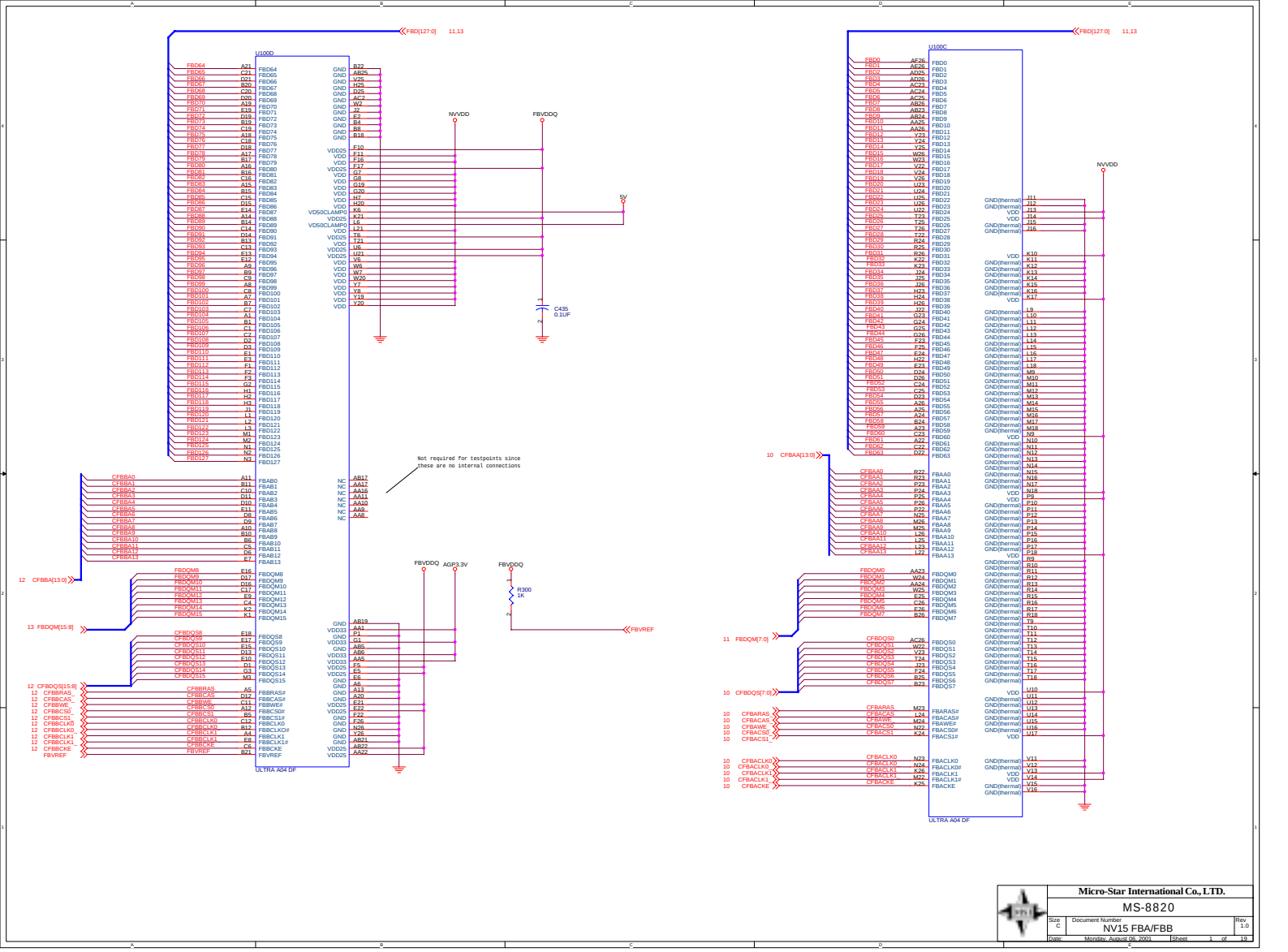

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MS-8820	
Size: C	Document Number: NV15 FBA/FBB
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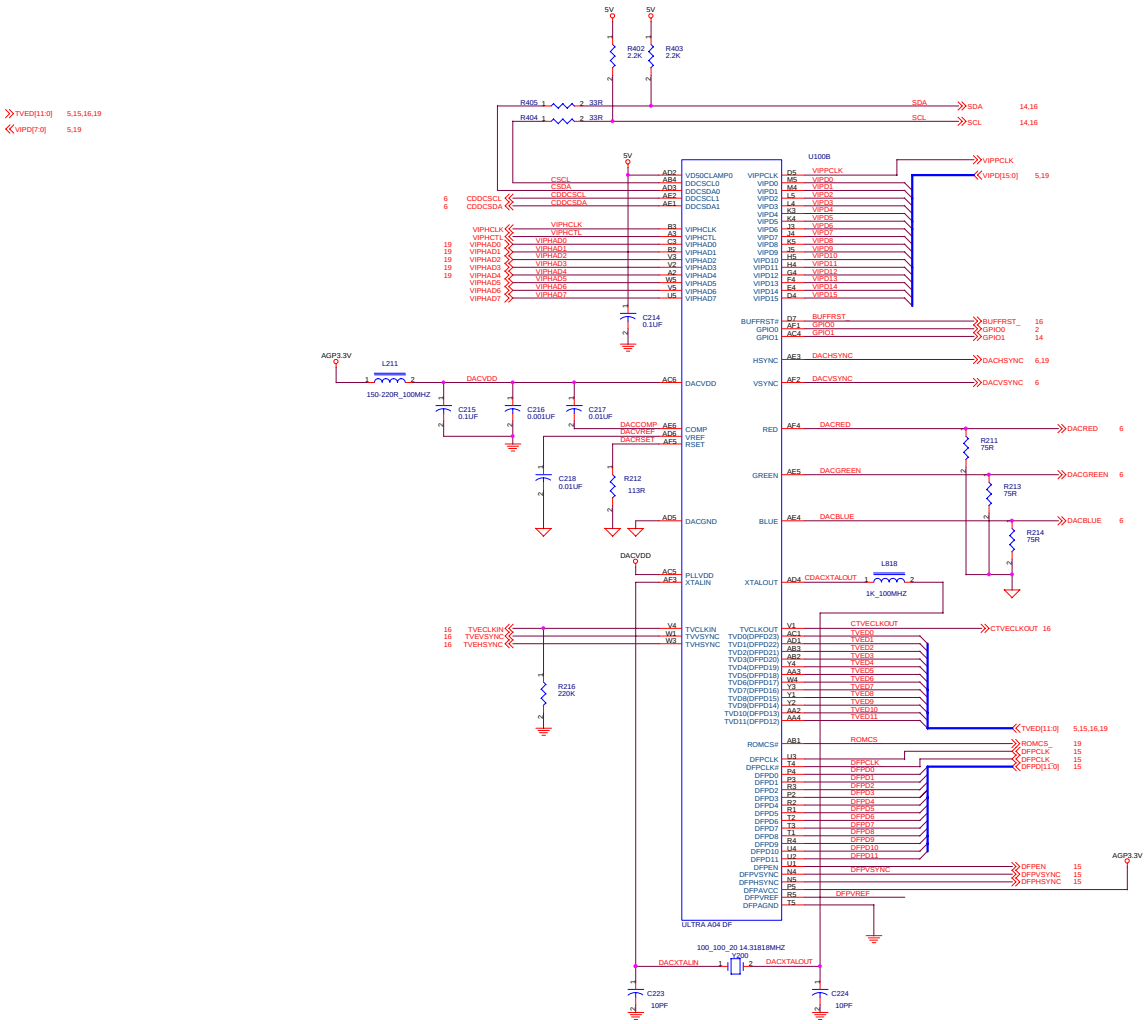

Use thick (non-impedance controlled) traces on XTALIN/OUT

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Size: C	Document Number: NV15 DAC & TV		Rev: 1.0
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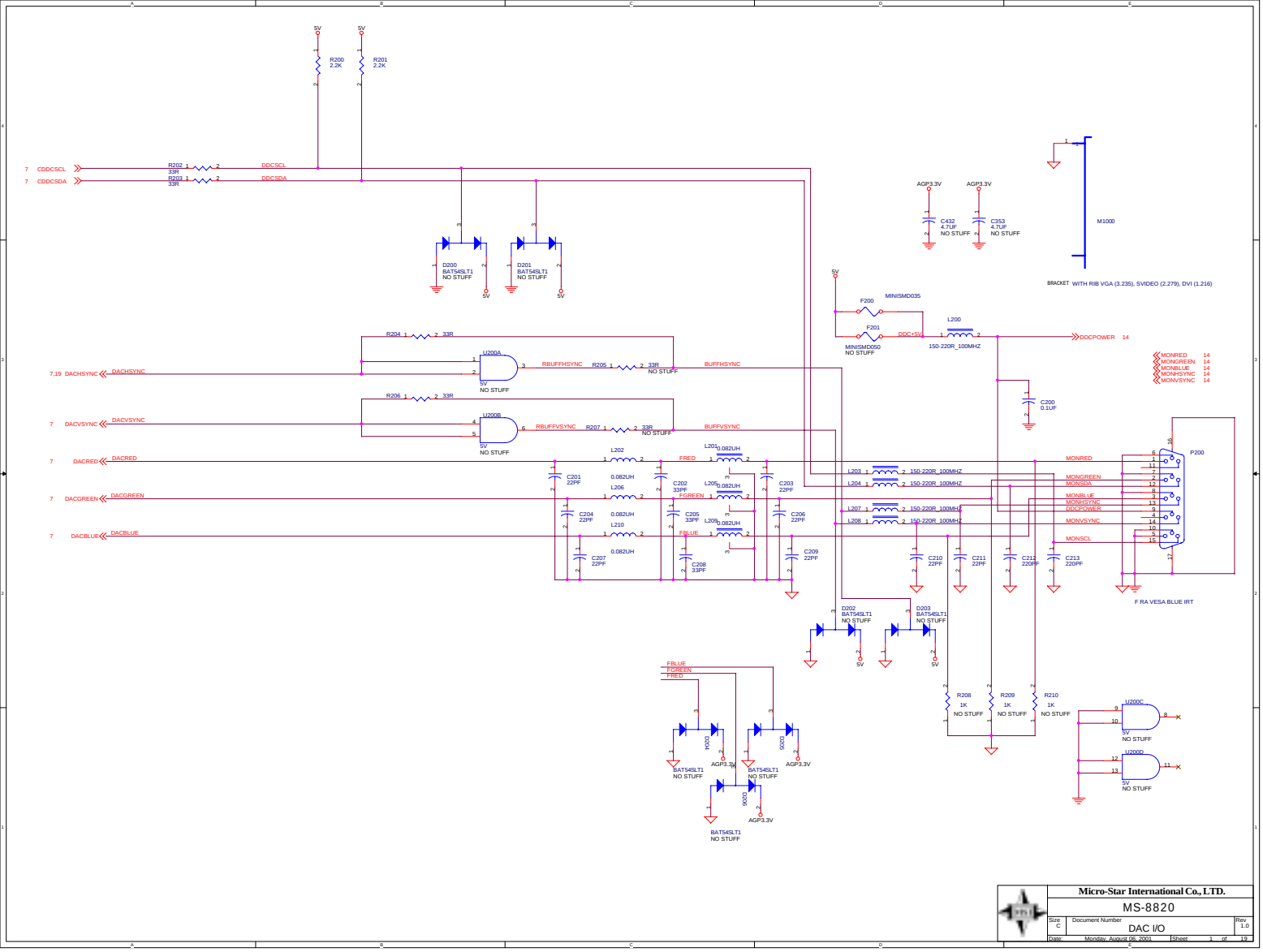

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	Micro-Star International Co., LTD.	
	MS-8820	
Size B	Document Number: NV15 HOST STRAPS	Rev 1.0
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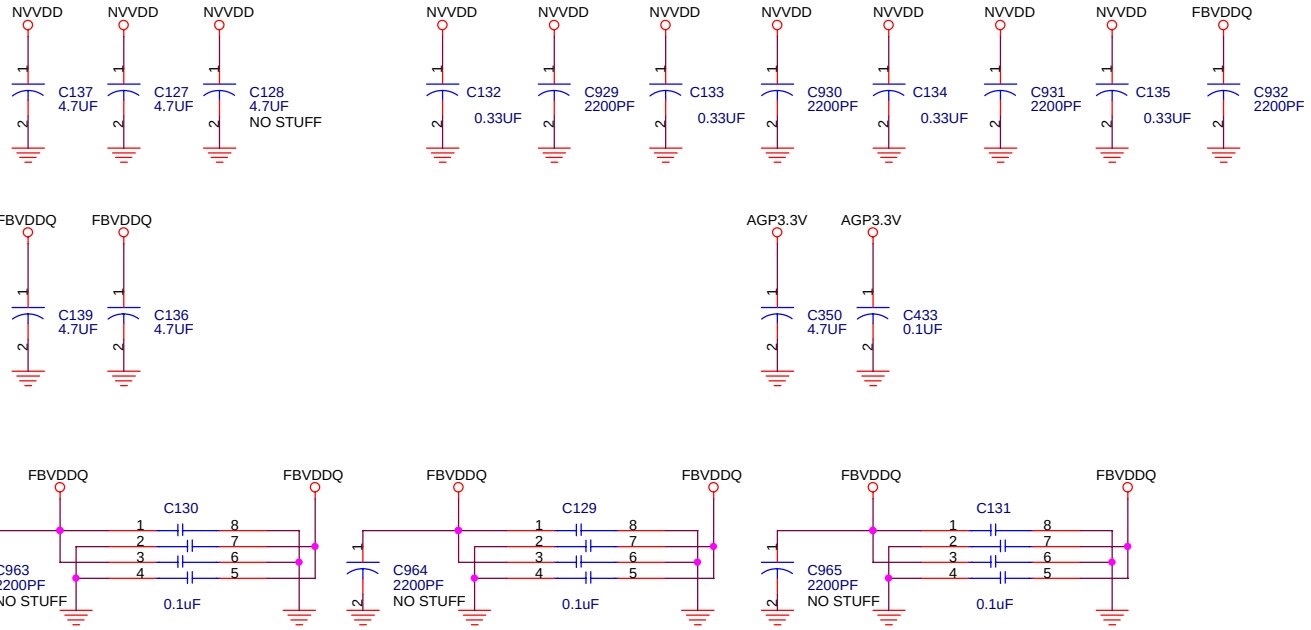

	Micro-Star International Co., LTD.	
	MS-8820	
Size: A	Document Number: NV15 DECOULPING	Rev: 1.0
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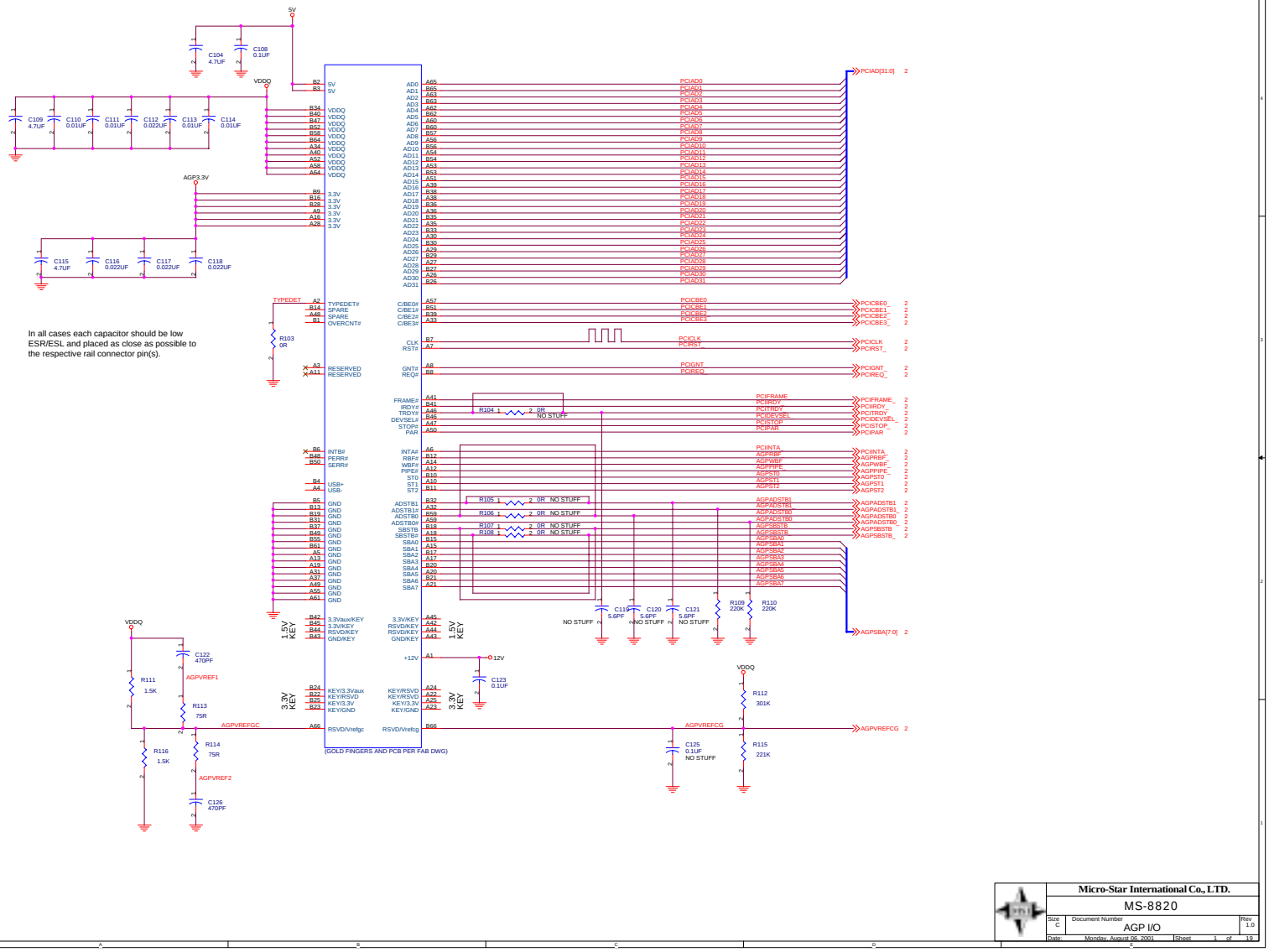

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MS-8820		
Size: C	Document Number: AGP I/O	Rev: 1.0
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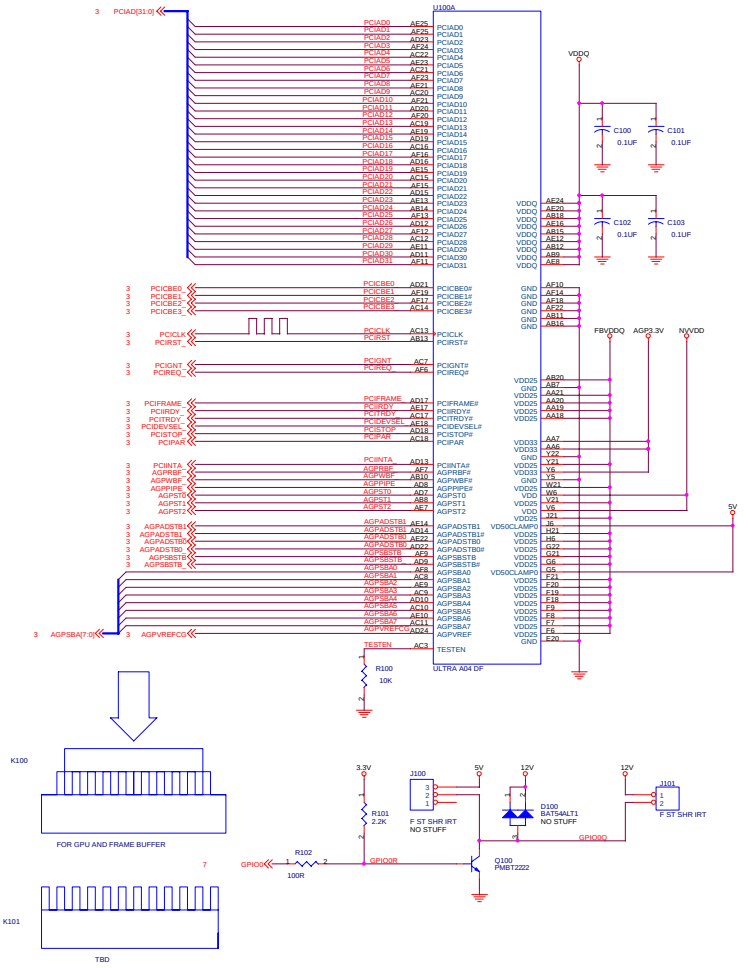

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NV15, 1/2MX32 DDR, RGB, TV_OUT, VIDEO_IN, AGP4X.

PCI DEVICE ID 0X0=0X150.

Design Notes:

B00:FIXED TMDS CLKs CONNECTIONS.
B00:ADDED 4MX32 SUPPORT.
B00:Corrected memory configuration resistors.
B00:Changed C809 to 4.7UF.
B00:Changed R811 from 100R to 105R
B00:C128 changed to no stuff
B00:Removed 4mx32 function (R876, R877= NO STUFF)

	Micro-Star International Co., LTD.	
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Size B	Document Number: Based on P-22-B00	Rev 1.0
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NVIDIA