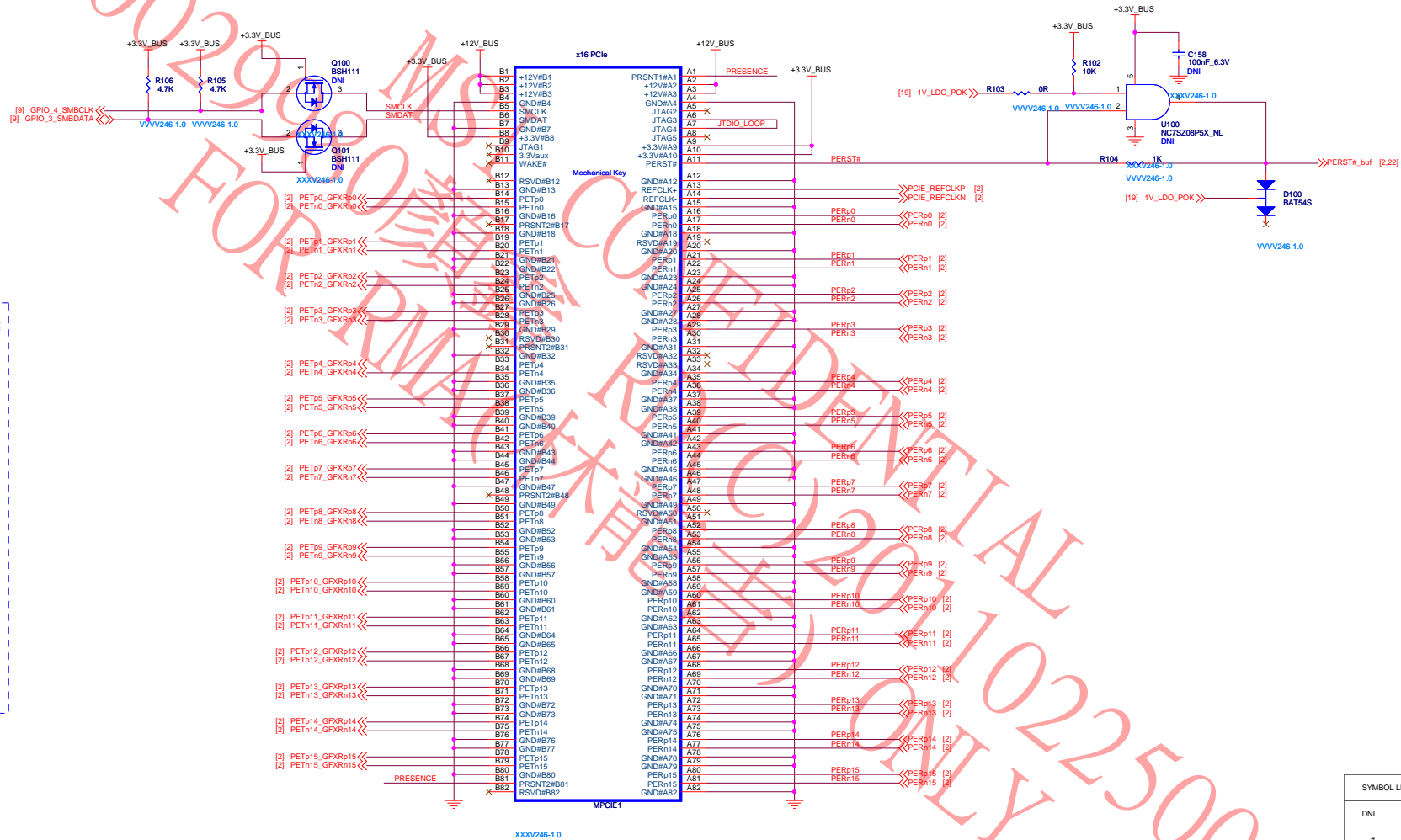


PCI-EXPRESS EDGE CONNECTOR

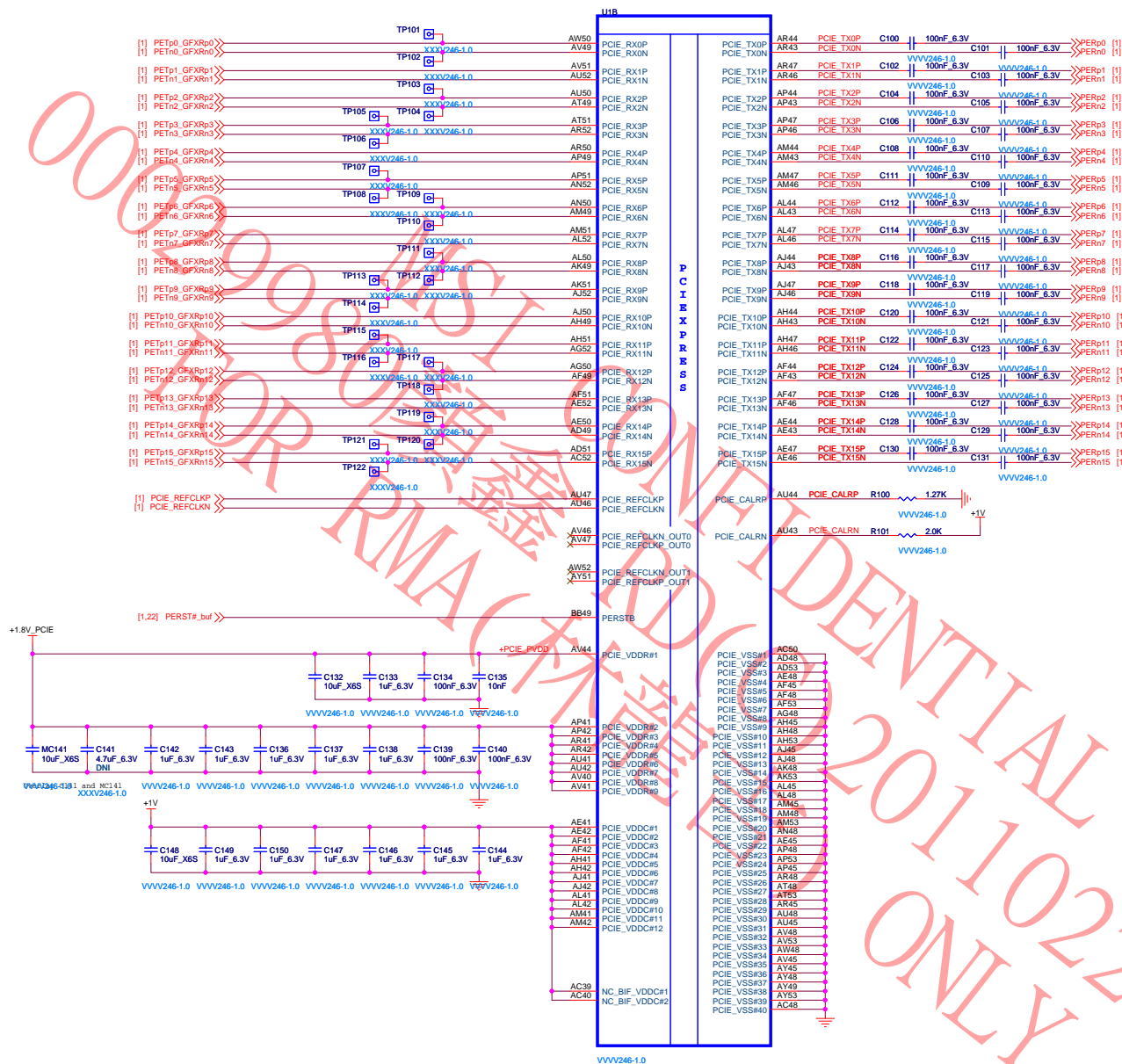
PCIe RESET Buffered



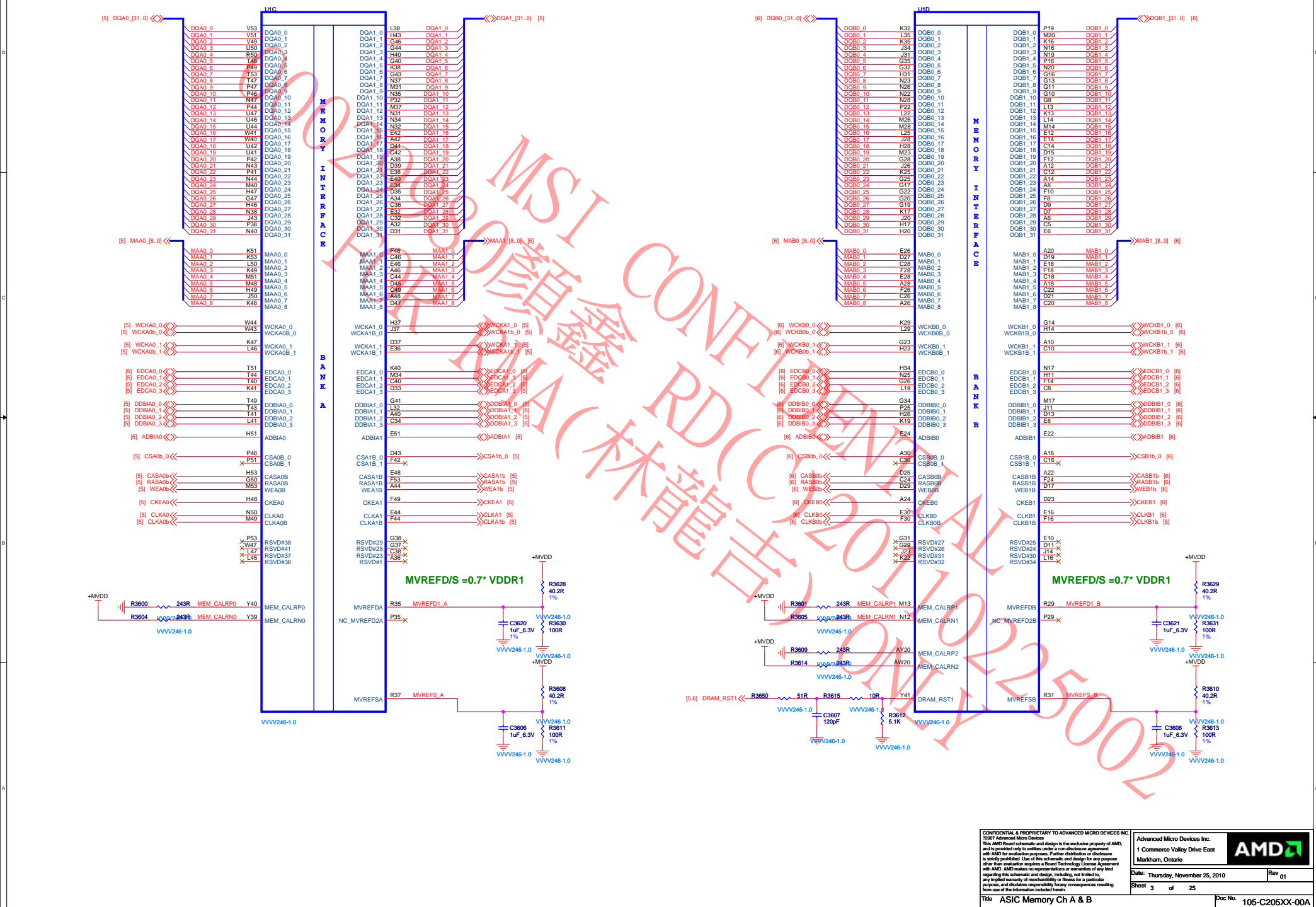
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 Rev 01

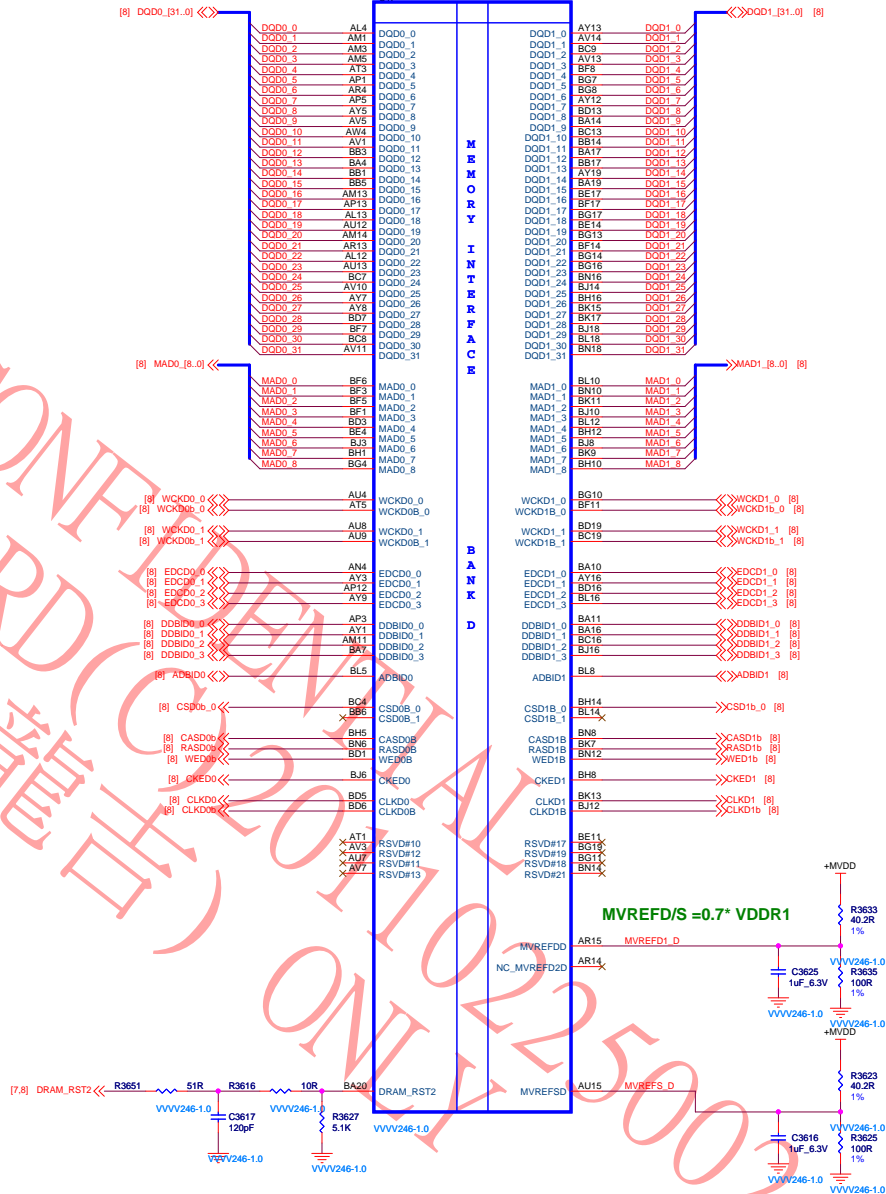
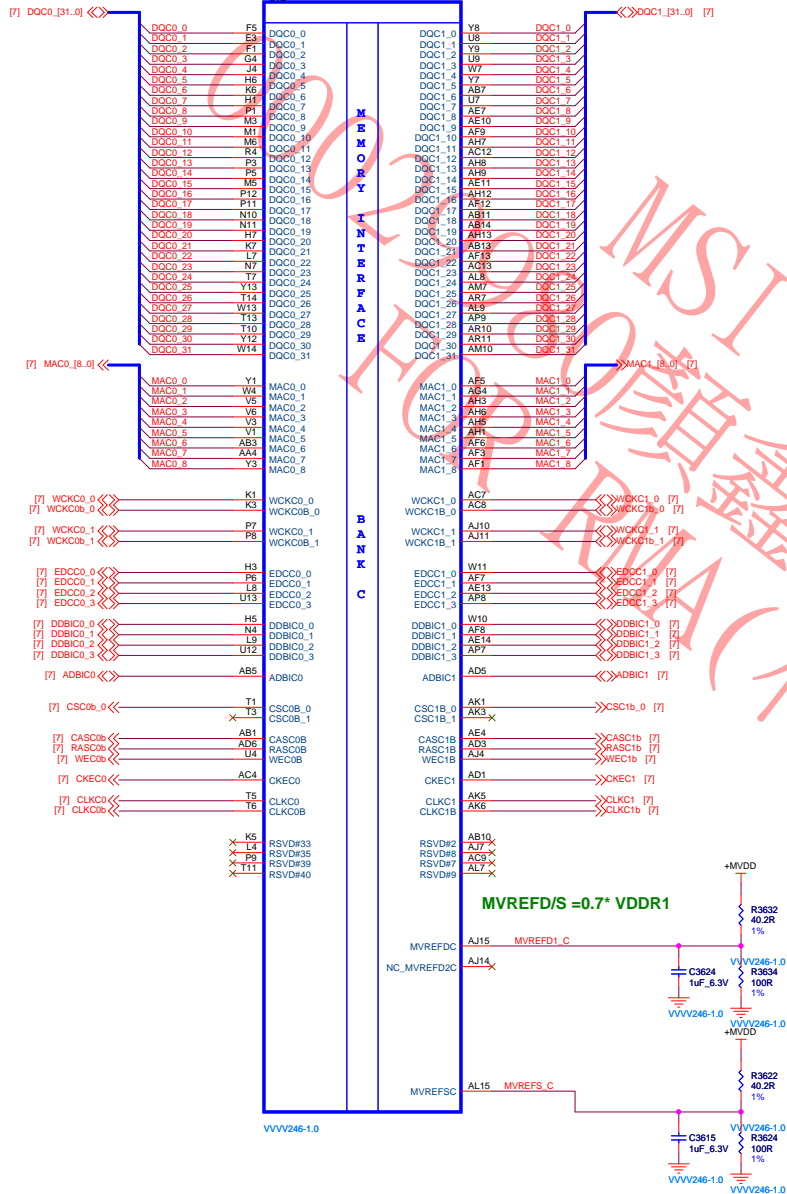
(2) CAMEN PCIe Interface



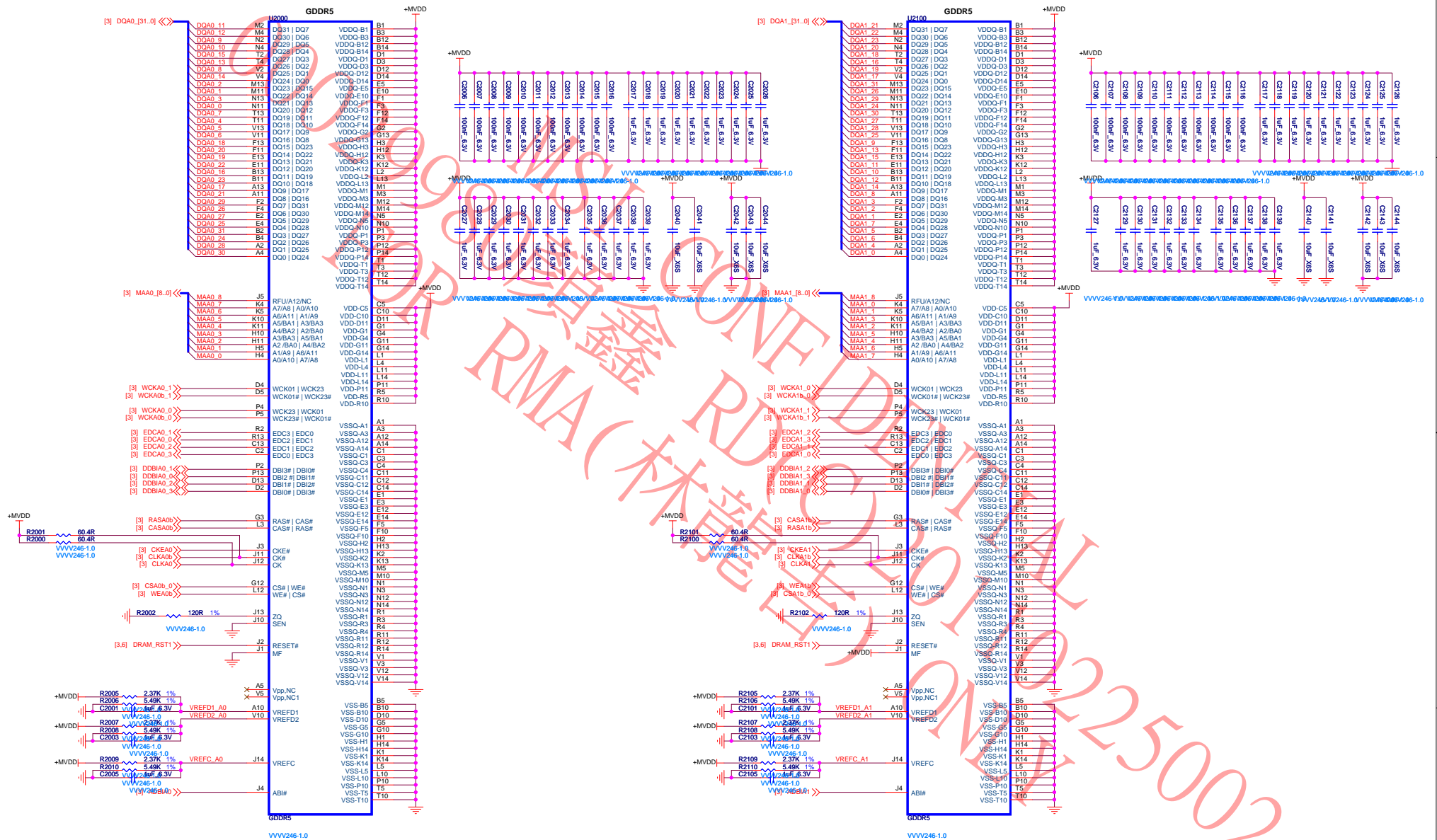
(3) CAYMEN MEM Interface Ch A&B



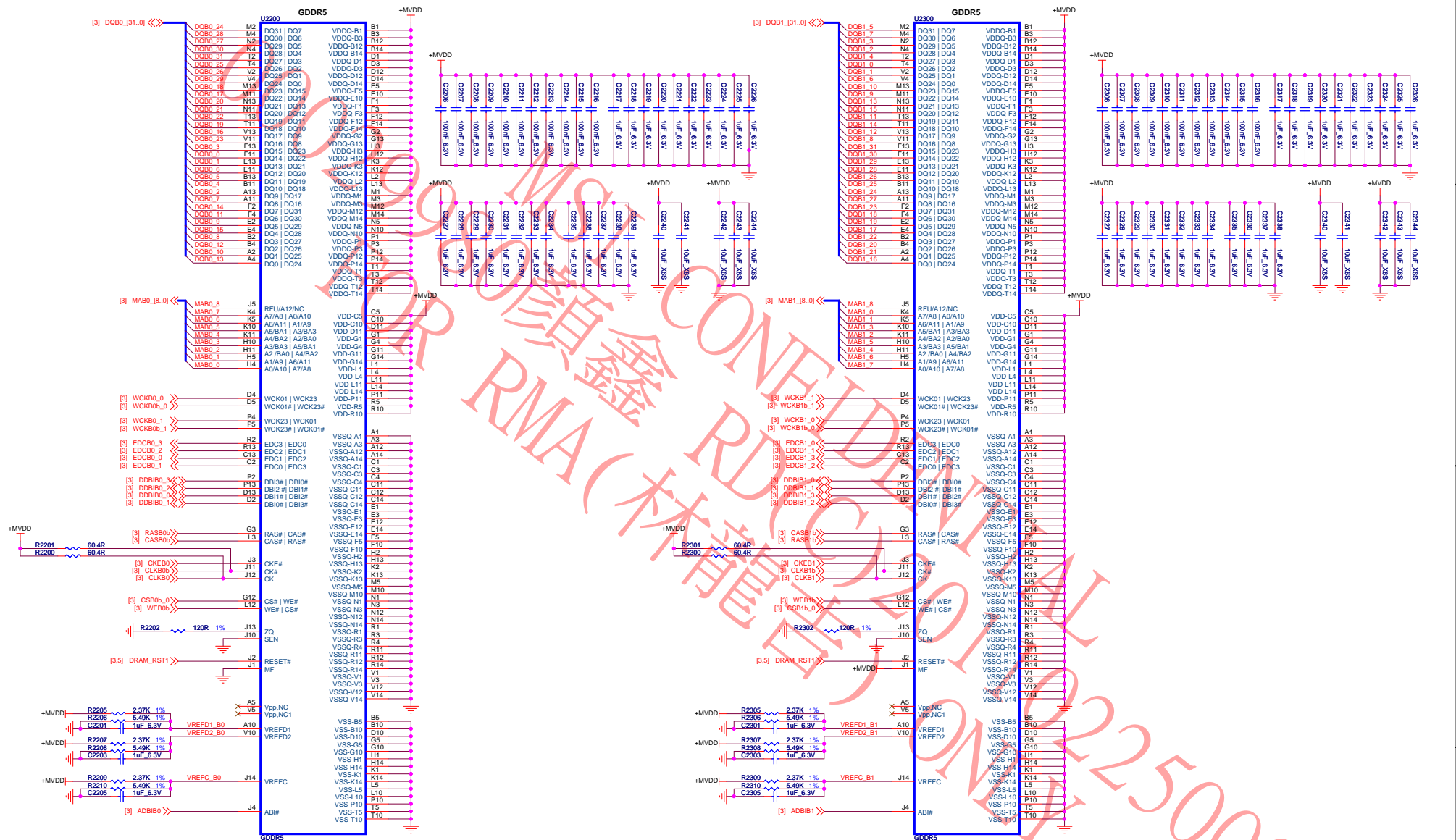
(4) CAYMEN MEM Interface Ch C&D



(5) GDDR5 Memory Channel A




(6) GDDR5 Memory Channel B

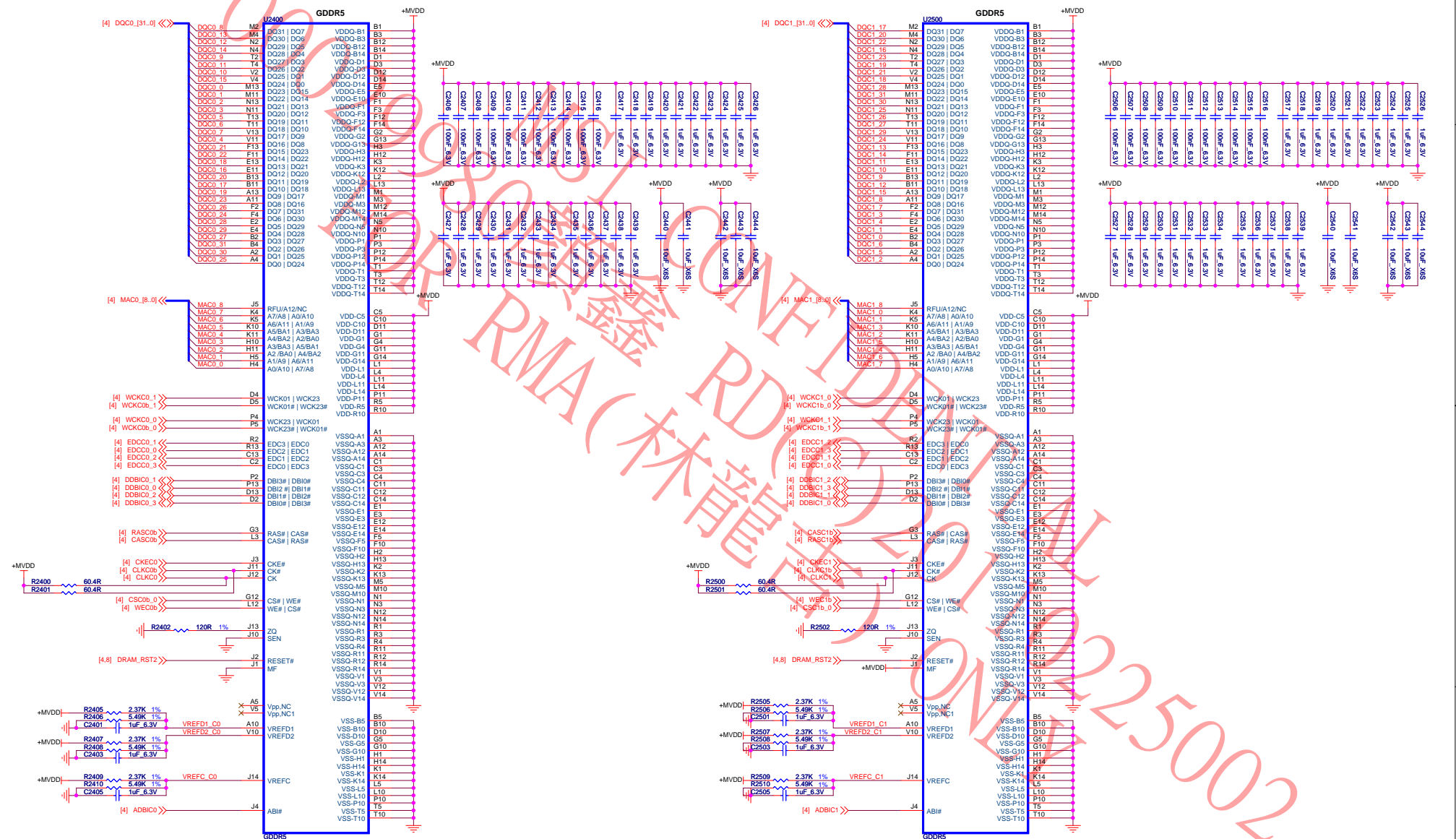


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(7) GDDR5 Memory Channel C

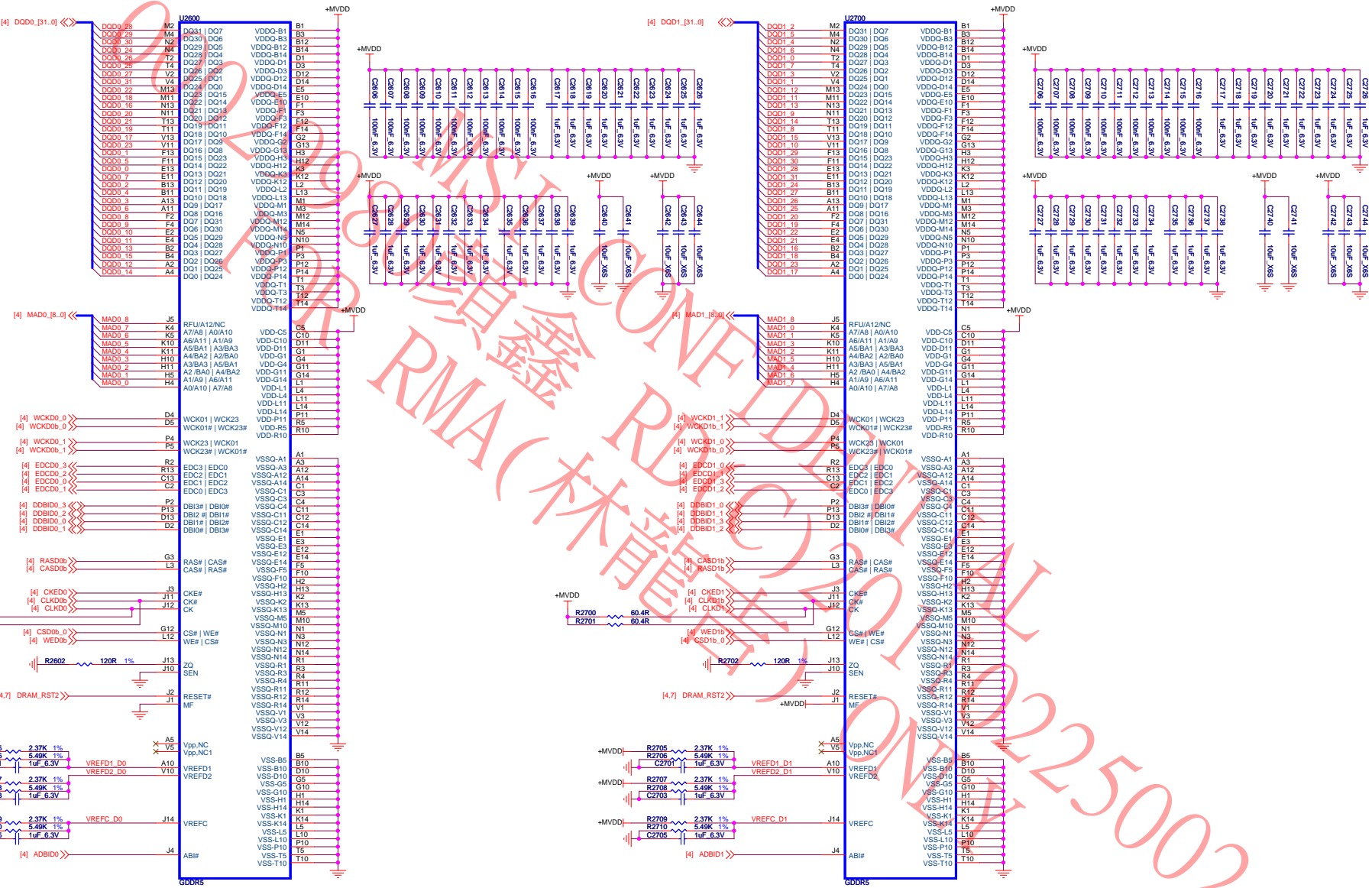


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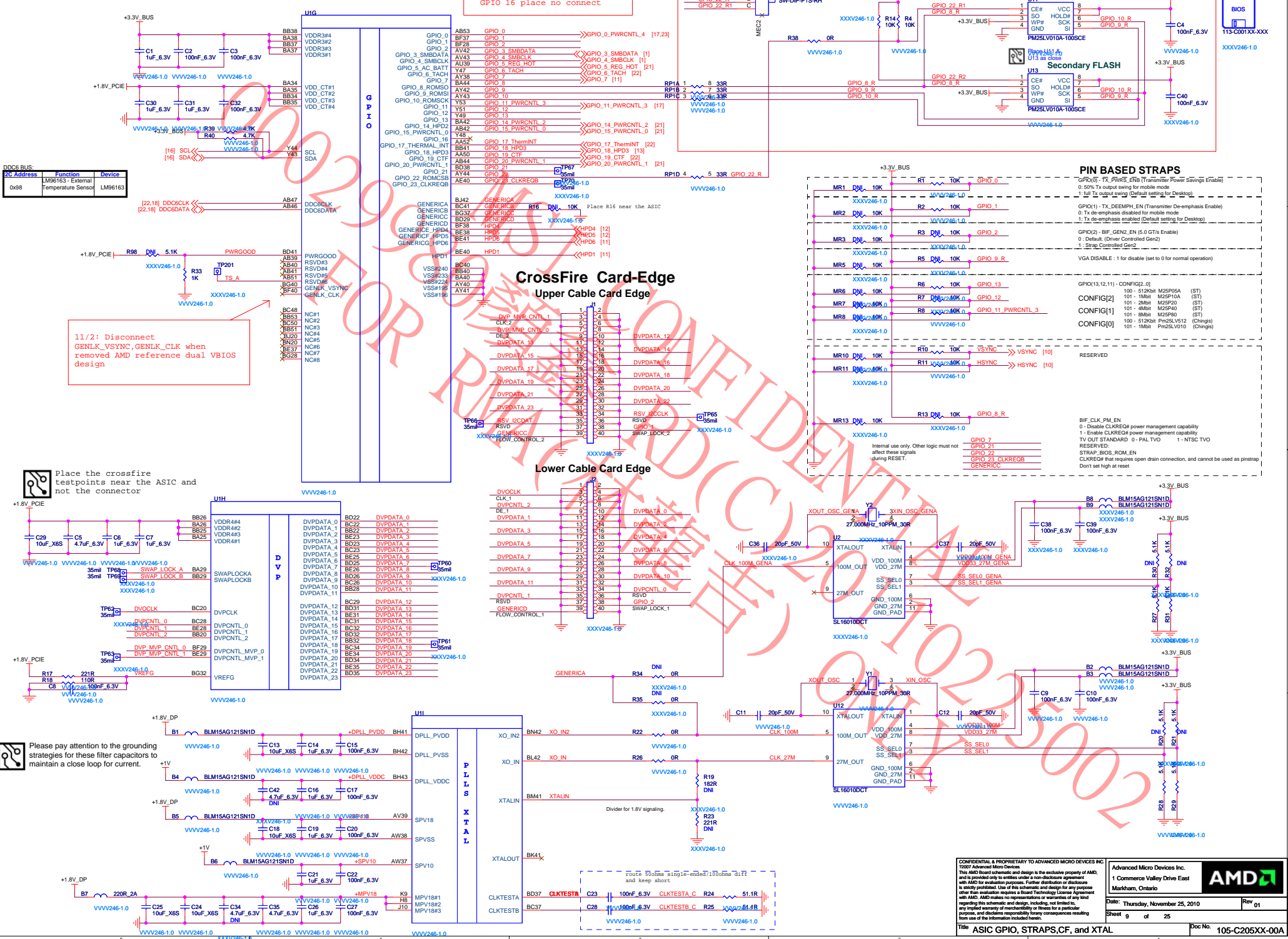
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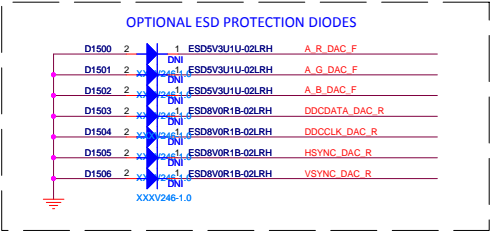
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(8) GDDR5 Memory Channel D

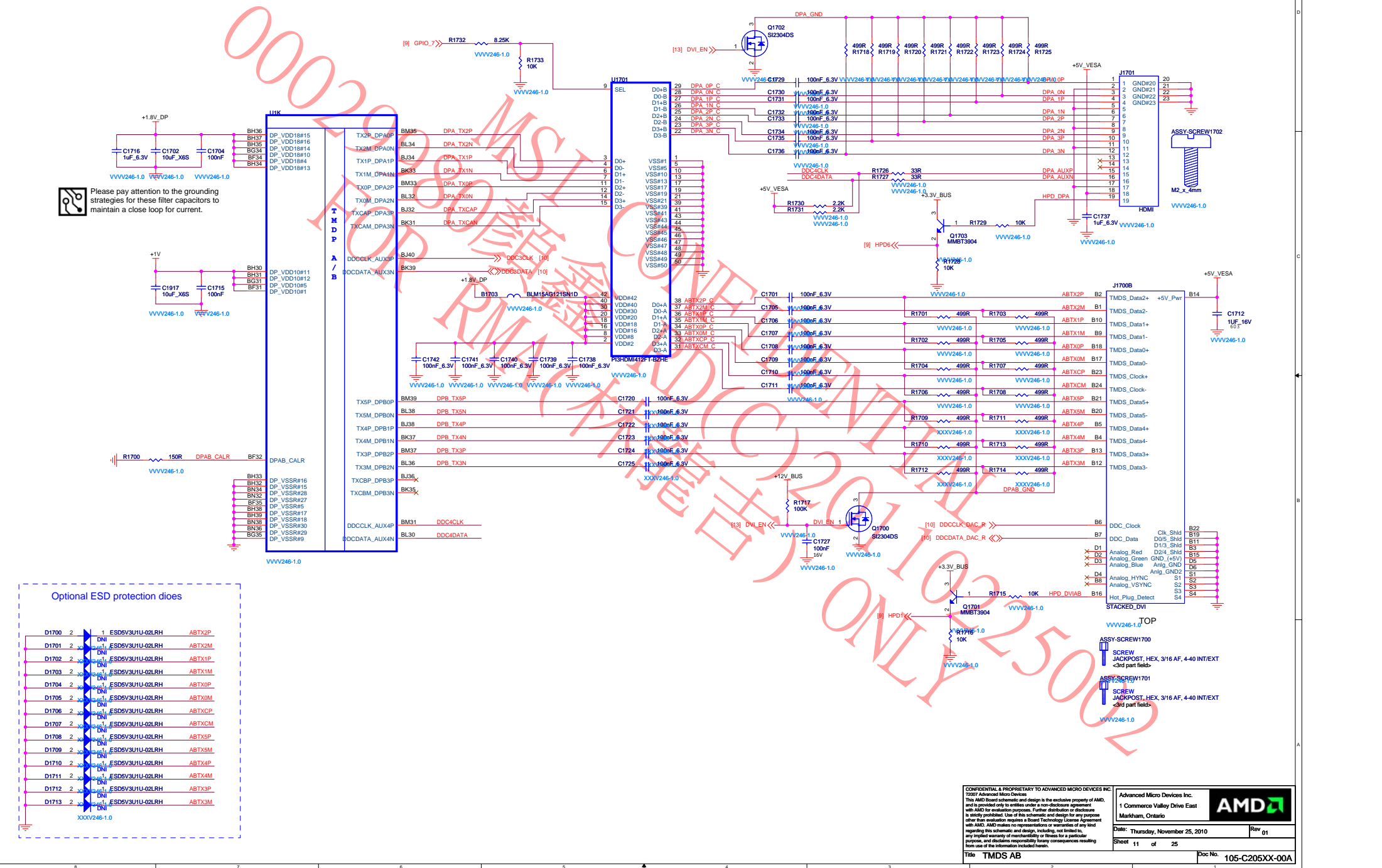


(09) CAYMEN GPIOs Strap CF XTAL OSC

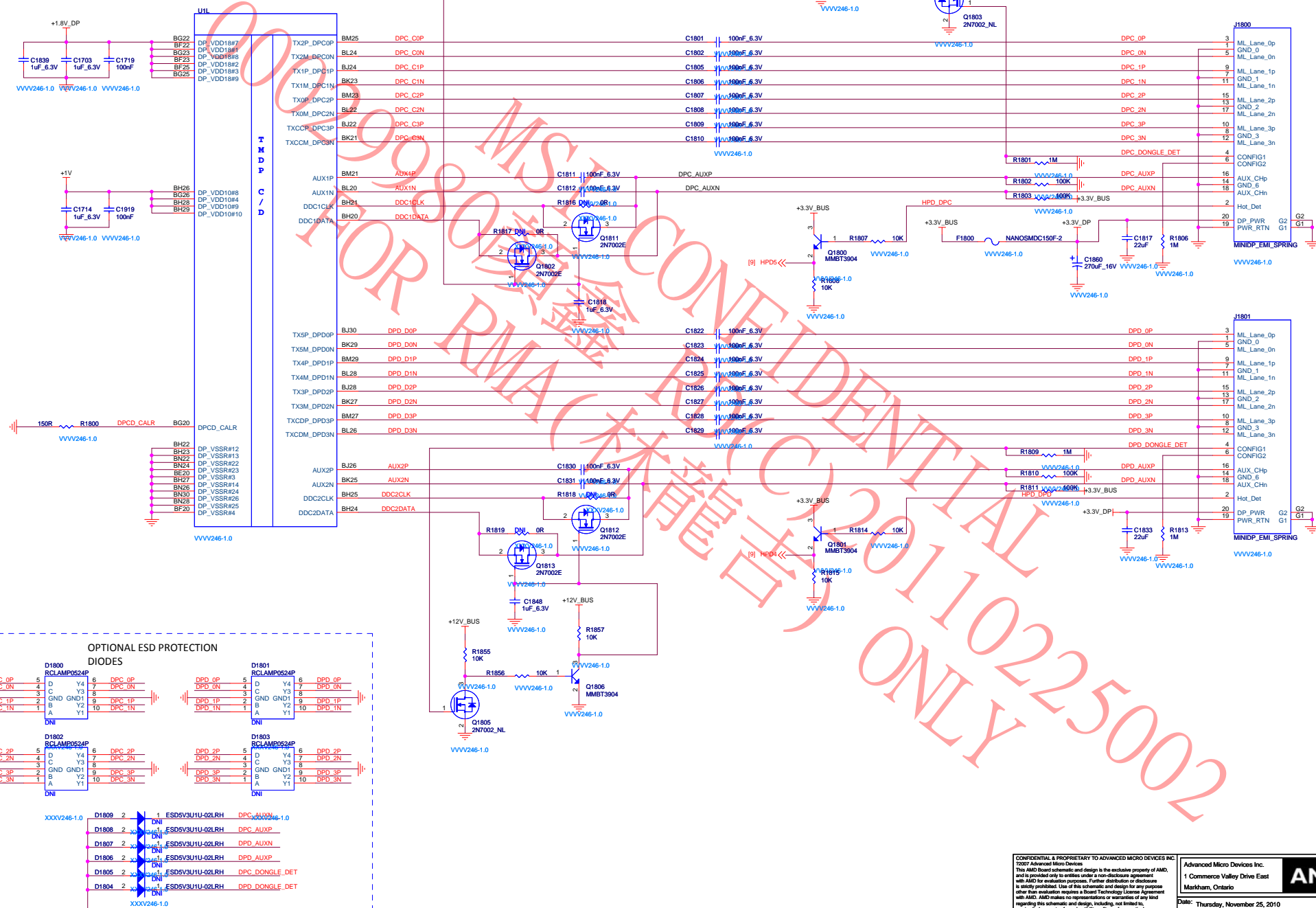




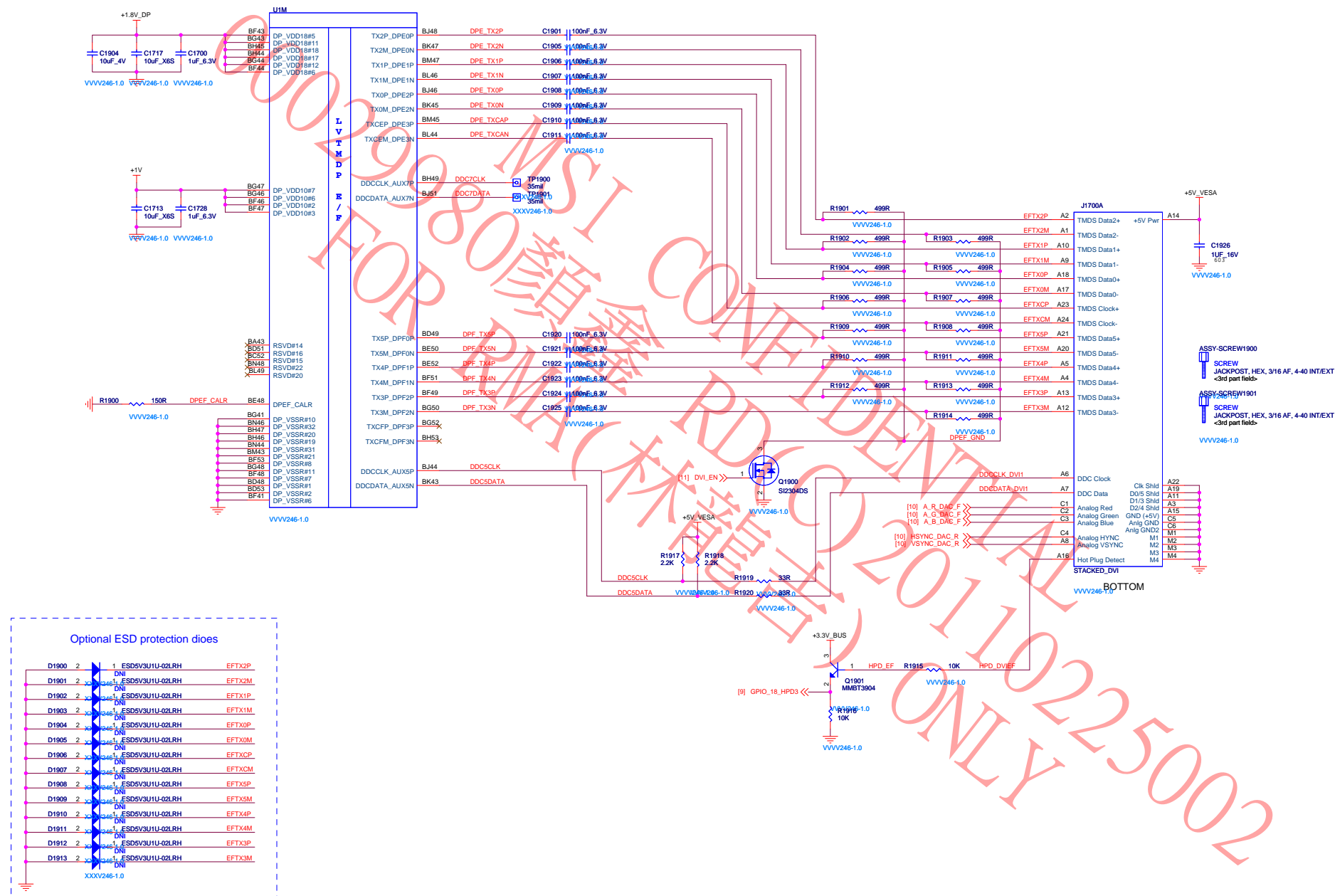
(11) CAYMEN TMDS A&B



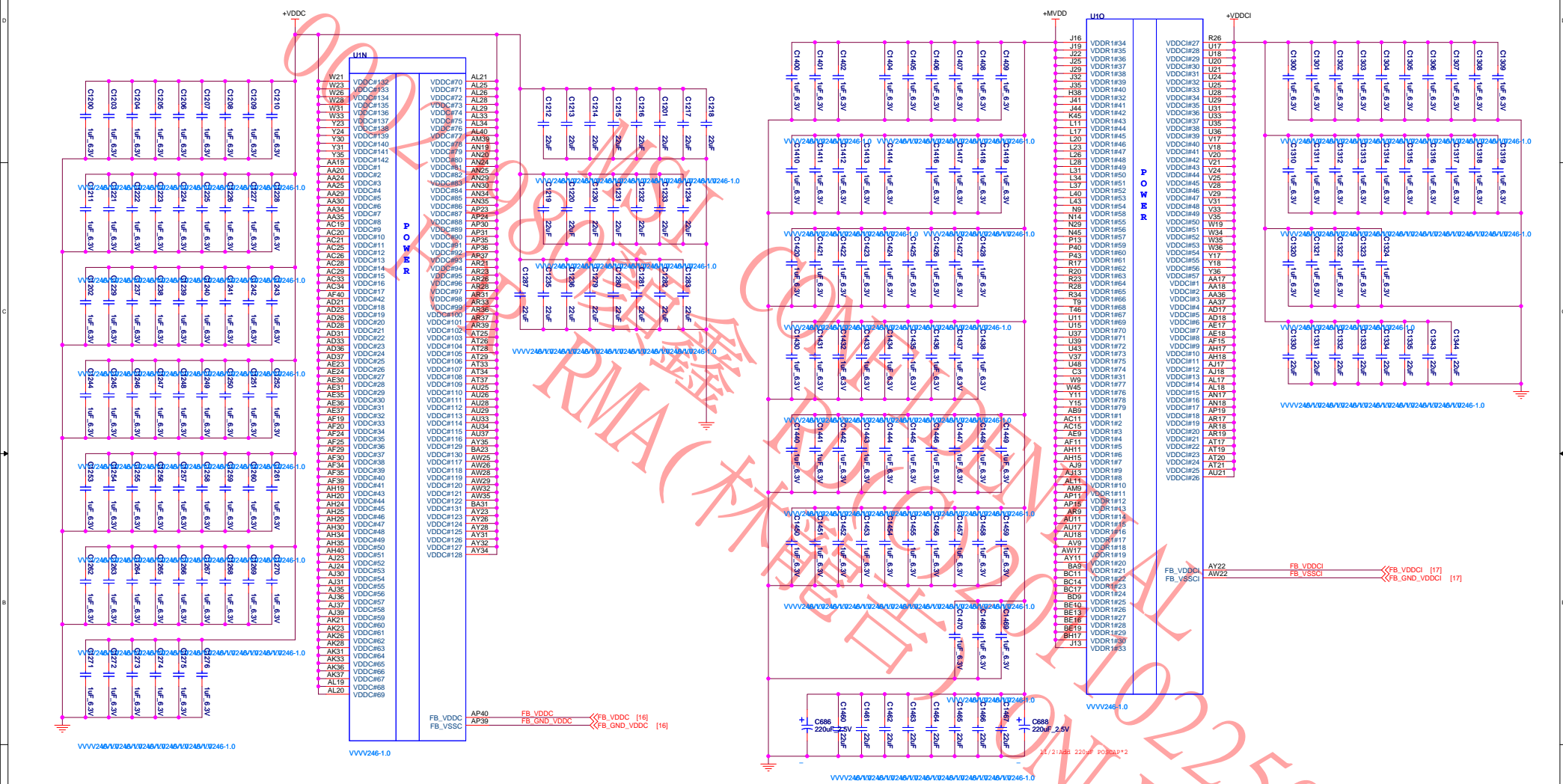
(12) CAYMEN DP C&D



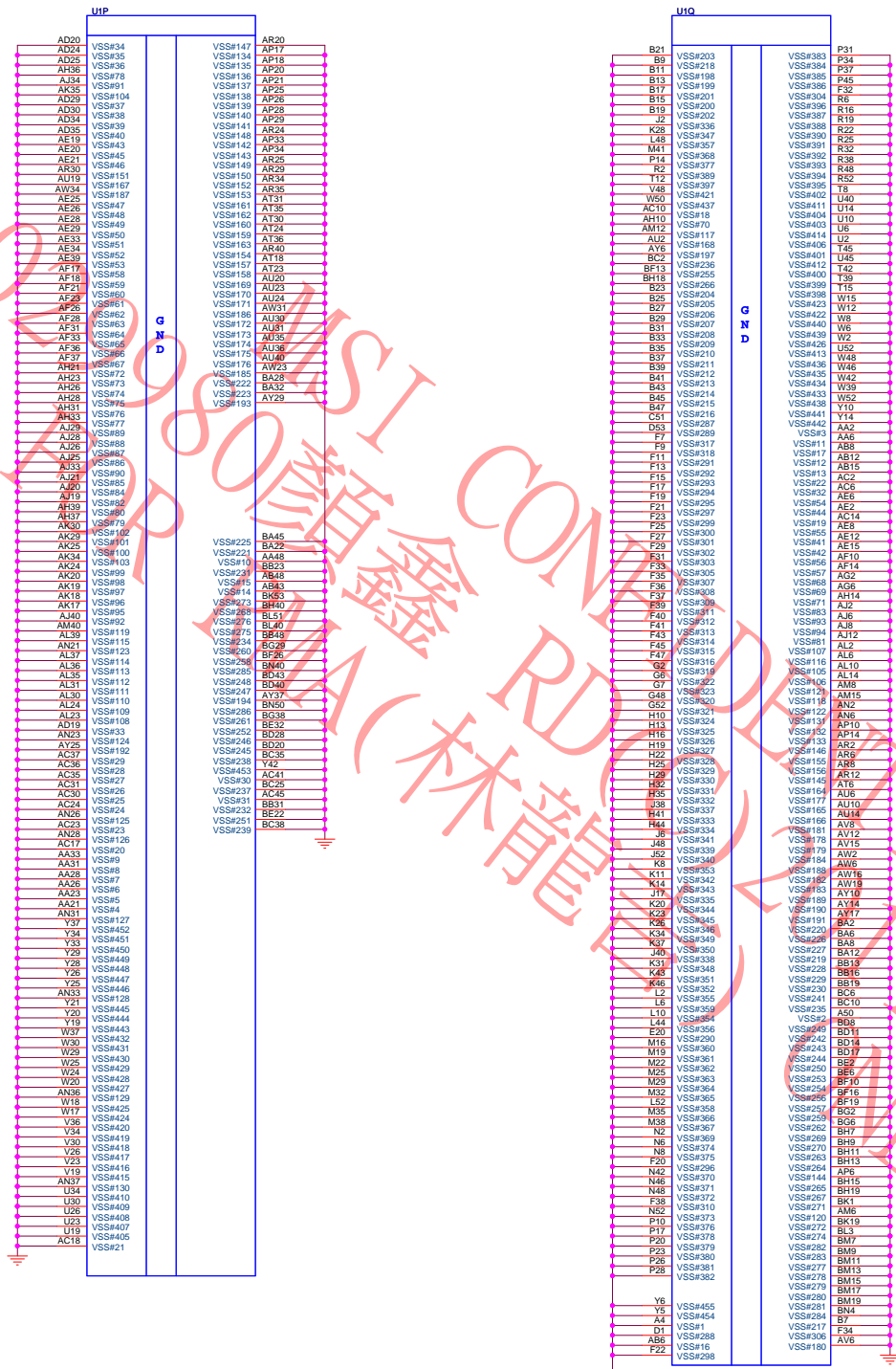
(13) CAYMEN LVTMDP E&F



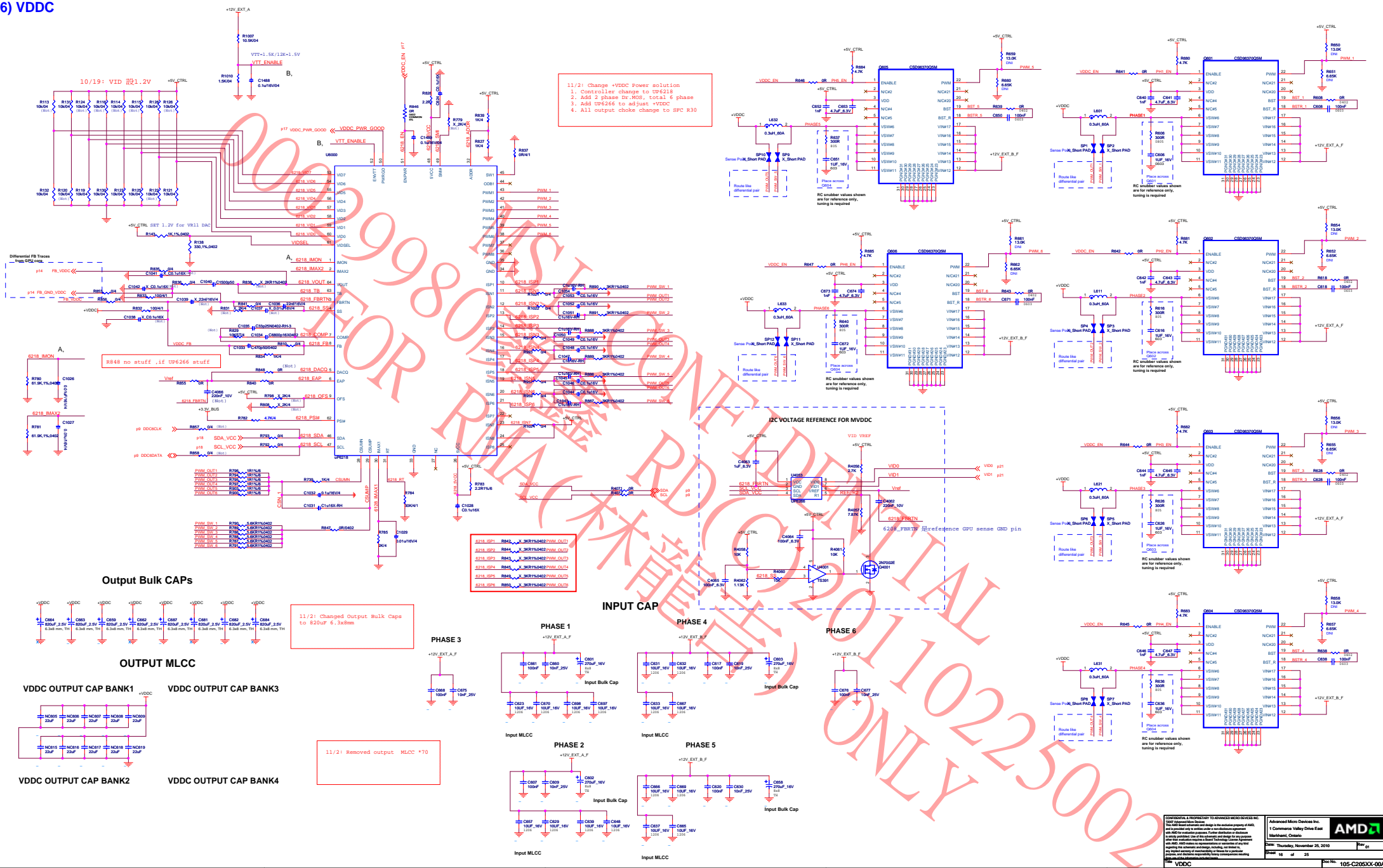
(14) CAYMEN Power

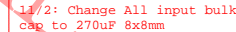


(15) CAYMEN GND



(16) VDDC



[illegible]

11/2: Changed MOSFET
Footprint to S08
Changed output choke to
SFC R30

11/2: Changed Output Bulk Caps
to 820uF 6.3x8mm

11/2: Lowside gate resistor

Place Rs and Cs across Q1
RC snubber values shown
are for reference only,
tuning is required

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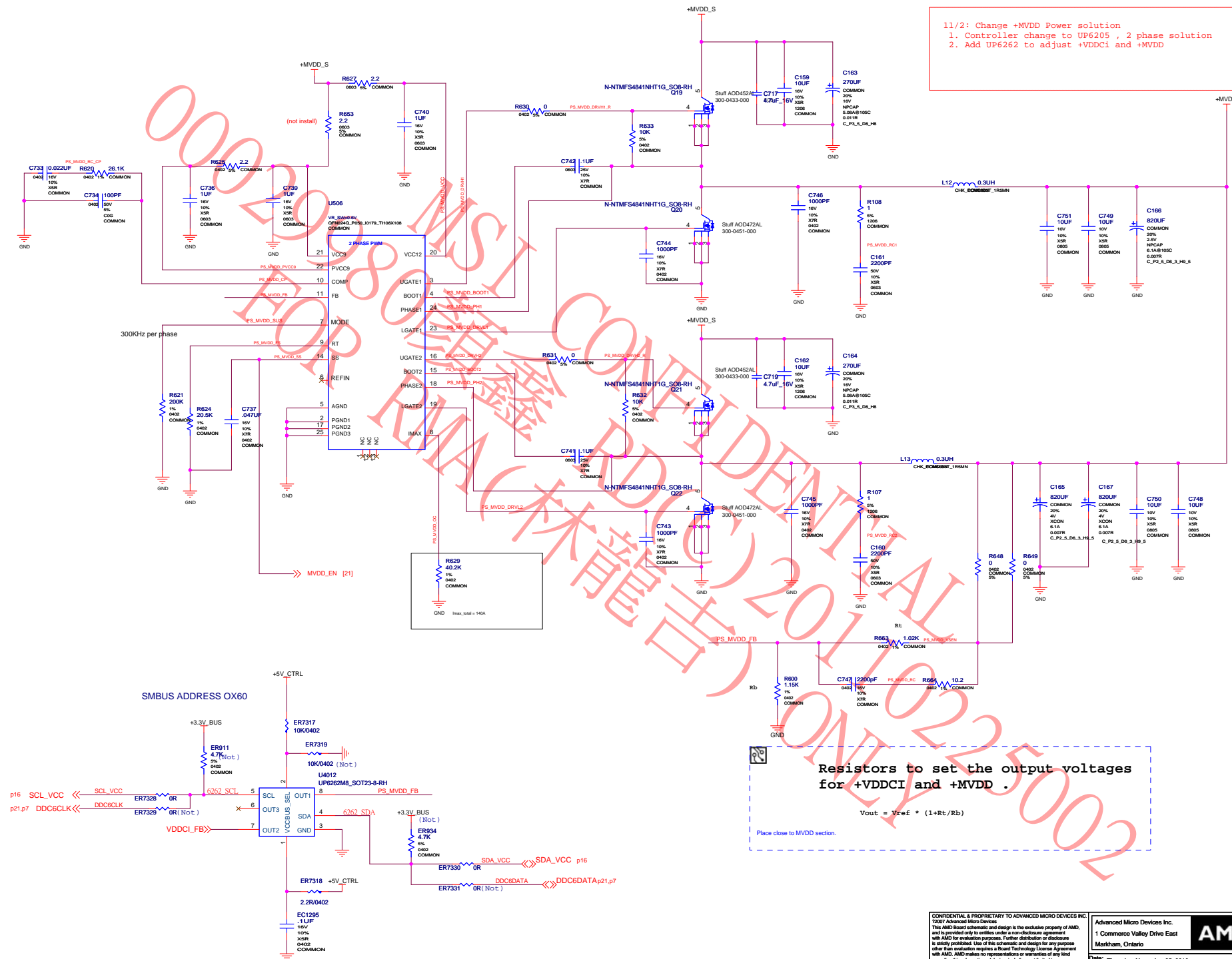
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Doc No. 105 G205XY 00A

Title VDDCI

(18) MVDD



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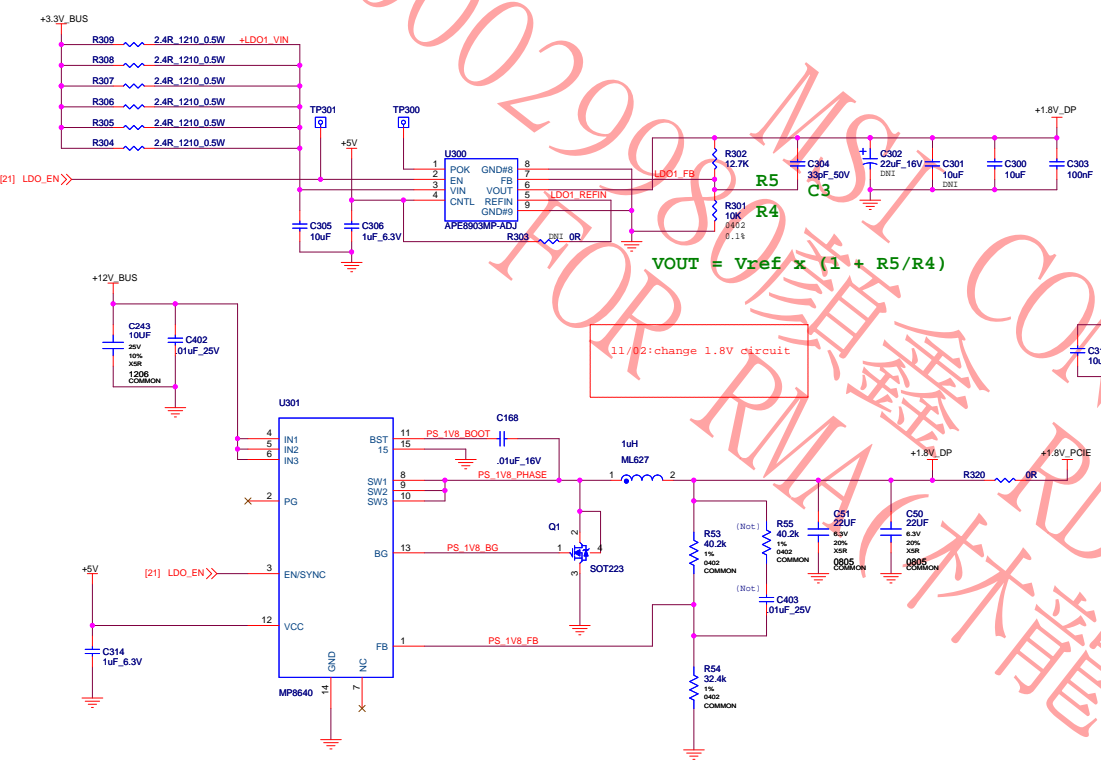
Rev 0

Title **MVDD**

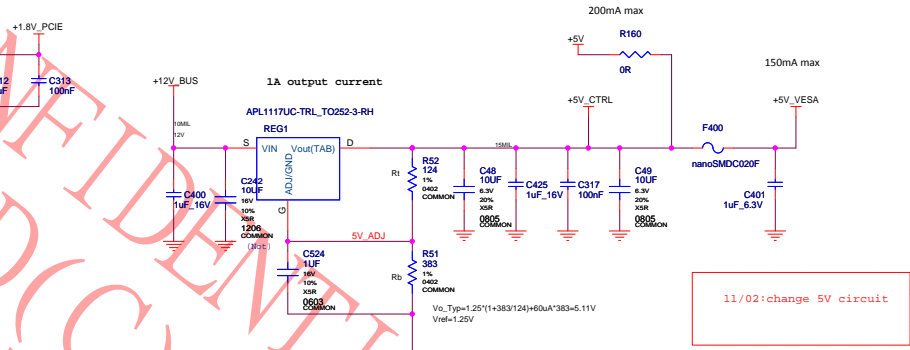
Doc No. 105-C205XX-00A

(19) CAYMEN Small Rail Regulators

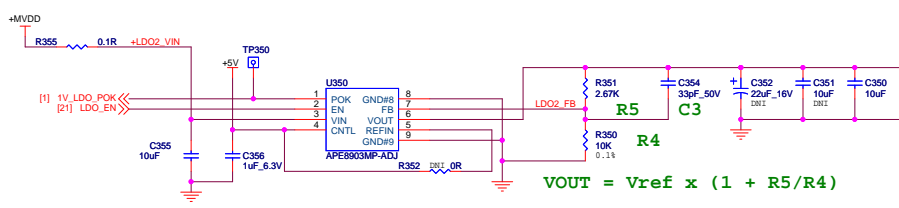
LDO #1: Vin = 2.3V to 3.6V MAX Vout = +1.8V +/- 2% Iout = 2.0A (TBV) RMS MAX
PCB: 50 to 70mm sq. copper area for cooling



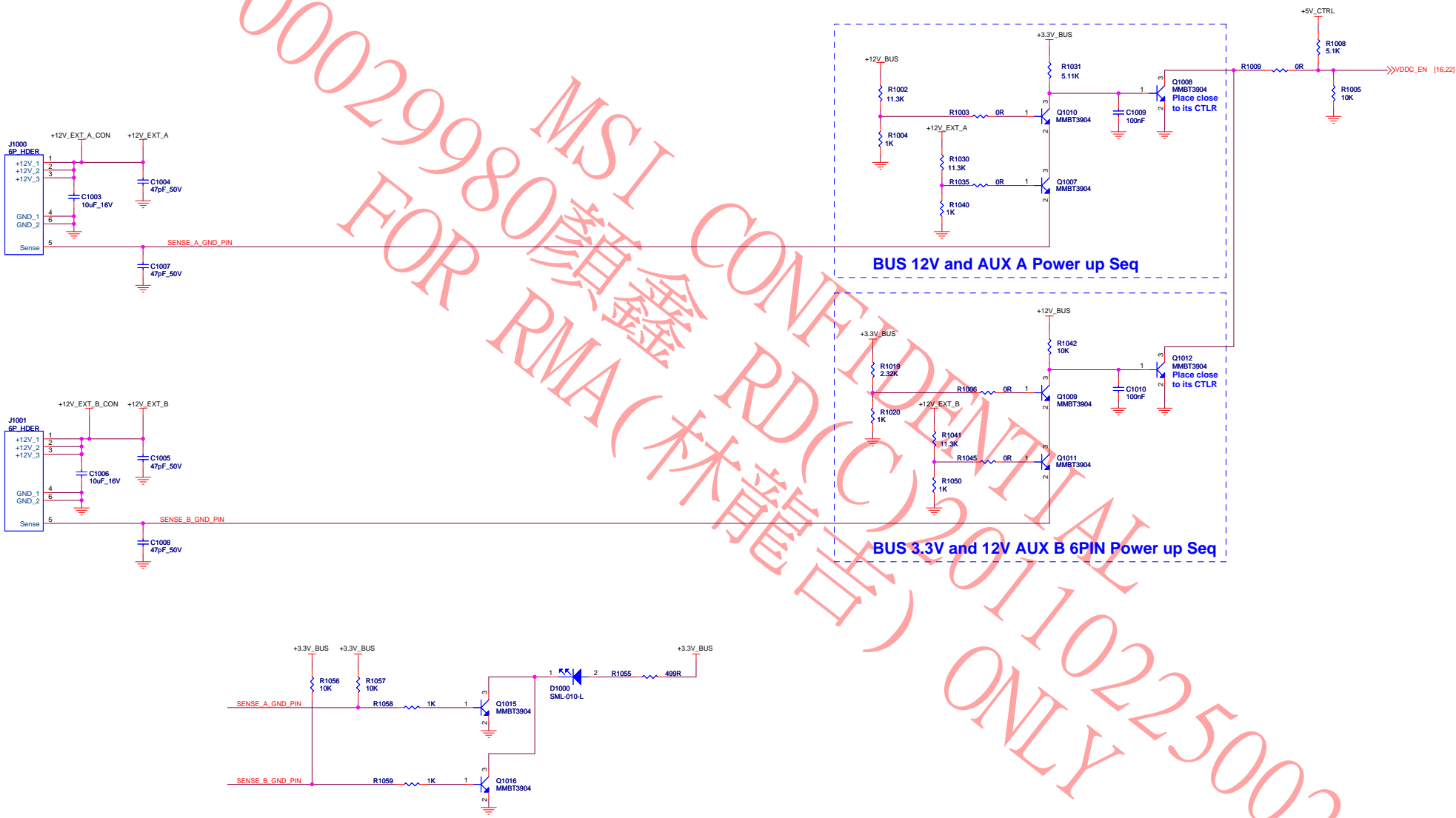
Regulators for +5V, +5V_VESA and +5V_HDMI
Iout max = 150mA (DVI+HDMI)



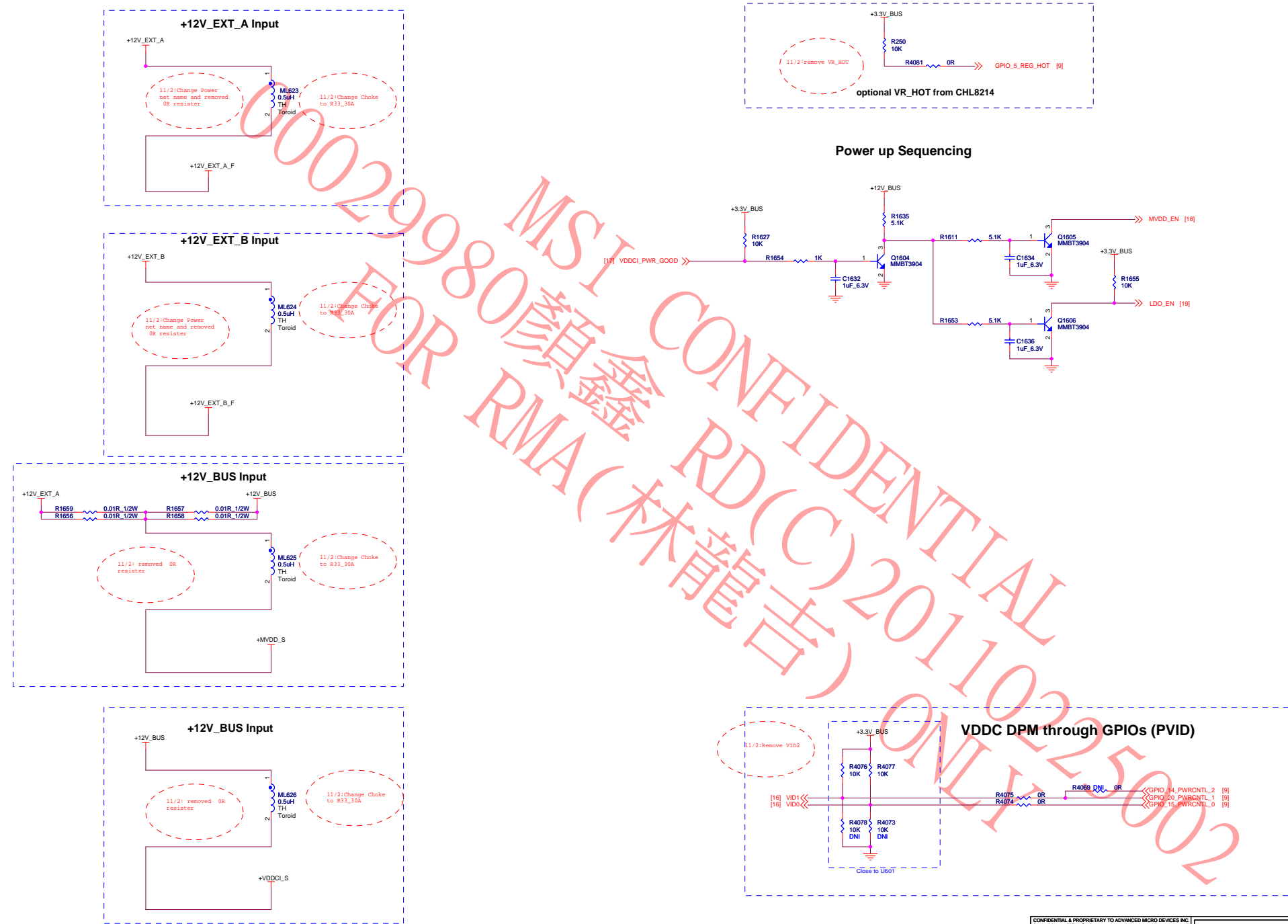
LDO #2: Vin = +1.40V to 1.8VMAX Vout = +1V +/- 2% Iout = 1.5A (TBV) RMS MAX
PCB: 50 to 70mm sq. copper area for cooling



(20) CAYMEN POWER MGMNT

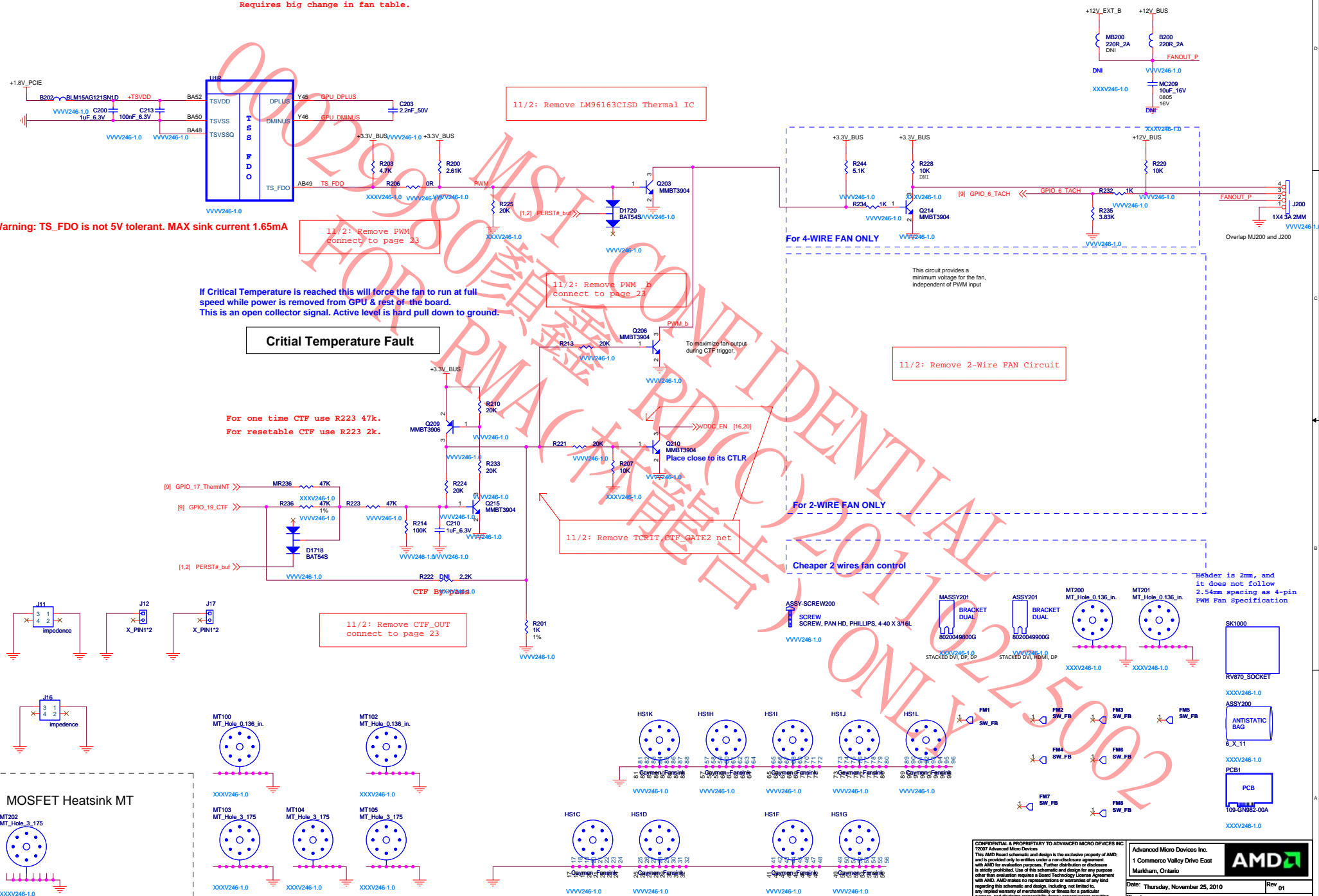


(21) CAYMEN POWER MANAGEMENT 2

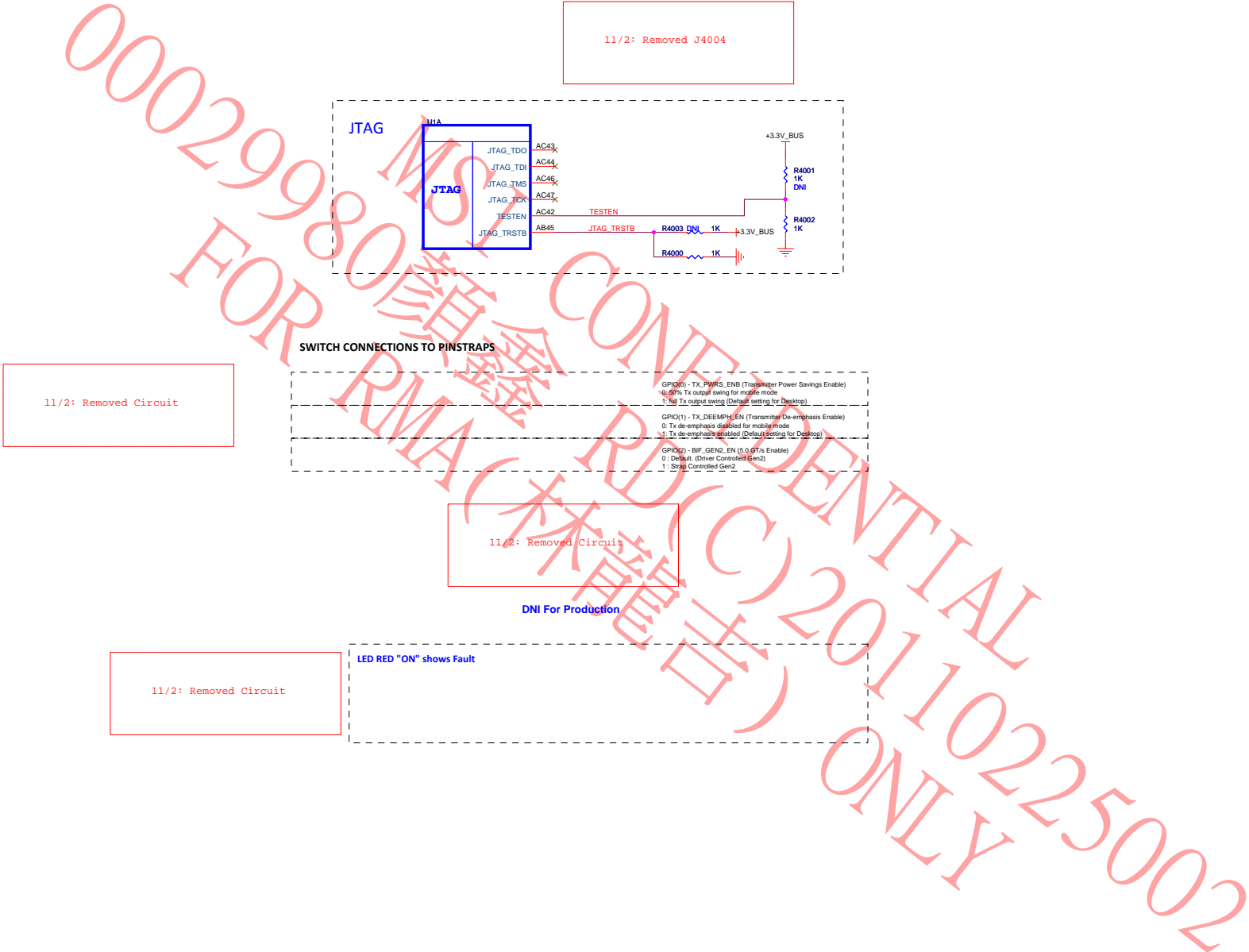


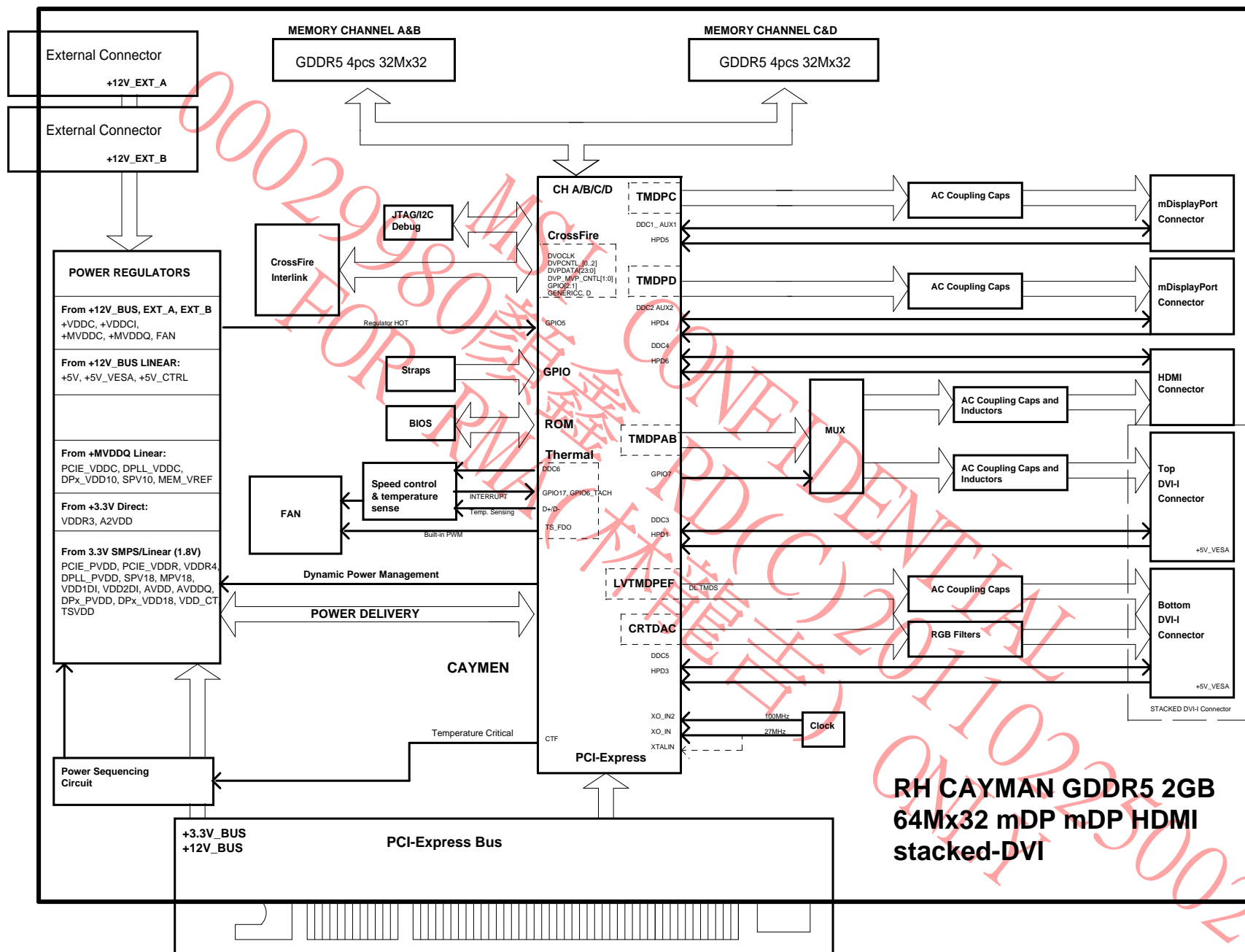
(22) CAYMEN Mechanical and Thermal Management

Use Q203 2020002300G, R200, R215, D1719 and C3609 for cheaper fan controller. Requires big change in fan table.



(23) CAYMEN Debug Circuits





**RH CAYMAN GDDR5 2GB
64Mx32 mDP mDP HDMI
stacked-DVI**



RH CAYMAN GDDR5 2GB 64Mx32 mDP mDP HDMI stacked-DVI

105-C205XX-00A

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REVISION HISTORY

Rev 01

PCB
Rev

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REVISION DESCRIPTION

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Please contact AMD representative to obtain latest BOM closest to the application desired.

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