

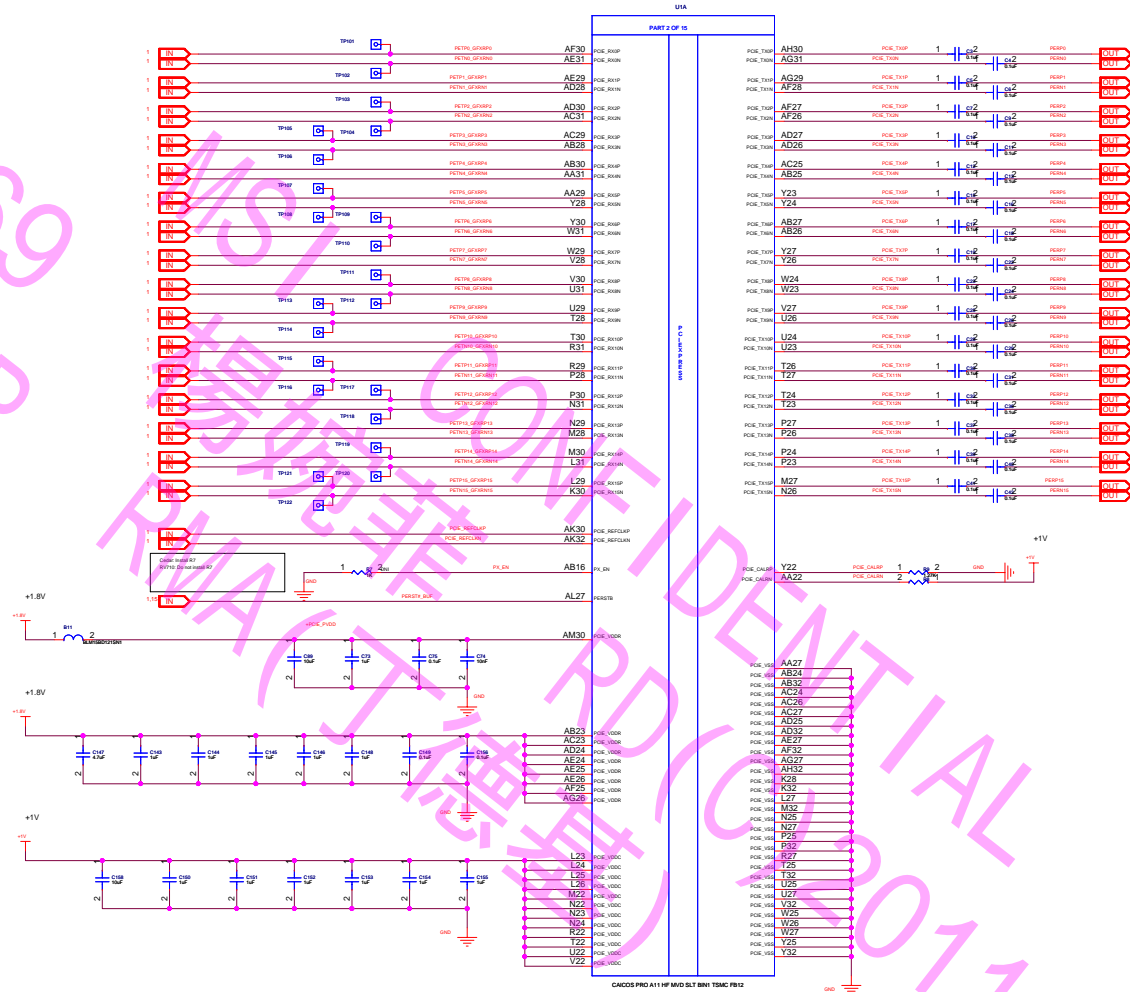


SYMBOL LEGEND	
DN	DO NOT INSTALL
A	ACTIVE LOW
	DIGITAL GROUND
	ANALOG GROUND
BUO	BRING UP ONLY

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<p><b>SHEET:</b>      <b>PCIE Edge Connector</b></p>		<p><b>DATE:</b> _____</p>		<p><b>REV:</b> _____</p>	
<p><b>SHEET NUMBER:</b> _____</p>		<p><b>OF</b> _____</p>		<p><b>TITLE:</b> _____</p>	
<p><b>DOCUMENT NUMBER:</b> _____</p>					

CAICOS/CEDAR PCIe Interface

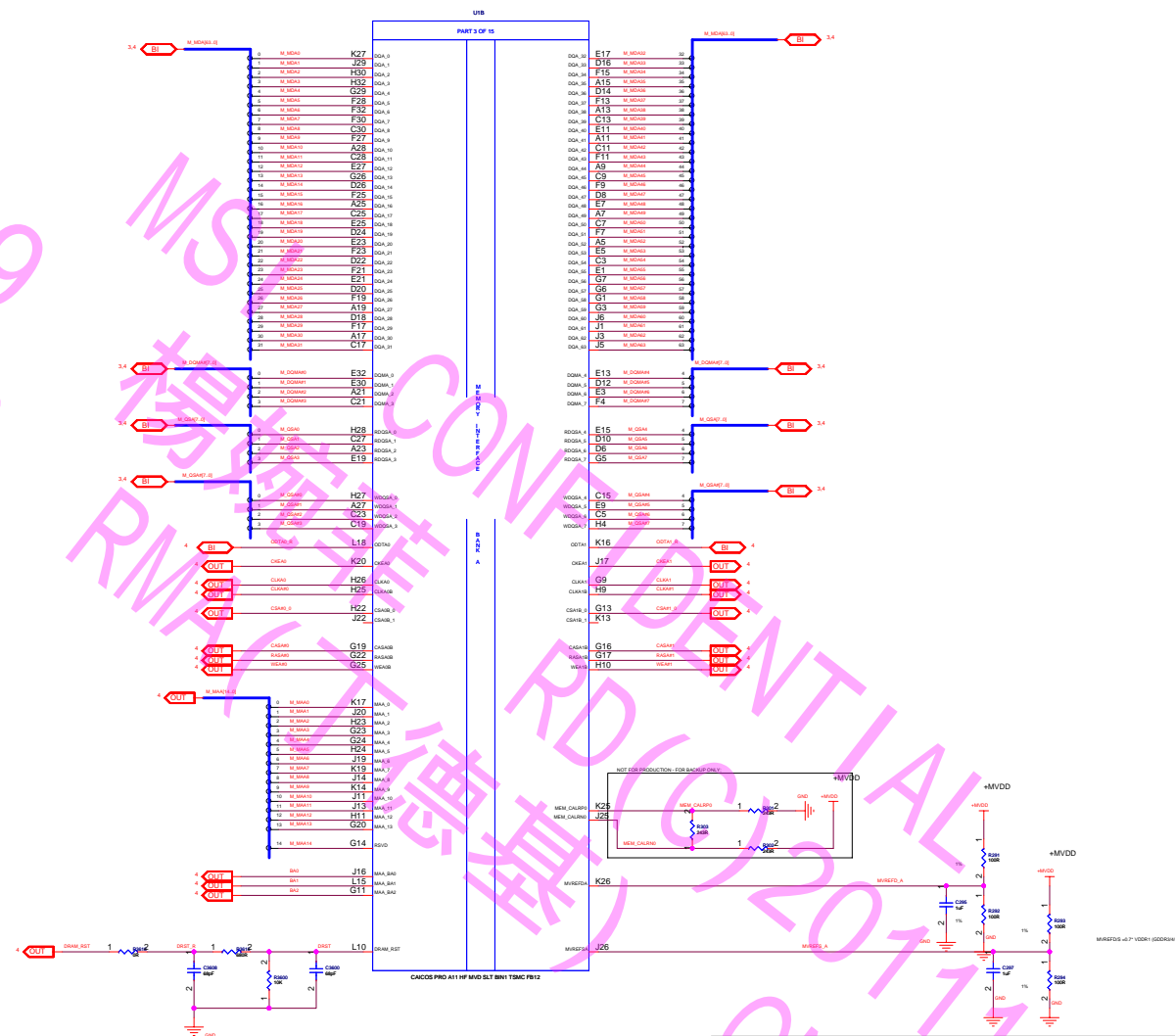
NOTE: Some of the PCIe testpoints will  
be available through vias on traces.



SHEET: CAICOS PCIe Interface	
DATE:	REV:
SHEET NUMBER:	OF
DOCUMENT NUMBER:	

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CAICOS/CEDAR MEM Interface Ch A&amp;B



SHEET: CAICOS MEM VF CHA

DATE: \_\_\_\_\_

SHEET NUMBER:

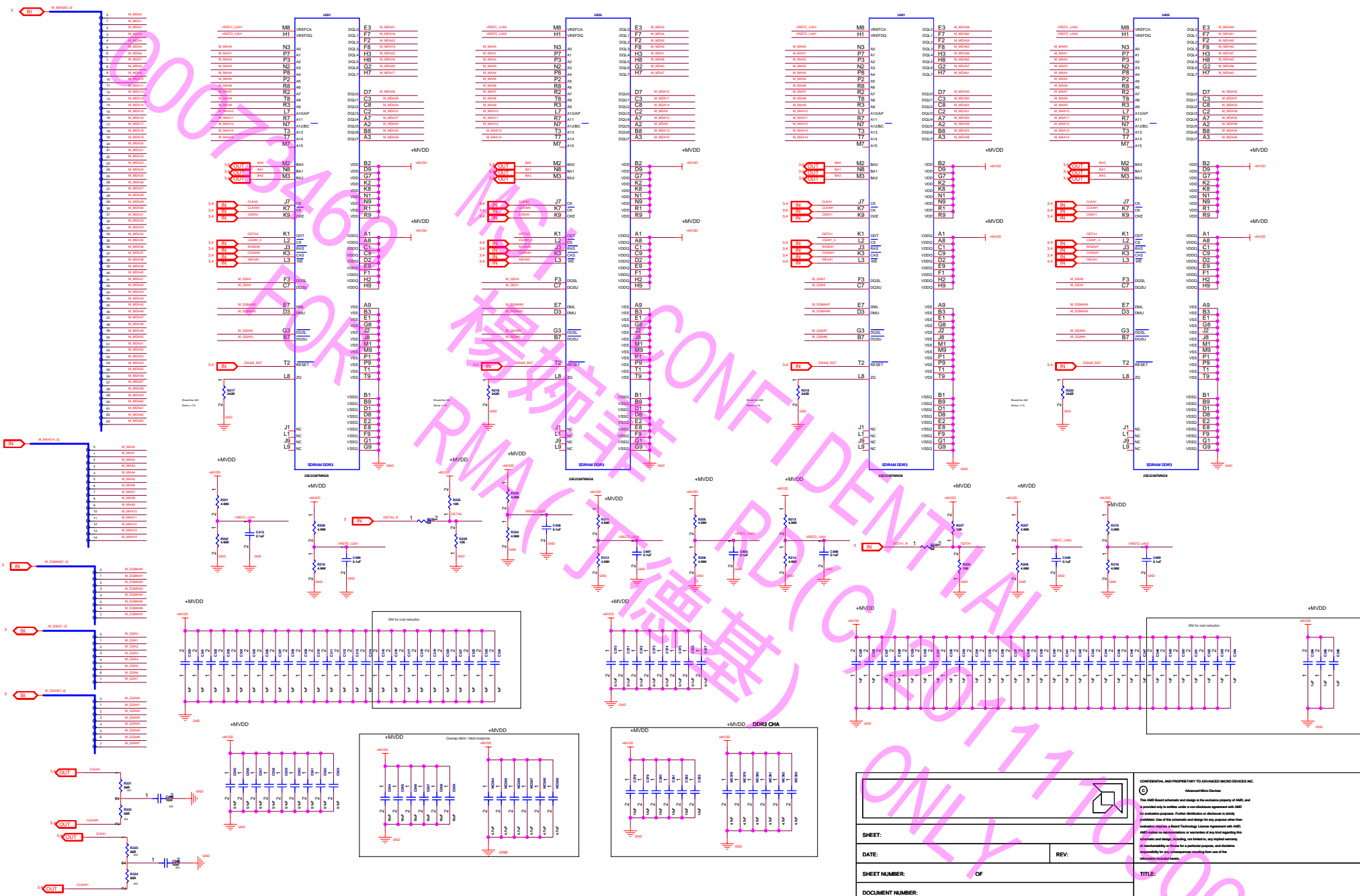
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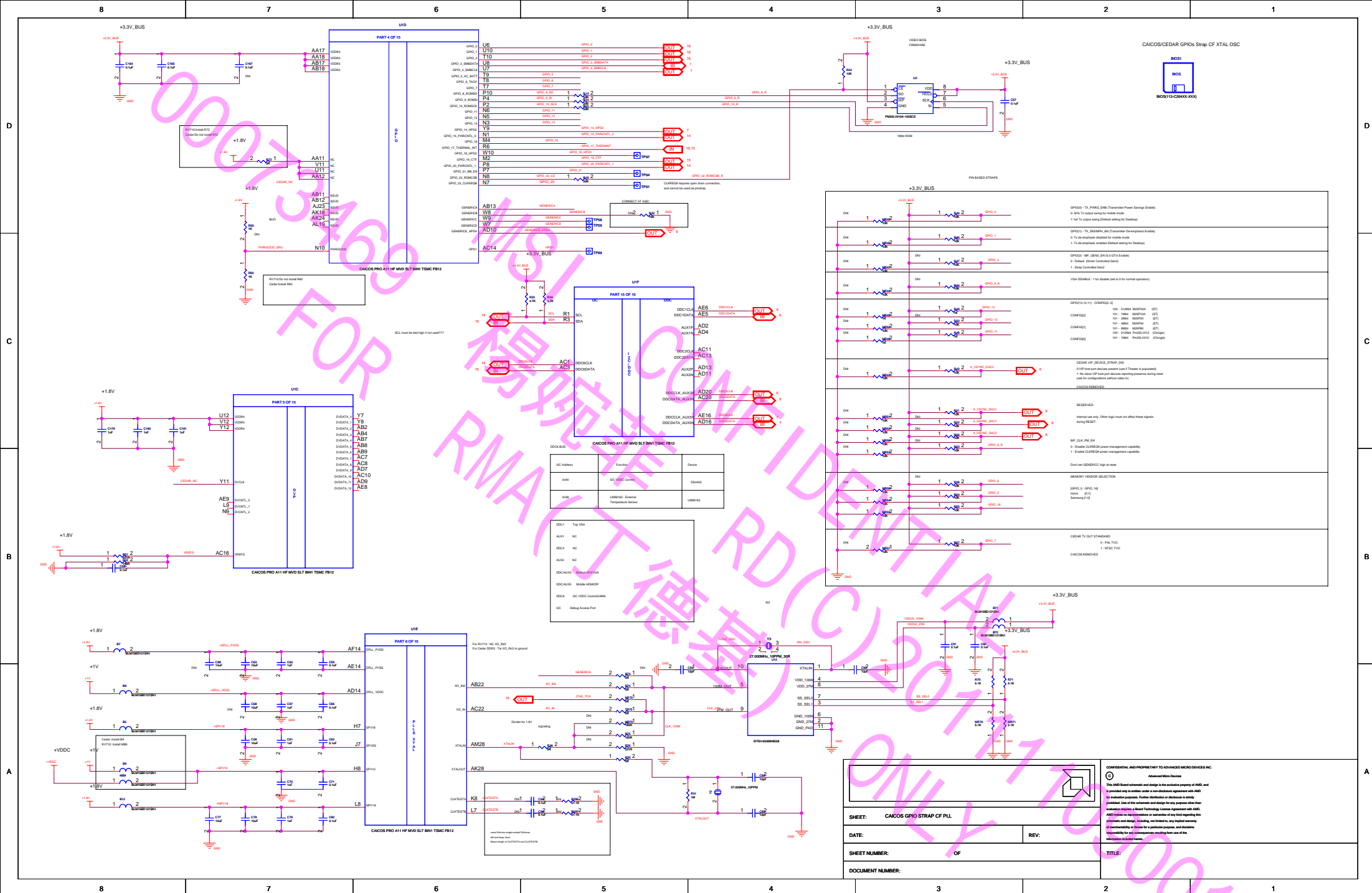
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**TITLE**

CHANNEL A: 256MB/512MB DDR3



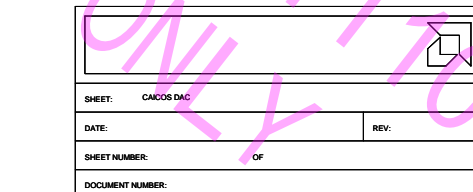


## D

## C

## B

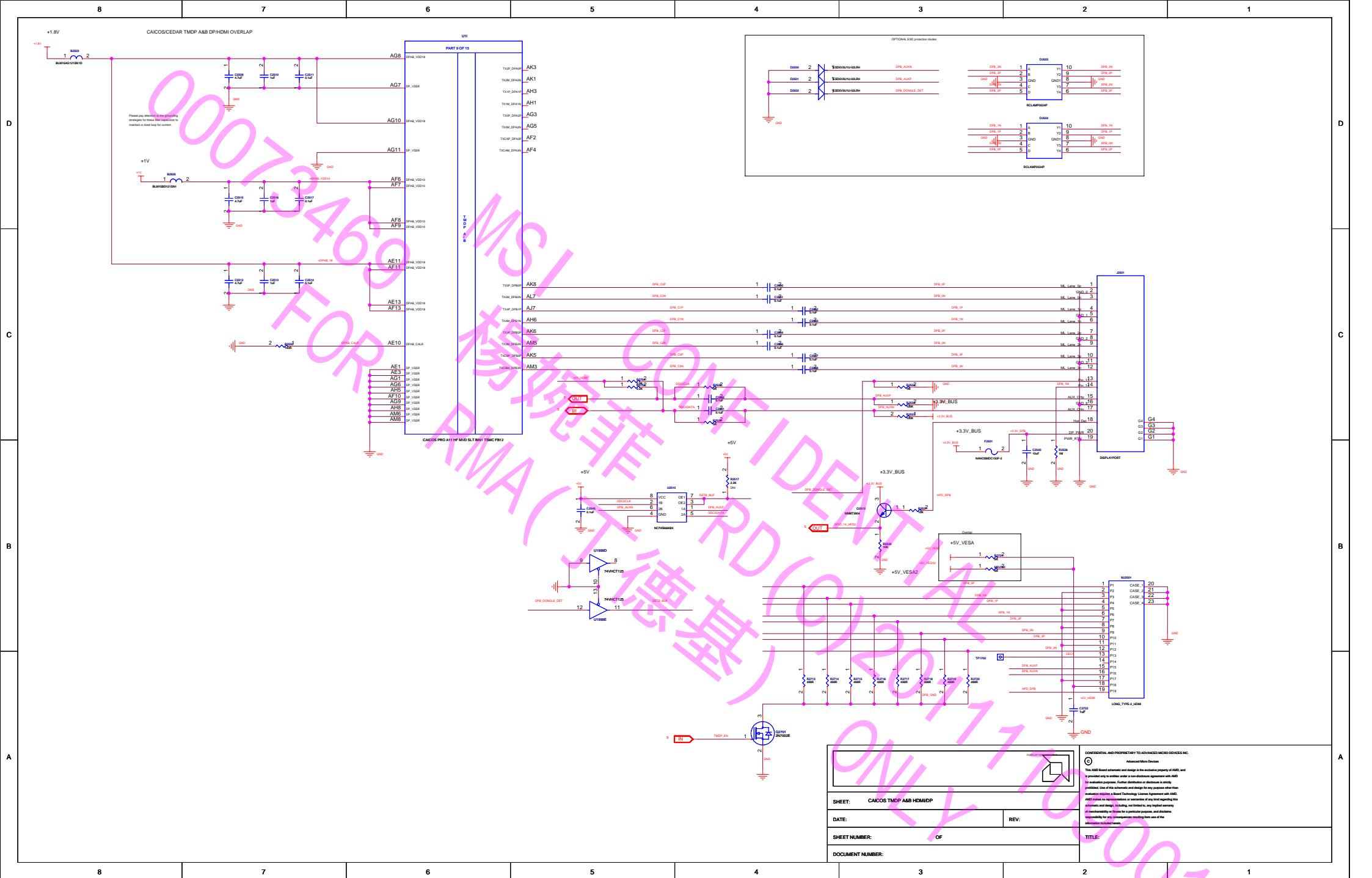
## A

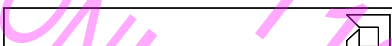


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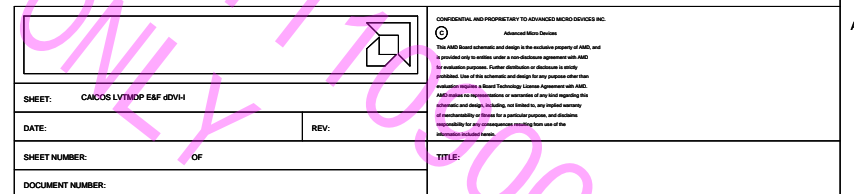
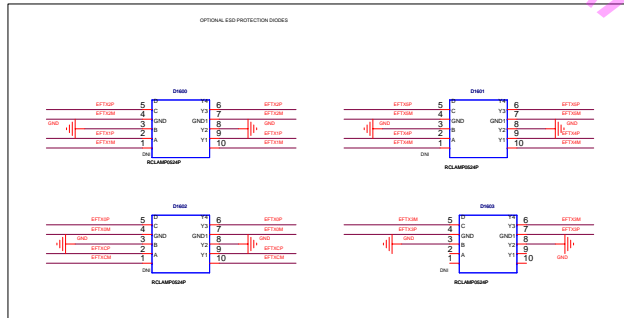
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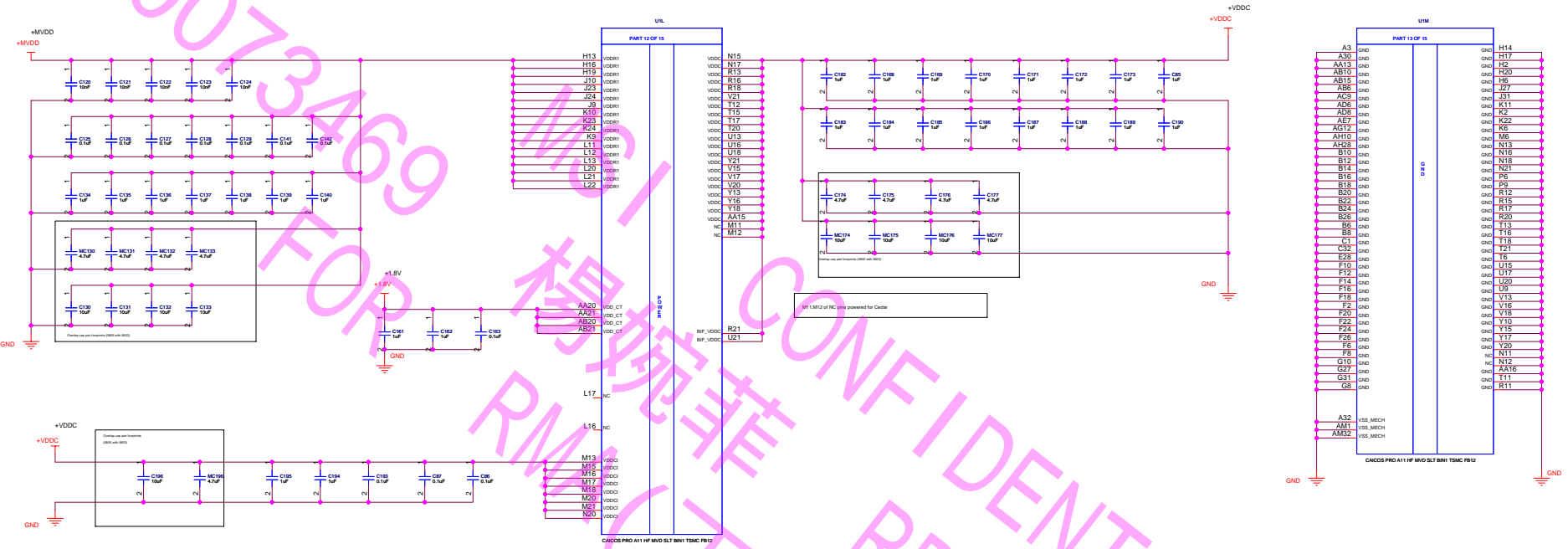


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SHEET: CACOS TMDP C (REVISED)		REV:	
DATE:			
SHEET NUMBER:	OF		
DOCUMENT NUMBER:			

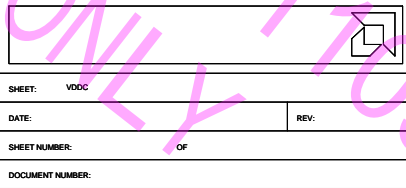




# CAICOS/CEDAR Power & GND



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SHEET: CAICOS Power&GND		REV:	
DATE:		TITLE:	
SHEET NUMBER: OF		DOCUMENT NUMBER:	



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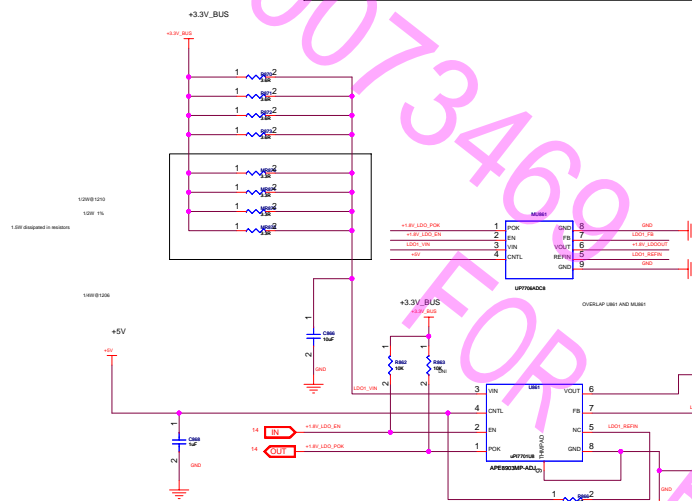
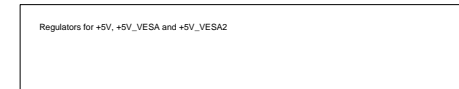
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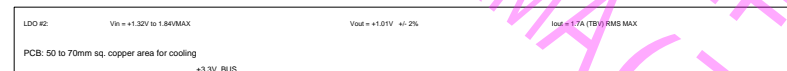
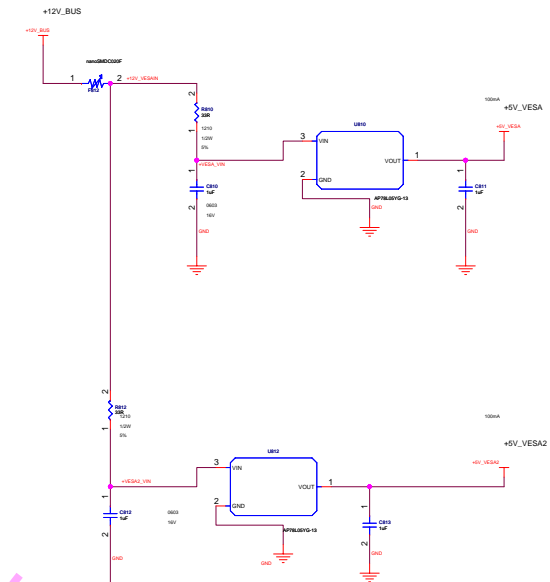
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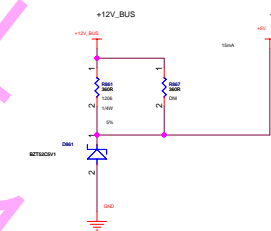
## Linear Regulators



1.5V WORST-CASE REQUIREMENT	
Display Config	Est. Current
DVI+HDMI+DP	1330mA



1. BY WORST-CASE REQUIREMENT	
Display Config	Est. Current
D/mini-Display	150mA

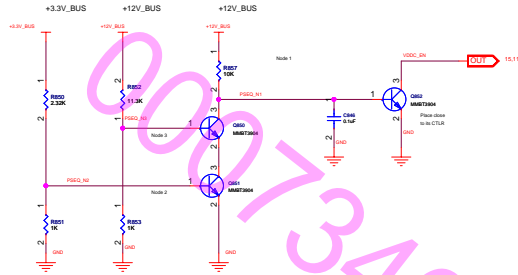


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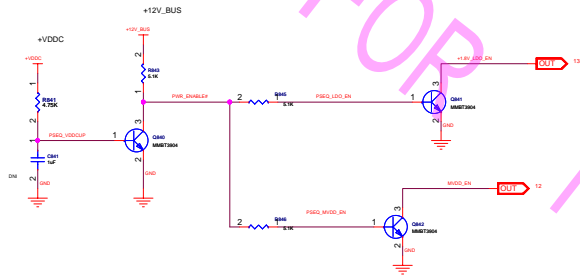
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12V\_BUS & 3V3\_BUS POWER SEQUENCING



POWER SEQUENCING CIRCUIT



Install R339 to gate TV LDO with 1.8V LDO.



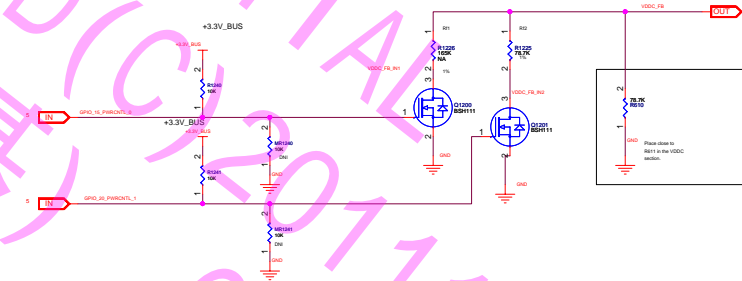
VDDC Low Side Divider

+1.8V LDO\_PON is fixed to 1.0V  
+1.8V LDO\_EN is fixed to 1.0V  
+1.8V LDO\_PON is fixed to 1.0V  
+1.8V LDO\_EN is fixed to 1.0V

VDDC Low Side Divider

+1.8V LDO\_PON is fixed to 1.0V  
+1.8V LDO\_EN is fixed to 1.0V  
+1.8V LDO\_PON is fixed to 1.0V  
+1.8V LDO\_EN is fixed to 1.0V

VDDC Low Side Divider



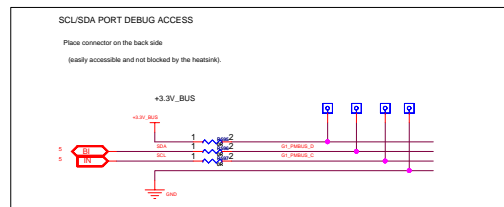
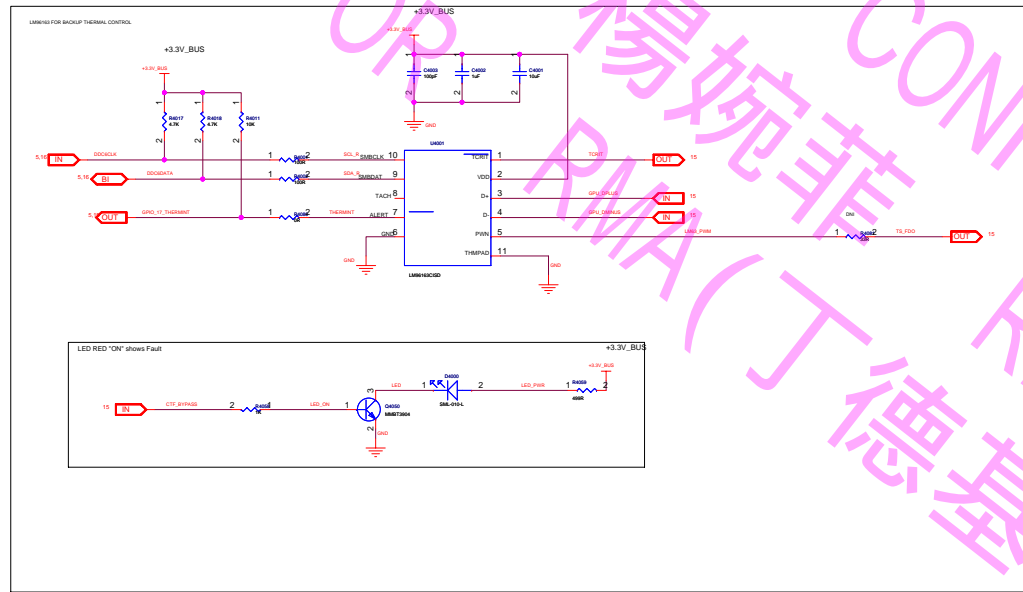
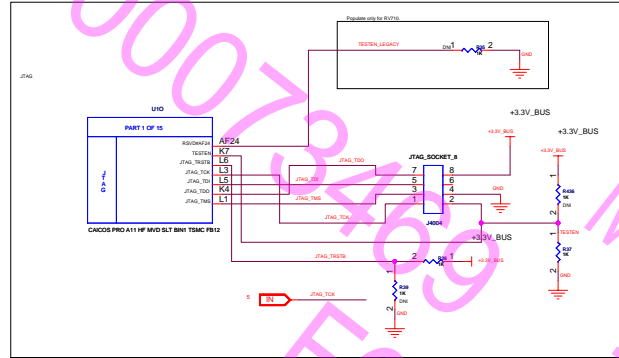
SHEET: PWR MGMT & SEQ	
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DOCUMENT NUMBER:	

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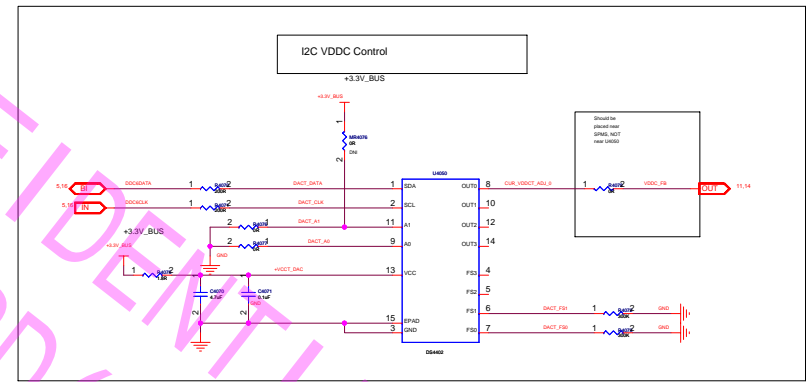
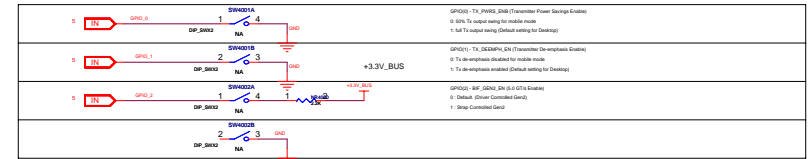
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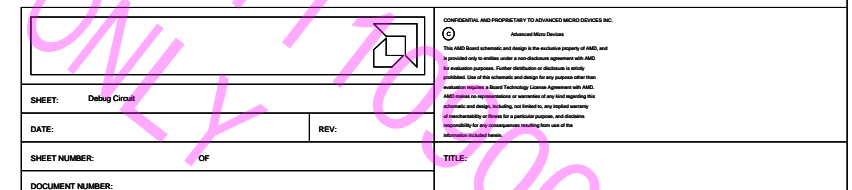




(19) Debug Circuits



### SWITCH CONNECTIONS TO PINSTRAPS



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			<b>TITLE:</b>
<b>SHEET:</b> <b>Debug Circuit</b>	<b>DATE:</b>	<b>REV:</b>	
<b>SHEET NUMBER:</b> <b>OF</b>			
<b>DOCUMENT NUMBER:</b>			



