



SYMBOL LEGEND	
DNI	DO NOT INSTALL
0-20 R	ACTIVE LOW
BUO	BURST UP ONLY
	DIGITAL GROUND
	ANALOG GROUND

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REV: 1.0

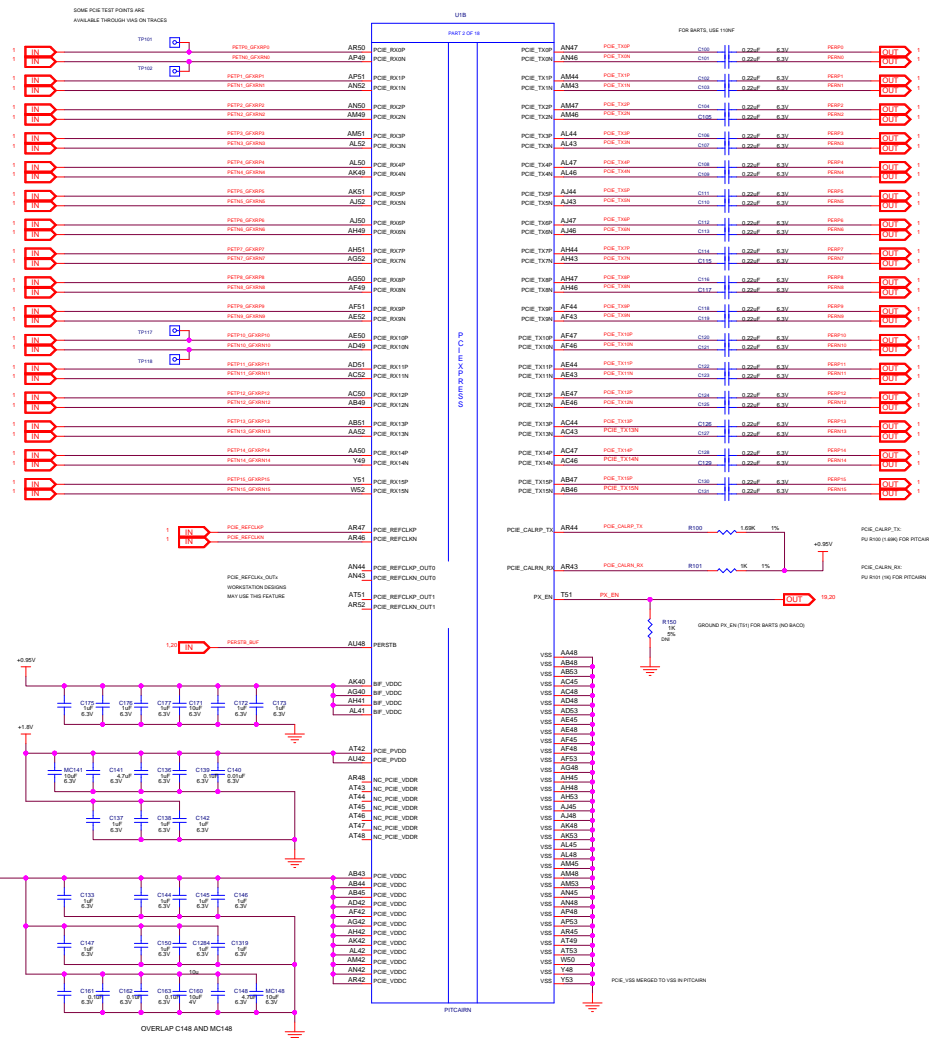
OF 23

DOCUMENT NUMBER: 105_C634xx_00B

TITLE:

Trinidad XT/PRO GD5

(2) TRINIDAD PCIE INTERFACE



AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: TRINIDAD PCIE INTERFACE

DATE: Thu Jan 15 02:26:19 2015

SHEET NUMBER: 2 OF 23

DOCUMENT NUMBER: 105_C634xx_00E

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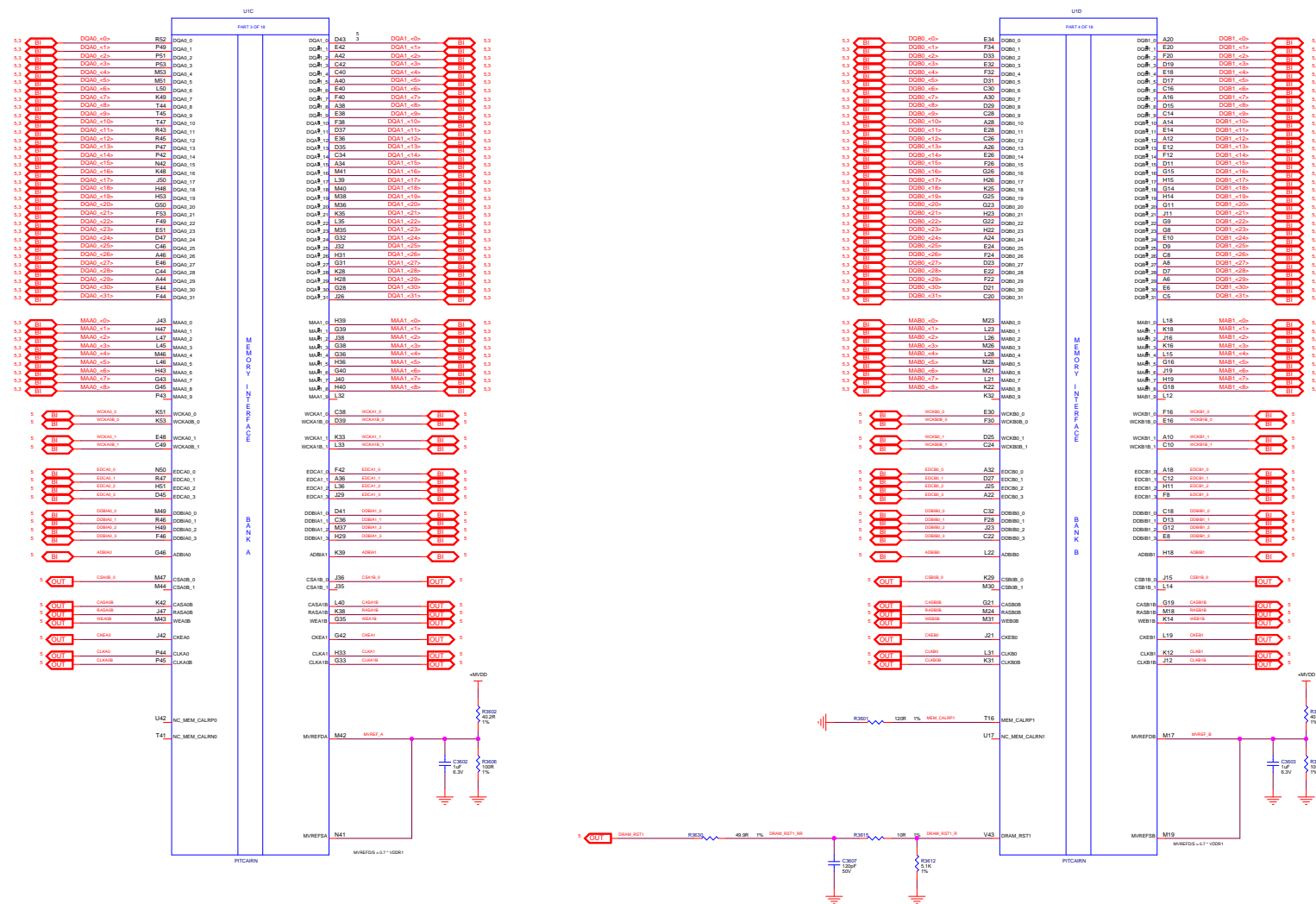
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TITLE:

Trinidad XT/PRO GD5

(3) TRINIDAD MEM INTERFACE CH A/B



AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: TRINIDAD MEM CH AB

DATE: Thu Jan 15 02:26:19 2015

SHEET NUMBER: 3 OF 23

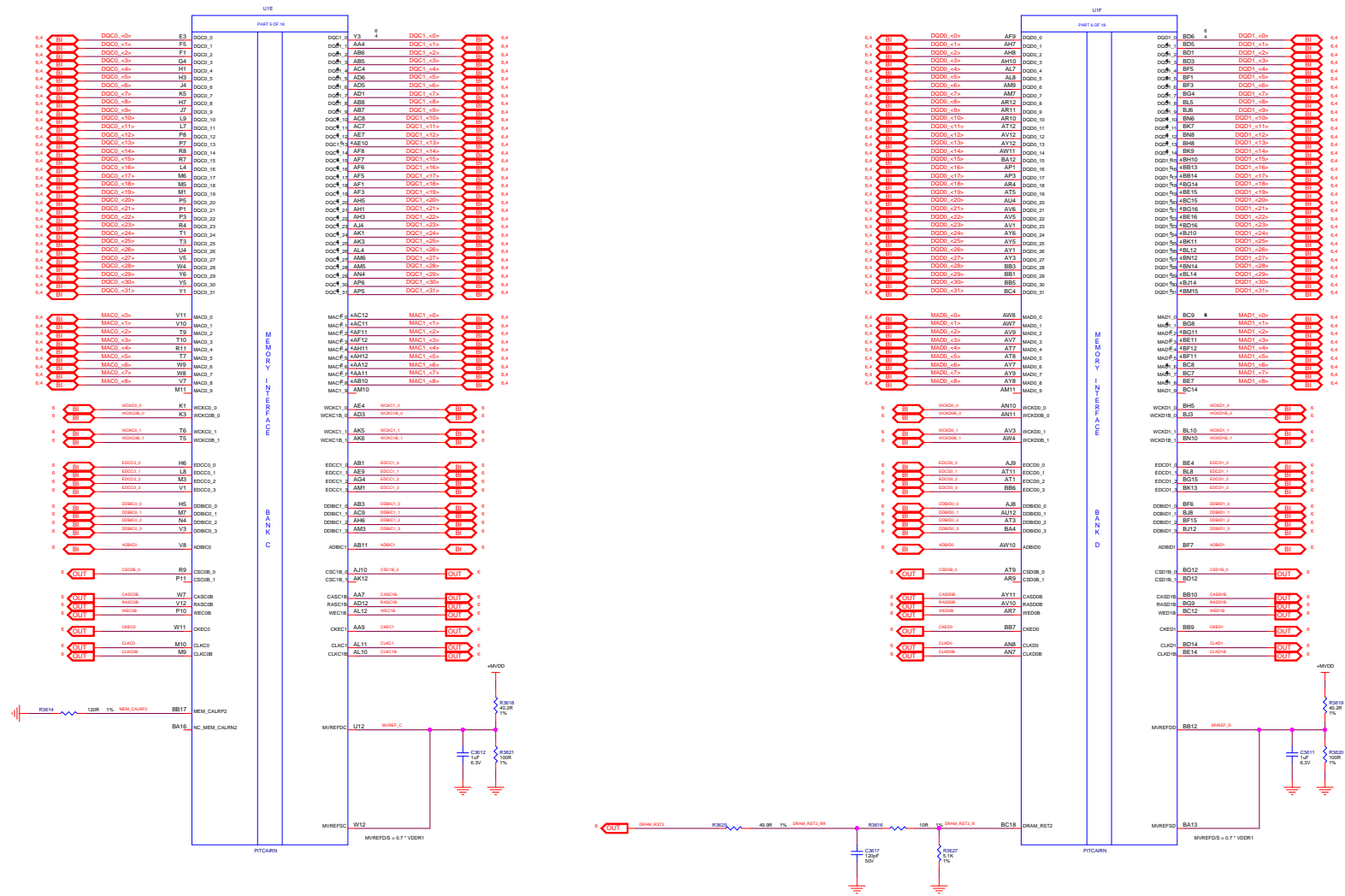
DOCUMENT NUMBER: 105_C634xx_0

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TITLE: **Trinidad XT/PRO GDS**

(4) TRINIDAD MEM INTERFACE CH C/D



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#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: TRINIDAD MEM CH C/D

DATE: Thu, Jan 15 02:26:20 2015

SHEET NUMBER: 4 OF 23

DOCUMENT NUMBER: 106_C634xx_008

REV: 1.0

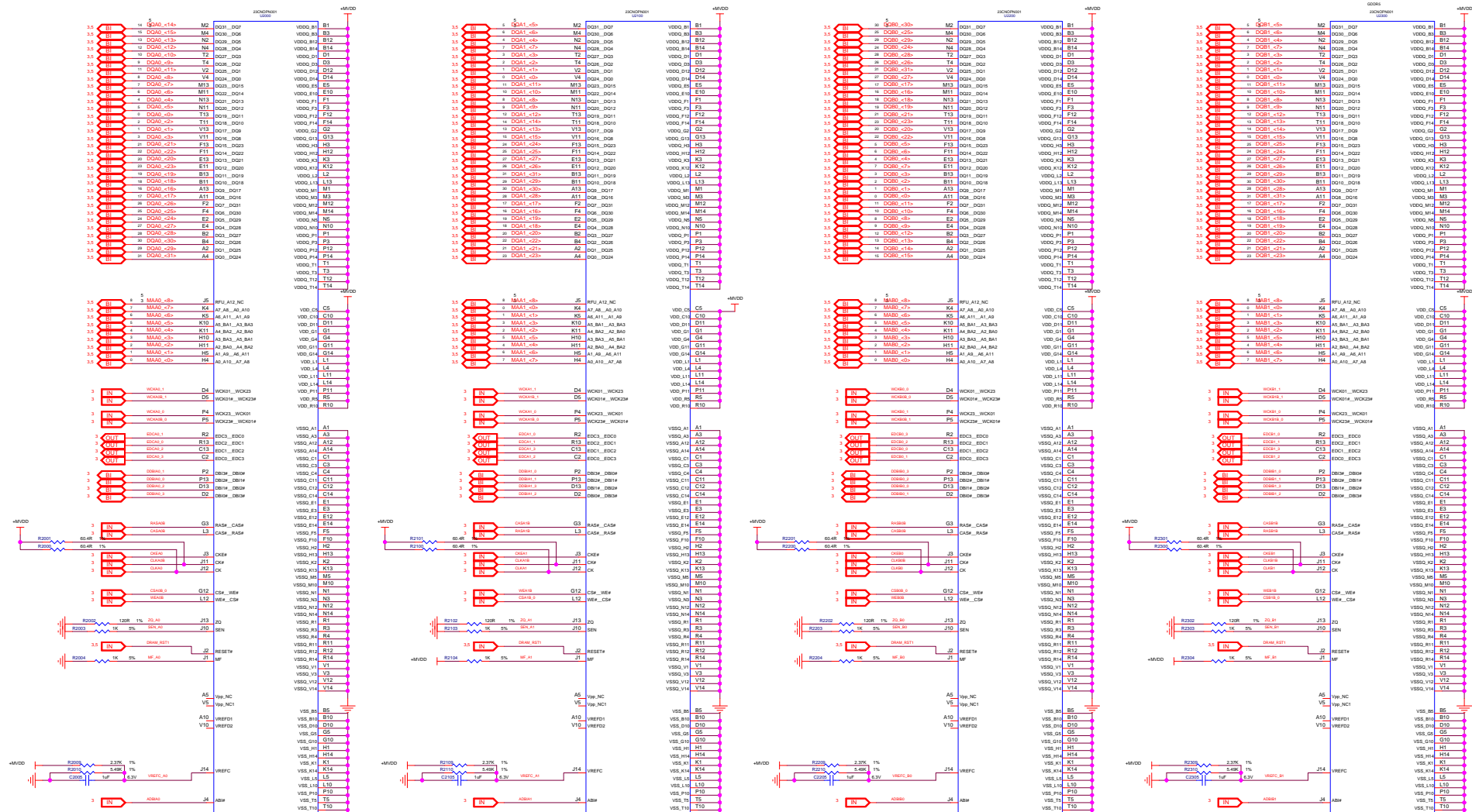
TITLE: Trinidad XT/PRO GD5

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(5) GDDR5 MEMORY CH A/B



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SHEET: GDDR5 MEM CH AB

DATE: Thu Jan 15 02:26:20 2015

SHEET NUMBER: 5 OF 22

DOCUMENT NUMBER: 105_C634xx_0

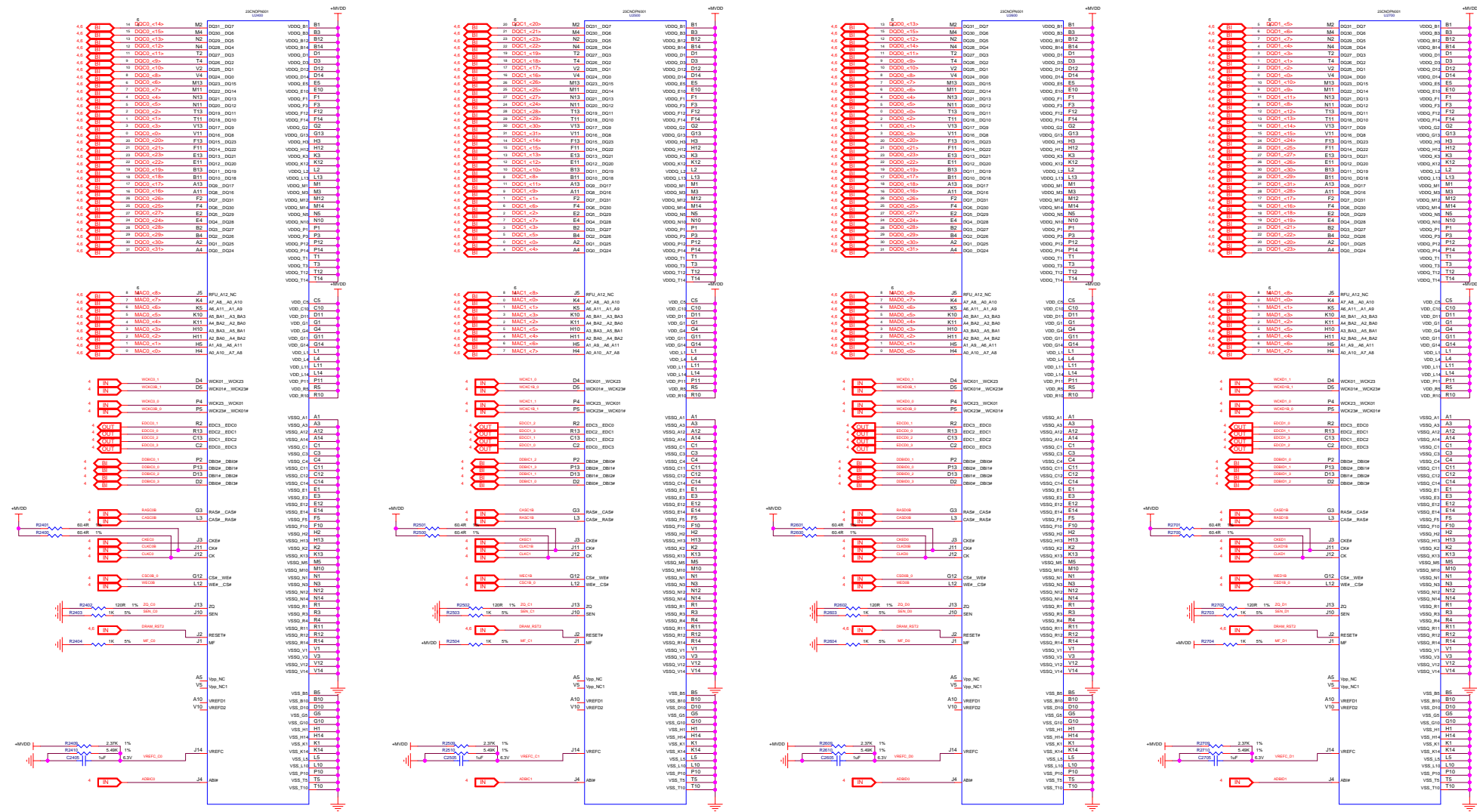
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TITLE: Trinidad XT/PRO GDS

(6) GDDR5 MEMORY CH C/D



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SHANGHAI, CHINA 201203

SHEET: GDDR5 MEM CH CD

DATE: Thu Jan 15 02:26:21 2015

SHEET NUMBER: 6 OF 22

DOCUMENT NUMBER: 105_C634xx

REV: 1.0

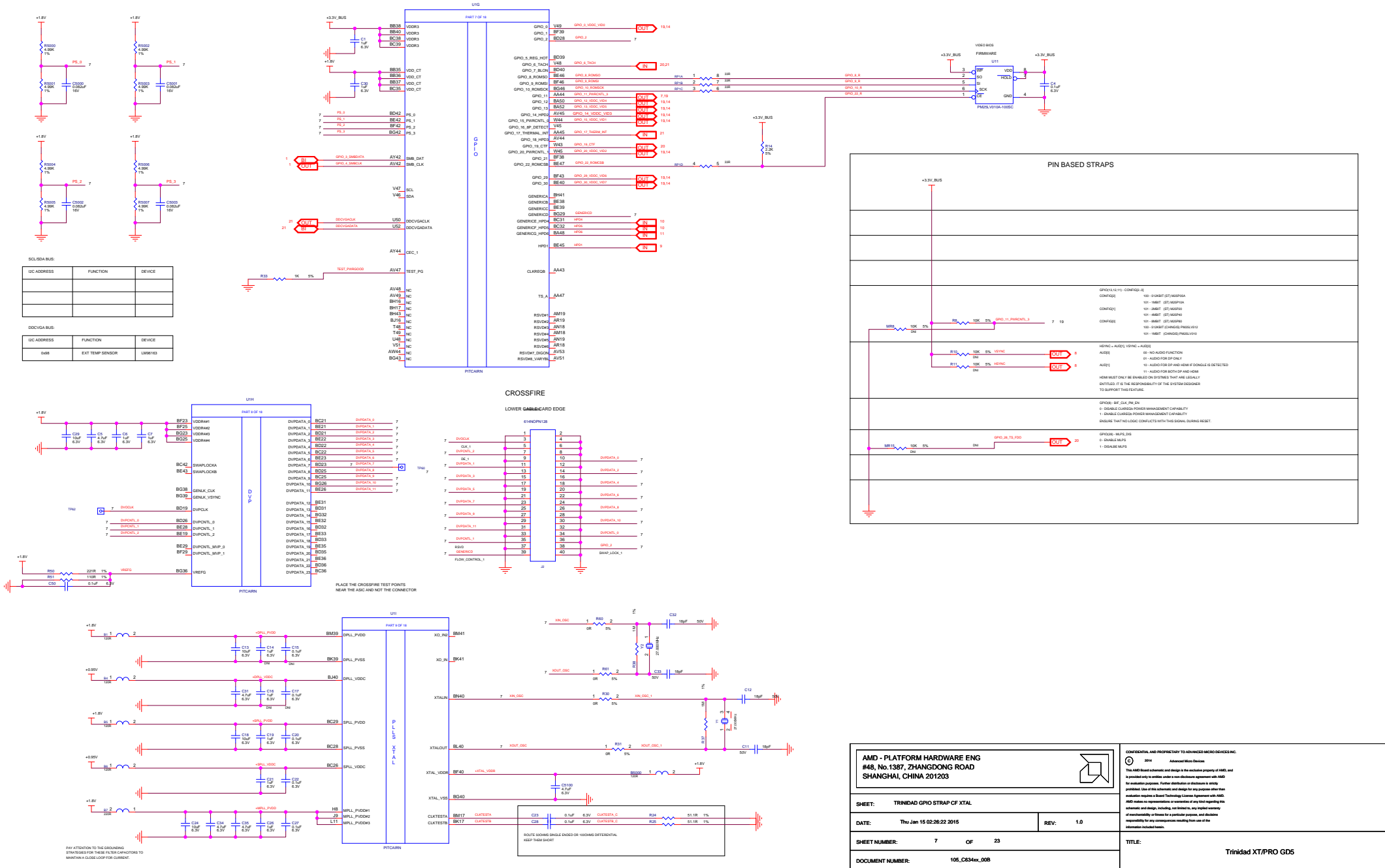
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Trinidad XT/PRO GD5

(7) TRINIDAD GPIO STRAP CF XTAL



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SHANGHAI, CHINA 201203

AMD

SHEET: TRINIDAD GPIO STRAP CF XTAL

DATE: Thu Jan 15 02:26:22 2015

SHEET NUMBER: 7 OF 23

DOCUMENT NUMBER: 106_C634xx_008

REV: 1.0

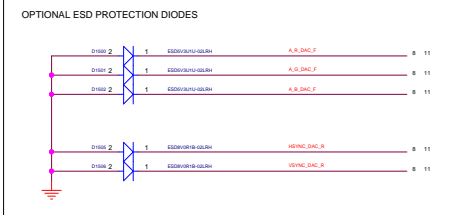
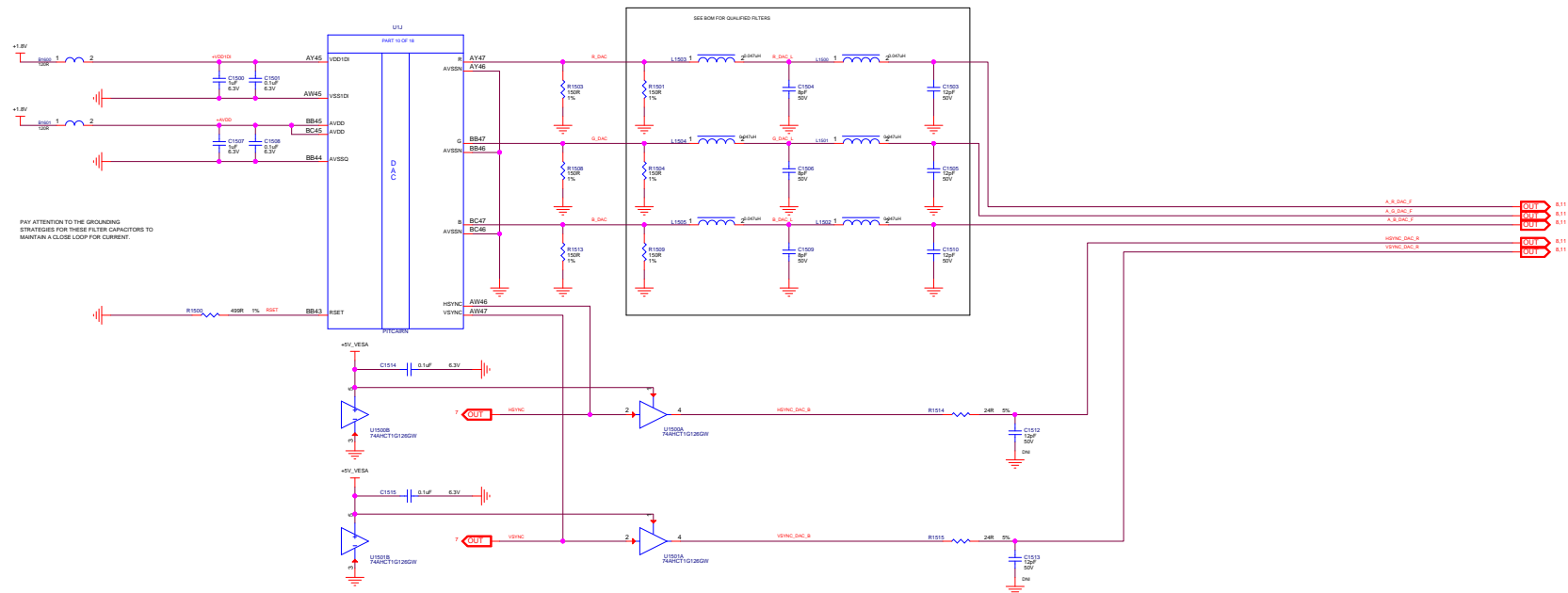
TITLE: Trinidad XT/PRO GD5

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(8) TRINIDAD DAC1 LOCK



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SHEET: TRINIDAD DAO

DATE: Wed Jul 30 07:10:44 2014

SHEET NUMBER: 8 OF 23

DOCUMENT NUMBER: 105_C634xx_00B



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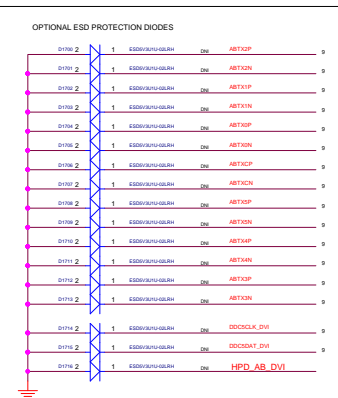
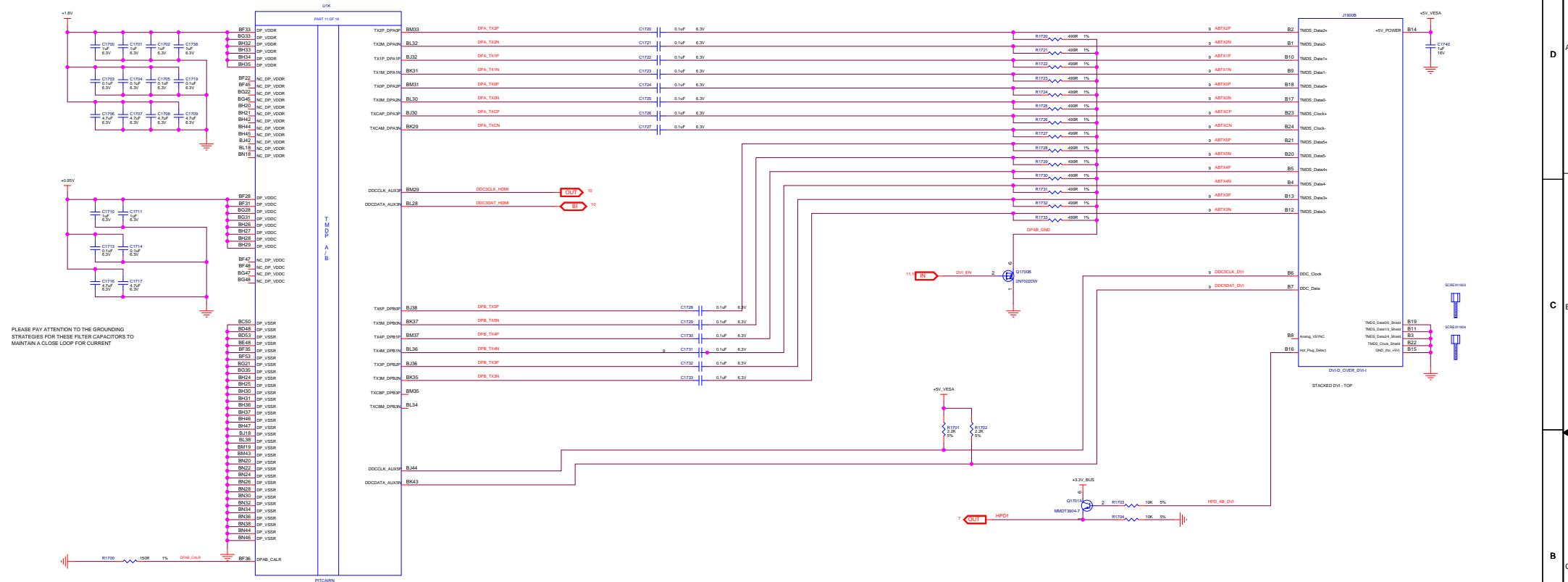
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TITLE:

Trinidad XT/PRO GD5

(9) TRINIDAD TMDP A/B



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SHEET: TRINIDAD TMDPAB dDVI TOP

DATE: Thu Jan 15 02:26:22 2015

SHEET NUMBER: 9 OF 23

DOCUMENT NUMBER: 105_C634xx_00B

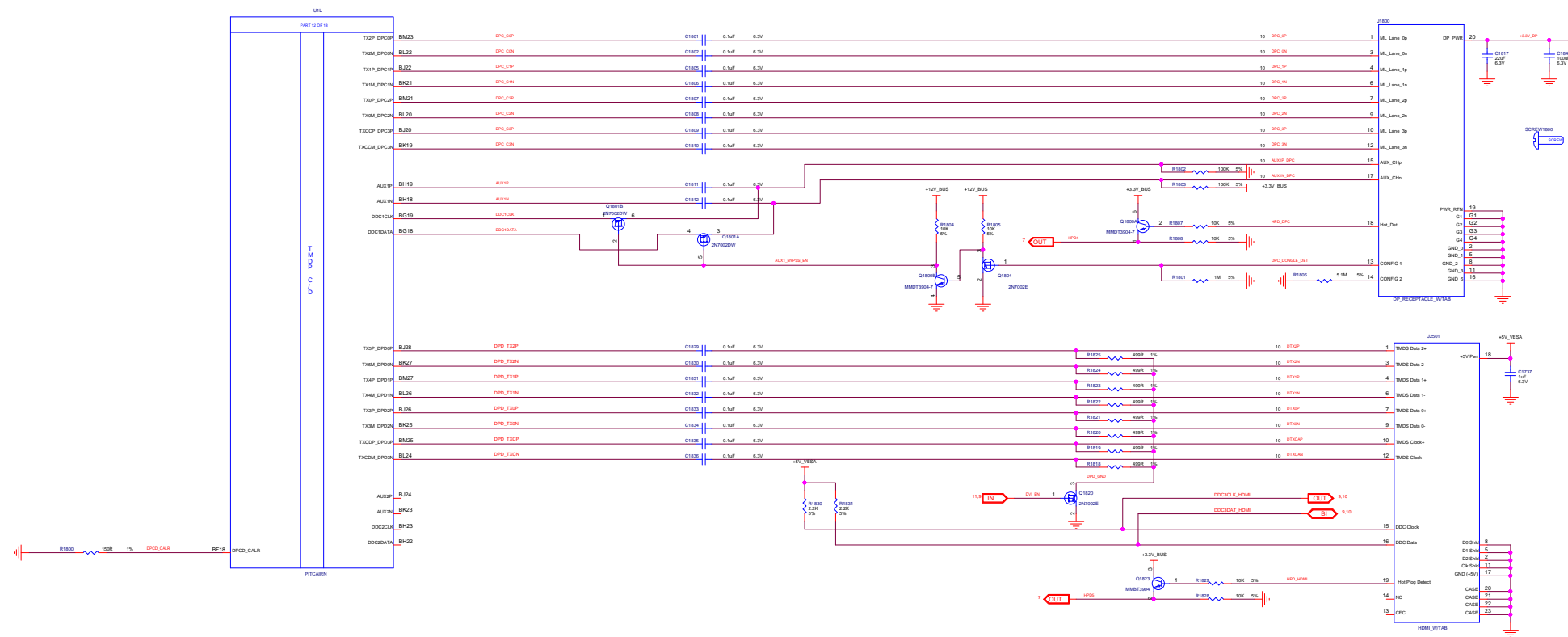
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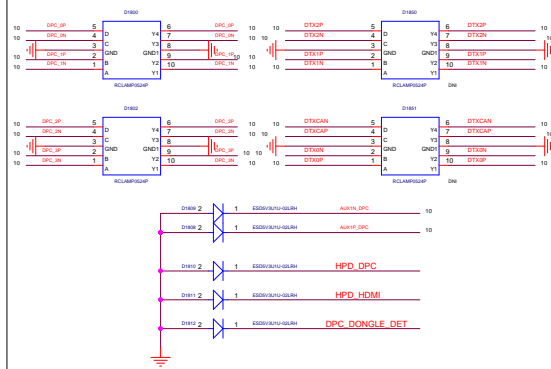
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TITLE: **Trinidad XT/PRO GL**

(10) TRINIDAD TMDP C/D



OPTIONAL ESD PROTECTION DIODES



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SHEET: TRINIDAD TMOFCO CP NOM

DATE: Thu Jan 15 02:26:22 2015

SHEET NUMBER: 10 OF 23

DOCUMENT NUMBER: 105_C634xx_00B

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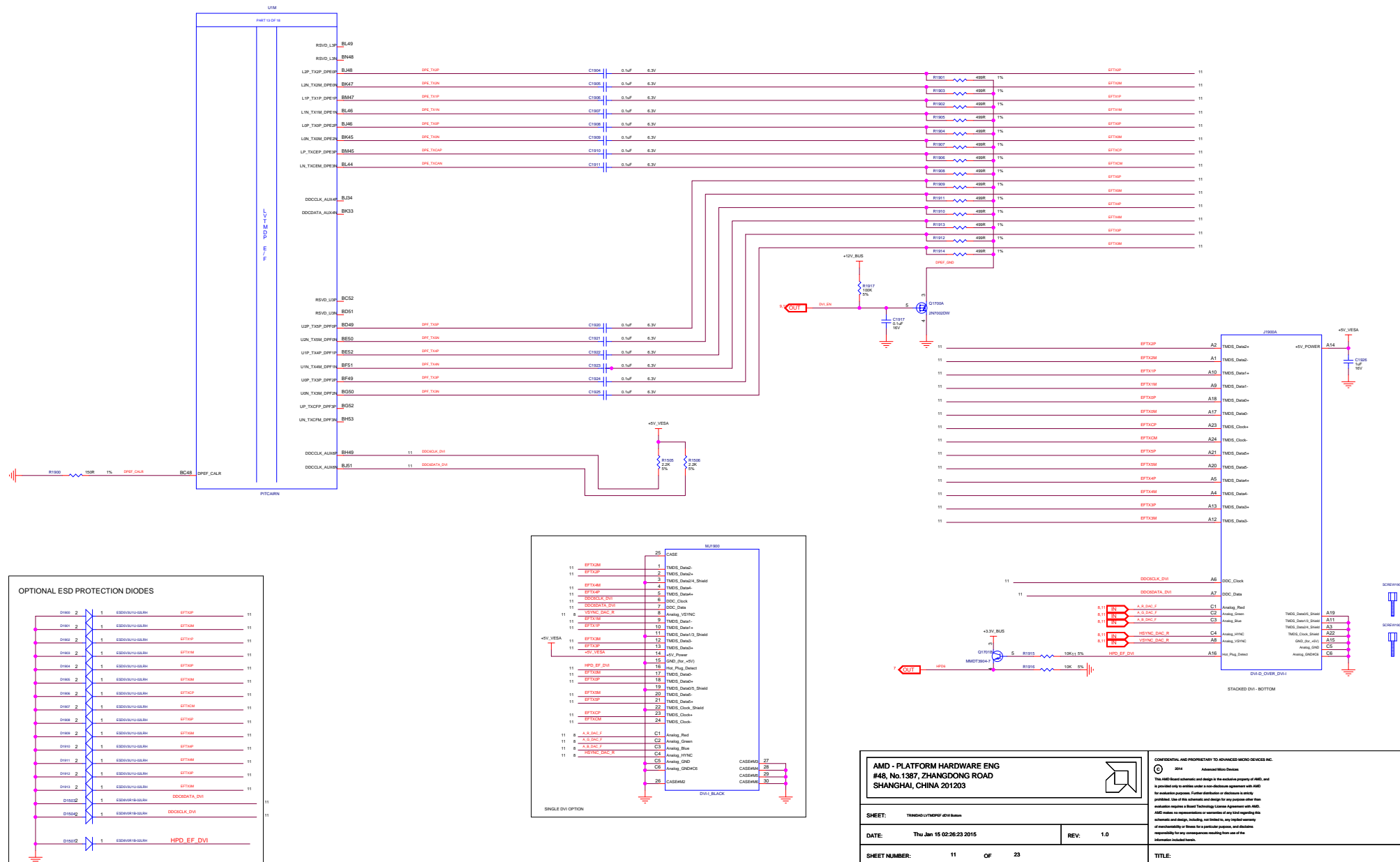
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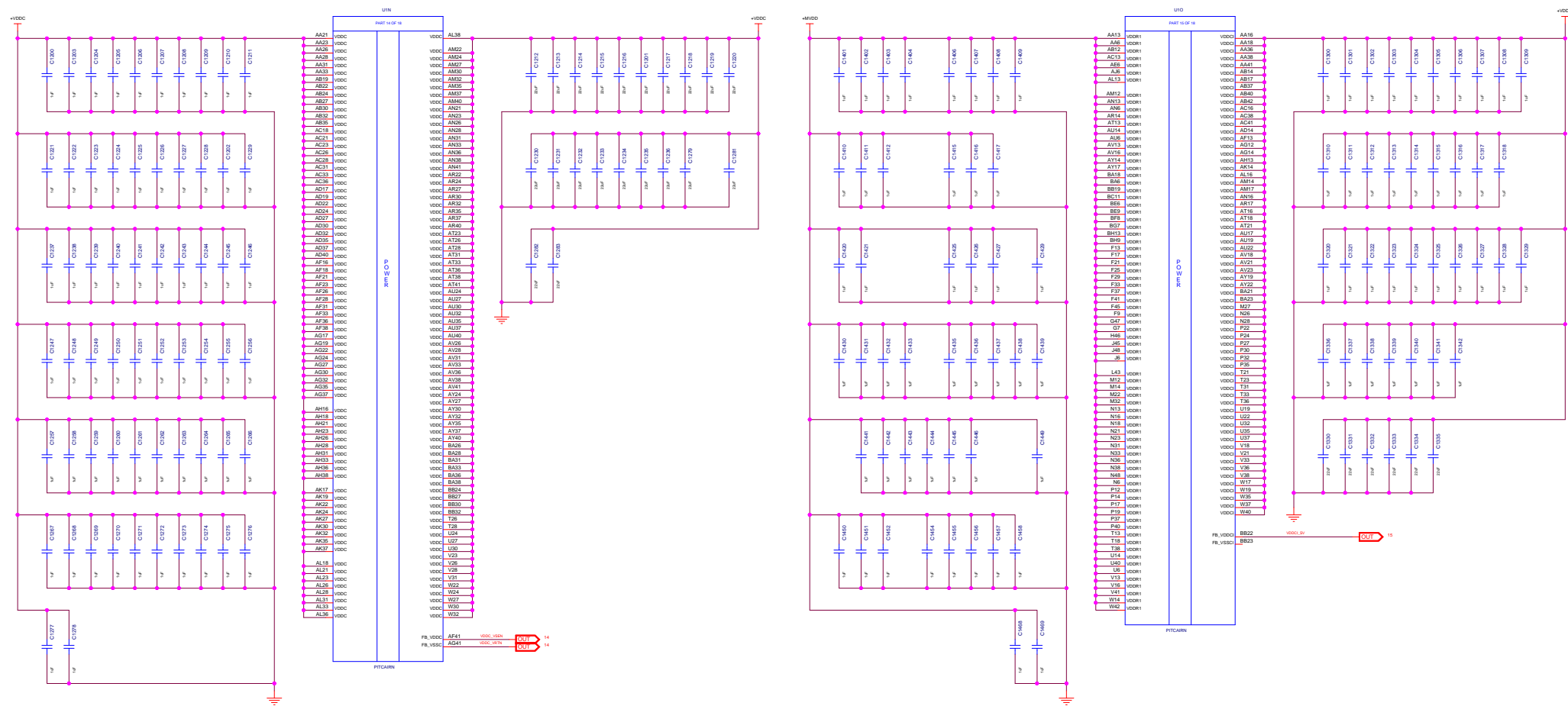
TITLE:	
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Trinidad XT/PRO GD5

(11) TRINIDAD LVTMDP E/F



(12) TRINIDAD POWER



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#48, No.1387, ZHANGDONG ROAD
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SHEET: TRINIDAD POWER

DATE: Thu Jan 15 02:26:23 2015

SHEET NUMBER- 12 OF 22

DOCUMENT NUMBER: 105_C634xx_00B

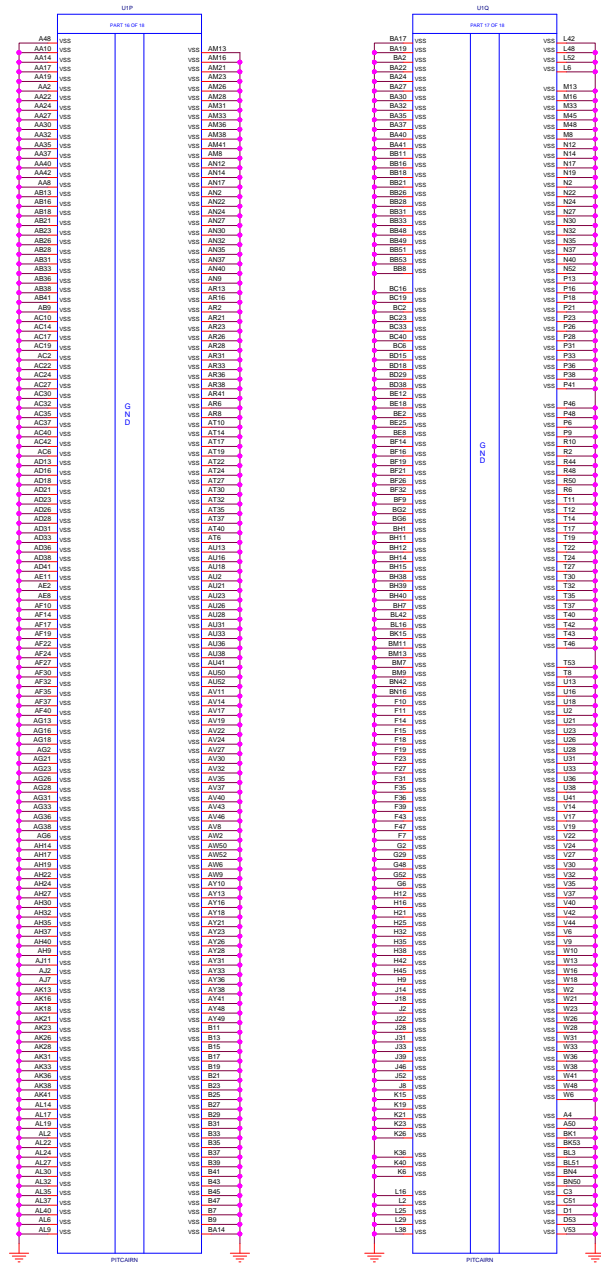



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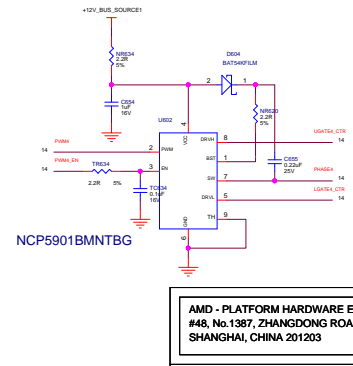
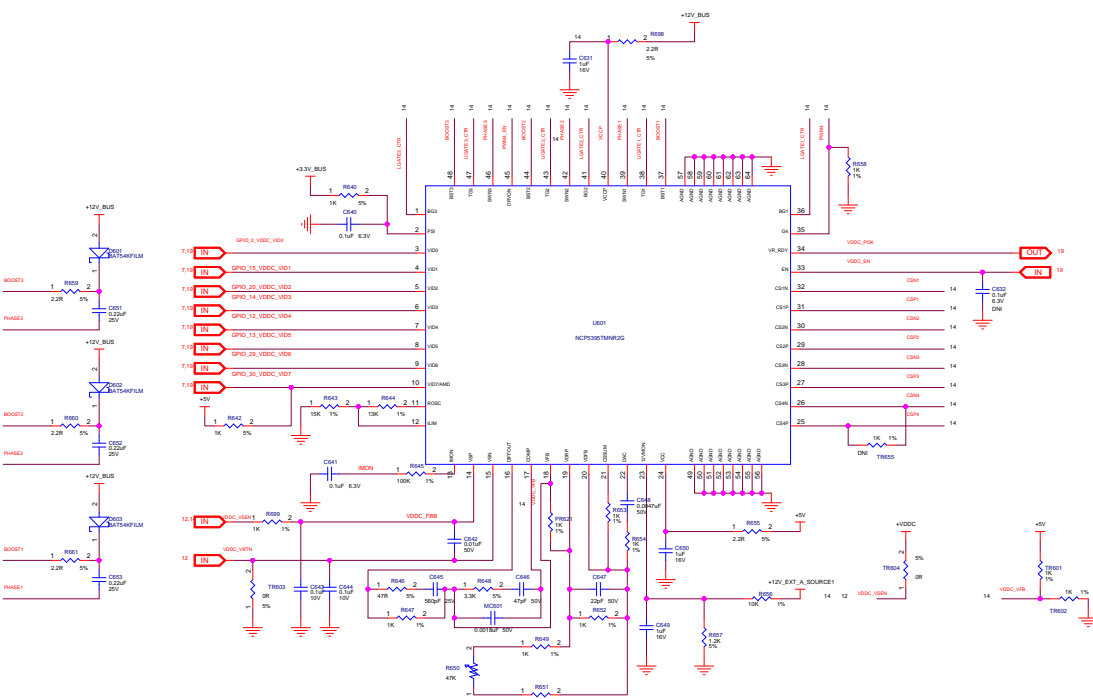
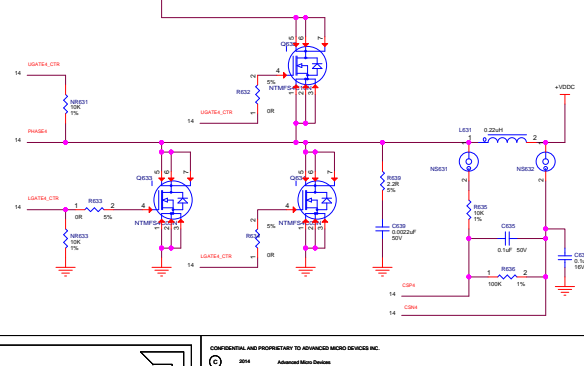
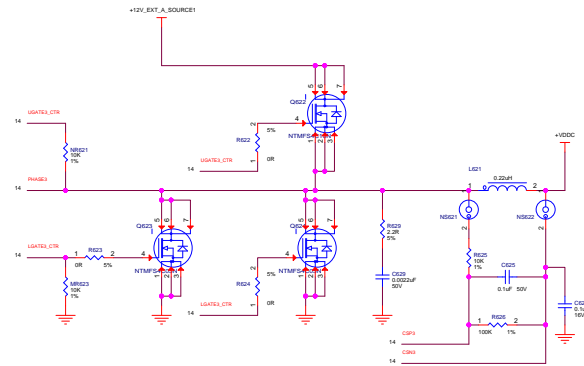
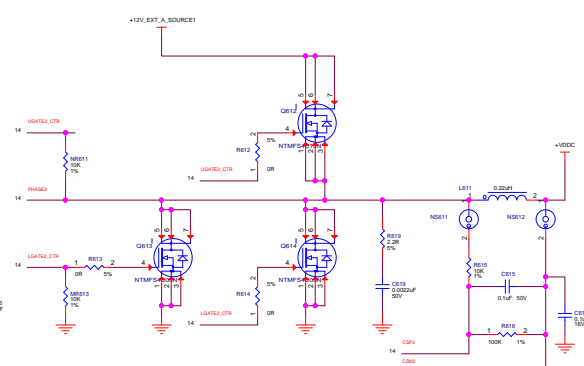
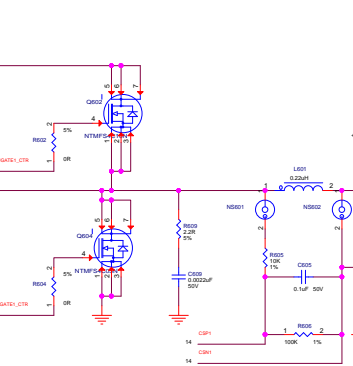
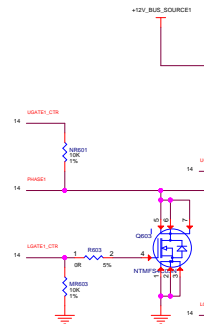
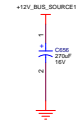
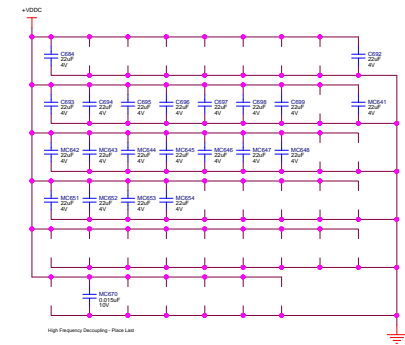
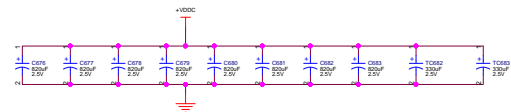
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TITLE: Trinidad XT/PRO GDS

(13) TRINIDAD GROUND



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SHEET: TRINIDAD GND			
DATE: Wed Jul 30 07:10:46 2014	REV: 1.0	TITLE: Trinidad XT/PRO GD5	
SHEET NUMBER: 13 OF 23			
DOCUMENT NUMBER: 106_C634xx_008			



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SHANGHAI, CHINA 201203

SHEET: 0000

DATE: Wed Jul 30 07:10:42 2014

SHEET NUMBER: 14 OF 23

DOCUMENT NUMBER: 106_C634xx_008

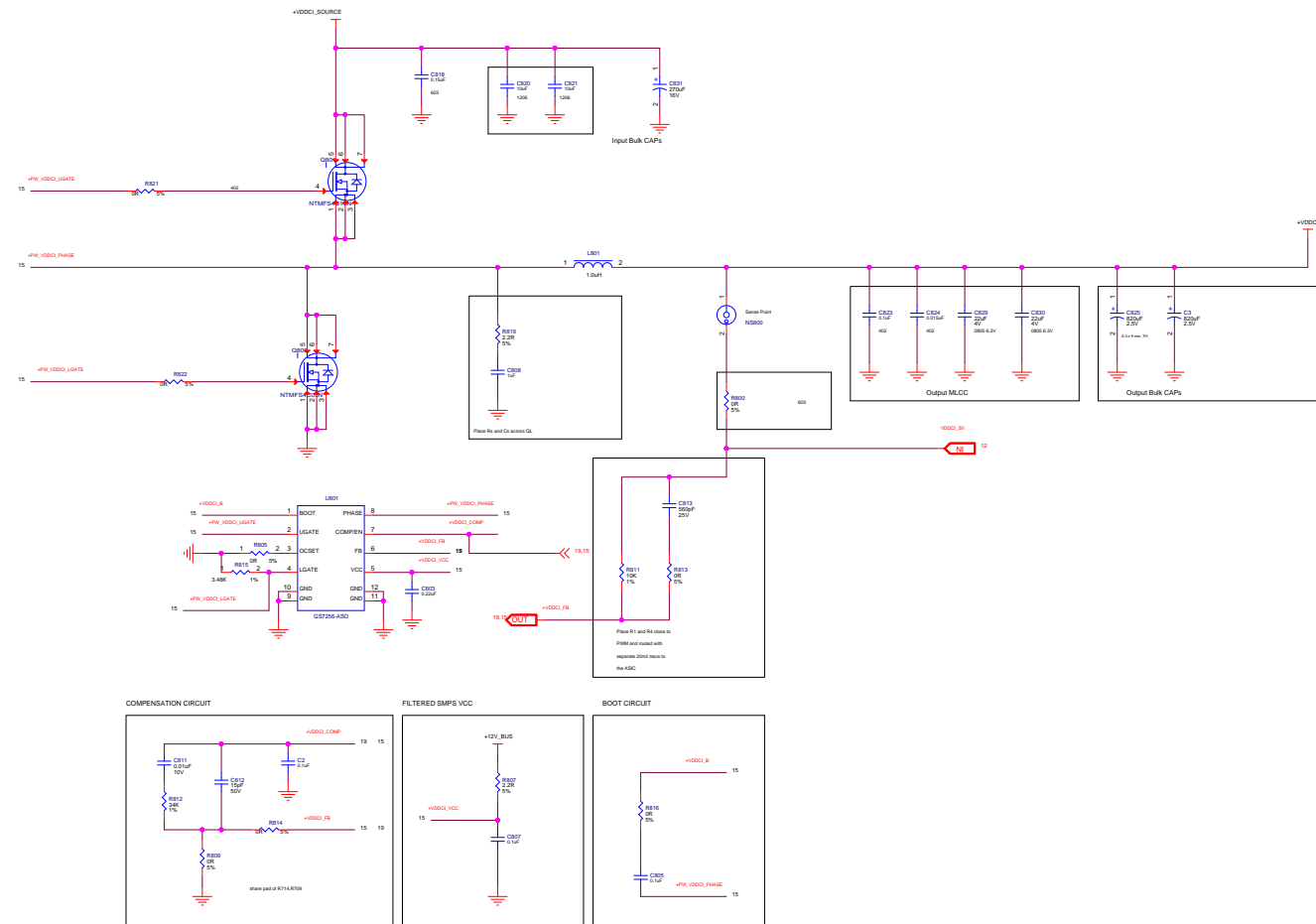
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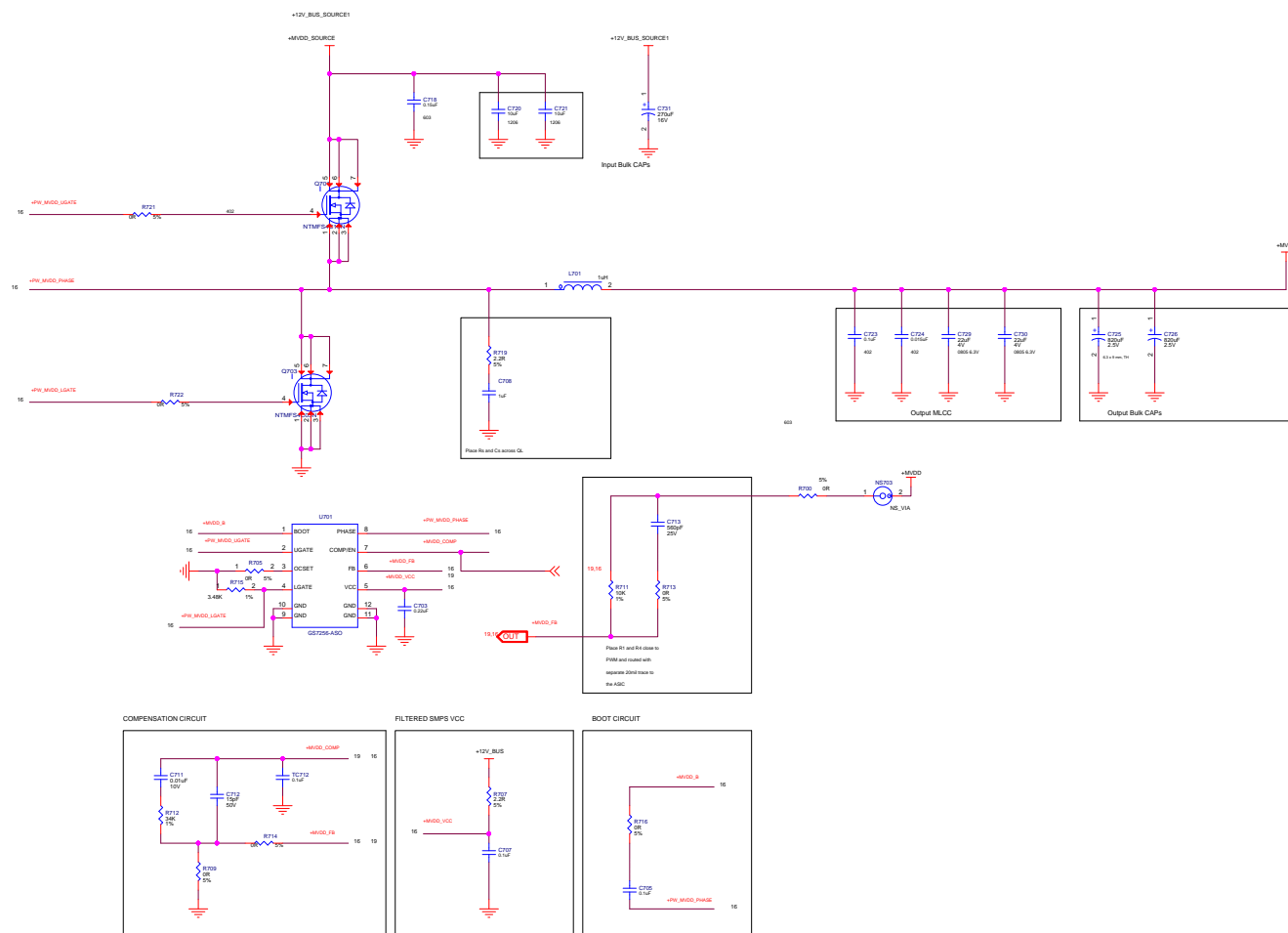
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TITLE: Trinidad XT/PRO GDS

(15) VDDCI



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SHEET: VDDCI		REV: 1.0		TITLE: Trinidad XT/PRO GD5	
DATE: Wed Jul 30 07:10:40 2014					
SHEET NUMBER: 15 OF 23					
DOCUMENT NUMBER: 106_C634xx_008					



REV: 1.0

OF 23

105_C634xx_00B

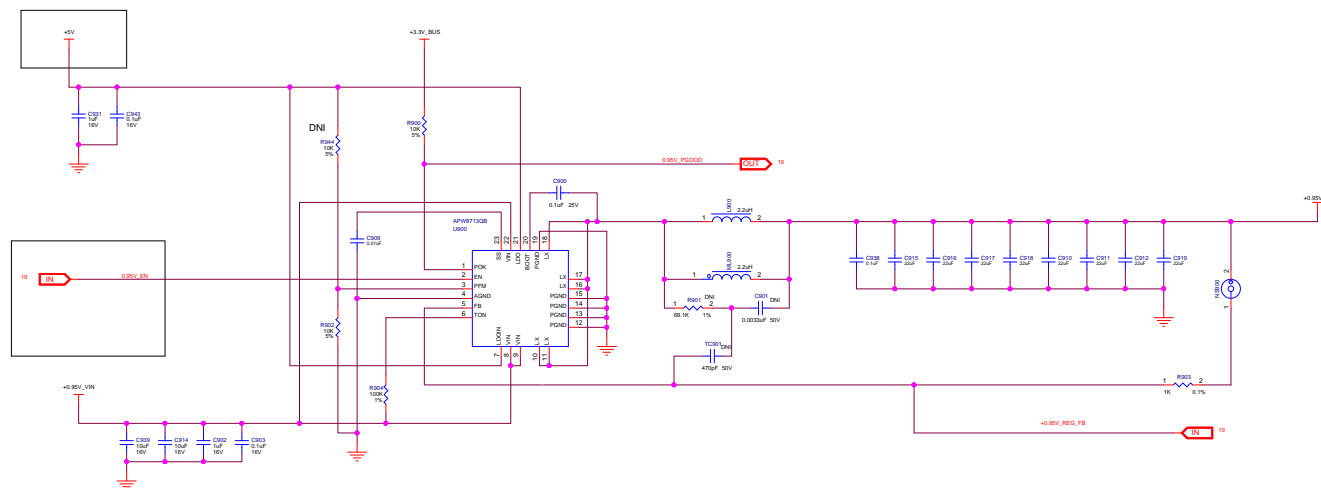
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TITLE:	Trinidad XT/PRO GD5
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(17) 0.95V



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SHEET: 0.95V REG

DATE: Thu Jan 15 02:32:13 2015

REV:	1.0
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SHEET NUMBER: 17 OF 23

DOCUMENT NUMBER: 105_C634xx_00B

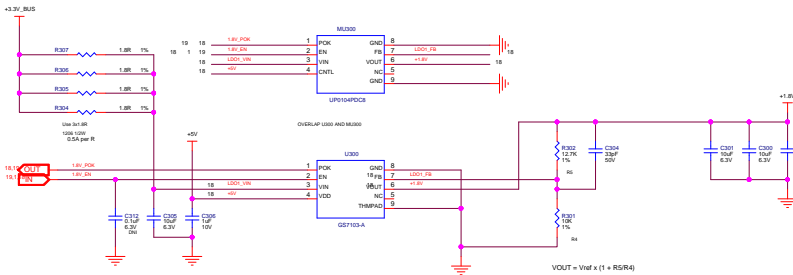
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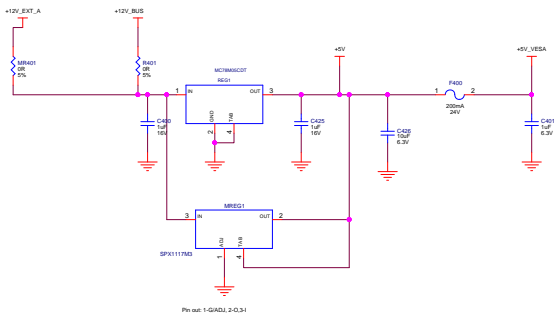
Trinidad XT/PRO GD5


(18) SMALL RAIL REGULATORS

LDO #1: VIN = 3.0V TO 3.6V MAX VOUT = +1.8V +/- 2% IOU = 1.3A RMS MAX
PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING



REGULATOR FOR +5V RAILS
IOU MAX = 150mA



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SHEET: SMALL RAIL REGULATORS		DATE: Thu Jan 15 02:26:23 2015			REV: 1.0
SHEET NUMBER: 18		OF 23			TITLE: Trinidad XT/PRO GDS
DOCUMENT NUMBER: 106_C634xx_008					

(19) POWER MANAGEMENT

D

C

B

A

D

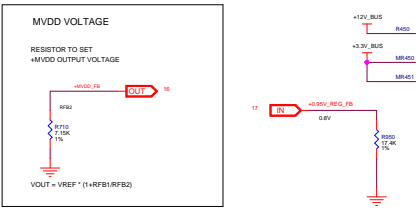
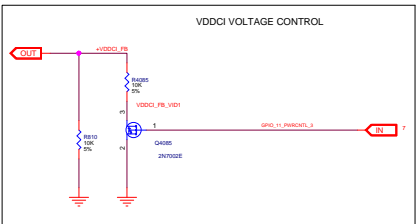
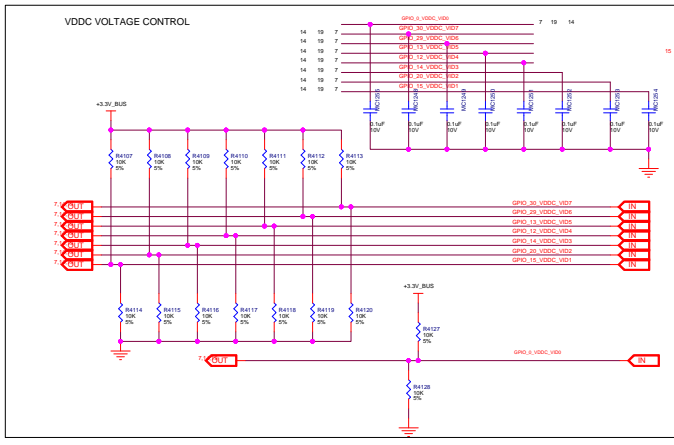
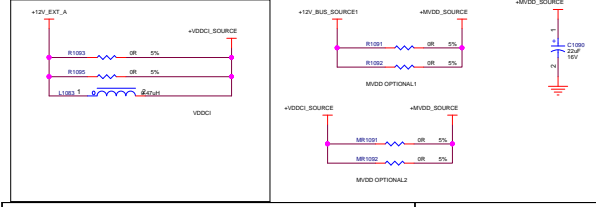
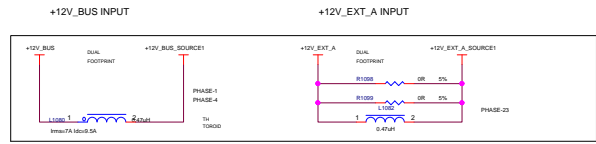
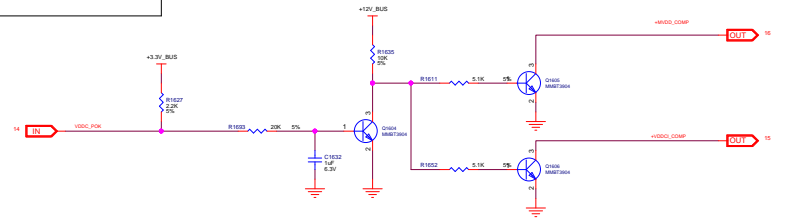
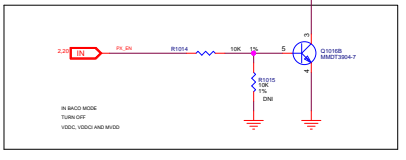
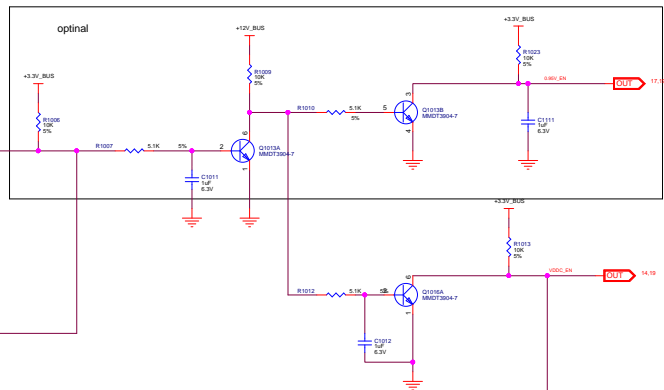
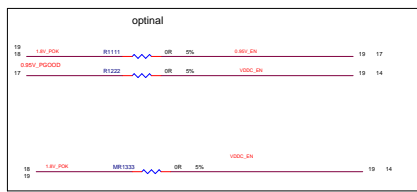
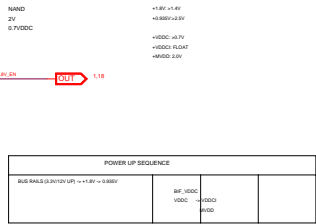
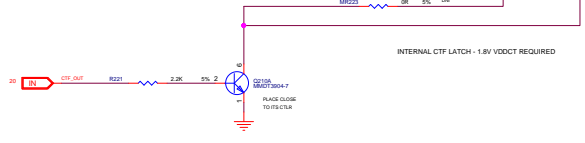
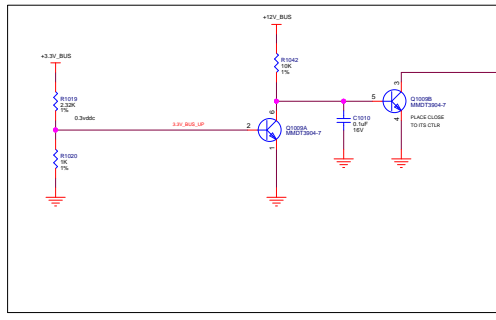
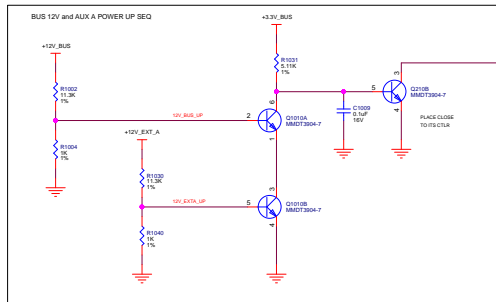
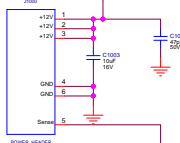
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B

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+VDDC SOURCE

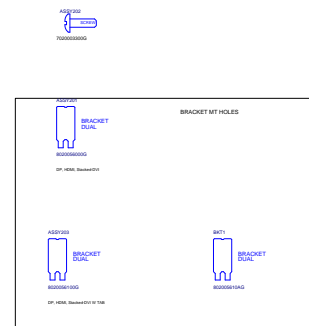
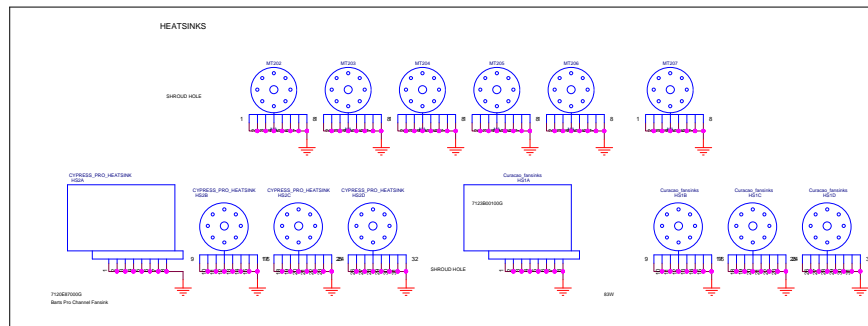
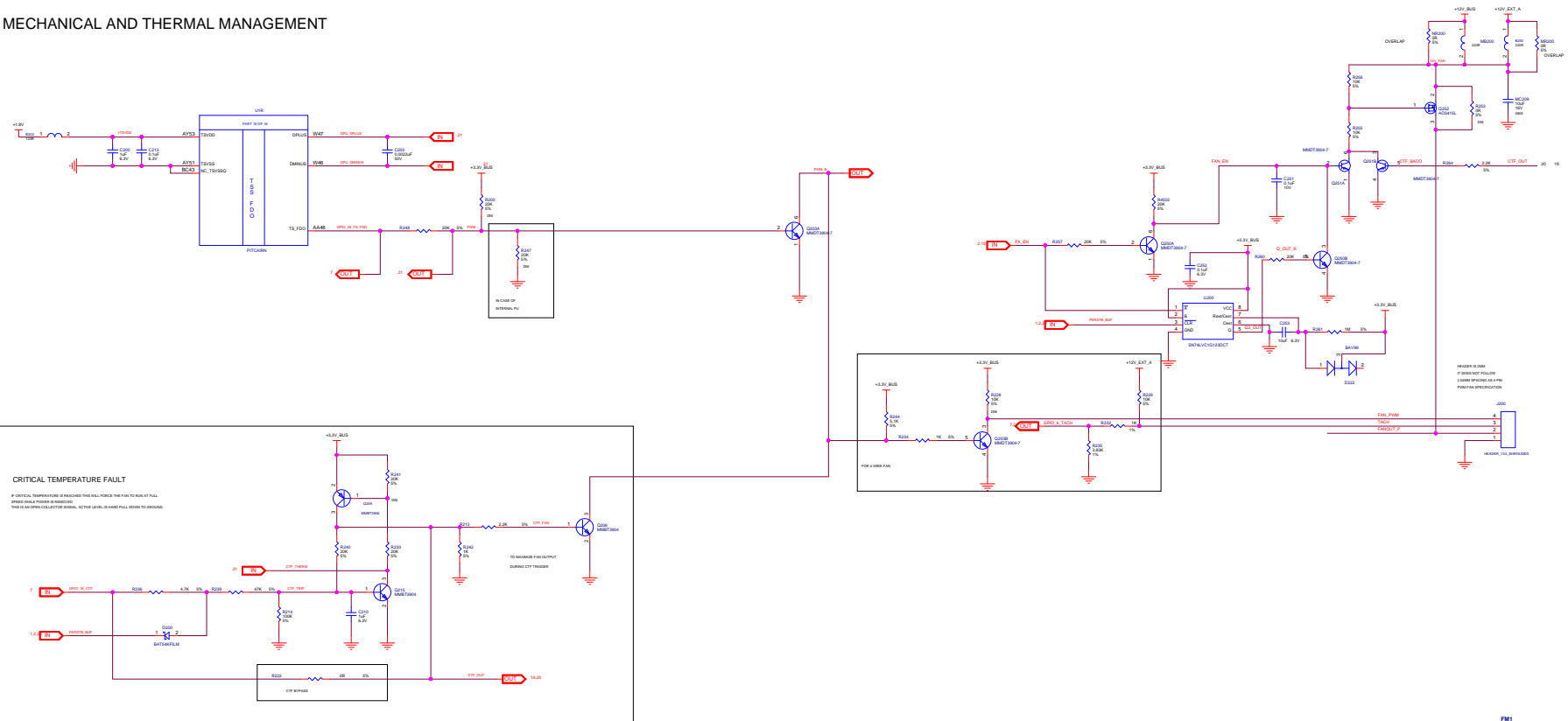
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#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203


SHEET: POWER MANAGEMENT
DATE: Wed Jul 30 07:14:28 2014
SHEET NUMBER: 19 OF 23
DOCUMENT NUMBER: 106_C634xx_008

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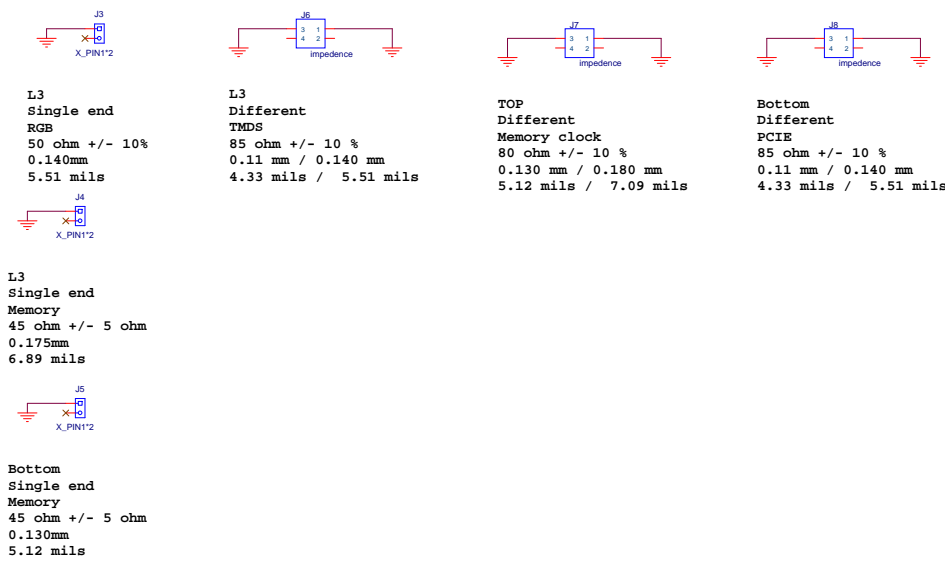
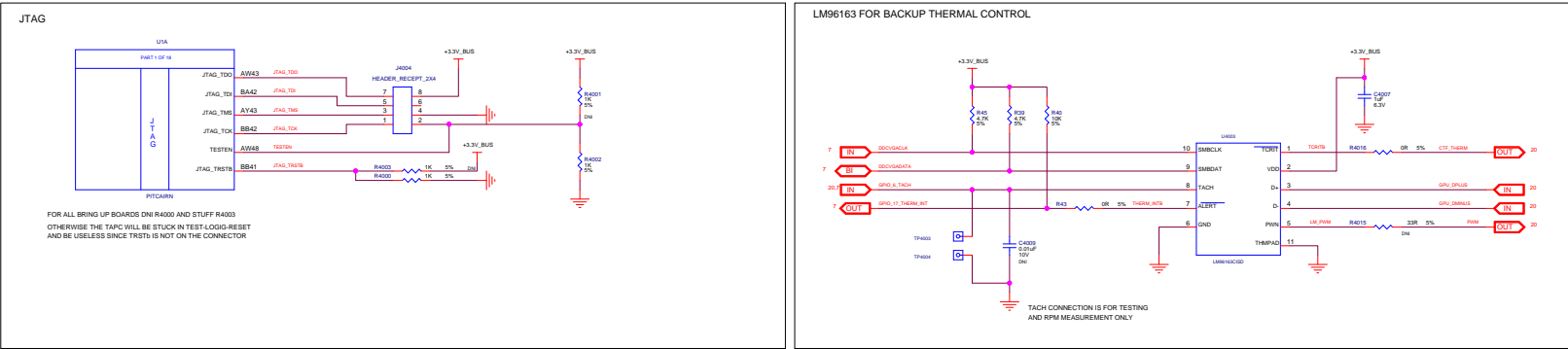
TITLE: Trinidad XT/PRO GD5


(20) MECHANICAL AND THERMAL MANAGEMENT

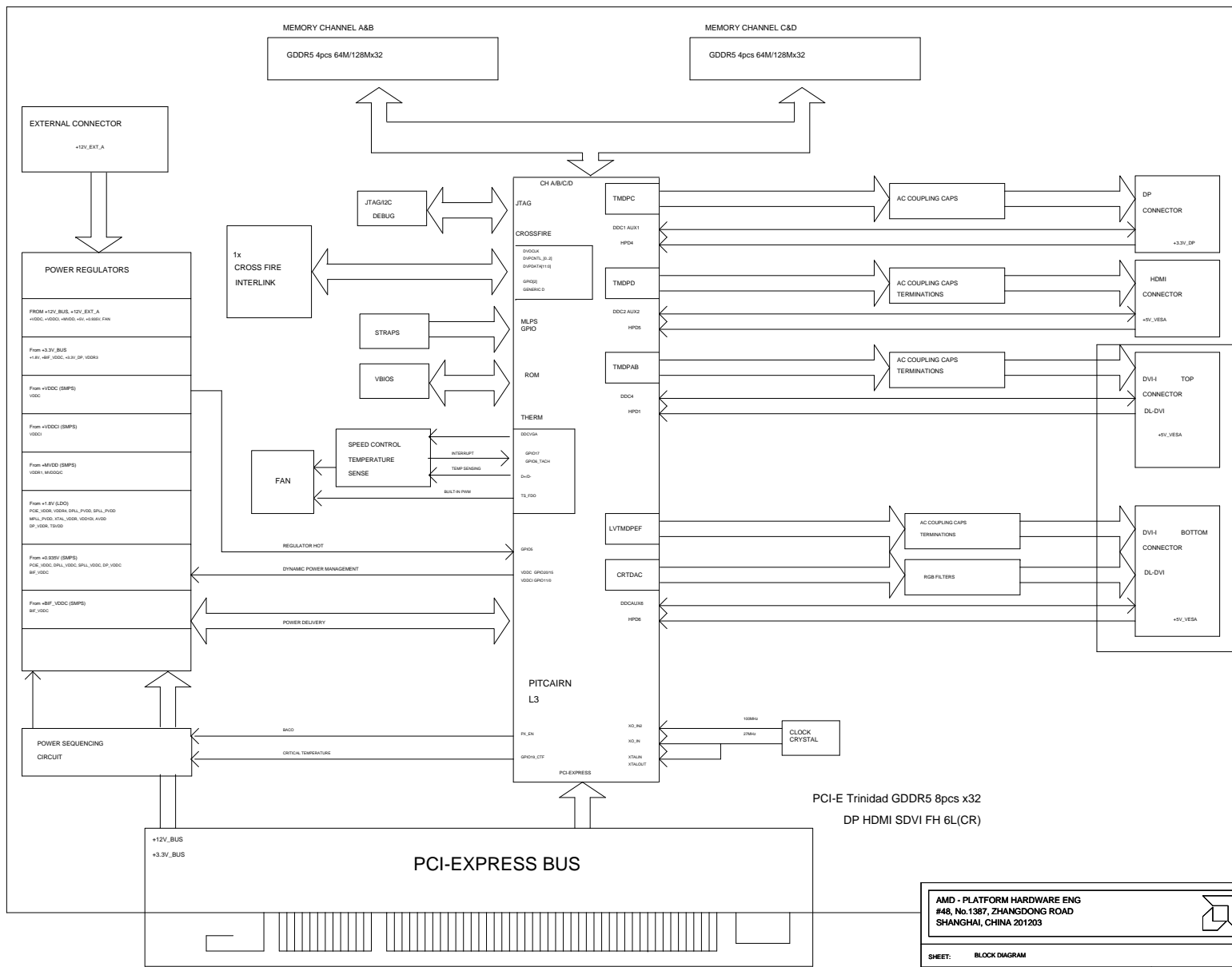


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<p>SHEET: MECHANICAL AND THERMAL</p>					
<p>DATE: Wed Jul 30 07:10:40 2014</p>		<p>REV: 1.0</p>			
<p>SHEET NUMBER: 20 OF 23</p>				<p>TITLE:</p> <p>Trinidat XT/PRO GDS</p>	
<p>DOCUMENT NUMBER: 106_C034xx_008</p>					
<p>NOTES: NOTE</p>					

(21) DEBUG CIRCUITS



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SHEET: DEBUG CIRCUITS		REV: 1.0		
DATE: Wed Jul 30 07:10:46 2014				
SHEET NUMBER: 21 OF 23		TITLE: Trinidad XT/PRO GD5		
DOCUMENT NUMBER: 106_C634xx_008				



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SHEET: BLOCK DIAGRAM

DATE: Wed May 14 09:29:44 2014

SHEET NUMBER: 22 OF 23

DOCUMENT NUMBER: 106_C634xx_008

REV: 1.0

TITLE: Trinidad XT/PRO GDS5

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Trinidad XT/PRO GD5

DOCUMENT NUMBER:

105_C634xxx_00E

DATE:

Wed Jul 23 09:06:58 2014

SHEET NUMBER:

23

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REV: 1.0

ENGINEER:

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C 20

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SCH Rev	PCB Rev	Date	REVISION DESCRIPTION
0	00A	20200808	
1	00B	20200916	1. add 3 decoupling caps for VDDC output 2. add 1 output cap for VDDIO 3. change VDDIO notation