

V075 0A : G78, DDR2 MEMORY 32Mx16

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REV HISTORY

A01

REV	VARIANT	NVPN	ASSEMBLY
B	BASE	600-10383xxx-100	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO. STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKL0000	600-10383-0000-100	P383 G72, 4G/333, 128MB/64Kx, 18Mx16 DDR2, DMS59, Desktop
2	SKL0001	600-10383-0001-100	P383 1V4H/V3, 2752M, 128MB/64Kx, 18Mx16 DDR2, DMS59, Workstation
3	SKL0002	600-10383-0001-100	P383 G72, 4G/333, 64MB/32Kx, 18Mx16 DDR2, DMS59, Desktop
4	SKL0003	600-52083-0001-100	P383 G72N/V3, 2752M, 128MB/64Kx, 18Mx16 DDR2, DMS59, Workstation
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO. STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	P383 Overview

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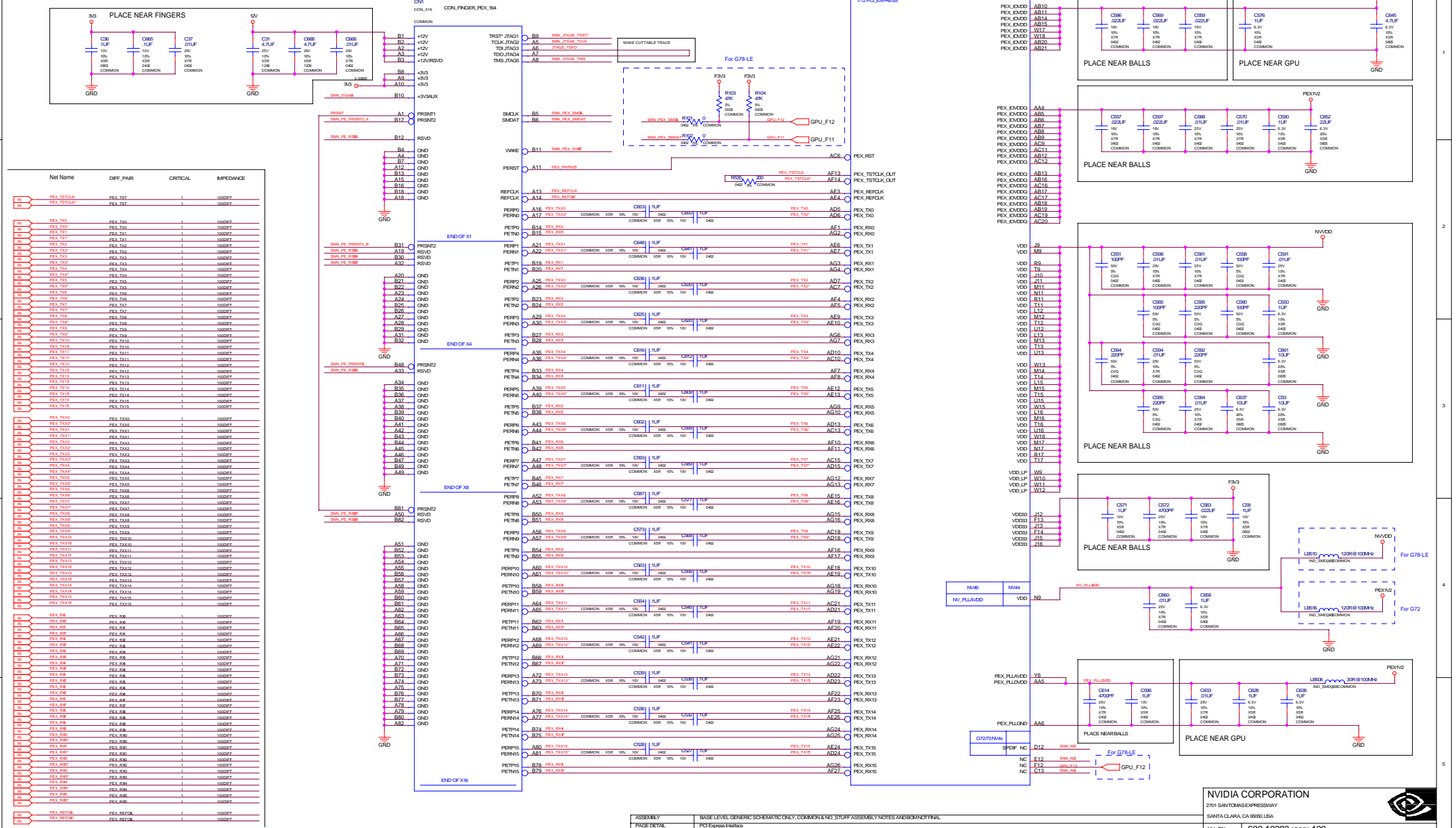
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PEX-Interface



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PAGE DETAIL	PCI Express Interface

ASSEMBLY	BASE LEVEL GEN
PAGE DETAIL	PCI Express Interfa

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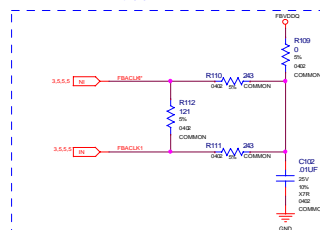
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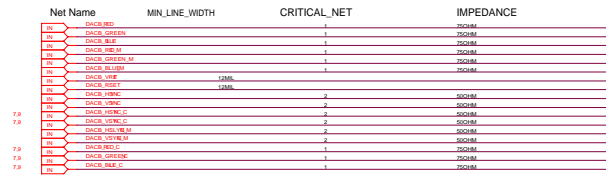
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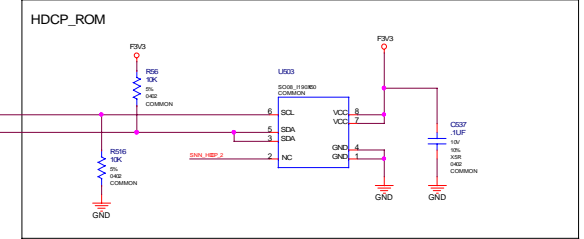
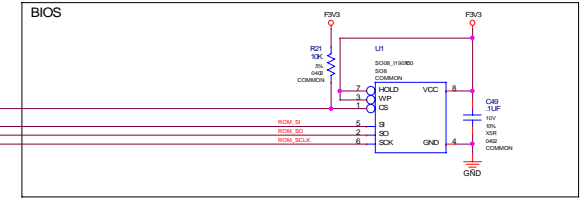
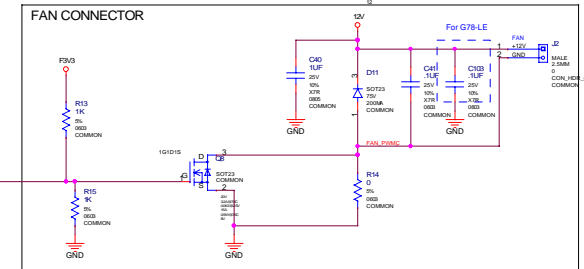
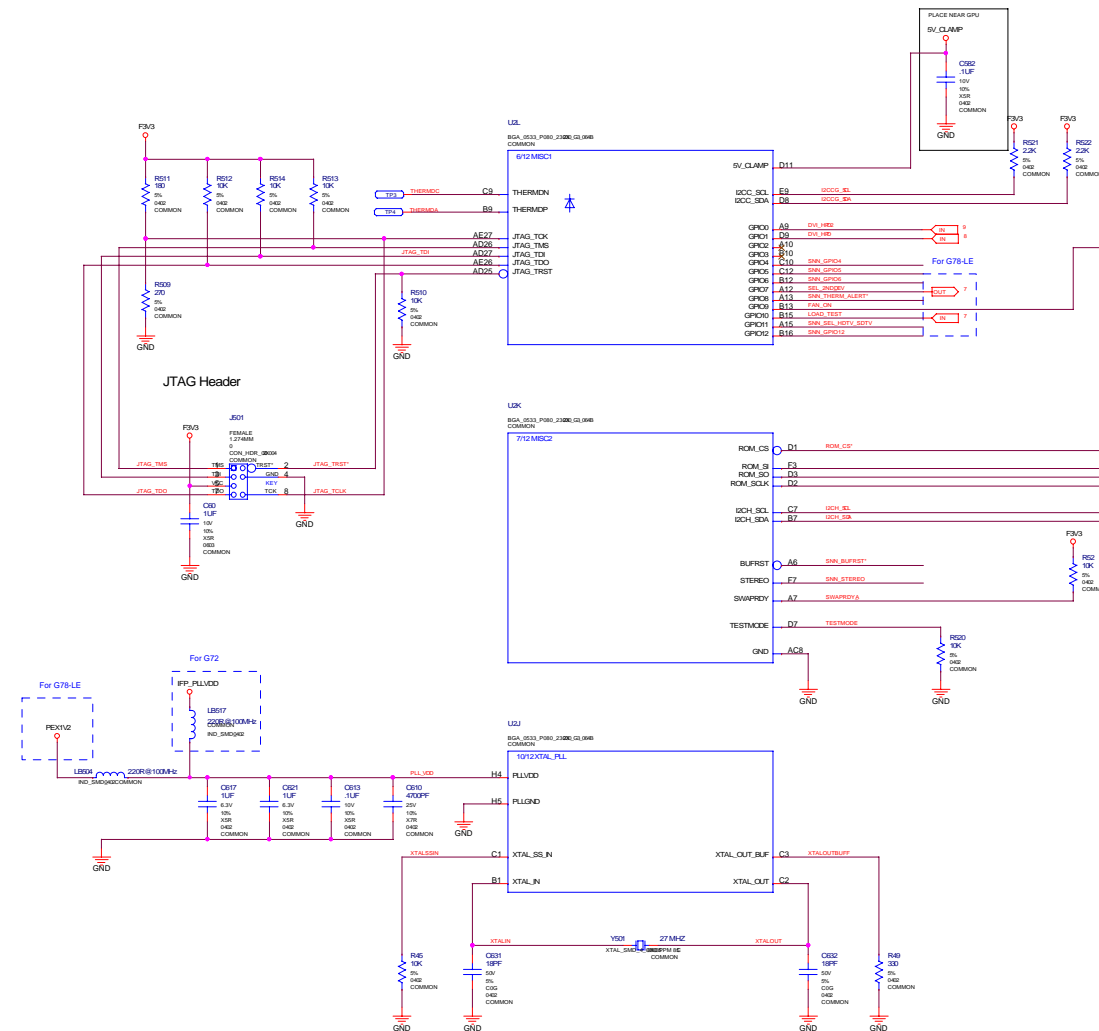
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XTAL, GPIO, BIOS, FAN, JTAG, THERMAL

Net Name	LINE_WIDTH	Voltage
SV_CLAMP	1.0MM	5.000V
TV	1.0MM	3.3V
CRITICAL_NET		IMPEDANCE
TV	1	50OHM
TV	1	50OHM

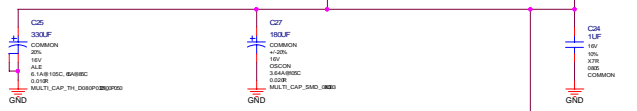
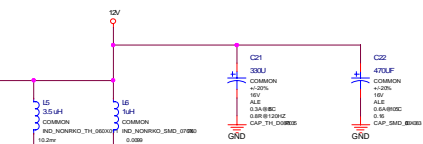
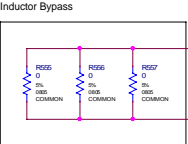
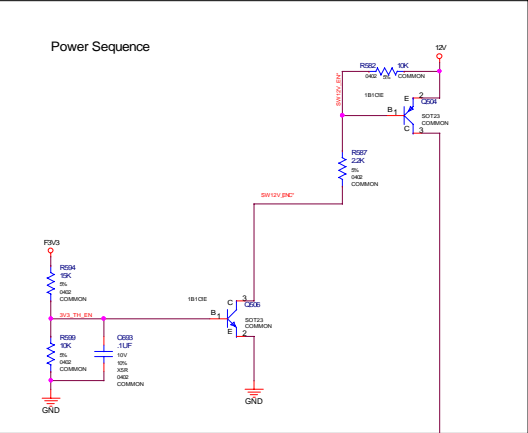


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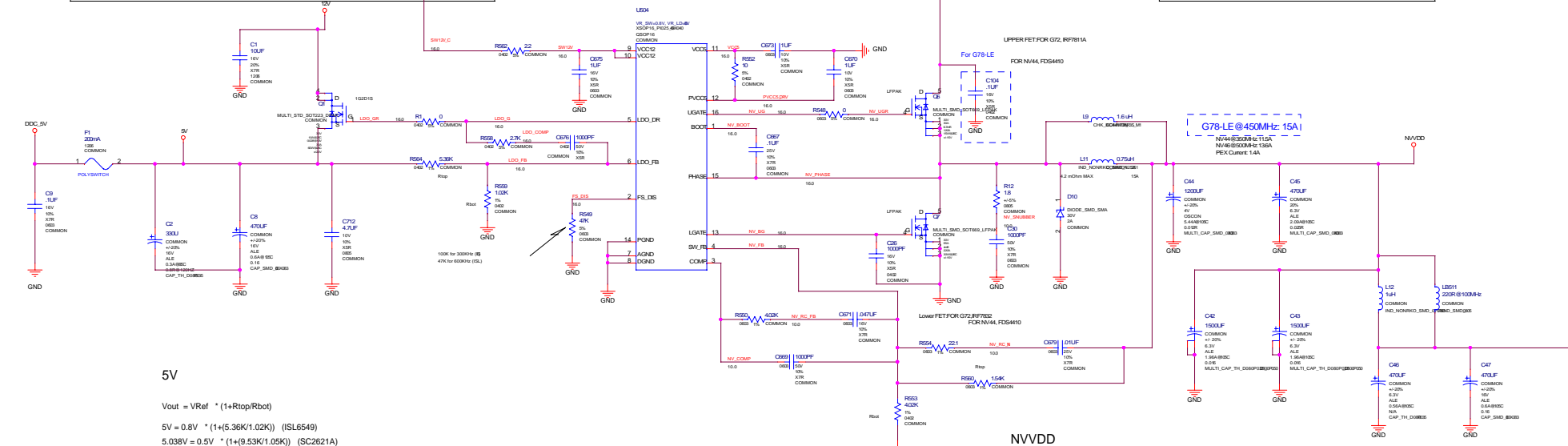
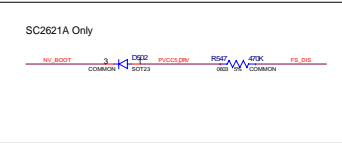
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PowerSupplyI: NVVDD, 5V

Net Name	LINE_WIDTH	Current	Voltage
12V	12V	3000	12
12V_F	12V_F	3000	12.000
NVVDD	NVVDD	15	1.2000
PEX1V2	PEX1V2	1.4	1.2000



ISL6549(SC2621A)



5V

$V_{out} = V_{Ref} \cdot (1 + R_{top}/R_{bot})$

$5V = 0.8V \cdot (1 + (5.36K/1.02K))$ (ISL6549)

$5.038V = 0.5V \cdot (1 + (9.53K/1.05K))$ (SC2621A)

NVVDD

$V_{out} = V_{Ref} \cdot (1 + R_{top}/R_{bot})$

$1.1V = 0.8V \cdot (1 + (1.54K/4.02K))$ (ISL6549)

$1.2V = 0.8V \cdot (1 + (1.54K/3.09K))$ (ISL6549)

$1.26V = 0.8V \cdot (1 + (1.54K/2.67K))$ (ISL6549)

$1.2V = 0.5V \cdot (1 + (14.7K/10.5K))$ (SC2621A)

ASSEMBLY: BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO STOP ASSEMBLY NOTES AND BOARD FINAL

PRICE DETAIL: PowerSupply NVVDD, 5V

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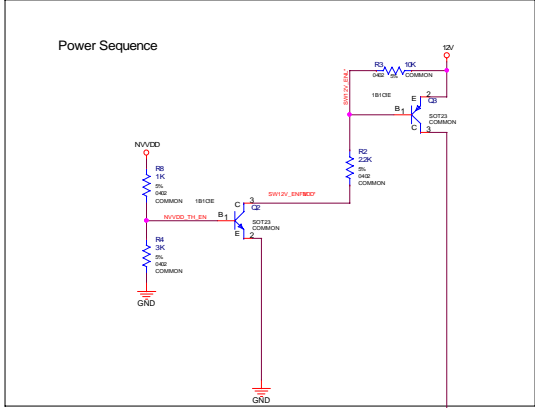
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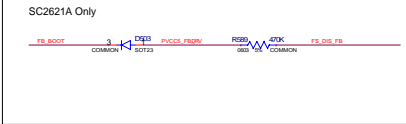
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PowerSupplyIII: FBVDDQ, FBVTT, PLLVDD



Net Name	LINE_WIDTH	Voltage
FBVDDQ	12MIL	2.000V
PLLVDD	10MIL	2.5V
GND	10MIL	0.000V



ISL6549 (SC2621A)

FBVDDQ:
 $V_{out} = V_{Ref} * (1 + R_{top}/R_{bot})$
 $1.7965V = 0.8V * (1 + (1.47K/1.18K))$ (ISL6549)
 $1.8057V = 0.5V * (1 + (3.16K/1.21K))$ (SC2621A)
 $2.01V = 0.8V * (1 + (1.54K/1.02K))$ (ISL6549)
 $2.01V = 0.5V * (1 + (14.7K/4.87K))$ (SC2621A)

Memory@400MHz: 4A

IFP_PLLVDD

$V_{out} = V_{Ref} * (1 + R_{top}/R_{bot})$
 $1.7965V = 0.8V * (1 + (1.47K/1.18K))$ (ISL6549)
 $2.49V = 0.8V * (1 + (2.43K/1.15K))$ (ISL6549 FOR NV46)
 $2.5095V = 0.5V * (1 + (4.22K/1.05K))$ (SC2621A FOR NV46)

ASSEMBLY: BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO. 3 TOP ASSEMBLY NOTES AND BOARD FINAL
PRICE DETAIL: PowerSupply FBVDDQ, FBVTT, PLLVDD

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