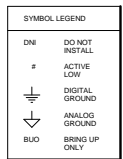


CEDAR AUSTINATOR



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Date: Wednesday, June 02, 2010 Sheet 1 of 18		Rev P02	
Title HF CEDAR DDR3 512MB TWO DM559		Doc No. 105-C090XX-00	

00029980 MSK RD IDENTICAL ONLY

FOR RMA (丁德基) 20110809001

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Date: Wednesday, June 02, 2010 Rev: pzz

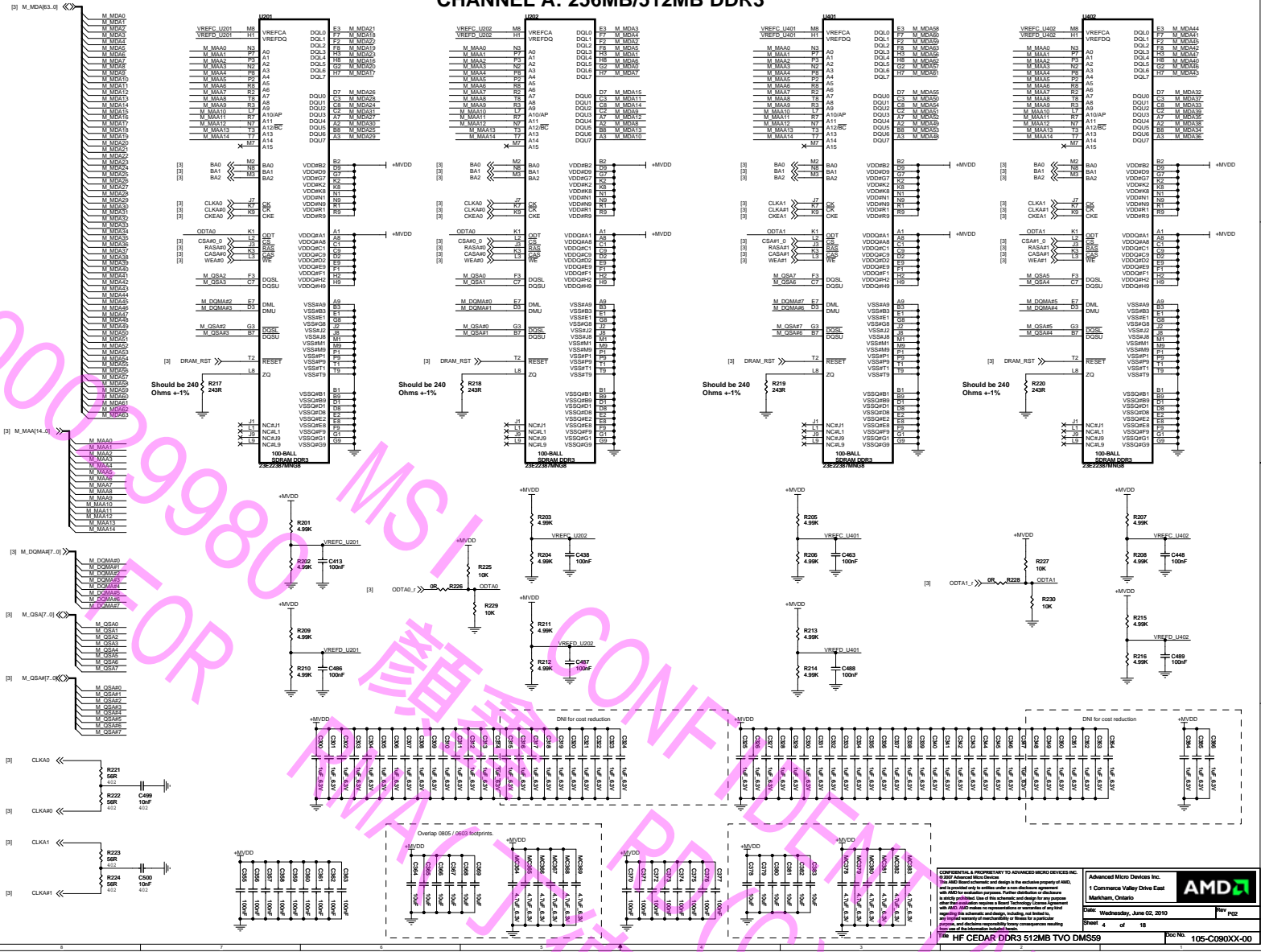
Sheet 2 of 16 Doc No.: 105-C090XX-00

Title: HF CEDAR DDR3 512MB TVO DMS59

U1B
PART 2 OF 15



CHANNEL A: 256MB/512MB DDR3



U10 (PART 15 OF 15)

DDC1	NC
AUX1	NC
DDC2	NC
AUX2	NC
DDC/AUX3	CONNECTOR1
DDC/AUX5	CONNECTOR2
DDC6	NC
I2C	NC

U1E (PART 6 OF 15)

U1F (PART 6 OF 15)

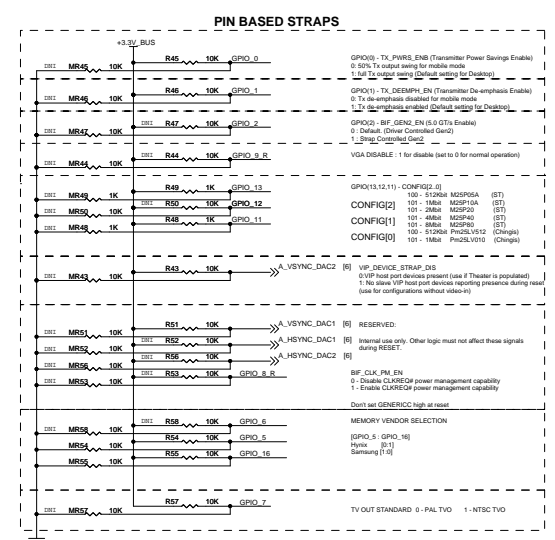
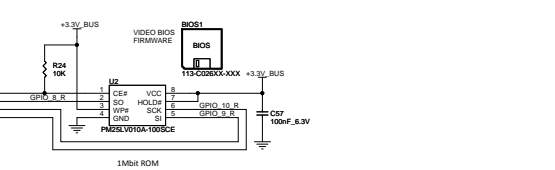
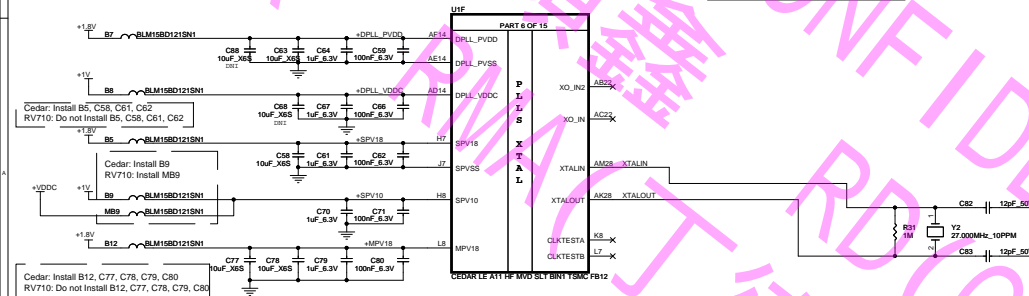
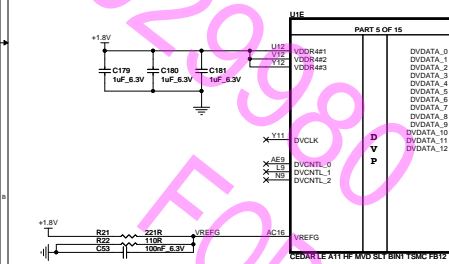
U1G (PART 6 OF 15)

Component Footprints:

- U10: AMD A11F MVD SLT B1T1 TSAC FB12
- U1E: AMD A11F MVD SLT B1T1 TSAC FB12
- U1F: AMD A11F MVD SLT B1T1 TSAC FB12
- U1G: AMD A11F MVD SLT B1T1 TSAC FB12

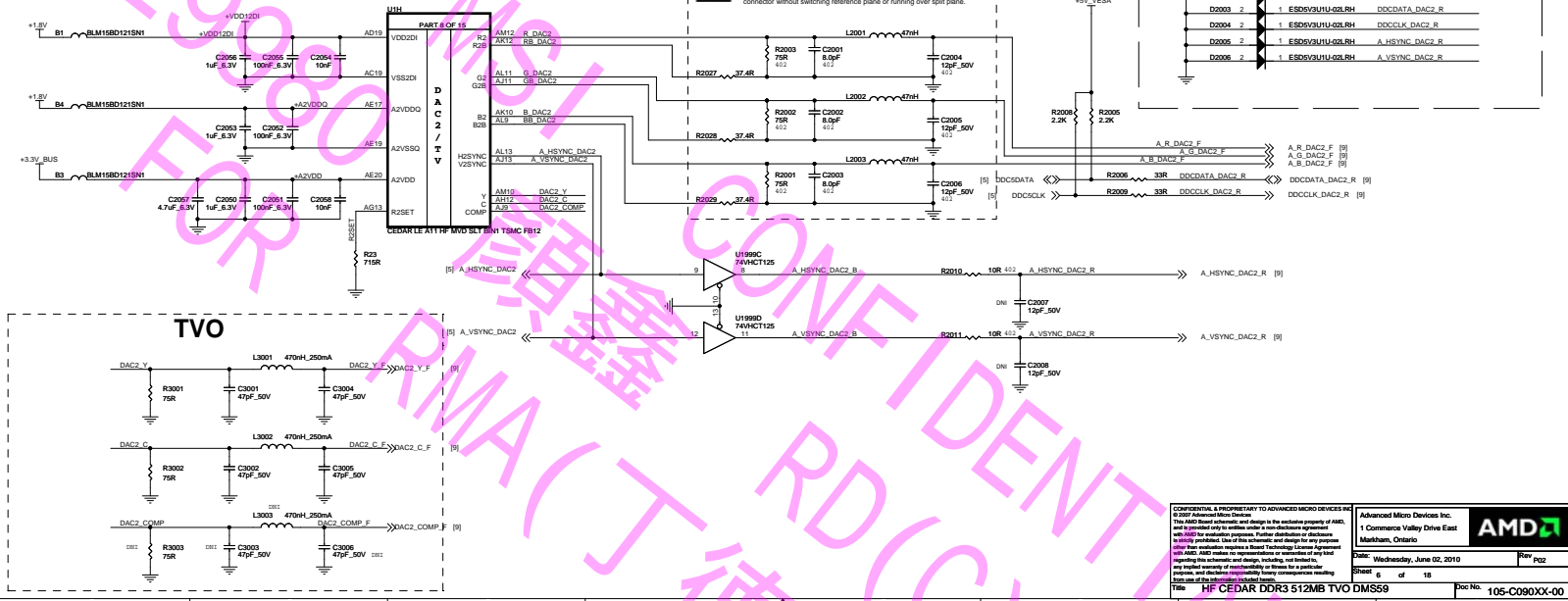
Table of Component Values:

Component	Value
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C180	1uF, 6.3V
C181	1uF, 6.3V
C182	1uF, 6.3V
C183	1uF, 6.3V
C184	1uF, 6.3V
C185	1uF, 6.3V
C186	1uF, 6.3V
C187	1uF, 6.3V
C188	1uF, 6.3V
C189	1uF, 6.3V
C190	1uF, 6.3V
C191	1uF, 6.3V
C192	1uF, 6.3V
C193	1uF, 6.3V
C194	1uF, 6.3V
C195	1uF, 6.3V
C196	1uF, 6.3V
C197	1uF, 6.3V
C198	1uF, 6.3V
C199	1uF, 6.3V
C200	1uF, 6.3V
C201	1uF, 6.3V
C202	1uF, 6.3V
C203	1uF, 6.3V
C204	1uF, 6.3V
C205	1uF, 6.3V
C206	1uF, 6.3V
C207	1uF, 6.3V
C208	1uF, 6.3V
C209	1uF, 6.3V
C210	1uF, 6.3V
C211	1uF, 6.3V
C212	1uF, 6.3V
C213	1uF, 6.3V
C214	1uF, 6.3V
C215	1uF, 6.3V
C216	1uF, 6.3V
C217	1uF, 6.3V
C218	1uF, 6.3V
C219	1uF, 6.3V
C220	1uF, 6.3V
C221	1uF, 6.3V
C222	1uF, 6.3V
C223	1uF, 6.3V
C224	1uF, 6.3V
C225	1uF, 6.3V
C226	1uF, 6.3V
C227	1uF, 6.3V
C228	1uF, 6.3V
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C238	1uF, 6.3V
C239	1uF, 6.3V
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C242	1uF, 6.3V
C243	1uF, 6.3V
C244	1uF, 6.3V
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C255	1uF, 6.3V
C256	1uF, 6.3V
C257	1uF, 6.3V
C258	1uF, 6.3V
C259	1uF, 6.3V
C260	1uF, 6.3V
C261	1uF, 6.3V
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C268	1uF, 6.3V
C269	1uF, 6.3V
C270	1uF, 6.3V
C271	1uF, 6.3V
C272	1uF, 6.3V
C273	1uF, 6.3V
C274	1uF, 6.3V
C275	1uF, 6.3V
C276	1uF, 6.3V
C277	1uF, 6.3V
C278	1uF, 6.3V
C279	1uF, 6.3V
C280	1uF, 6.3V
C281	1uF, 6.3V
C282	1uF, 6.3V
C283	1uF, 6.3V
C284	1uF, 6.3V
C285	1uF, 6.3V
C286	1uF, 6.3V
C287	1uF, 6.3V
C288	1uF, 6.3V
C289	1uF, 6.3V
C290	1uF, 6.3V



DDC1	NC
AUX1	NC
DDC2	NC
AUX2	NC
DDC/AUX3	CONNECTOR1
DDC/AUX5	CONNECTOR2
DDC6	NC
I2C	NC

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Title HF CEDAR DDR3 512MB 20 DMS59		Date Wednesday, June 02, 2010	Rev PO2
Sheet 5 of 18			
Doc No 105-C090XX-00			

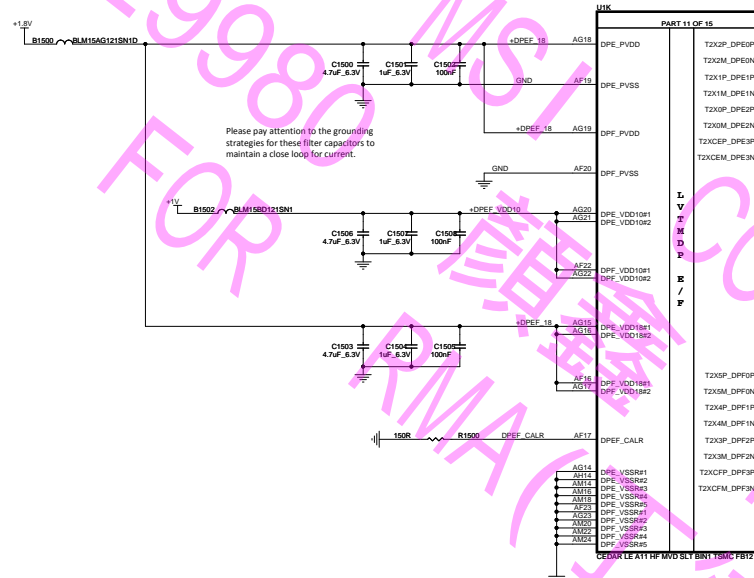
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00029980 MS

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20110809001



Port	Device	Protocol	Port	Device	Protocol
T2X2P_DIF0P	AM18	DPE_T2XP	→		DPE_T2XP [0]
T2X2M_DIF0N	ΔI17	DPE_T2XM	→		DPE_T2XM [0]
T2X1P_DIF0P	ΔI17	DPE_TX1P	→		DPE_TX1P [0]
T2X1M_DIF0N	AM16	DPE_TX1M	→		DPE_TX1M [0]
T2X0P_DIF0P	AM16	DPE_T0XP	→		DPE_T0XP [0]
T2X0M_DIF0N	ΔI15	DPE_T0XM	→		DPE_T0XM [0]
T2XC6P_DIF0P	AL15	DPE_TC6AP	→		DPE_TC6AP [0]
T2XC6M_DIF0N	AK14	DPE_TC6AM	→		DPE_TC6AM [0]

[illegible]

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RD(C)20110809001

MSI

FOR

00029980

Cedar: DPCD_CALR (150-ohm)
RV710: MEM_CALRP1 (243-ohm)

150Ω R298 DPCD_CALR

J18
AA1
DT1
U1
W1
Y5

DPC_CALR
DPC_VSSRP1
DPC_VSSRP2
DPC_VSSRP3
DPC_VSSRP4
DPC_VSSRP5

CEDAR LE A11 HF MVD SLY BNT TSMC FB12

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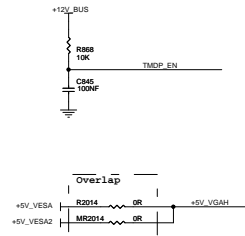
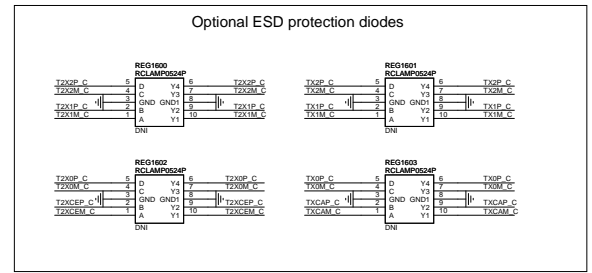
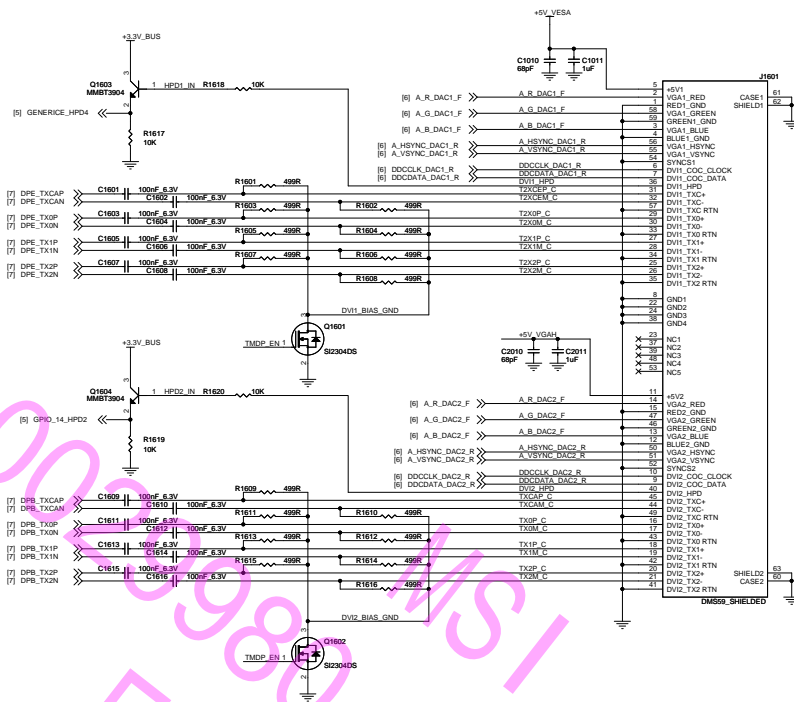
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Date: Wednesday, June 02, 2010 Rev: PG2

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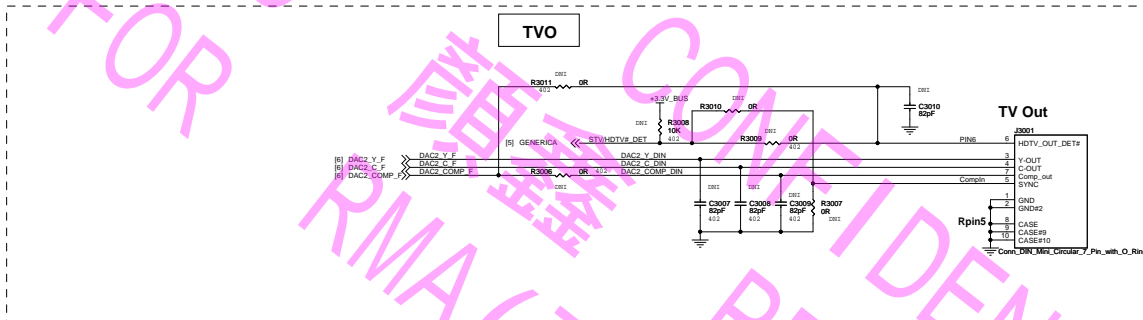
Title: HF CEDAR DDR3 512MB TWO DIMS59

DMS-59 DPB / DPE OUTPUT

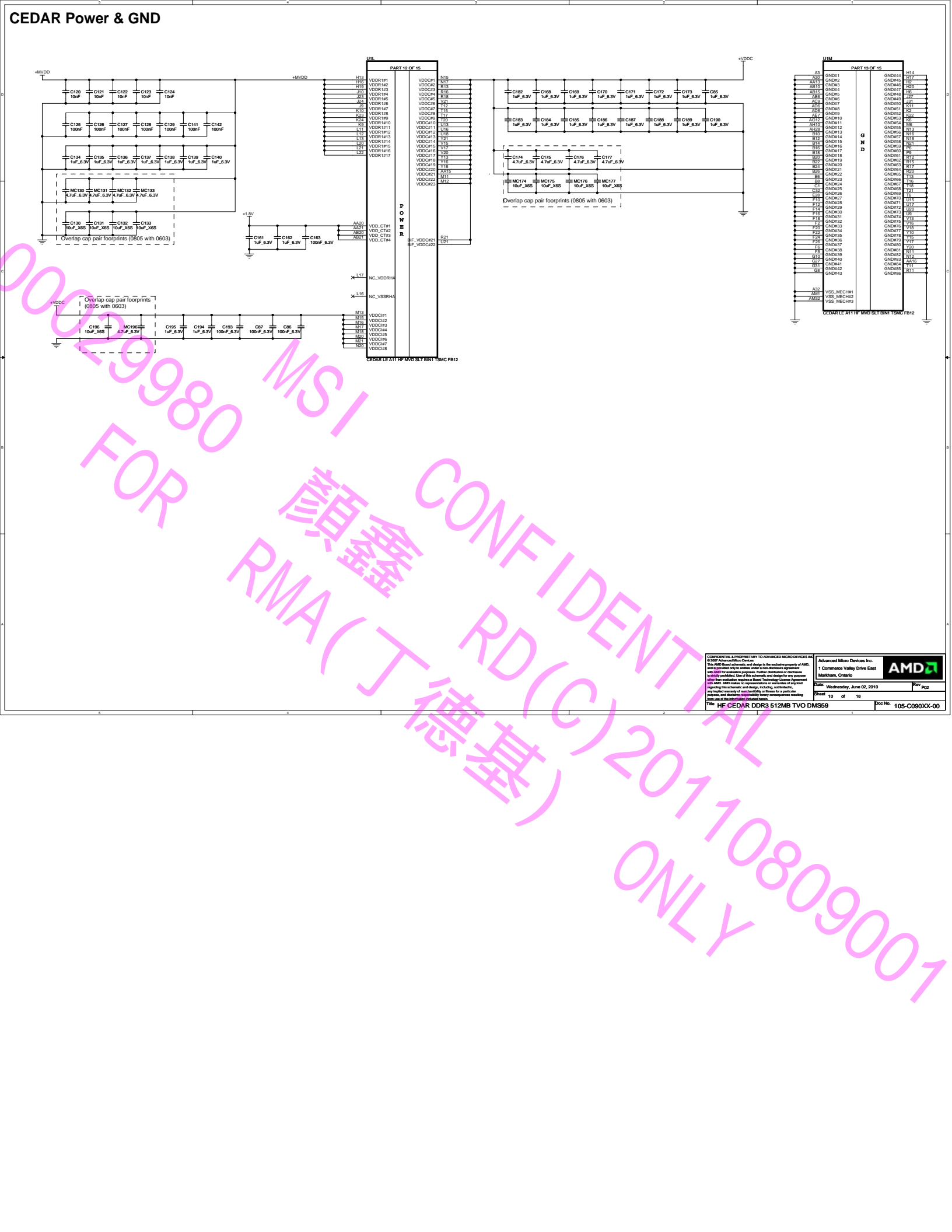


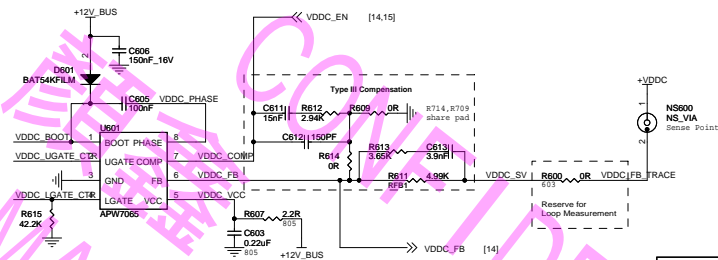
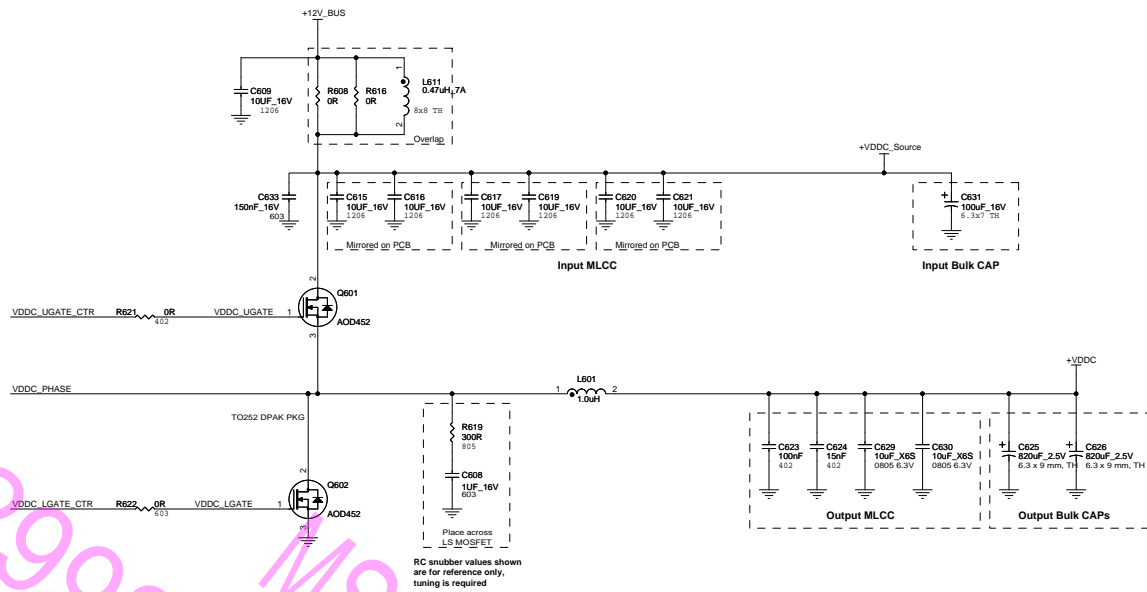
Connector 1	
Signals	Mapping
VGA:	DAC1
DVI:	Internal LVTMDP E
HPD:	HPD4
DDC:	DDC3
5V:	+5V_VESA

Connector 2	
Signals	Mapping
VGA:	DAC2 (TVDAC)
DVI:	Internal TMDP B
HPD:	HPD2
DDC:	DDC5
5V:	+5V_VESA



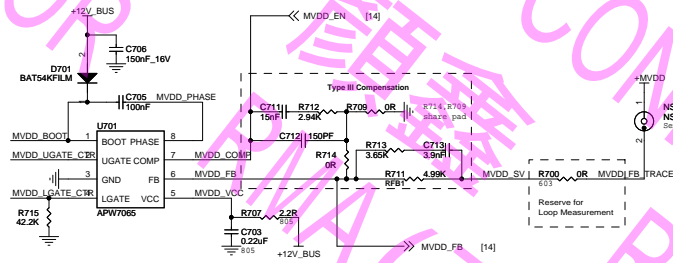
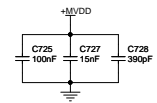
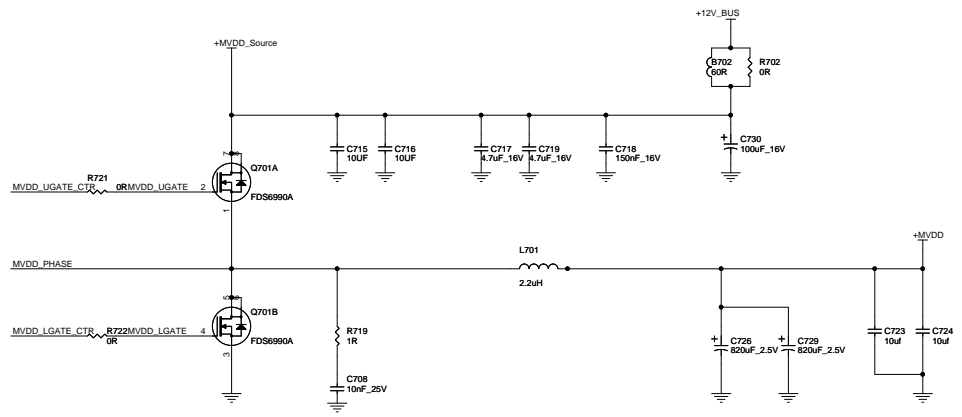
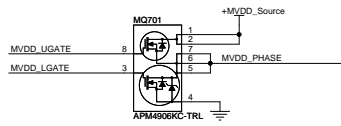
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Date: Wednesday, June 02, 2010	Rev: P02
Sheet: 9 of 18	Doc No: 405-C090XX-00
Title: HP CEDAR DDR3 512MB TVO DMS59	





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Date: Wednesday, June 02, 2010	Rev p02
Sheet 11 of 18	Doc No. 105-C090XX-00
Title HF CEDAR DDR3 512MB TVO DMS59	



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Date: Wednesday, June 02, 2010	Rev: p02
Sheet: 12 of 18	Doc No.: 105-C090XX-00
Title: HF CEDAR DDR3 512MB TYO DMSS9	



Display Config	Est. Current
DVI+HDMI+DP	1330mA

140



Display Config	Est. Current
DVI+HDMI+DP	1560mA

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AMD

Date: Wednesday, June 02, 2010 11:02 PM
Sheet 13 of 18

Title: **HF CSDH4932 512MB TPO DMS59**

Dwg No: **105-C909XX-00**

<div>AMD</div>			Title		Schematic No.		Date:		
			HF CEDAR DDR3 512MB TVO DMS59		105-C090XX-00		Wednesday, June 02, 2010		
REVISION HISTORY			NOTE: This schematic represents the PCB, it does not represent any specific SKU. For Stuffing options (component values, DNI's, ...) please consult the product specific BOM. Please contact AMD representative to obtain latest BOM closest to the application desired.					Rev	P02
Sch Rev	PCB Rev	Date	REVISION DESCRIPTION						
00	00A	2010/03/25	Initial Cedar schematic, based on C026xx-10						
01	00B	2010/04/22	Added TVO feature						
02	00	2010/06/02	Release 105-C090XX-00 schematic;						

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