


8624 : ADD2+ Card ( ATI RIO550 PRO + CONEXANT IEOPARD CX25905 SDVO )

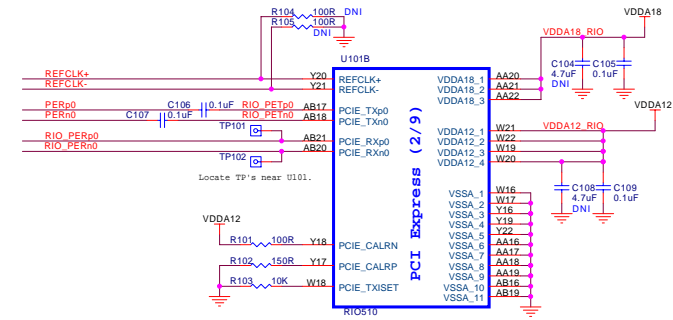
SCH : 04/11/30 Base on ATI Maverick 0B Modify.

	Micro-Star International Co., LTD.		
	COVER		
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# Leopard ADD2 Non-Lane Reversed

All resistors and capacitors are  
0603 package and 5% tolerance  
unless otherwise noted

Make the line for SDVOB\_CLK+/- and  
SDVOC\_CLK+/- a 50 Ohms for  
characteristic Impedance and 100  
Ohms differential Impedance

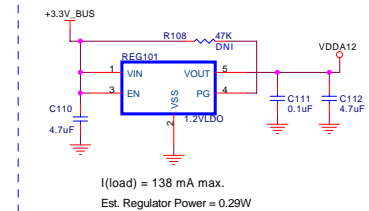


Note: PCIe connector is labelled from system board perspective.  
(i.e. you must cross connect receive & transmit signals)

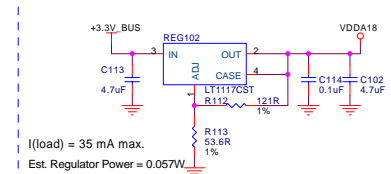
Note: n0 and p0 signals are swapped between RIO and connector. This is allowed by PCIe spec.

Place test point via's on REFCLK+, REFCLK-, RIO\_PETP0 and RIO\_PETN0 where trace transitions from bottom to top layer near U101.

## +1.2V Regulator for RIO Digital PCIe Core



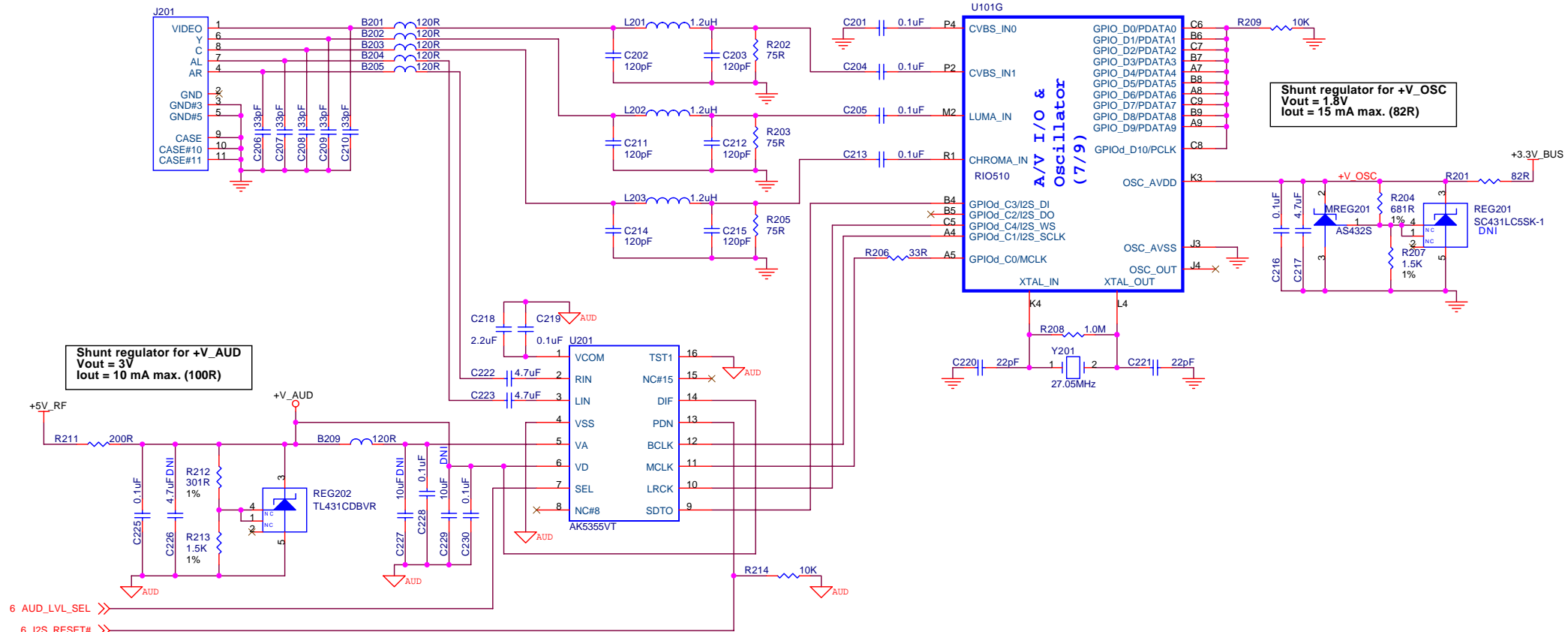
## +1.8V Regulator for Analog PCIe



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Maverick PCIe Rio Interface

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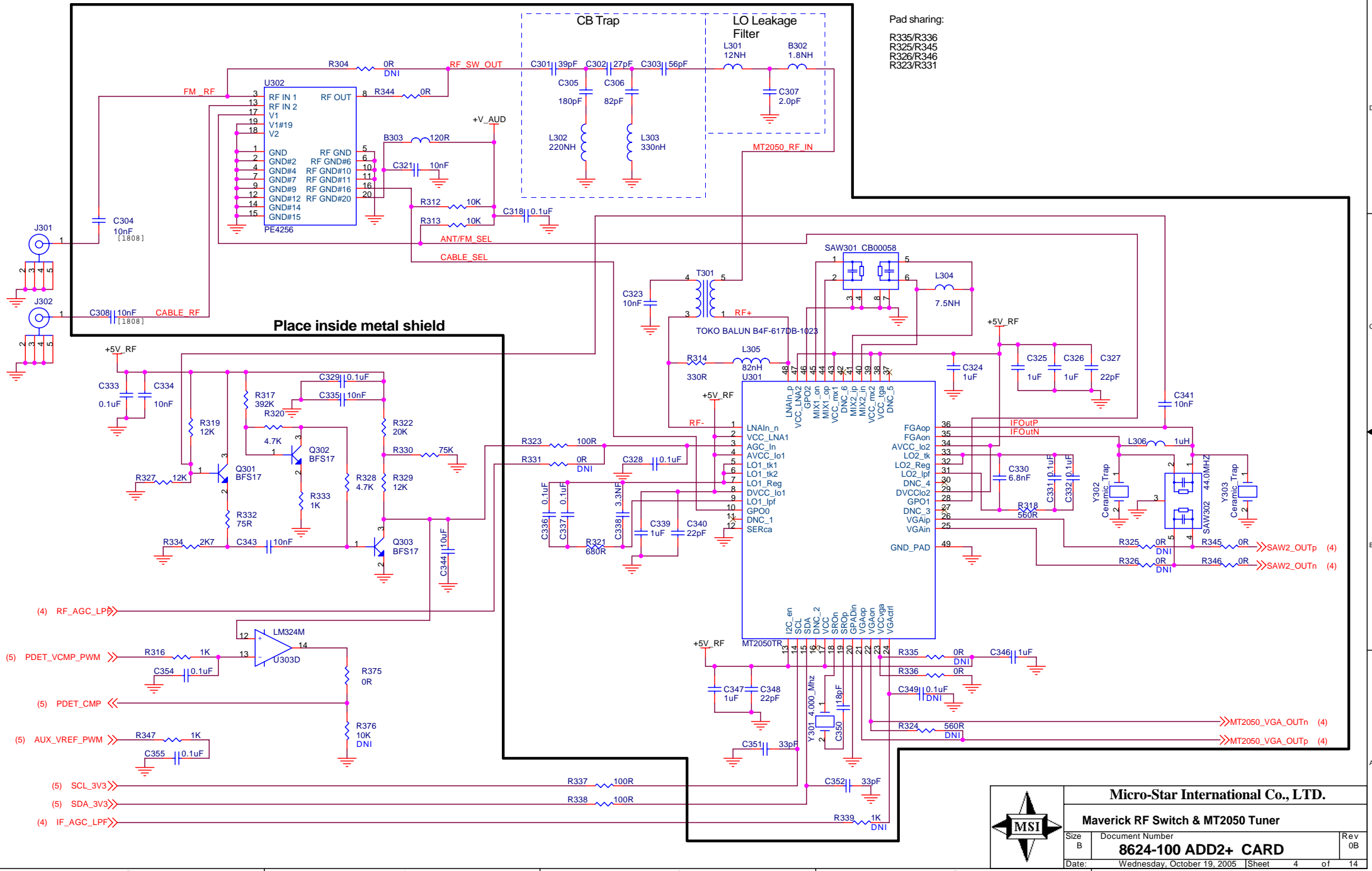
Pad sharing:  
REG201/MREG201  
REG202/MREG202




Micro-Star International Co., LTD.

Maverick A/V Input & Rio Oscillator

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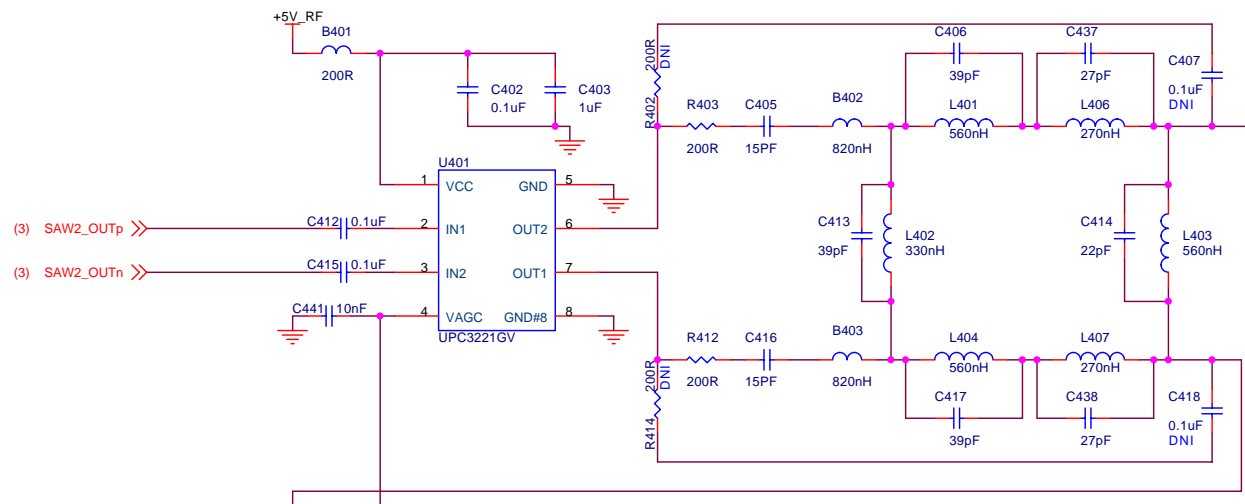
Pad sharing:  
R335/R336  
R325/R345  
R326/R346  
R329/R331



**Micro-Star International Co., LTD.**

**Maverick RF Switch & MT2050 Tuner**

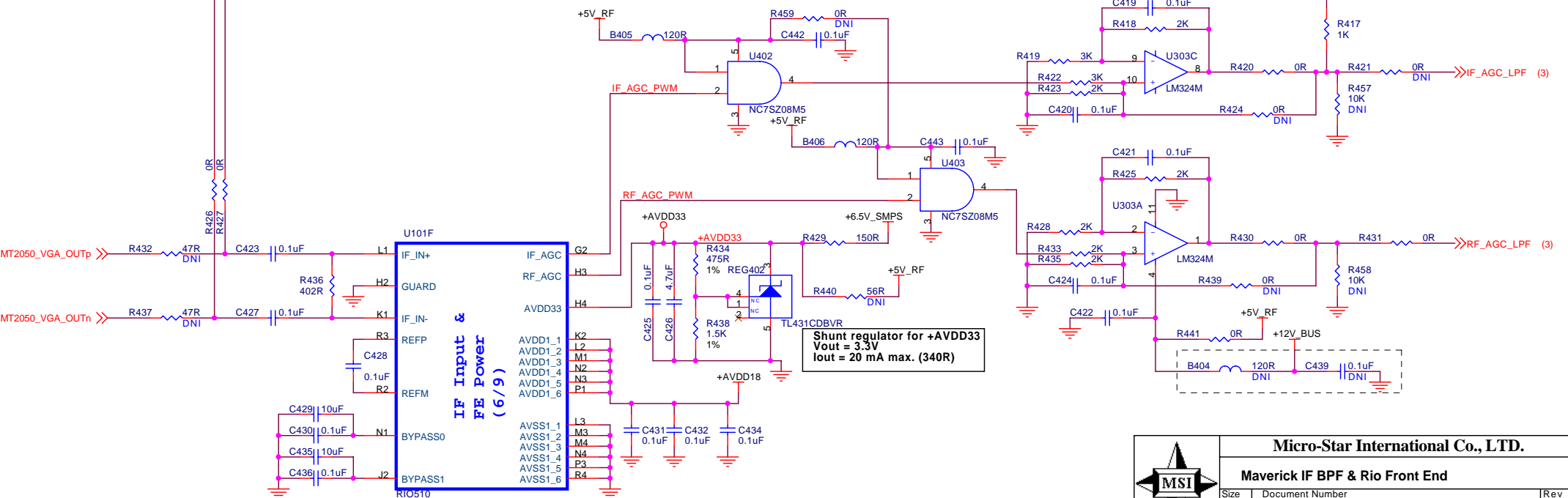
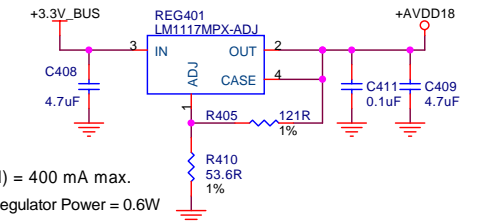
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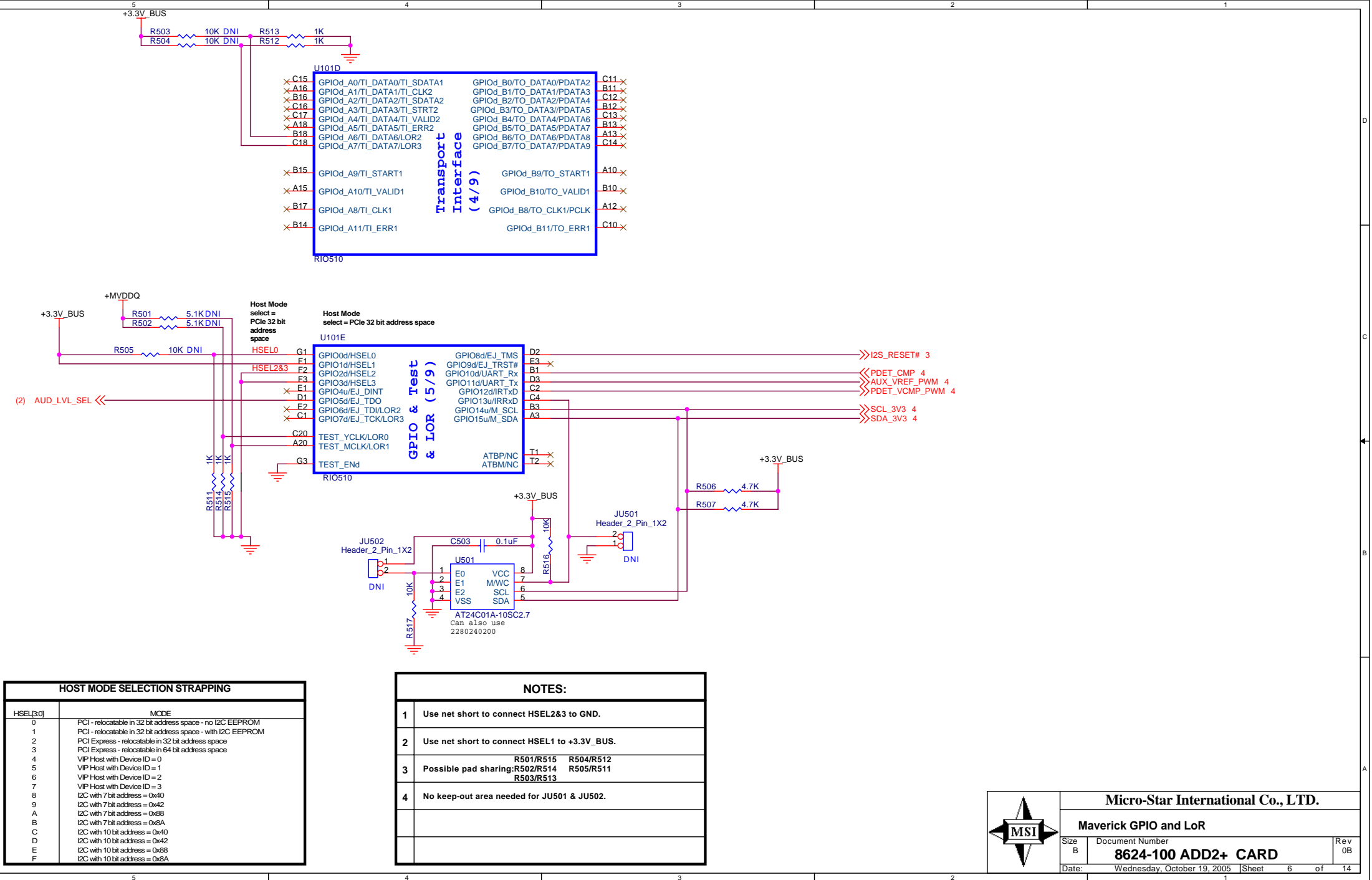


Pad sharing:

R402/R403  
R412/R414  
R432/R427  
R437/R426  
REG402/MREG402

### +1.8V Regulator for analog FE power supplies



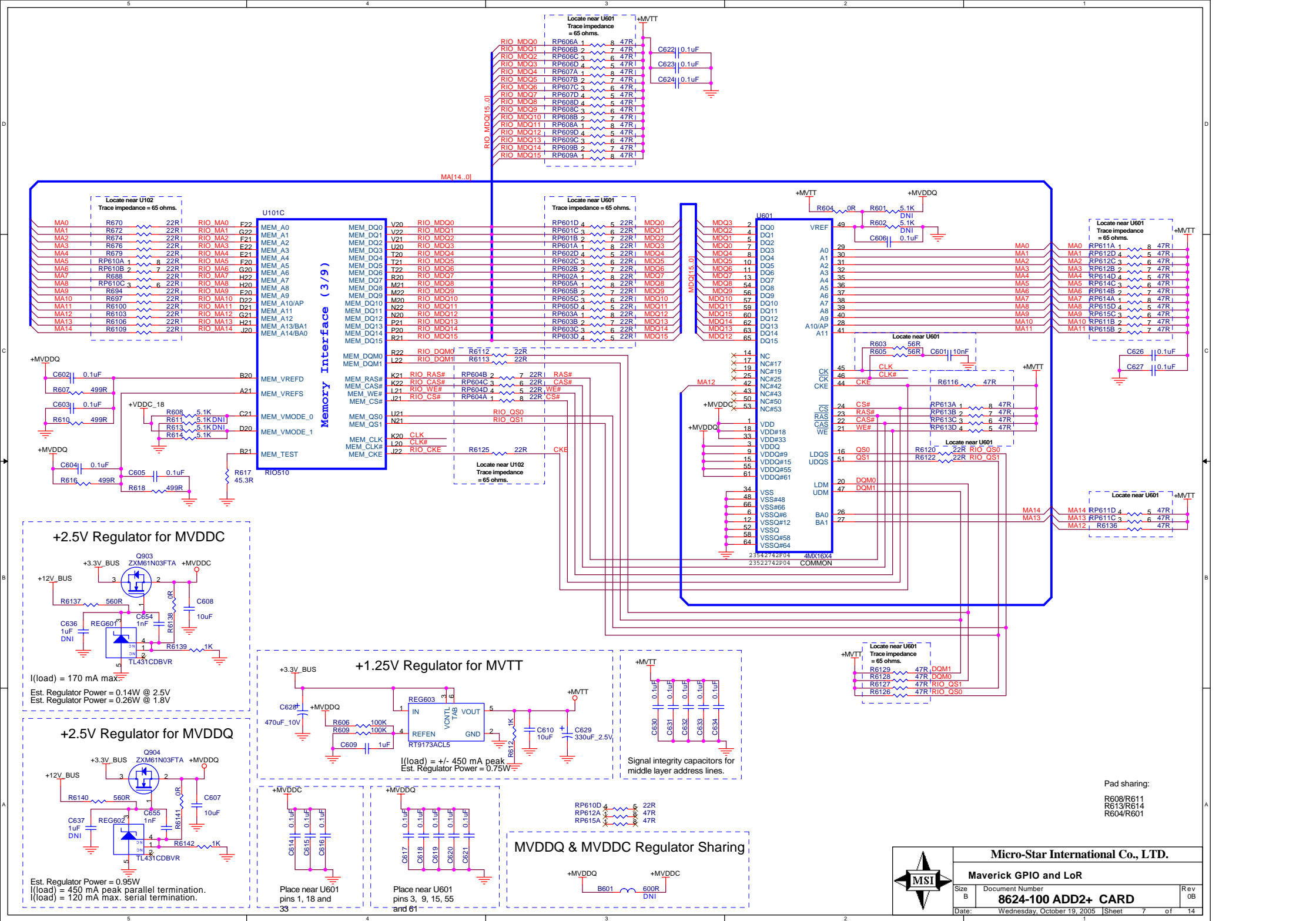


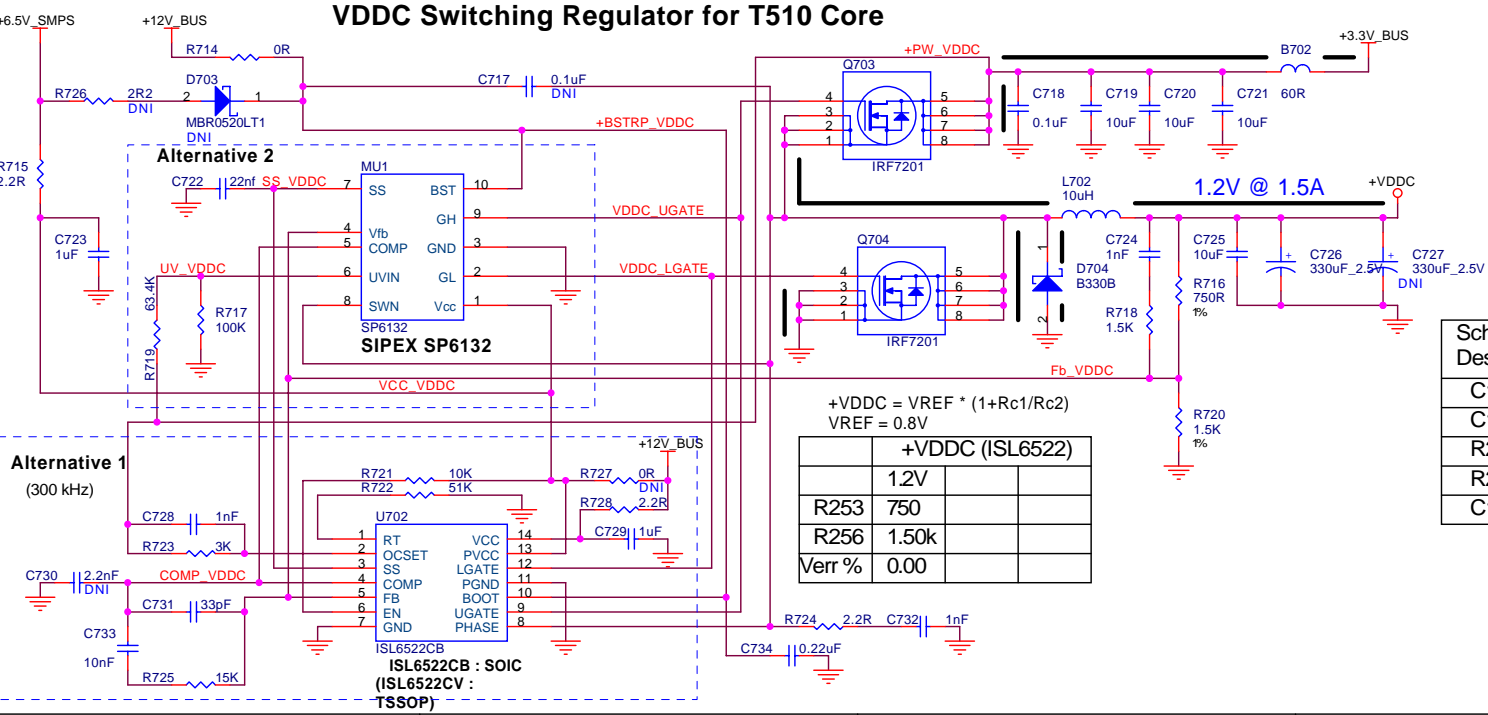
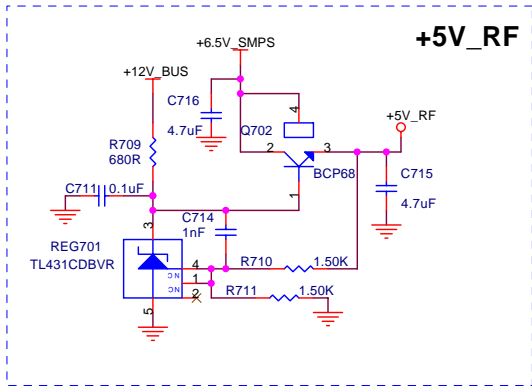
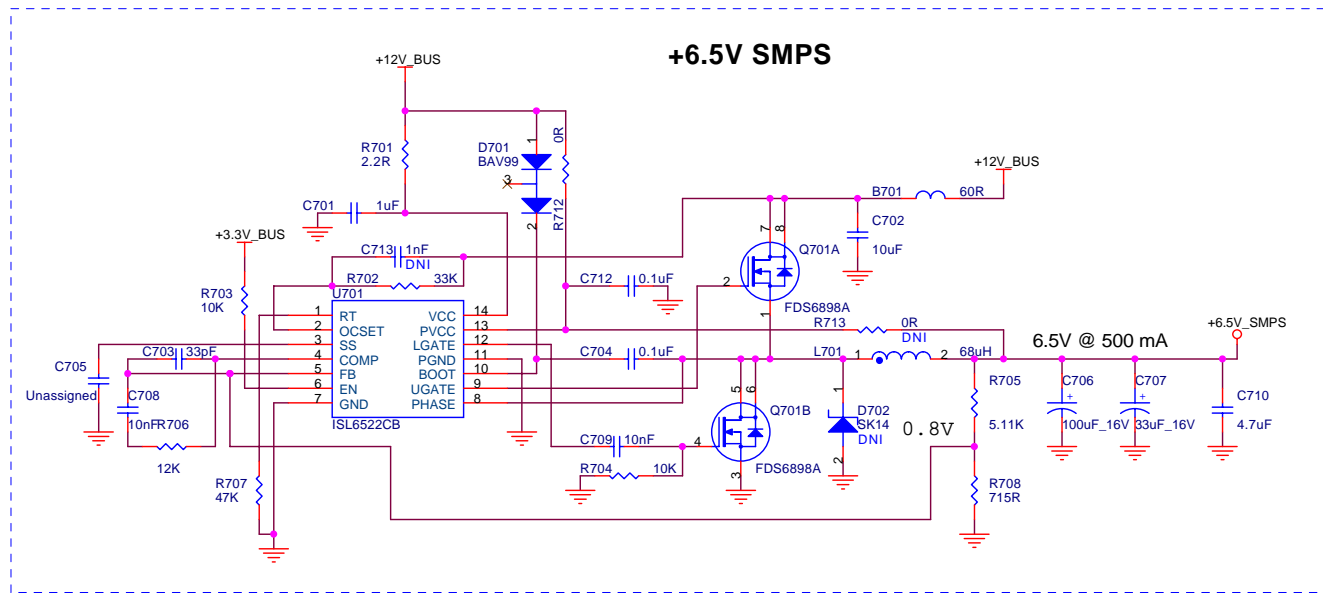
HOST MODE SELECTION STRAPPING	
HSEL[3:0]	MODE
0	PCI - relocatable in 32 bit address space - no I2C EEPROM
1	PCI - relocatable in 32 bit address space - with I2C EEPROM
2	PCI Express - relocatable in 32 bit address space
3	PCI Express - relocatable in 64 bit address space
4	VIP Host with Device ID = 0
5	VIP Host with Device ID = 1
6	VIP Host with Device ID = 2
7	VIP Host with Device ID = 3
8	I2C with 7 bit address = 0x40
9	I2C with 7 bit address = 0x42
A	I2C with 7 bit address = 0x68
B	I2C with 7 bit address = 0x6A
C	I2C with 10 bit address = 0x40
D	I2C with 10 bit address = 0x42
E	I2C with 10 bit address = 0x68
F	I2C with 10 bit address = 0x6A

NOTES:	
1	Use net short to connect HSEL2&3 to GND.
2	Use net short to connect HSEL1 to +3.3V_BUS.
3	Possible pad sharing: R502/R514 R505/R511 R503/R513
4	No keep-out area needed for JU501 & JU502.



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Maverick GPIO and LoR		
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Schematic Designator	ISL6522		SP6132	
	Value	ATI Part Number	Value	ATI Part Number
C142	1uF	4220010500	10uF	
C122	100nF	4250010400	1uF	
R251	0R	3230000000	bead	
R254	1.5K	3230015200	1nf	4250010200
C110	1nf	4250010200	1.5K	3230015200



**Micro-Star International Co., LTD.**

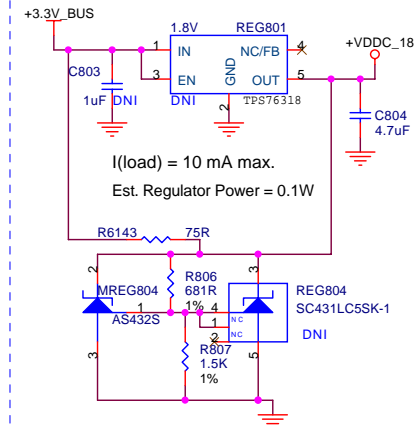
**Maverick Tuner SMPS & +5V\_RF Regulator**

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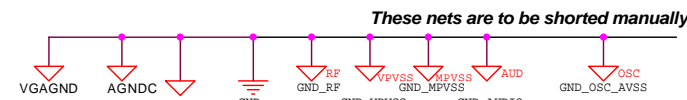


**Shunt regulator for +MPVDD**  
**Vout = 1.8V**  
**Iout = 20 mA max. (75R)**

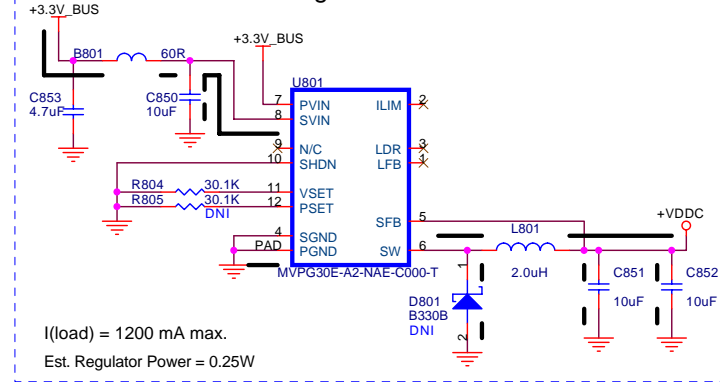
**+1.8V Regulator for VDDC18**



**Linear regulator for +PVDD**  
**Vout = 1.8V**  
**Iout = 130 mA max.**  
**Pdiss= 0.2W max.**

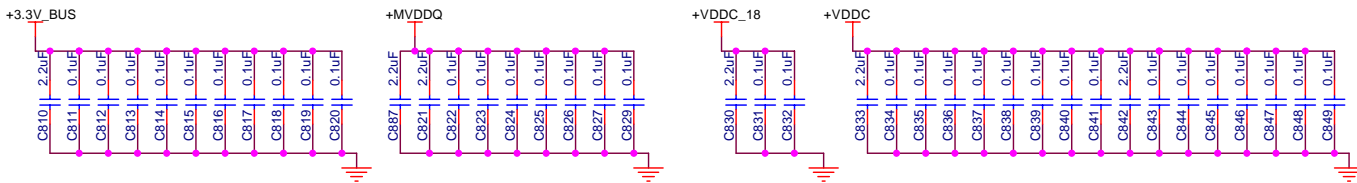
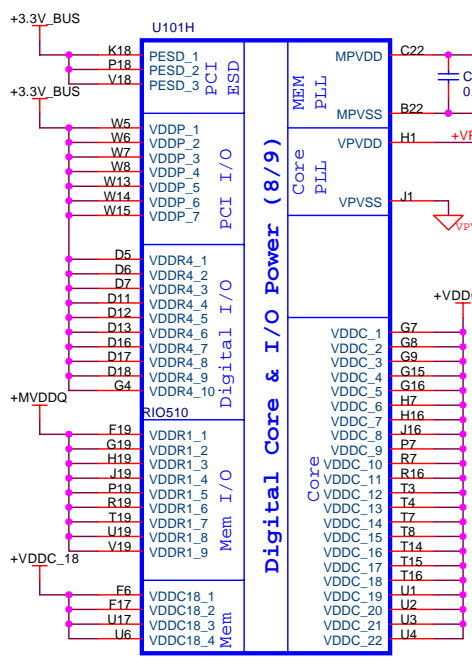
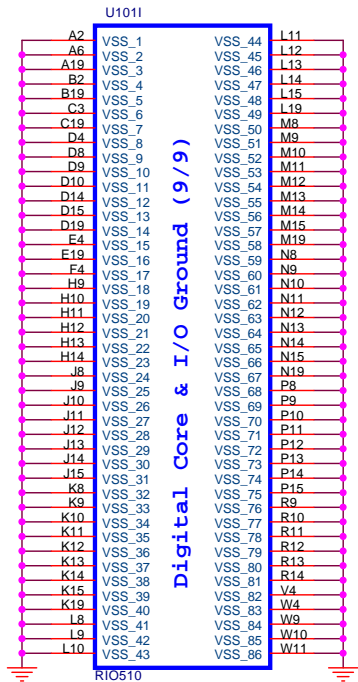


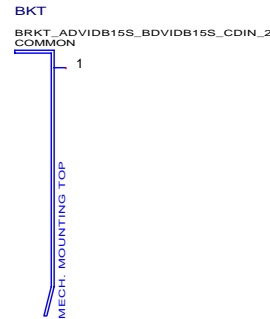
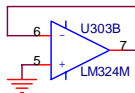
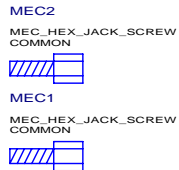
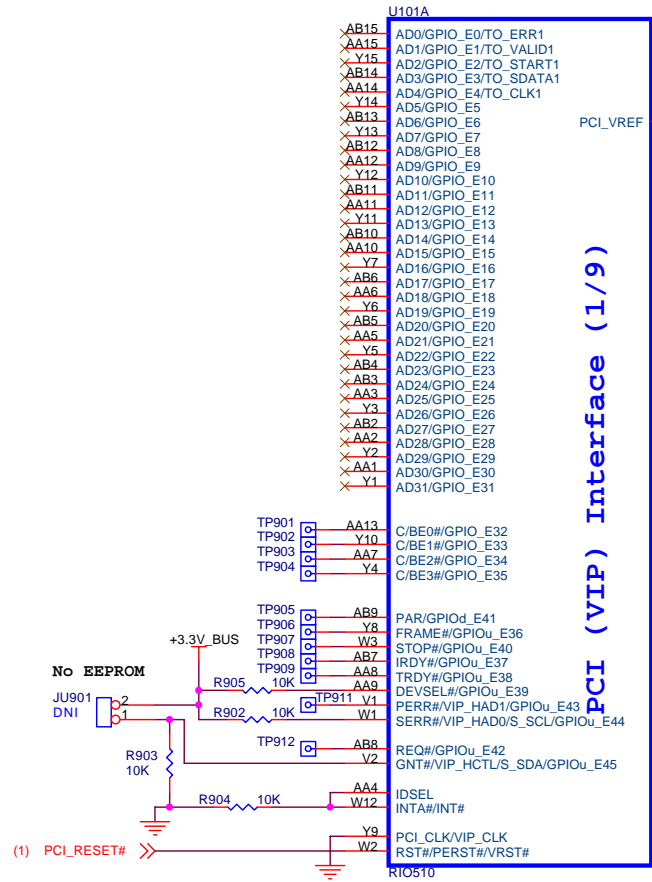
**+1.2V Regulator for VDDC**



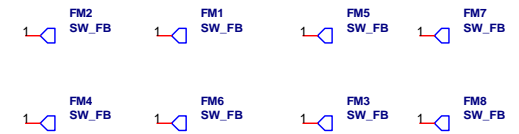
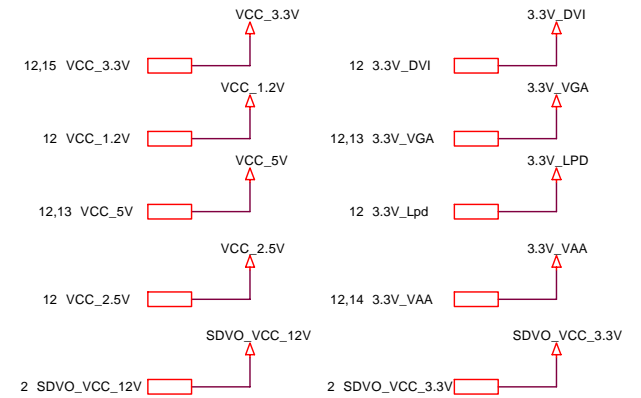
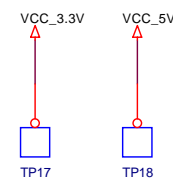
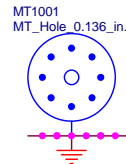
Possible  
 Pad sharing:  
 REG802/MREG802  
 REG804/MREG804

			<b>Micro-Star International Co., LTD.</b>	
			<b>Maverick Rio Power</b>	
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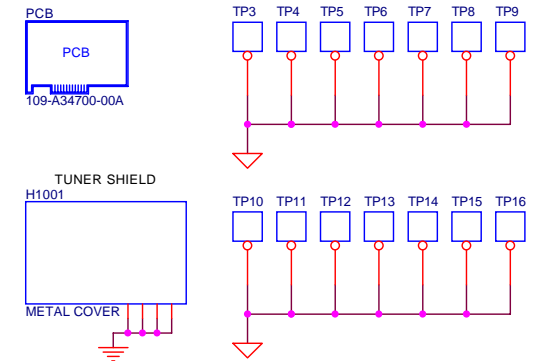




BRACKET MOUNTING PARTS  
Place bracket here when  
part/symbol available  
- ASSY1003.

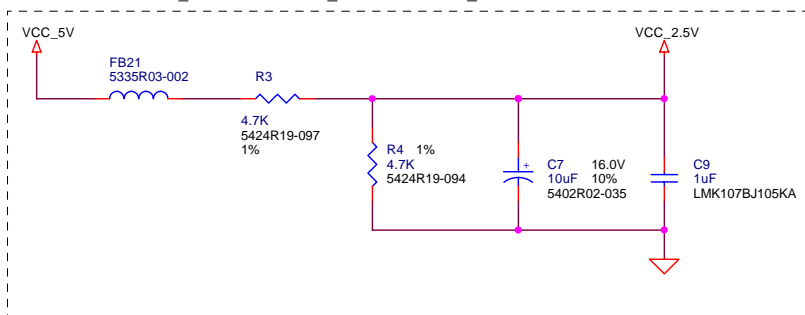


### Test Points

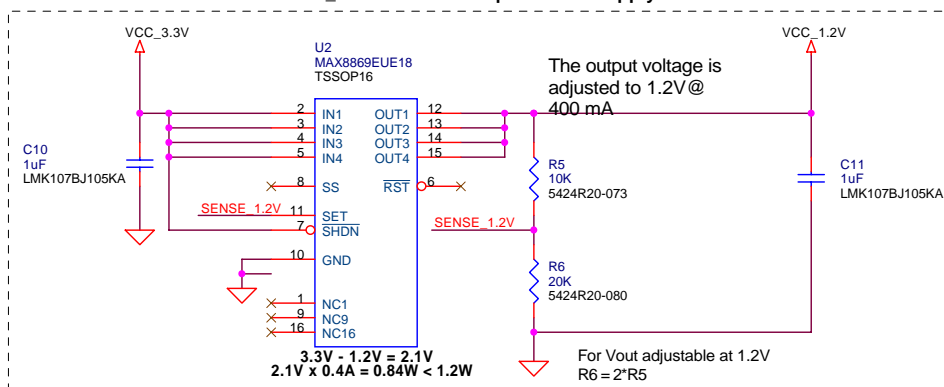


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Maverick Spare & Unused Devices			
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### VCC\_2.5V SDVO Ctrl\_Clock and Ctrl\_Data

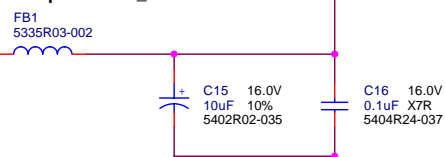


### VCC\_1.2V DVI and Leopard Core Supply

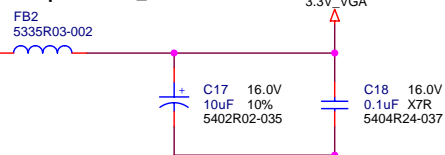


All resistors and capacitors are 0603 package and 5% tolerance unless otherwise noted

### Leopard 3.3V\_DVI SUPPLY



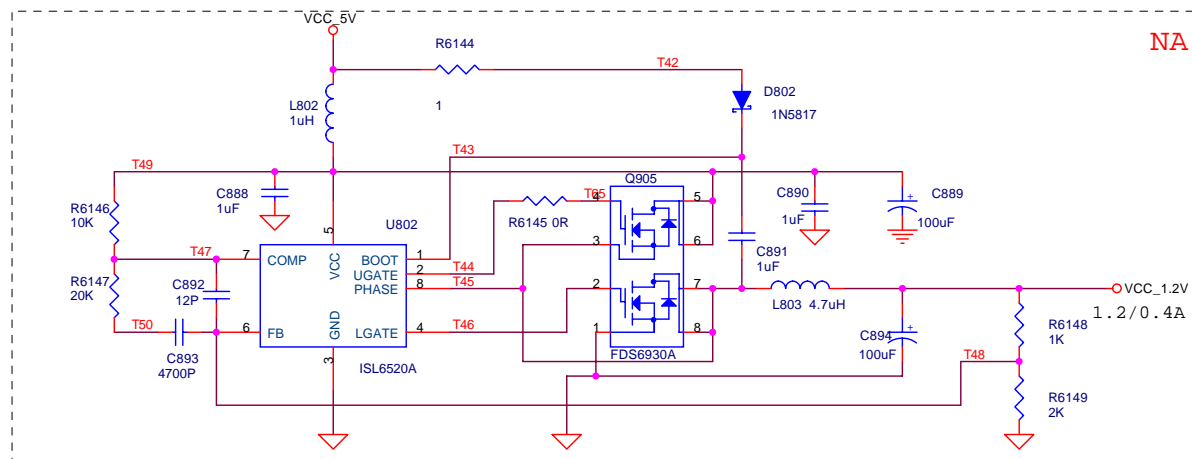
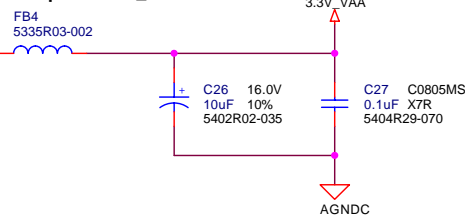
### Leopard 3.3V\_VGA SUPPLY



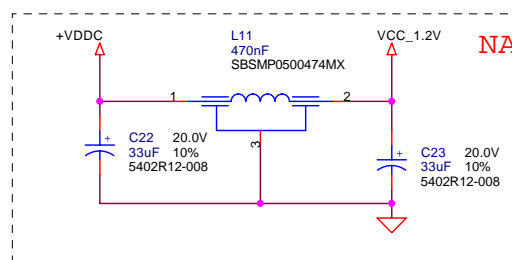
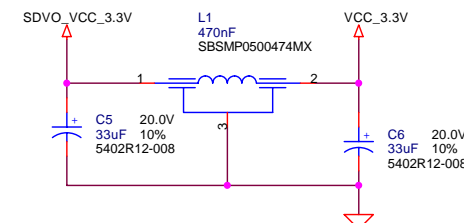
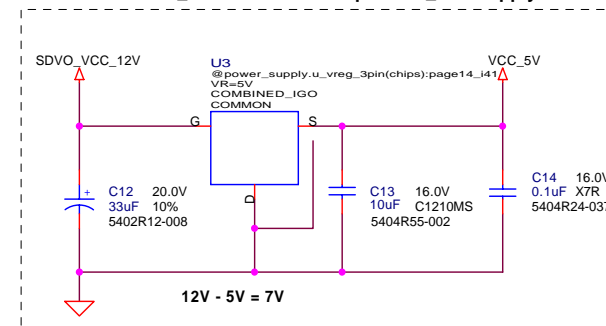
### Leopard 3.3V\_I/O SUPPLY



### Leopard 3.3V\_VAA SUPPLY



### VCC\_5V is used for Leopard 5V\_VIO Supply

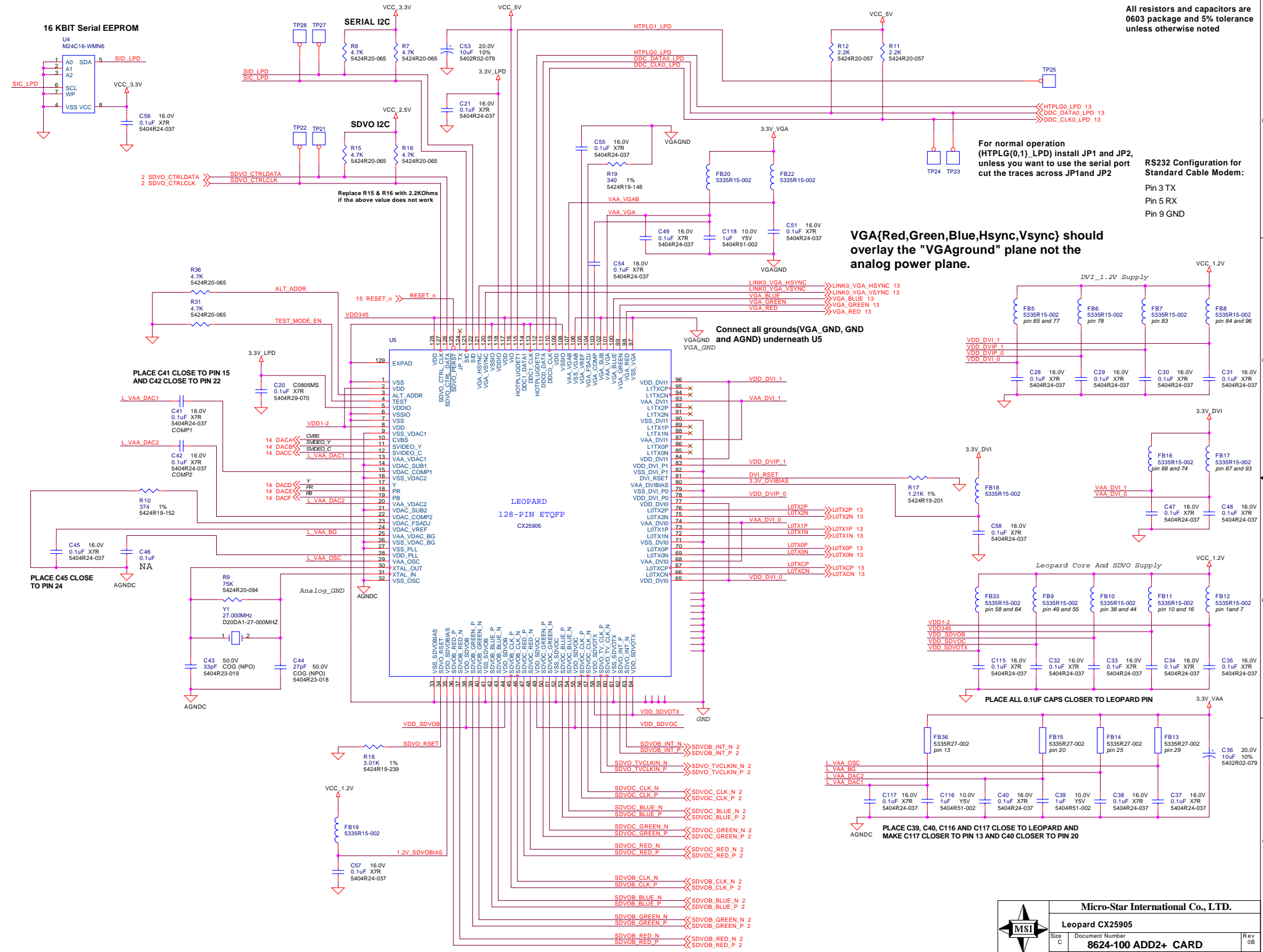


Micro-Star International Co., LTD.

Leopard power supply

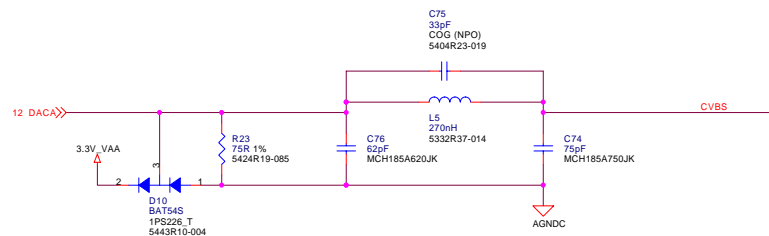
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All resistors and capacitors are 0603 package and 5% tolerance unless otherwise noted

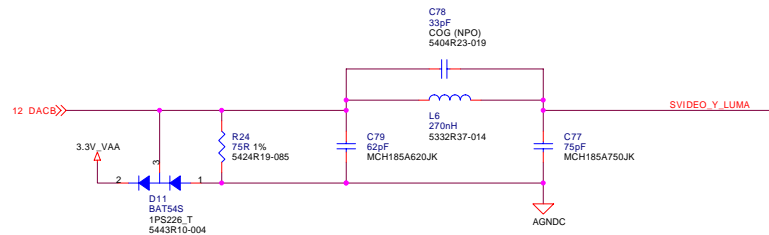




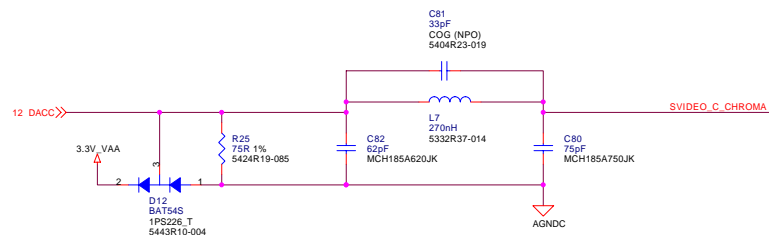
All resistors and capacitors are  
0603 package and 5% tolerance  
unless otherwise noted



DACA  
DEFAULT = CVBS

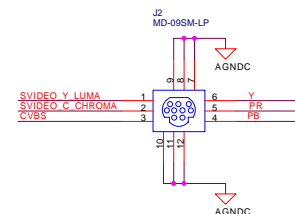


DACB  
DEFAULT = LUMA (Y)

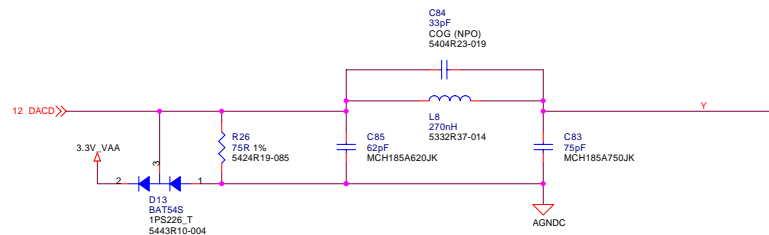


DACC  
DEFAULT = CHROMA (C)

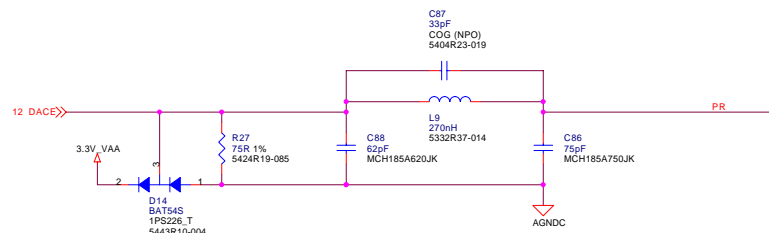
9 Pin Mini Din



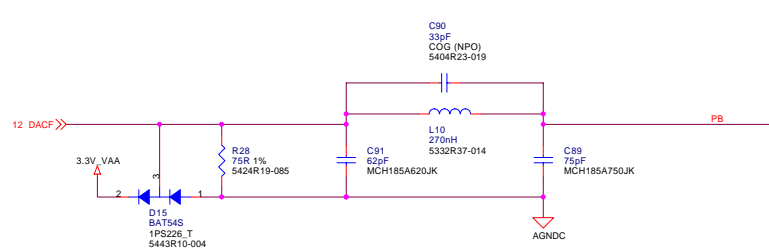
The cable split into 4 RCA and S-Video



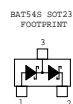
3 BNC for  
YPrPb/RGB  
DACD  
DEFAULT = Y-GREEN



DACE  
DEFAULT = Pr-RED



DACF  
DEFAULT = Pb-BLUE



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TV Output		
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