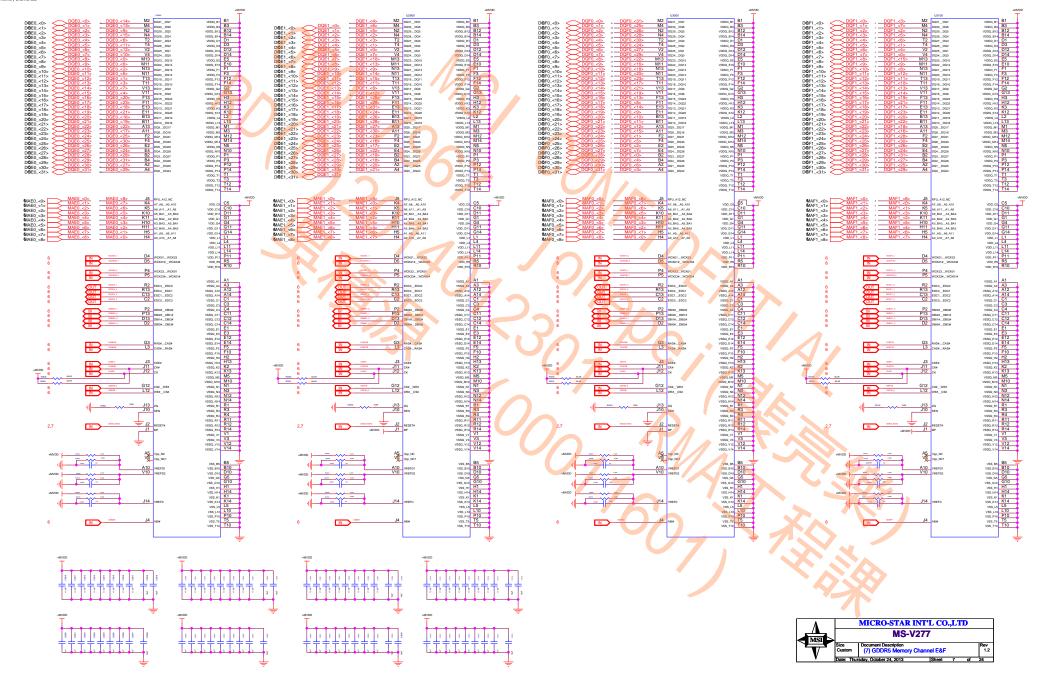
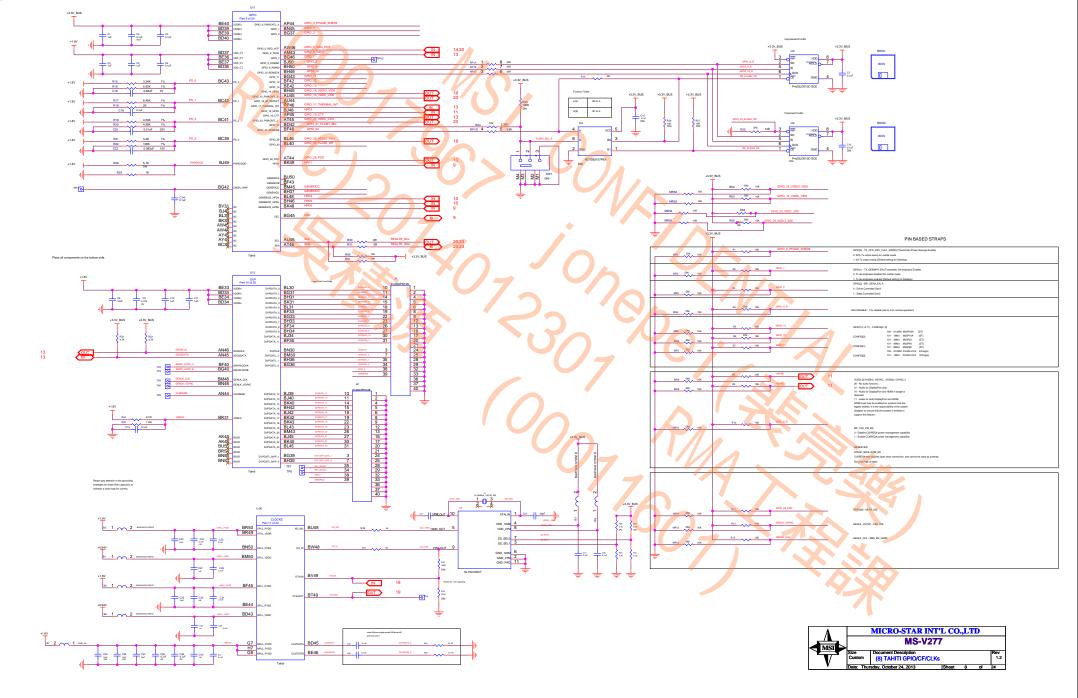
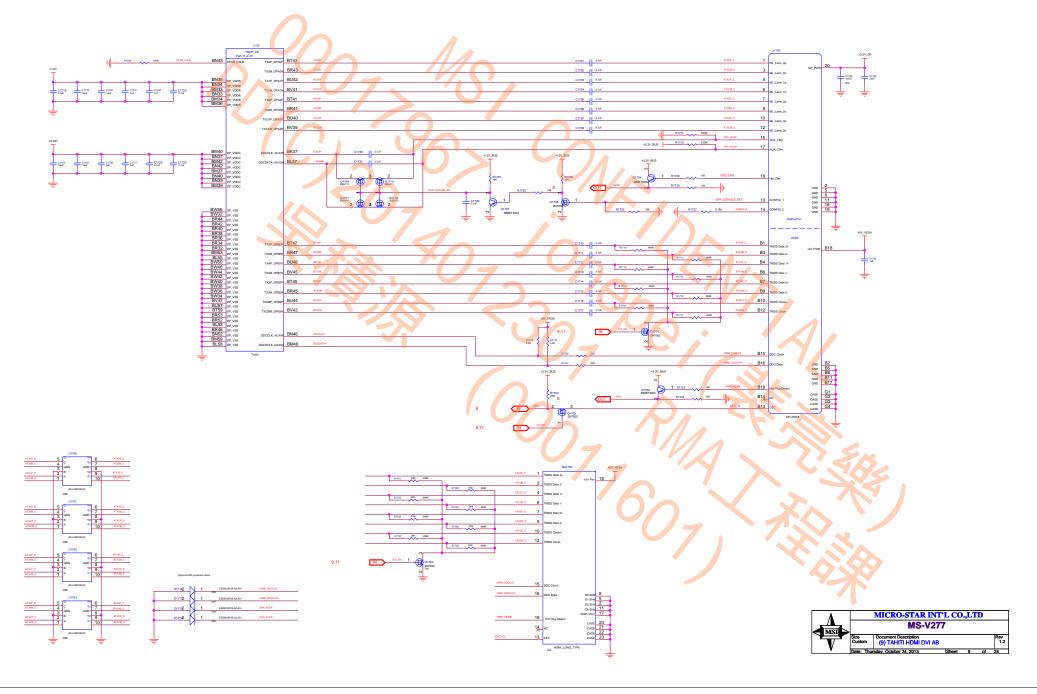


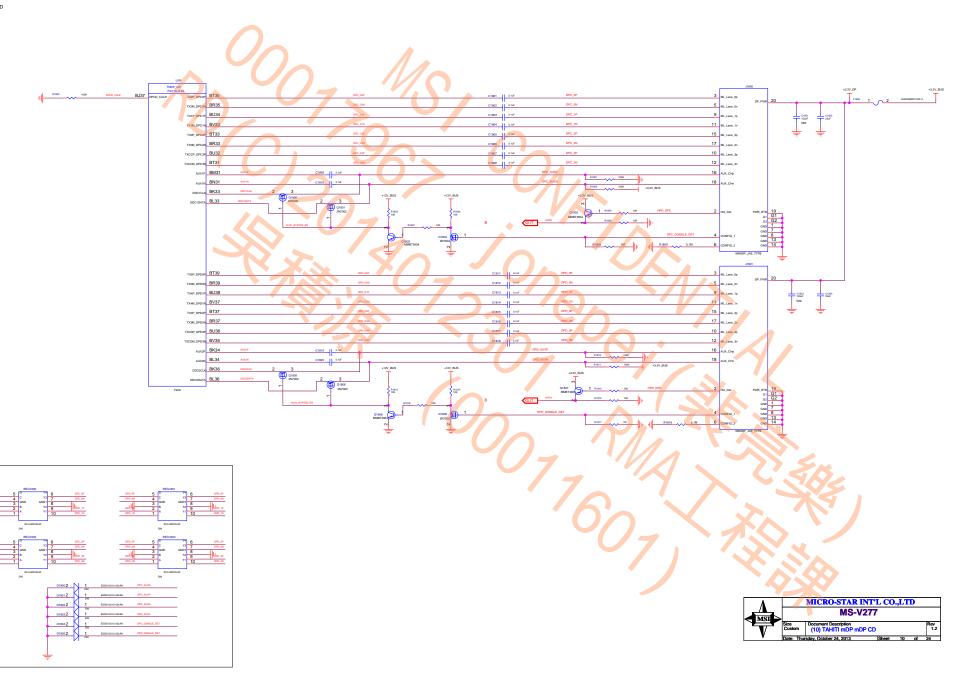
MST	MICRO-STAR INT'L CO.,LTD		
	MS-V277		
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V		Custom (6) TAHITI Memory Channel E&F Date: Thursday, October 24, 2013 Sheet 6 of	

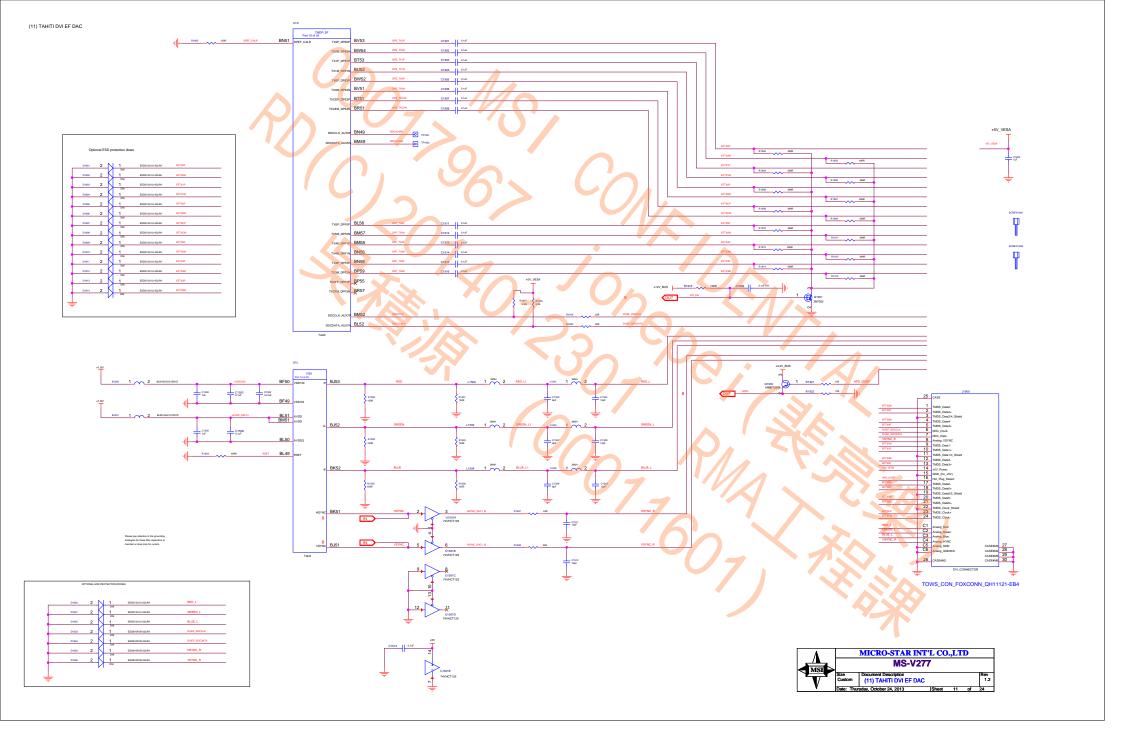


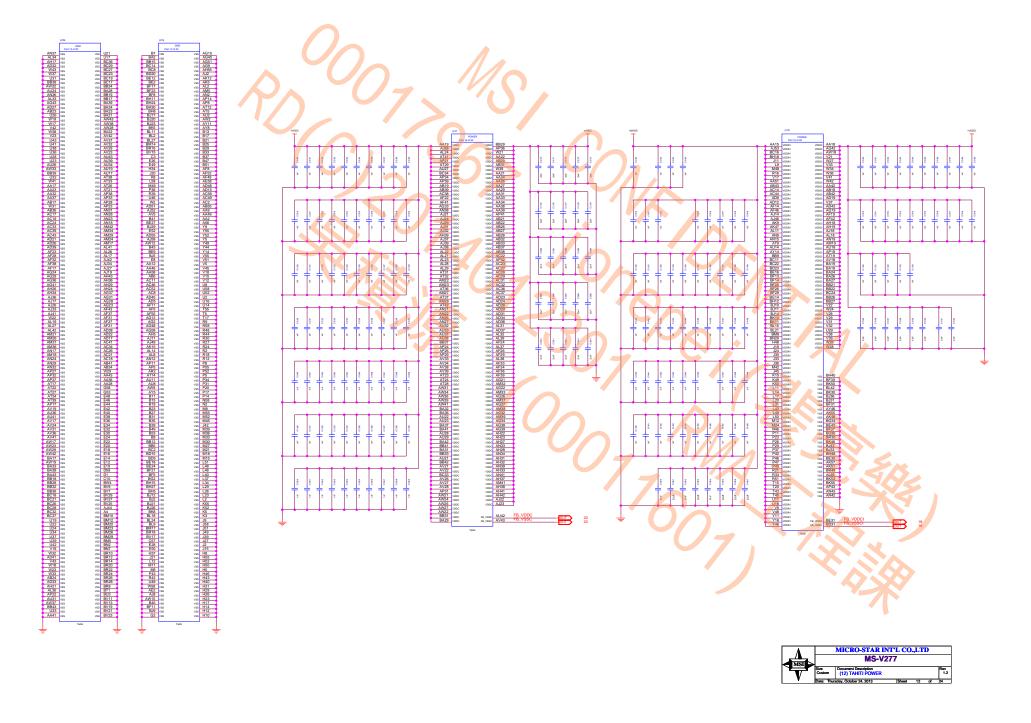


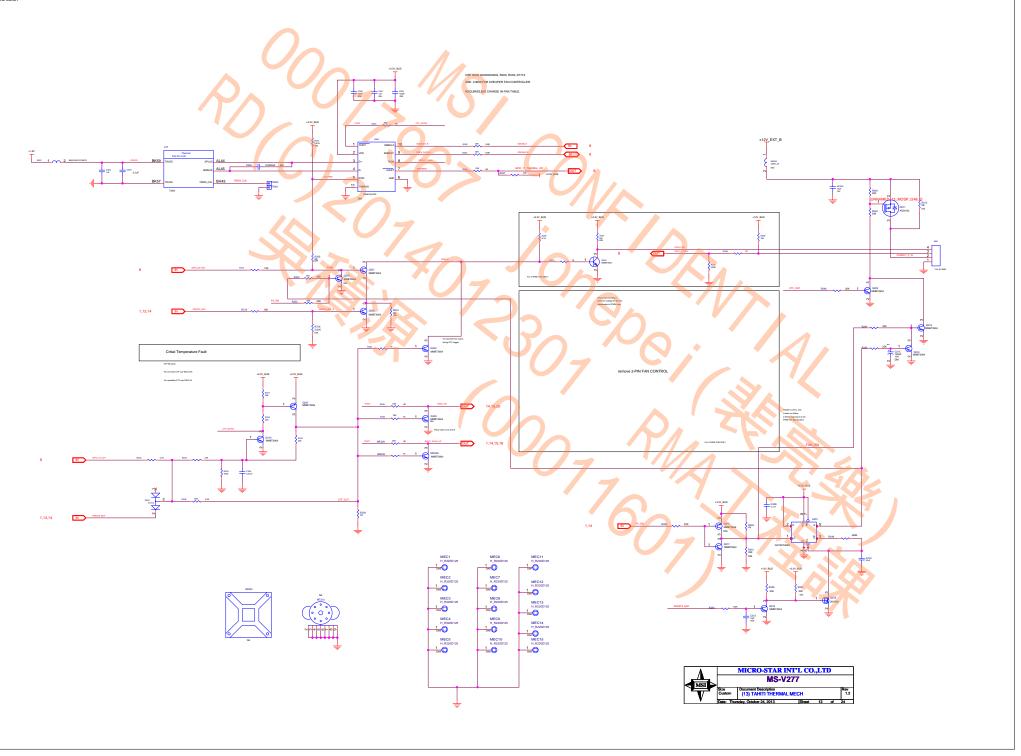


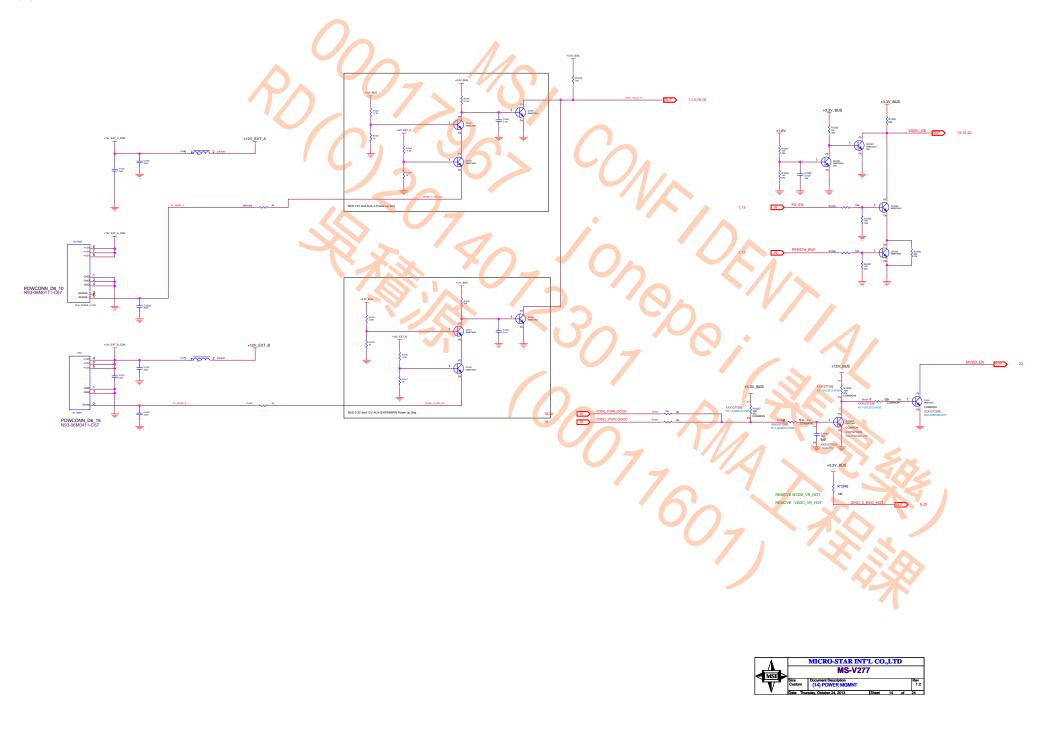
OPTIONAL ESD PROTECTION DIODES

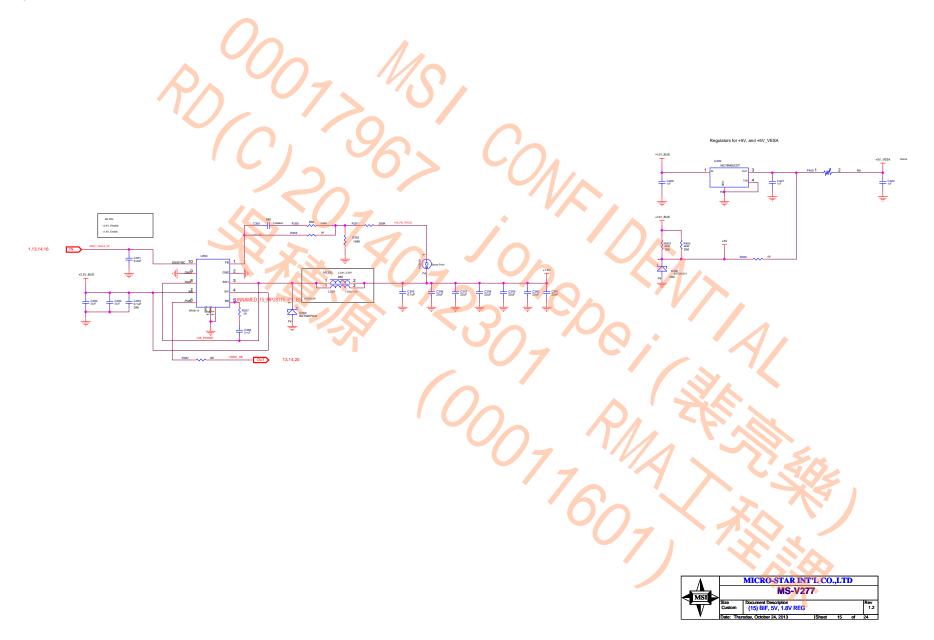


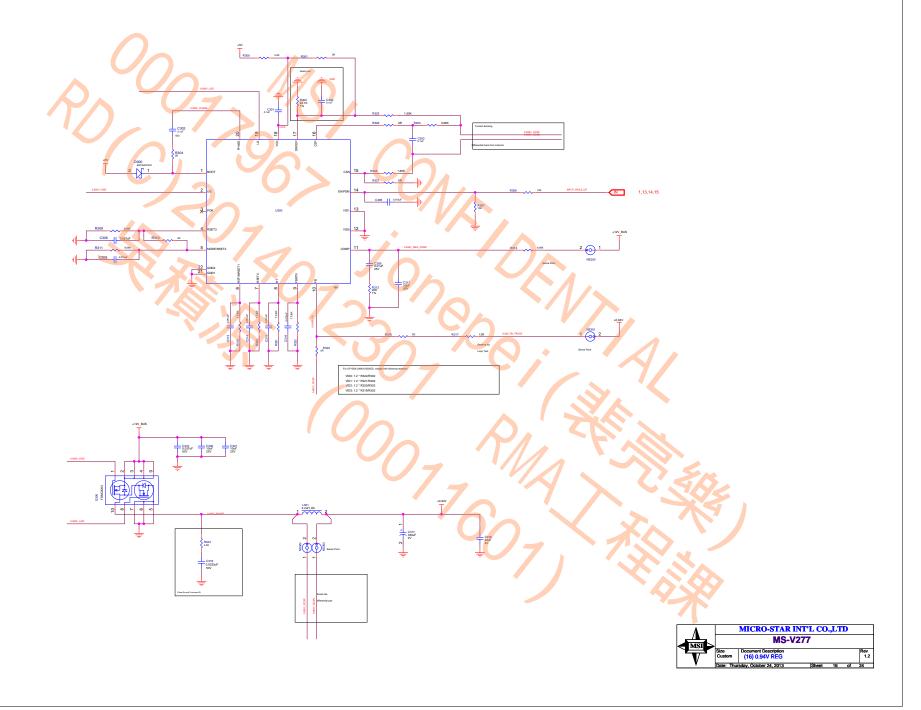


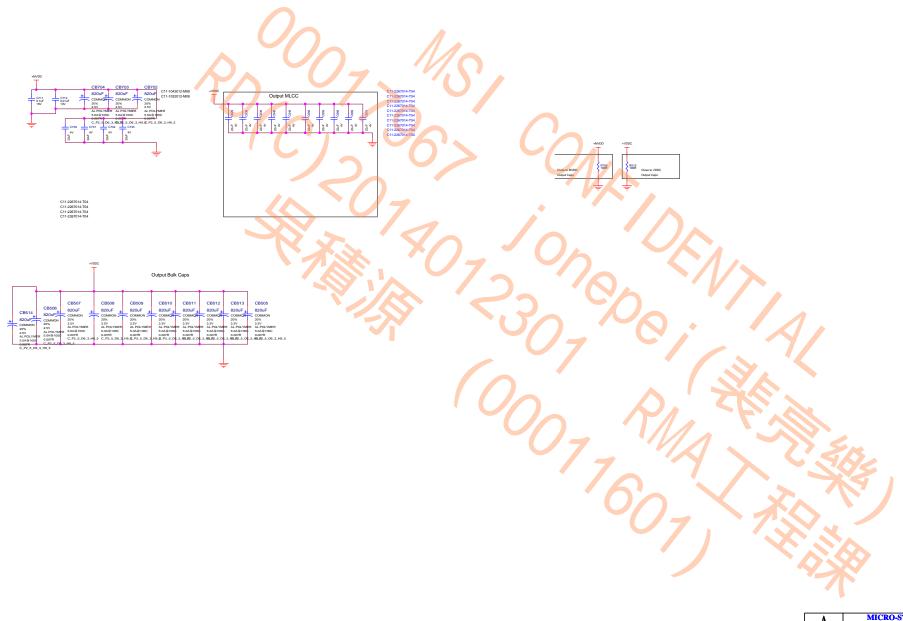




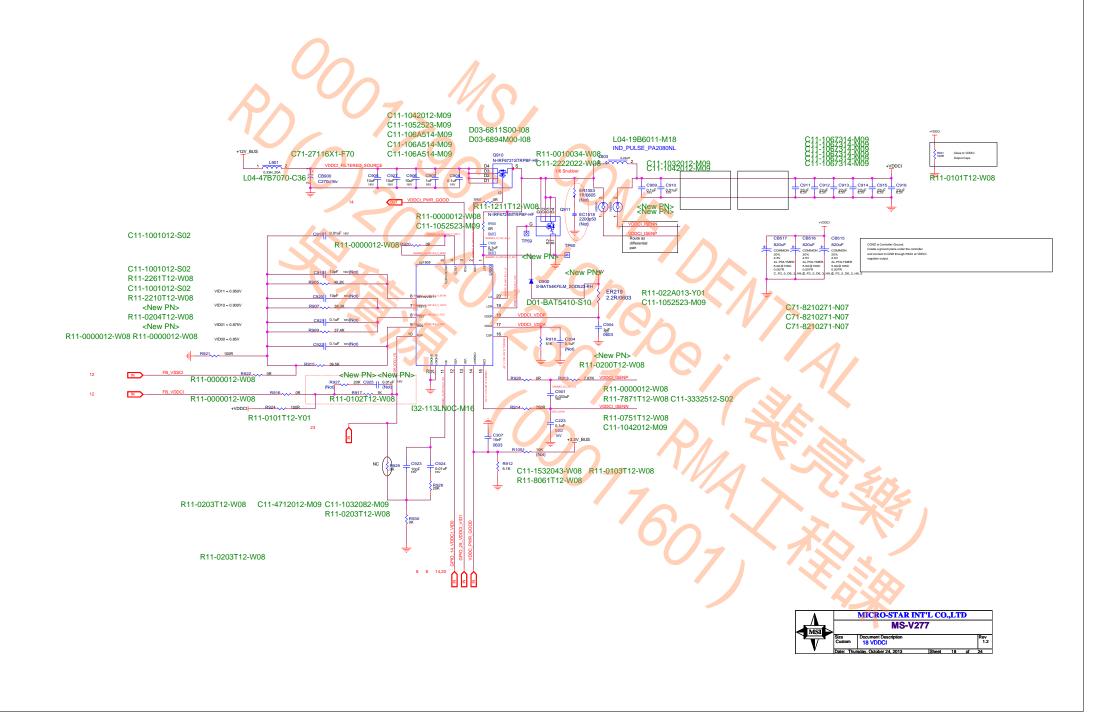


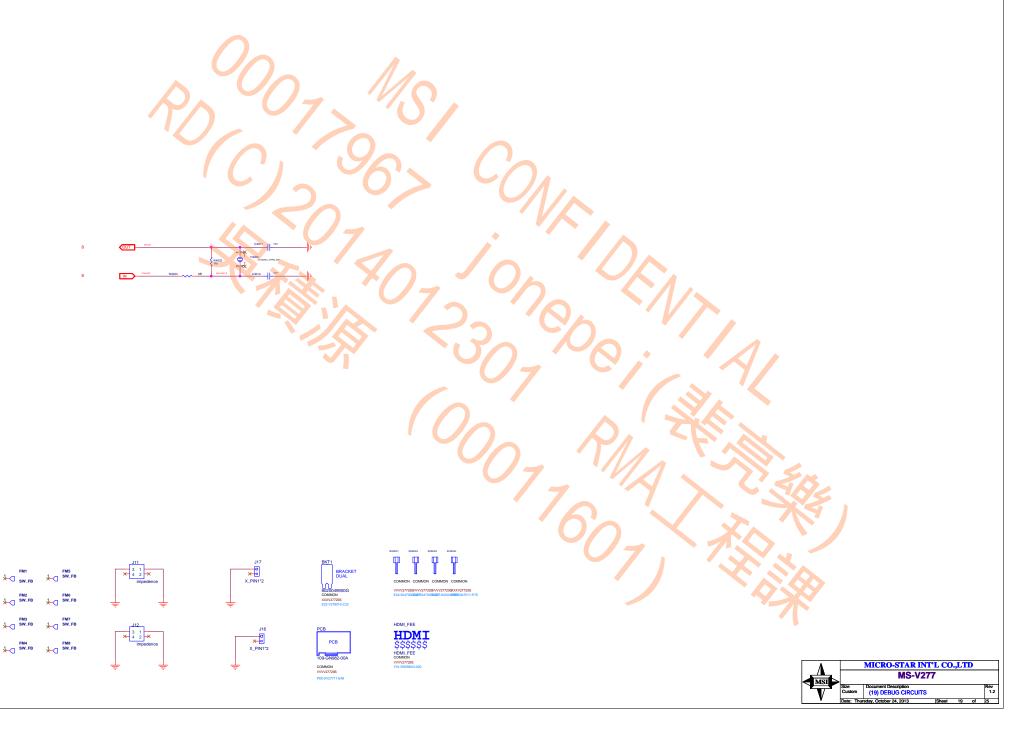


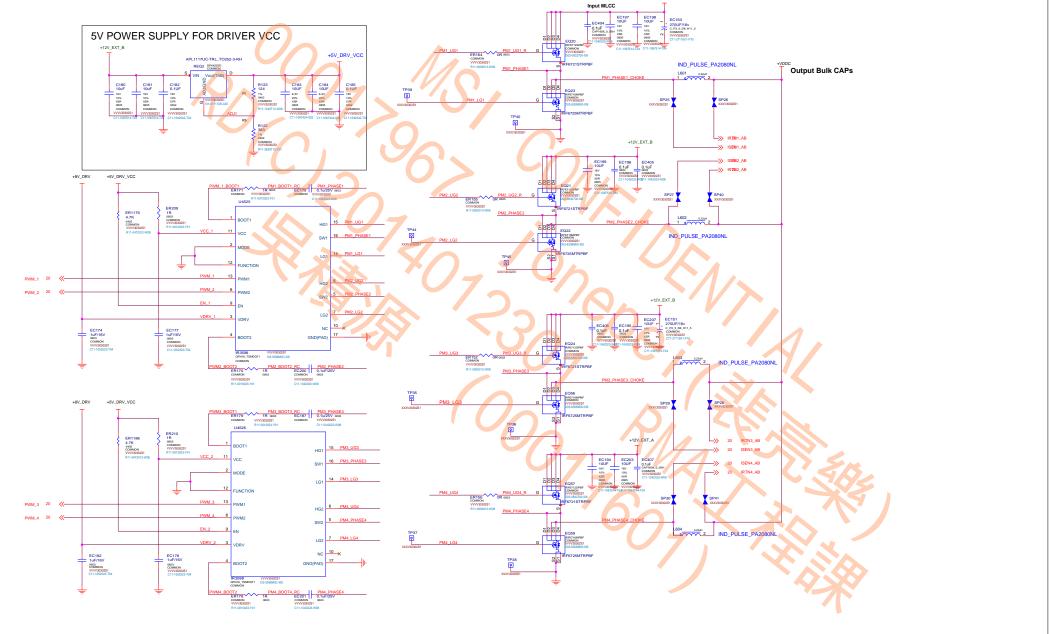




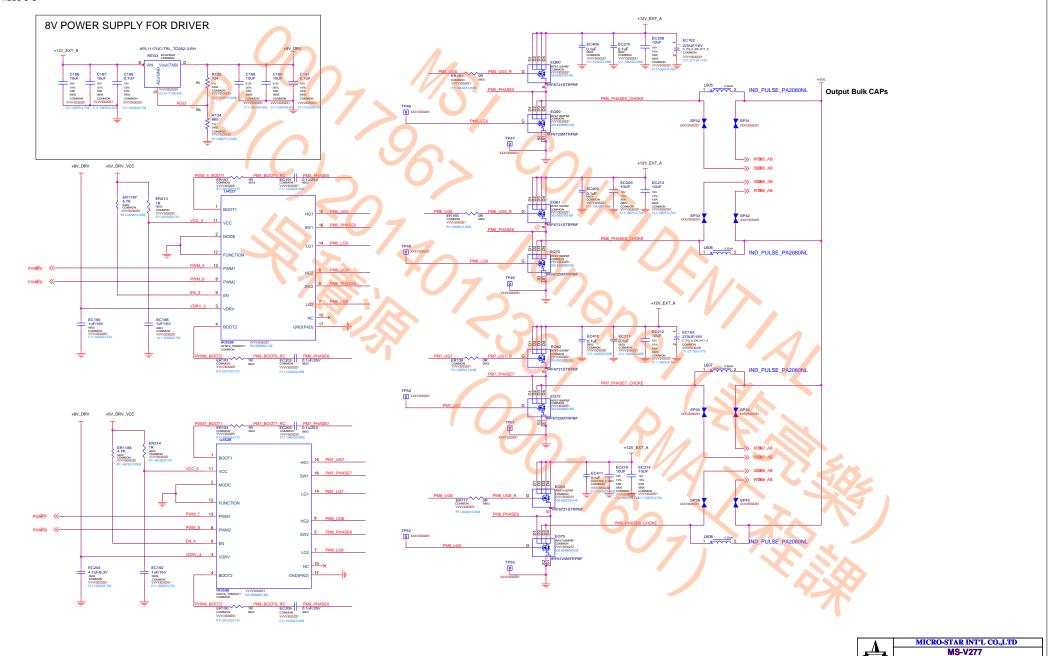




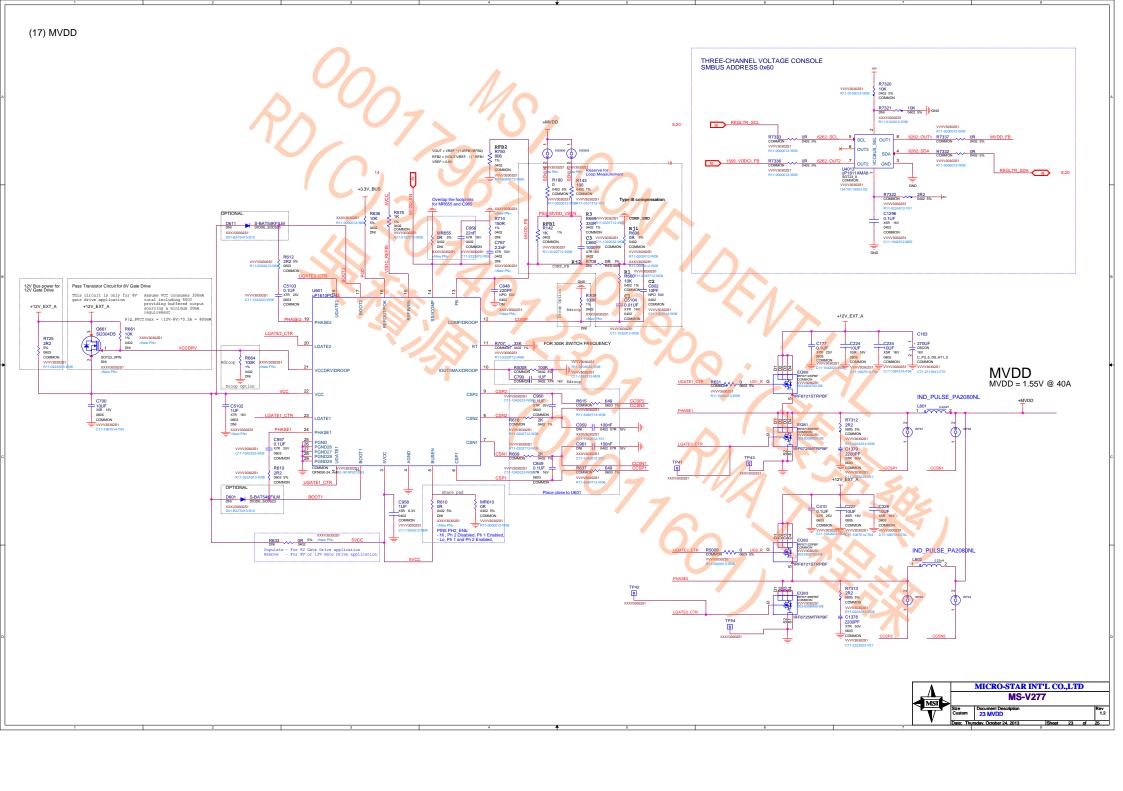


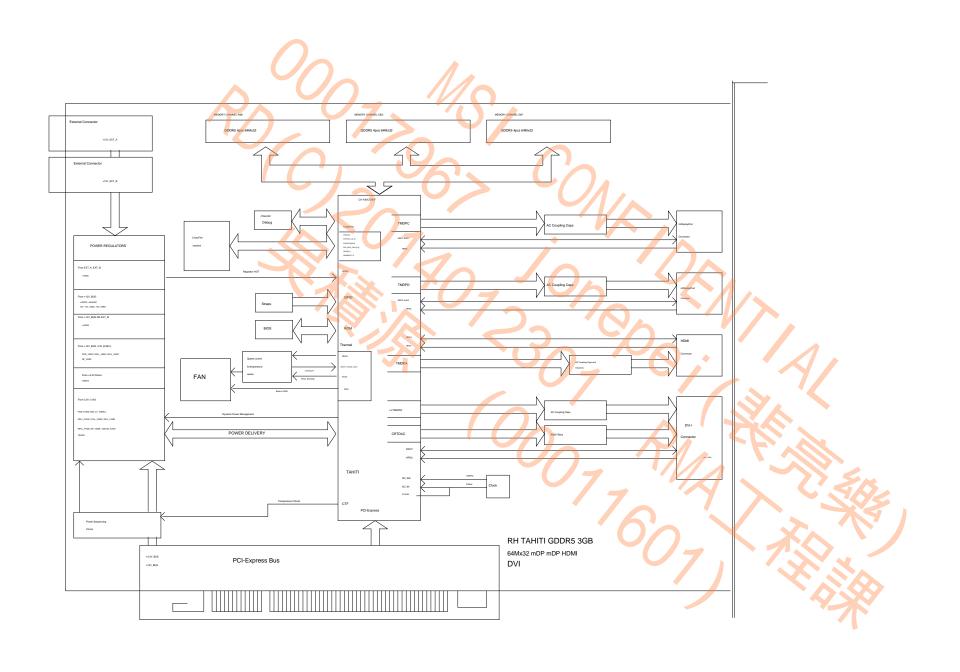






Document Description 22 VDDC Phase 5-8 say, October 24, 2013







P13 REMOVE 2PIN 風扇控制 更換單顆螺絲孔

P14 更換MVDD SEQUENCE

P15 移除+5_VR 線路

P18 更換V278 VDDCI 線路

P19 移除公版預留DEBUG線路

P21 移除原本3567線路

P22~P25 更換成V303 VDDC/MVDD線路

