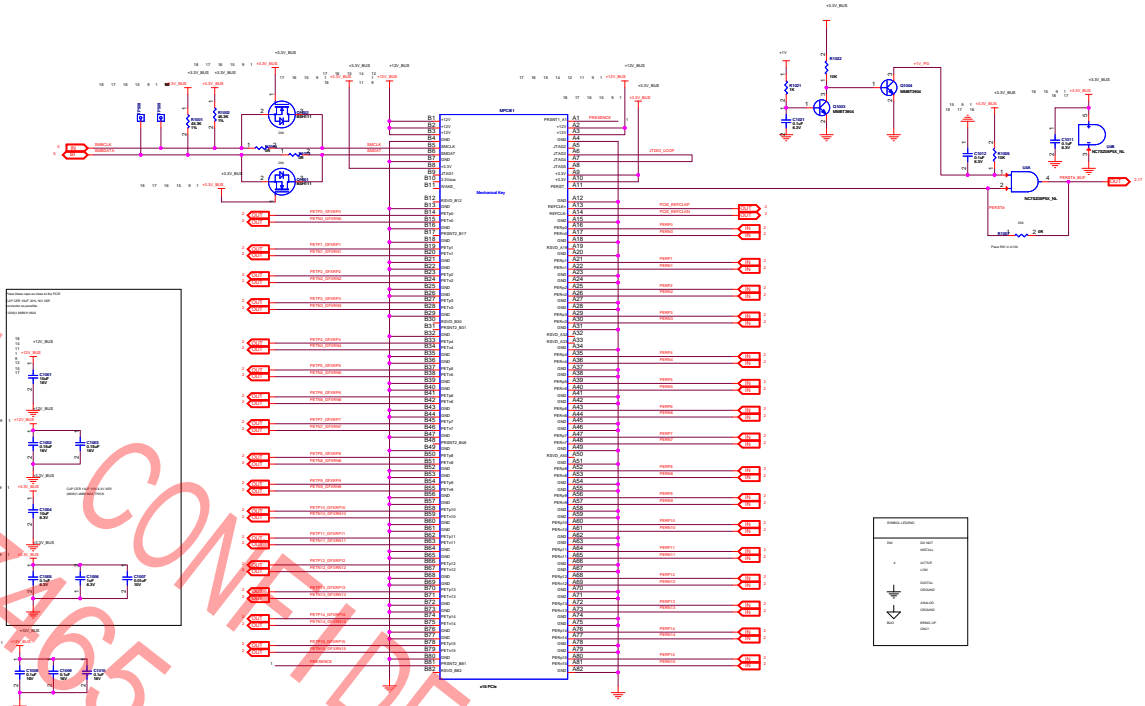


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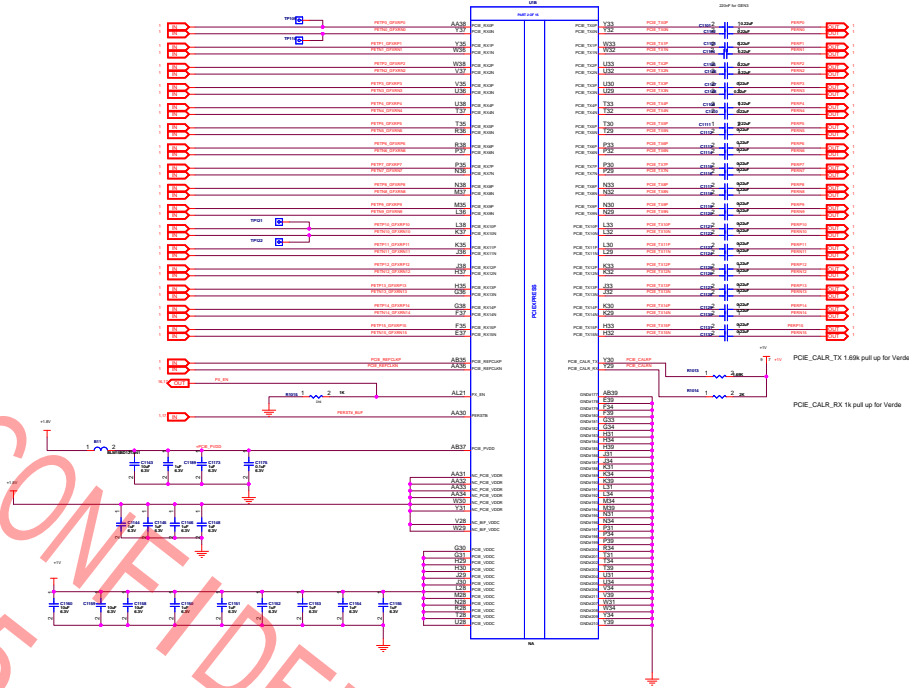
PCI-EXPRESS EDGE CONNECTOR



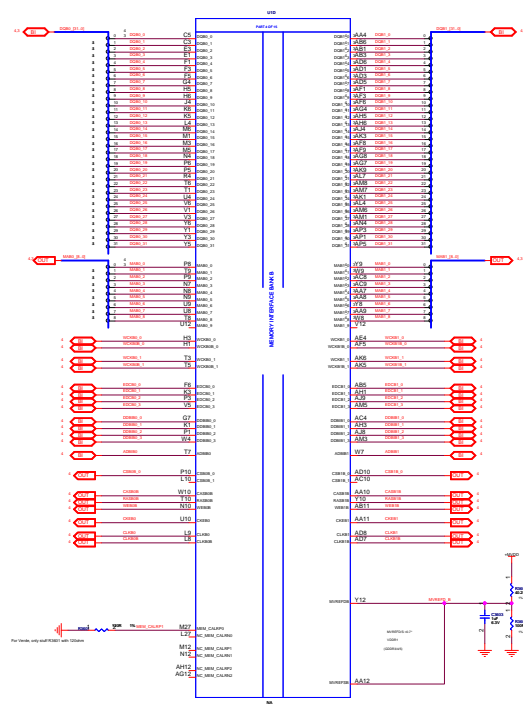
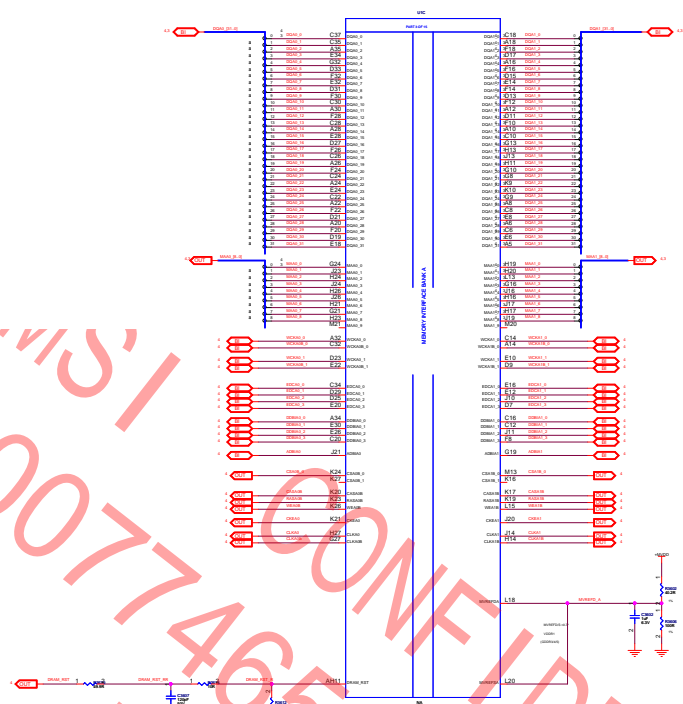
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|--|--|---|--|--------------------|--|
| AMD - PLATFORM HARDWARE ENG No. 1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203 | | SHEET: PCI-E Edge Connector DATE: Sun Dec 04 10:38:13 2011 SHEET NUMBER: 1 OF 20 DOCUMENT NUMBER: 100-000000-001 | | REV: 1.0 TITLE: | |
|--|--|---|--|--------------------|--|

Cape_Verde PCIe Interface

NOTE: Some of the PCIe signals will
be available through cable on boards.



| | | | |
|--|----------|----------------------------------|--|
| AMD - PLATFORM HARDWARE ENG No.1367, ZHANGDONG ROAD SHANGHAI, CHINA 201203 | | SHEET: Cape Verde PCIe Interface | |
| DATE: Thu Dec 01 18:16:30 2011 | REV: 1.0 | SHEET NUMBER: 2 OF 20 | |
| DOCUMENT NUMBER: 100-000000-001 | | TITLE: | |

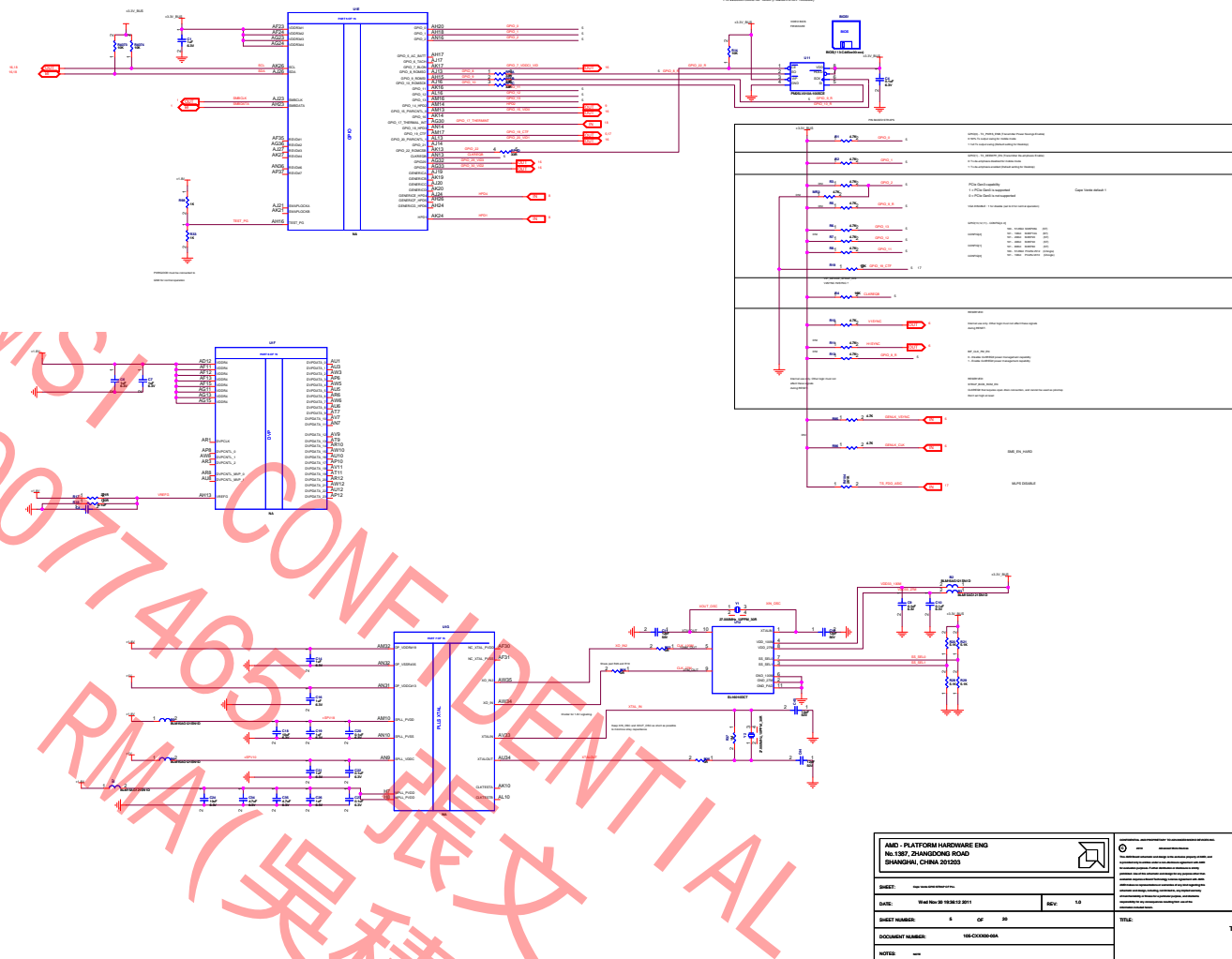


| | | | |
|---|--|----------------|--|
| AMD - PLATFORM HARDWARE ENG No. 1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203 | | SHEET: 3 OF 30 | |
| DATE: Thu Dec 01 10:14:54 2011 | | REV: 1.0 | |
| DOCUMENT NUMBER: 100-000000-000 | | TITLE: | |



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|-----------------------------|--------------------------|
| AMD - PLATFORM HARDWARE ENG | |
| No. 1887, ZHANGJING ROAD | |
| SHANGHAI, CHINA 201205 | |
| DESIGN: | |
| DATE: | Thu Dec 01 16:43:00 2011 |
| SHEET NUMBER: | 4 OF 20 |
| DOCUMENT NUMBER: | 100-000000-001 |
| NOTES: | |

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| TITLE: | |
| TITLE: | |

AMD - PLATFORM HARDWARE ENG
No. 1887, ZHANGKONG ROAD
SHANGHAI, CHINA 201205

SHEET:

DATE: Sat Nov 01 16:00:00 2014

SHEET NUMBER: 5

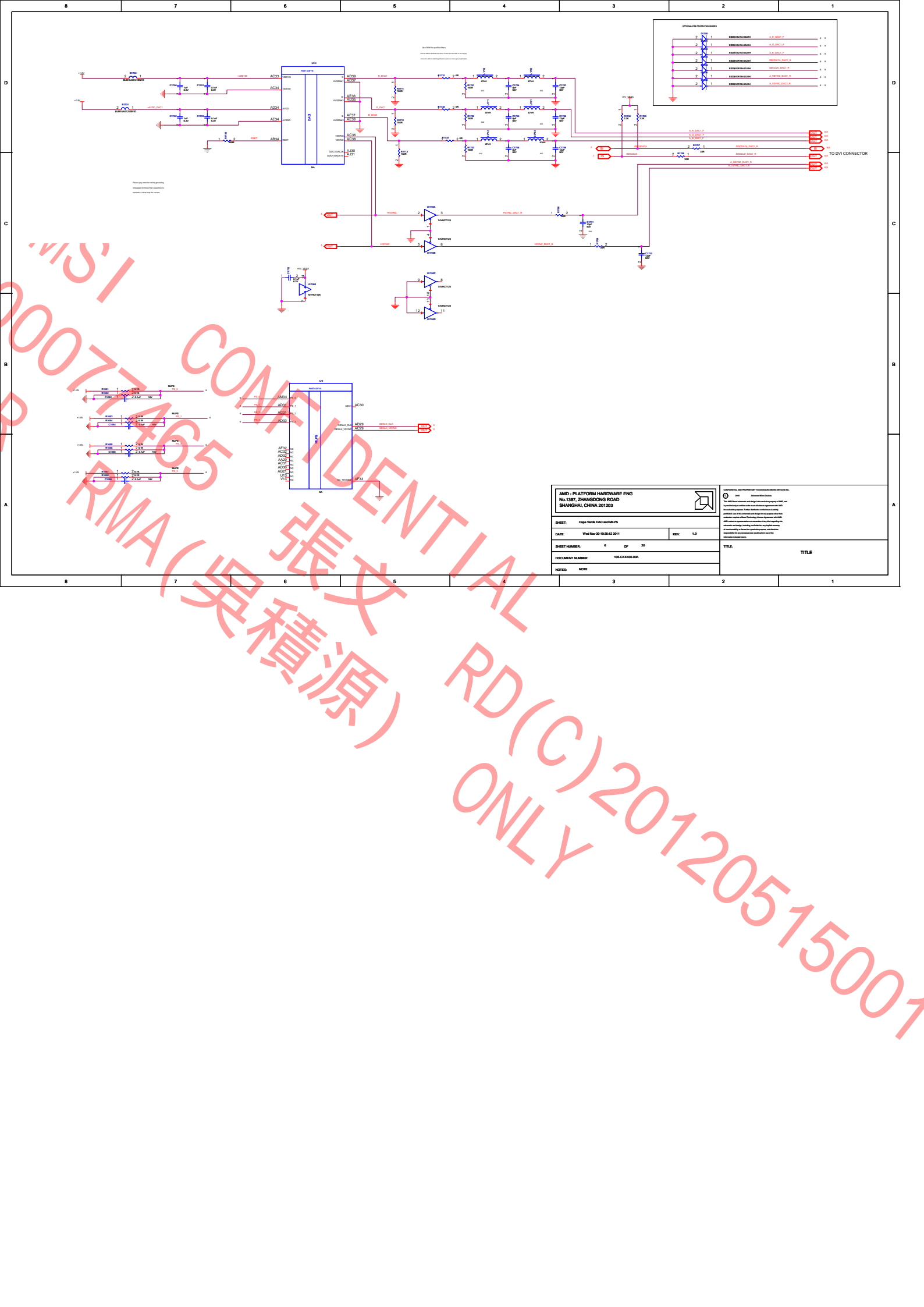
DOCUMENT NUMBER: 100-000000-001

NOTES:

REV: 1.0

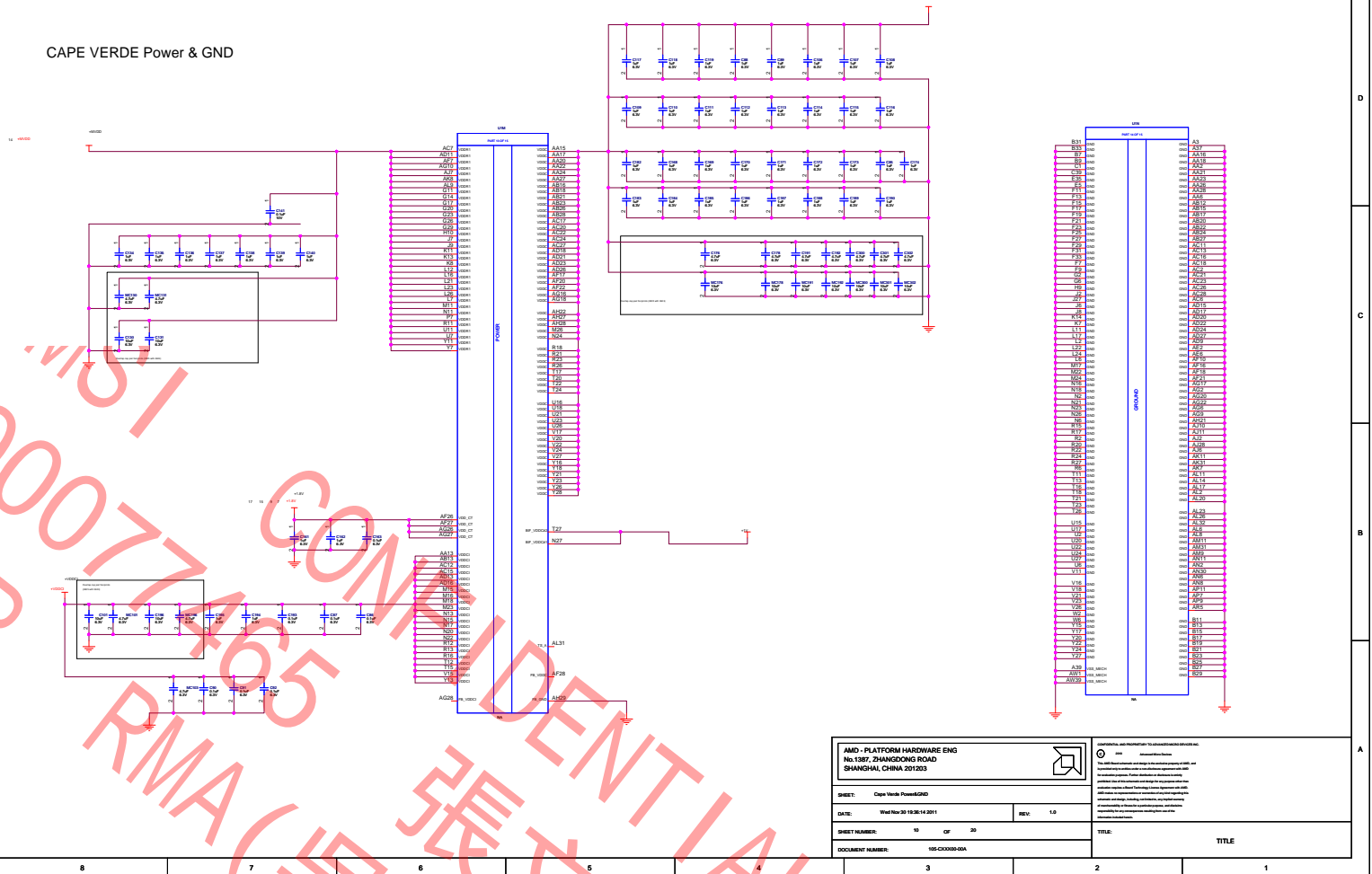
TITLE:


TITLE



| | | | |
|---|--|--------------------------------|--|
| AMD PLATFORM HARDWARE ENG No. 1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203 | | SHEET: Cape Verde DMC and MLPS | |
| DATE: Wed Mar 29 16:36:12 2011 | | REV: 1.0 | |
| SHEET NUMBER: 6 | | OF 20 | |
| DOCUMENT NUMBER: 100-000000-00A | | TITLE: | |
| NOTES: NOTE | | TITLE | |

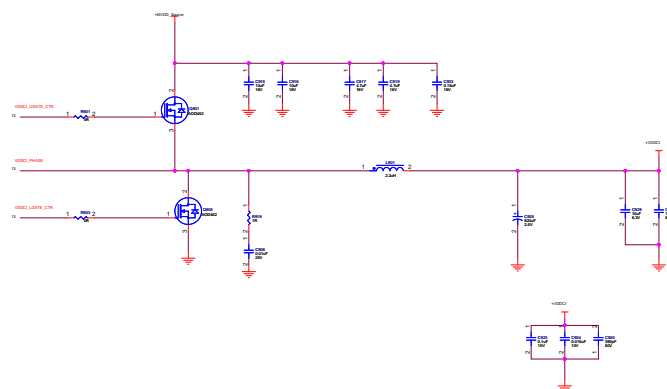
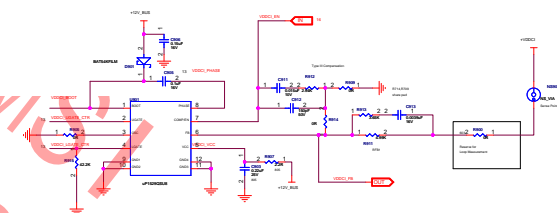






| | | |
|---|----------------------|---|
| AMD - PLATFORM HARDWARE ENG No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203 | |  |
| SHEET: Cape Verde Power&GND | | |
| DATE: Wed Nov 30 19:36:14 2011 | REV: 1.0 | |
| SHEET NUMBER: 10 OF 20 | | |
| DOCUMENT NUMBER: 105-0X000-00A | | |

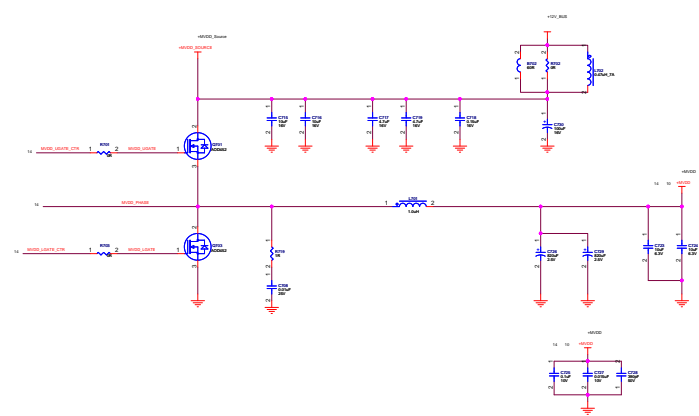
| | |
|---|------------------------|
| COMPANY/INSTITUTION, please prefill this entry for all submitted documents (including addendums). | |
| 2008 | Advanced Micro Devices |
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| TITLE: | TITLE |







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|--|--|---|--|---|--|
| AMD - PLATFORM HARDWARE ENG No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203 | |  | |  | |
| SHEET: 0000 | | DATE: 2014-03-10 10:35:15 | | REV: 1.0 | |
| SHEET NUMBER: 13 | | CP: 30 | | TITLE: | |
| DOCUMENT NUMBER: 105-CK0000-00A | | | | TITLE | |
| NOTES: | | NOTE | | | |

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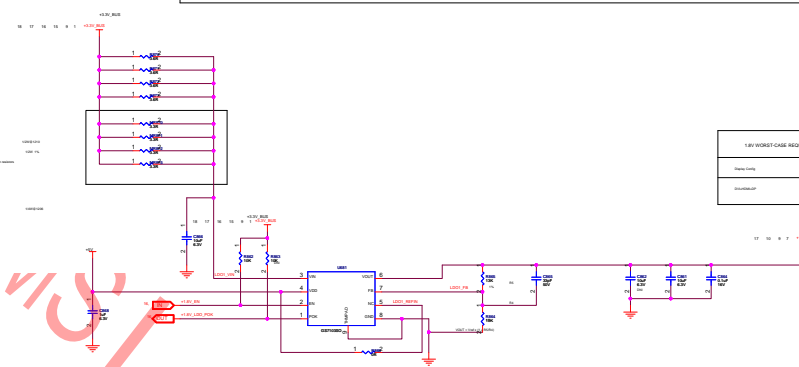


| | | | | | |
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| AMD - PLATFORM HARDWARE ENG No.1367, ZHANGDONG ROAD SHANGHAI, CHINA 201203 | |  | |  AMD CONFIDENTIAL | |
| SHEET: MICO | | | | | |
| DATE: Wed Nov 23 15:36:14 2011 | | REV: 1.0 | | | |
| SHEET NUMBER: 14 | | OF 20 | | TITLE: TITLE | |
| DOCUMENT NUMBER: 100-000000-000 | | | | | |

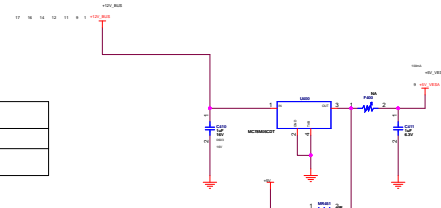
Linear Regulators

| | | | |
|--|--------------------------------------|-----------------------|-----------------------|
| Load Reg. | 100 = 0.0007 to 0.001 (1.0% to 1.0%) | Load = +1.8V to +1.9V | Load = 1.8A (20% MAX) |
| FOR 10 to 100ms eq. output delay for loading | | | |

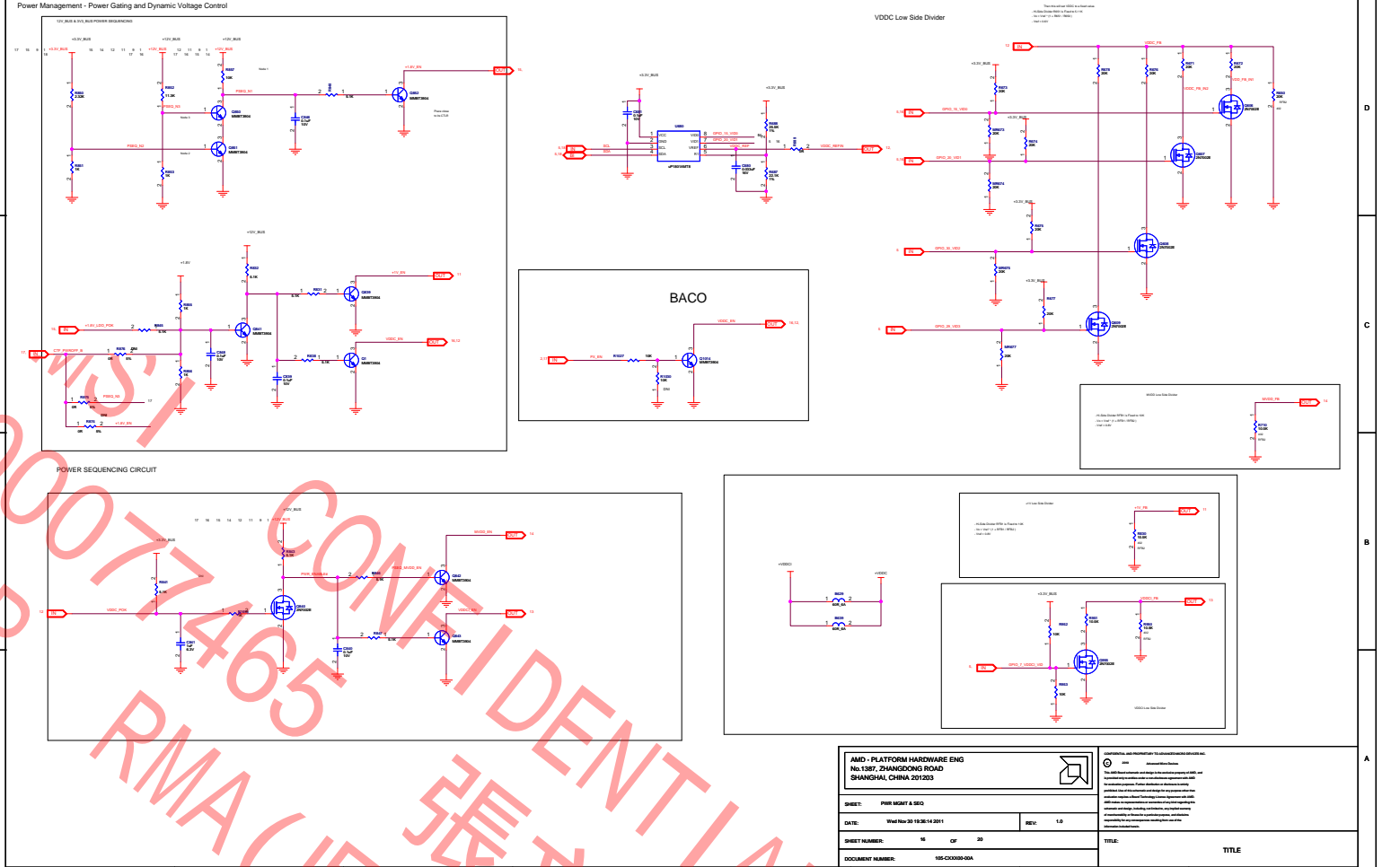
Regulators for +5V_VESA and +5V_VESA2



| 1V5 VOLTAGE REGULATOR | |
|-----------------------|------|
| Output Voltage | 1.5V |
| Input Voltage | 5.0V |
| Output Current | 1.0A |

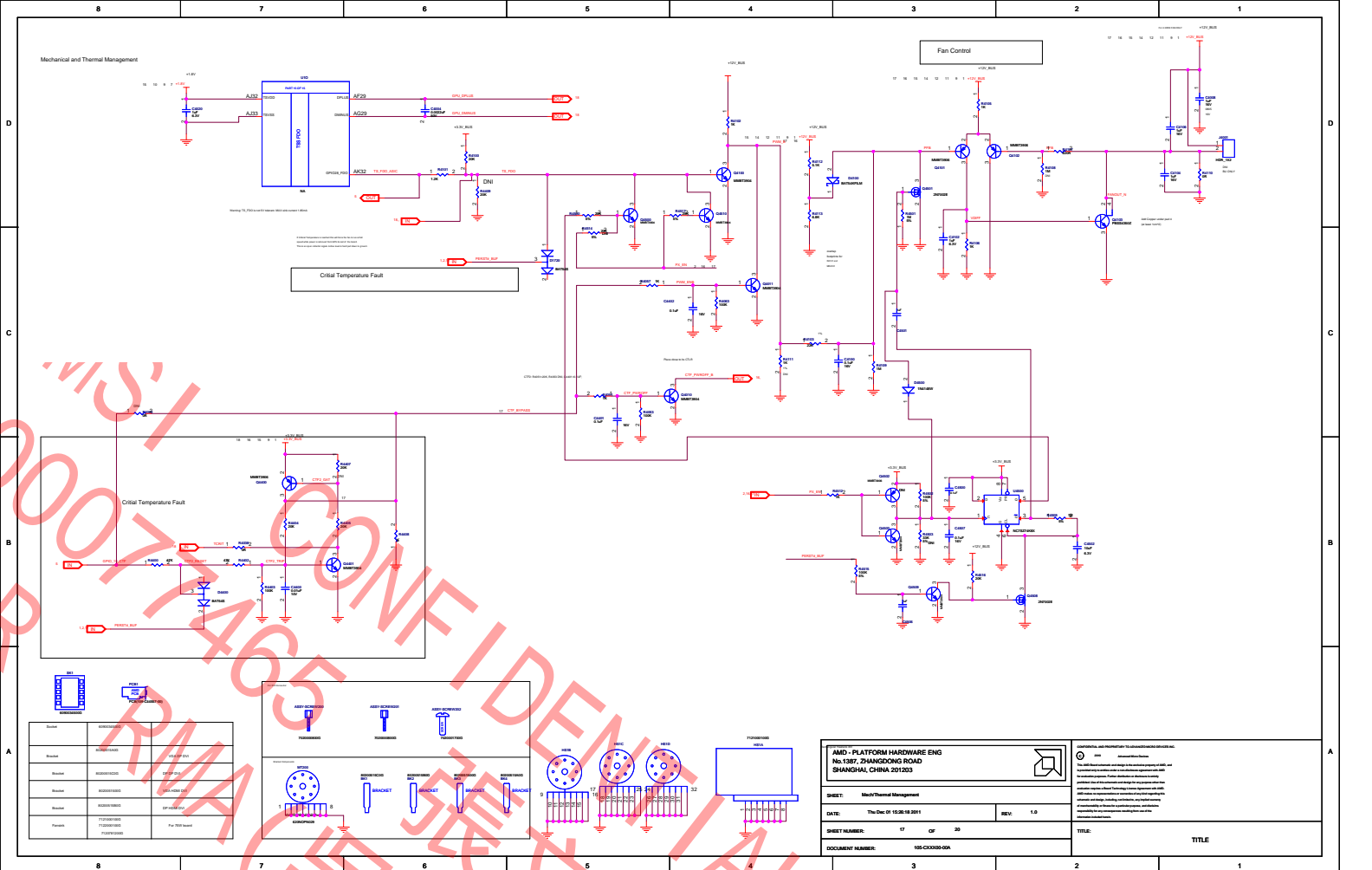


| | | | |
|--|--|------------------------|--|
| AMD - PLATFORM HARDWARE ENG No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203 | | TITLE | |
| SHEET: Linear Regulators | | REV: 1.0 | |
| DATE: Wed Nov 20 10:26:14 2011 | | SHEET NUMBER: 15 OF 20 | |
| DOCUMENT NUMBER: 100-000000-000 | | TITLE | |

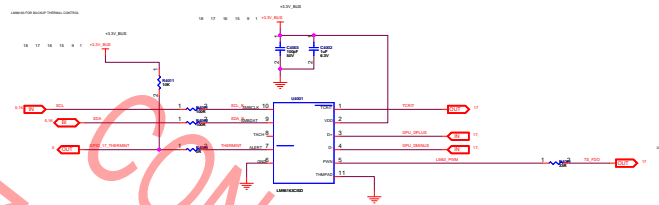
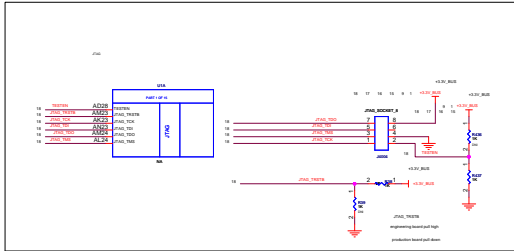



| | | | |
|--|--|-----------------------|--|
| AMD - PLATFORM HARDWARE ENG No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203 | | SHEET: PWR MGAT & SEQ | |
| DATE: Wed Nov 20 15:26:14 2011 | | REV: 1.0 | |
| SHEET NUMBER: 16 | | OF 20 | |
| DOCUMENT NUMBER: 100-000000-00A | | TITLE: | |

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(19) Debug Circuits



| | | | |
|---|--|---|--|
| AUD - PLATFORM HARDWARE ENG No.1387, XANGDONG ROAD SHANGHAI, CHINA 201203 | |  | |
| SHEET: Drawing Sheet | | REV: 1.0 | |
| DATE: Wed Nov 30 10:30:51 2011 | | TITLE: | |
| SHEET NUMBER: 16 OF 30 | | TITLE: | |
| DOCUMENT NUMBER: US-00000-00A | | TITLE: | |

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No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

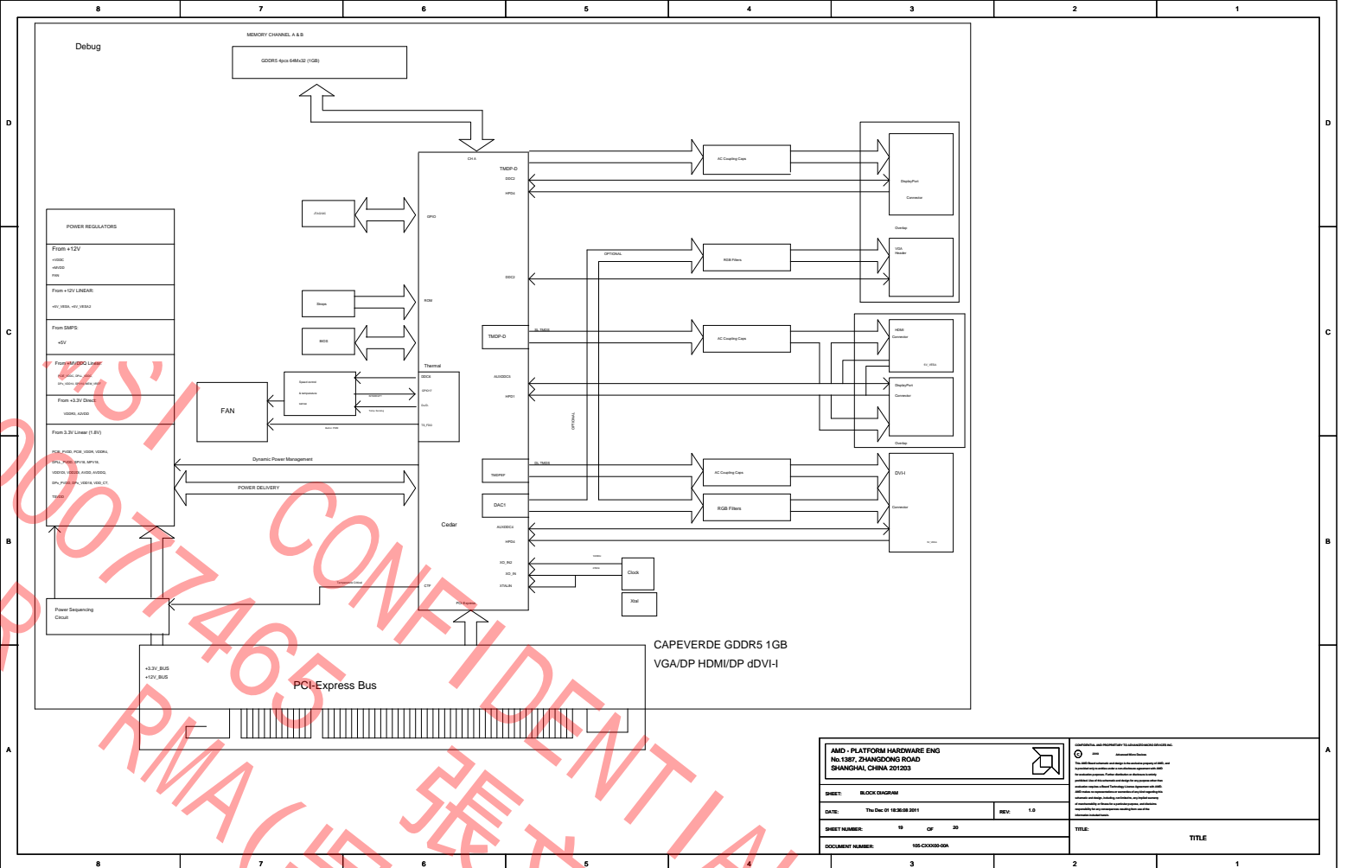
Sheet: Defining Circuit

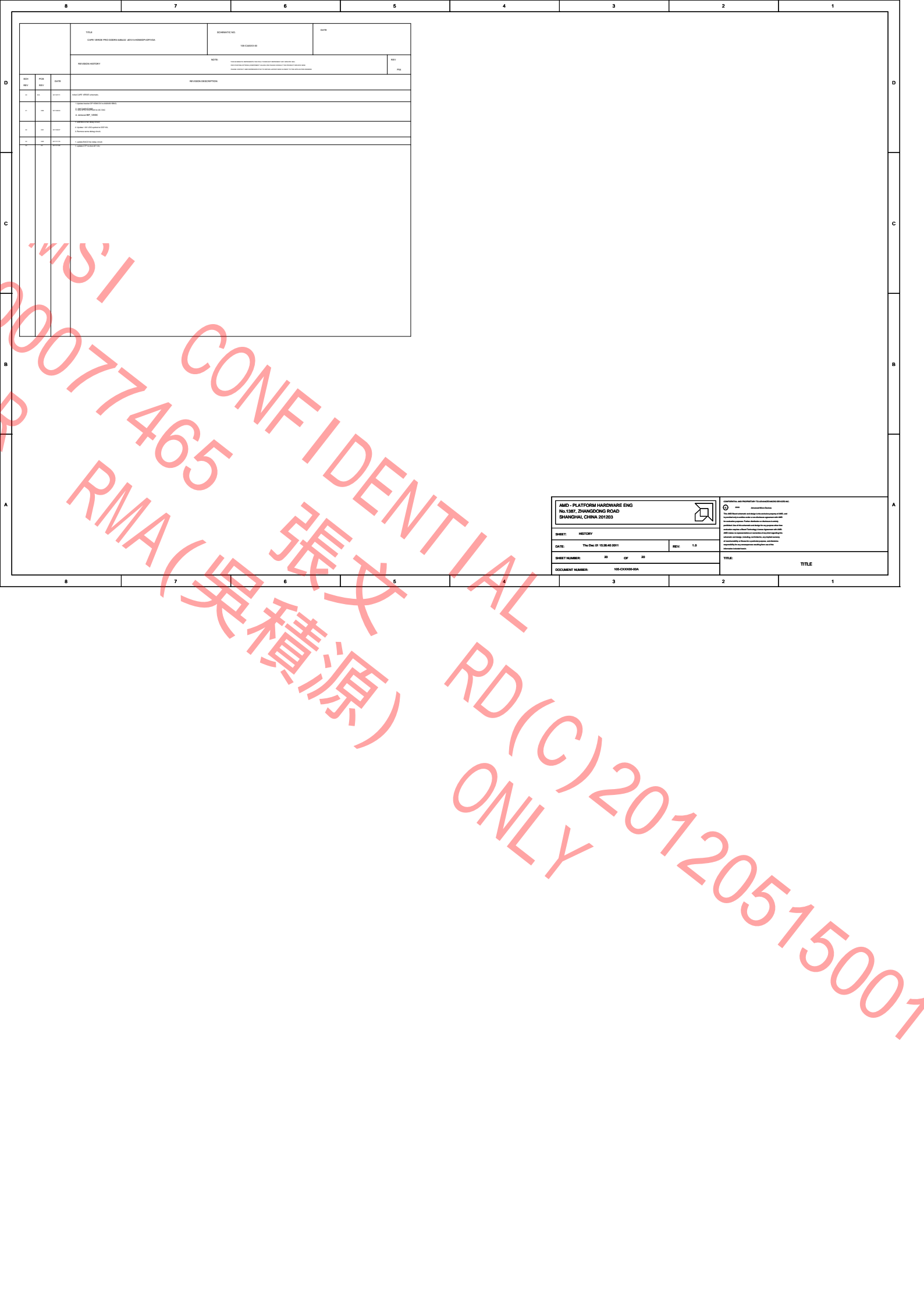
DATE: Wed Nov 20 15:36:16 2011 REV: 1.0

SHEET NUMBER: 15 OF 20

DOCUMENT NUMBER: 100-000000-00A

TITLE:





| REV | | PCB | DATE | REVISION DESCRIPTION |
|-----|-----|-----|------------|---|
| 1 | 1.0 | 1.0 | 2011.05.15 | Initial release of the document. |
| 2 | 1.0 | 1.0 | 2011.05.15 | Added the title block and revision table. |
| 3 | 1.0 | 1.0 | 2011.05.15 | Added the drawing area. |
| 4 | 1.0 | 1.0 | 2011.05.15 | Added the title block and revision table. |
| 5 | 1.0 | 1.0 | 2011.05.15 | Added the drawing area. |
| 6 | 1.0 | 1.0 | 2011.05.15 | Added the title block and revision table. |
| 7 | 1.0 | 1.0 | 2011.05.15 | Added the drawing area. |
| 8 | 1.0 | 1.0 | 2011.05.15 | Added the title block and revision table. |

| AMD - PLATFORM HARDWARE ENG | | TITLE | |
|---------------------------------|--|-------|--|
| No. 1387, ZHANGDONG ROAD | | TITLE | |
| SHANGHAI, CHINA 201203 | | TITLE | |
| SHEET: HISTORY | | TITLE | |
| DATE: The Date of 15.05.2011 | | TITLE | |
| SHEET NUMBER: 30 OF 30 | | TITLE | |
| DOCUMENT NUMBER: 100-000000-001 | | TITLE | |