

PG301 A02

Comanche 192b GDDR5, <150W, 2-way SLI


Tall DVI-I + DP + DP + DP/HDMI + DP

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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
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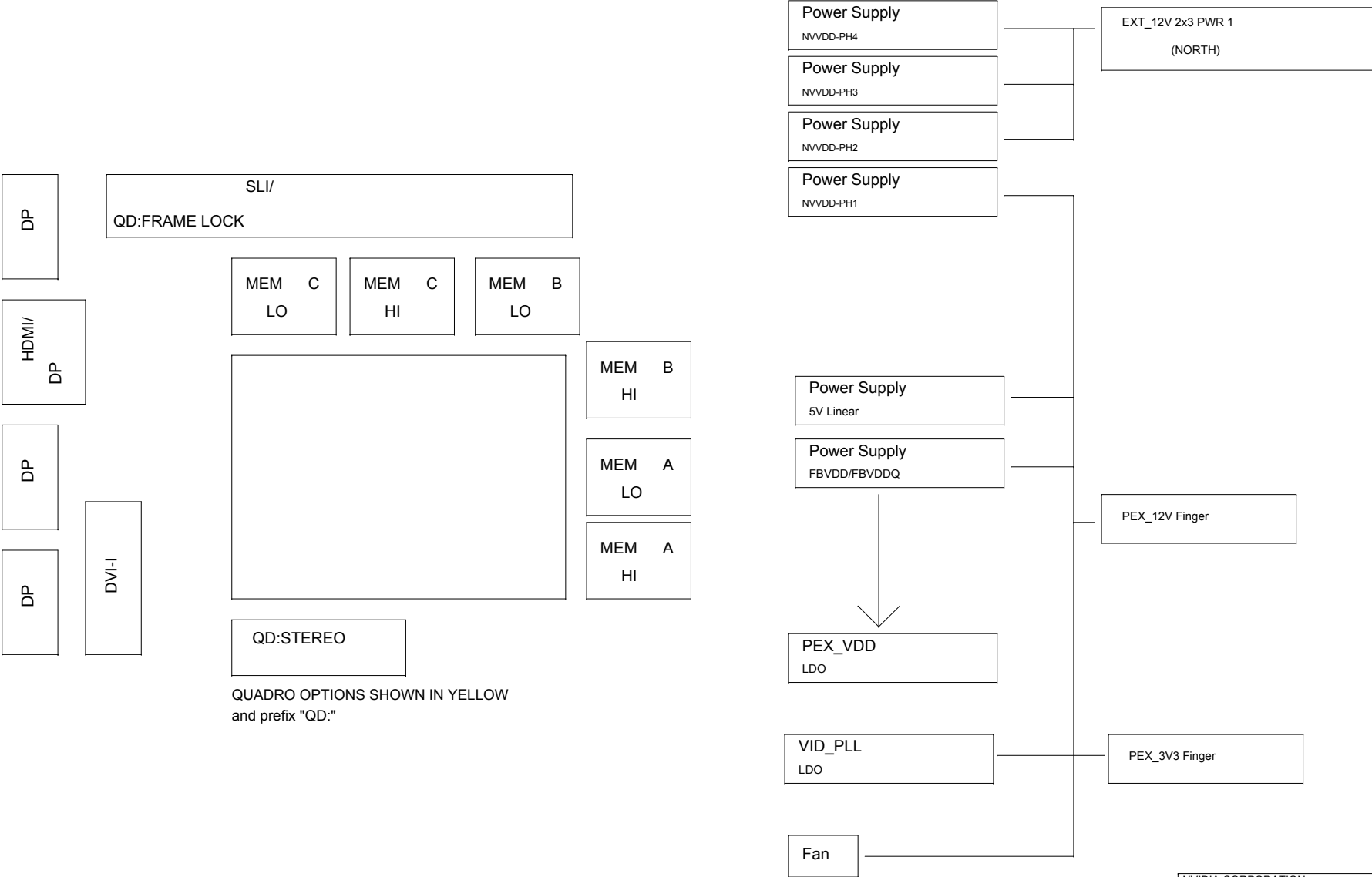
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Block Diagram



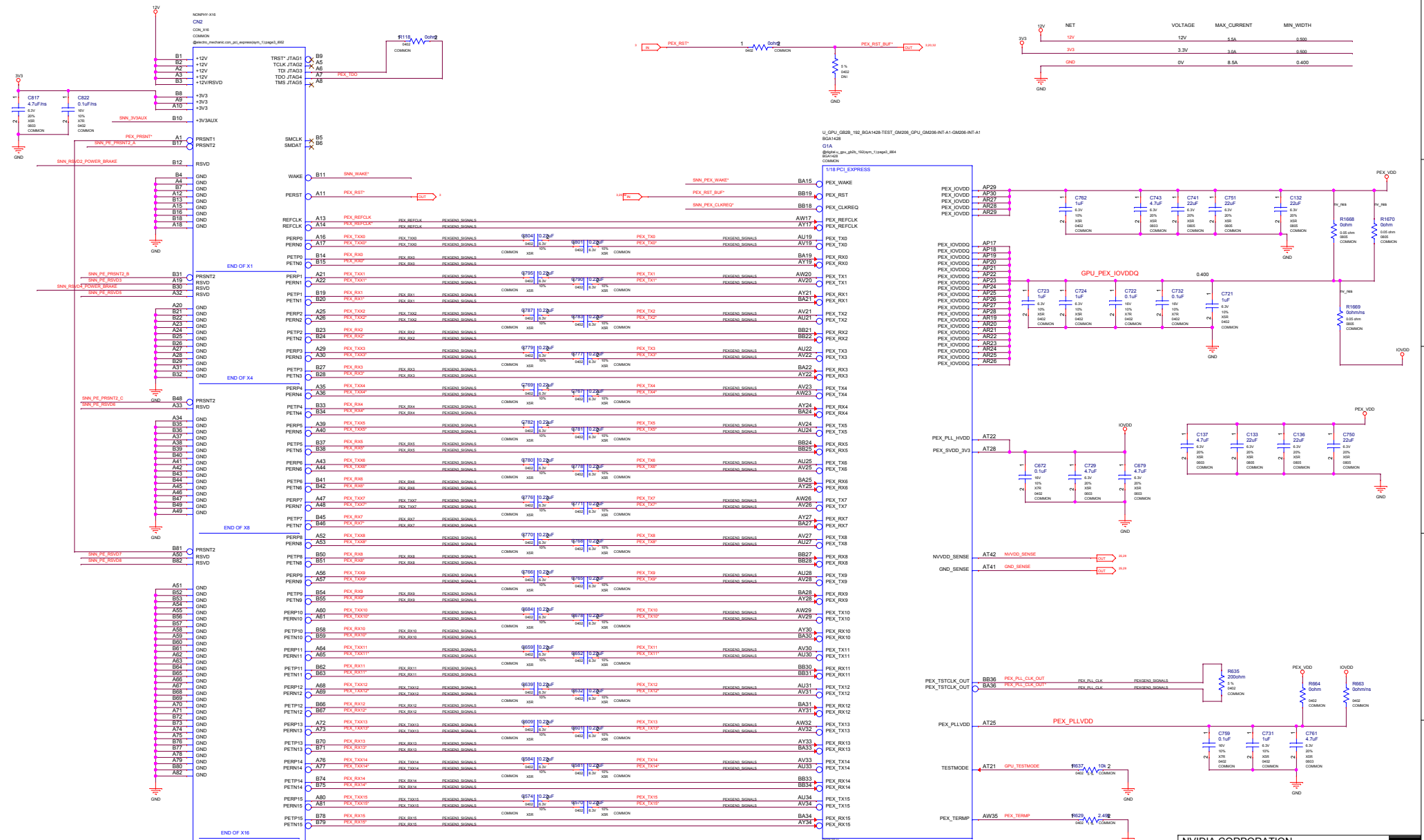
QUADRO OPTIONS SHOWN IN YELLOW
and prefix "QD:"

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PCI Express



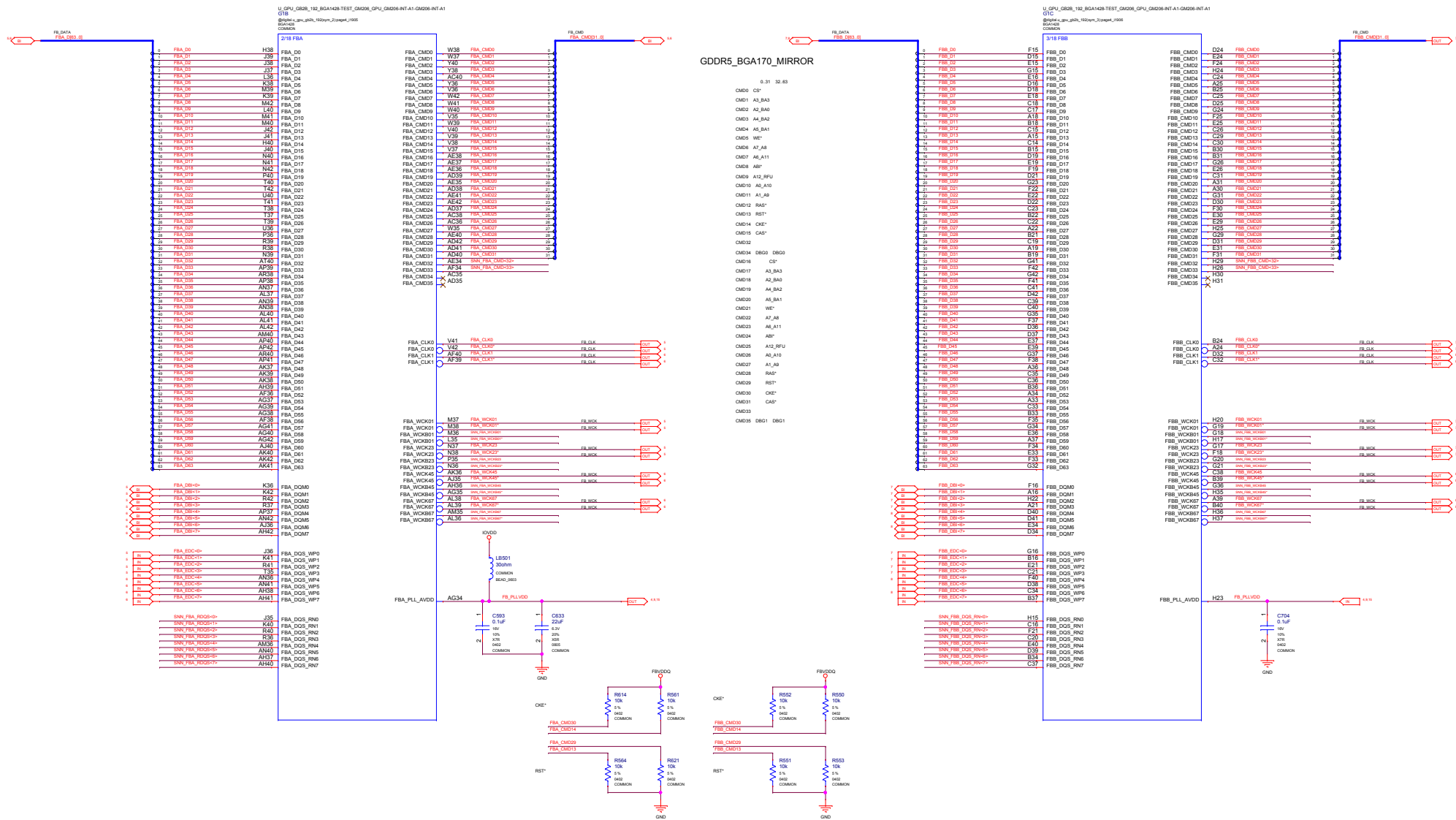
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MEMORY: GPU Partition A/B

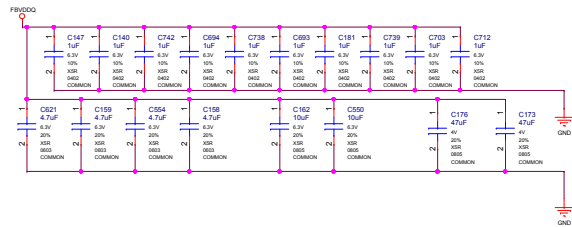
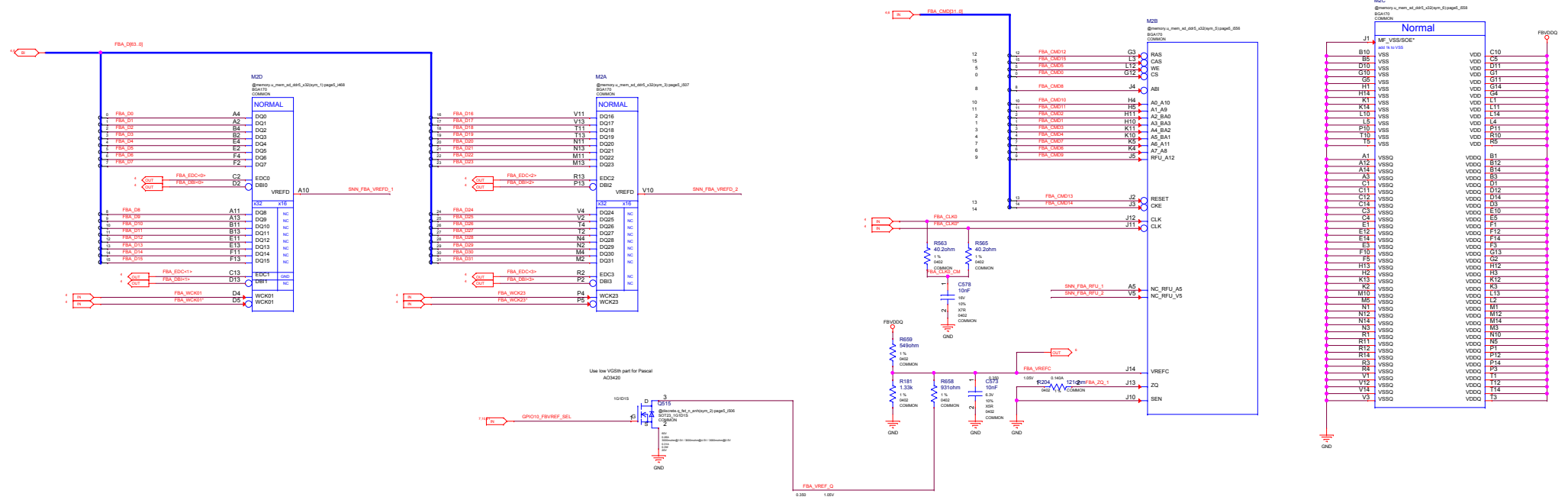


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MEMORY: FBA Partition 31..0



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PAGE DETAIL	MEMORY: FBA[31.0]

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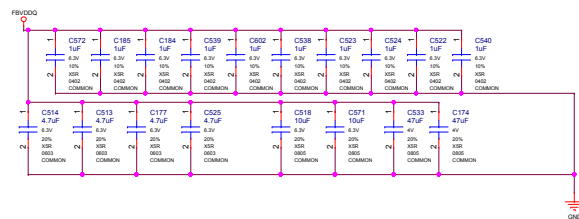
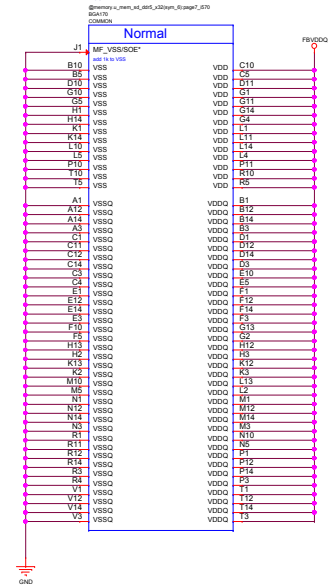
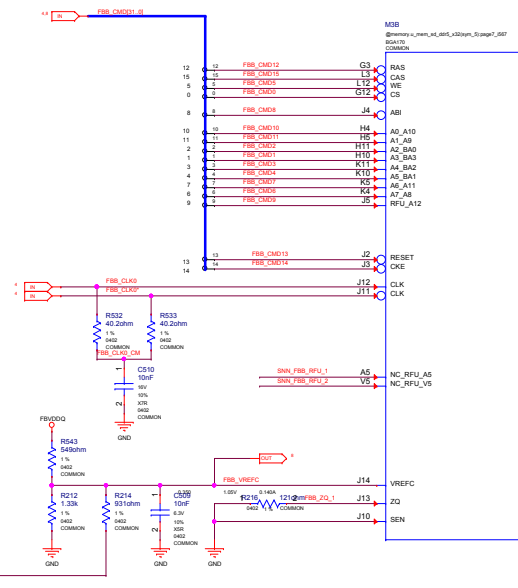
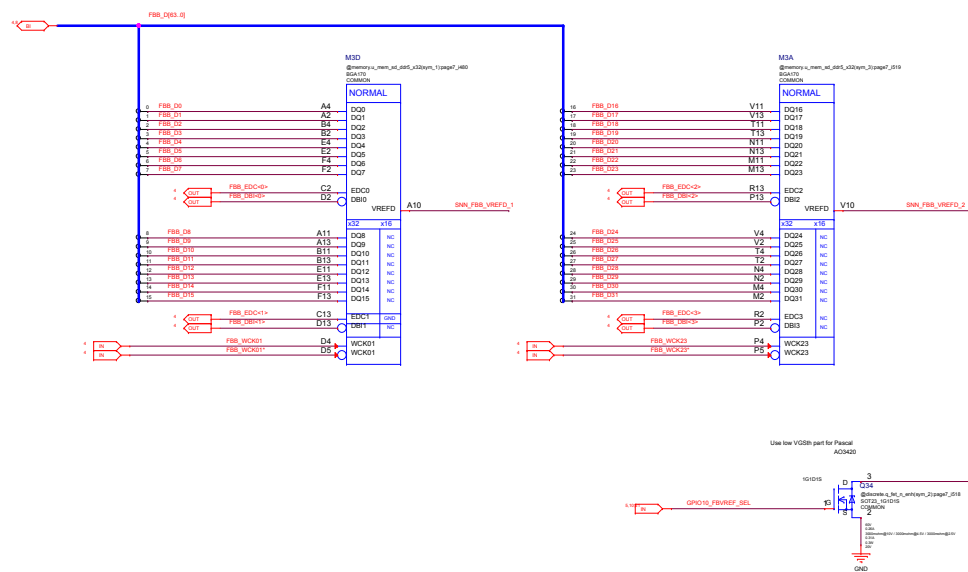
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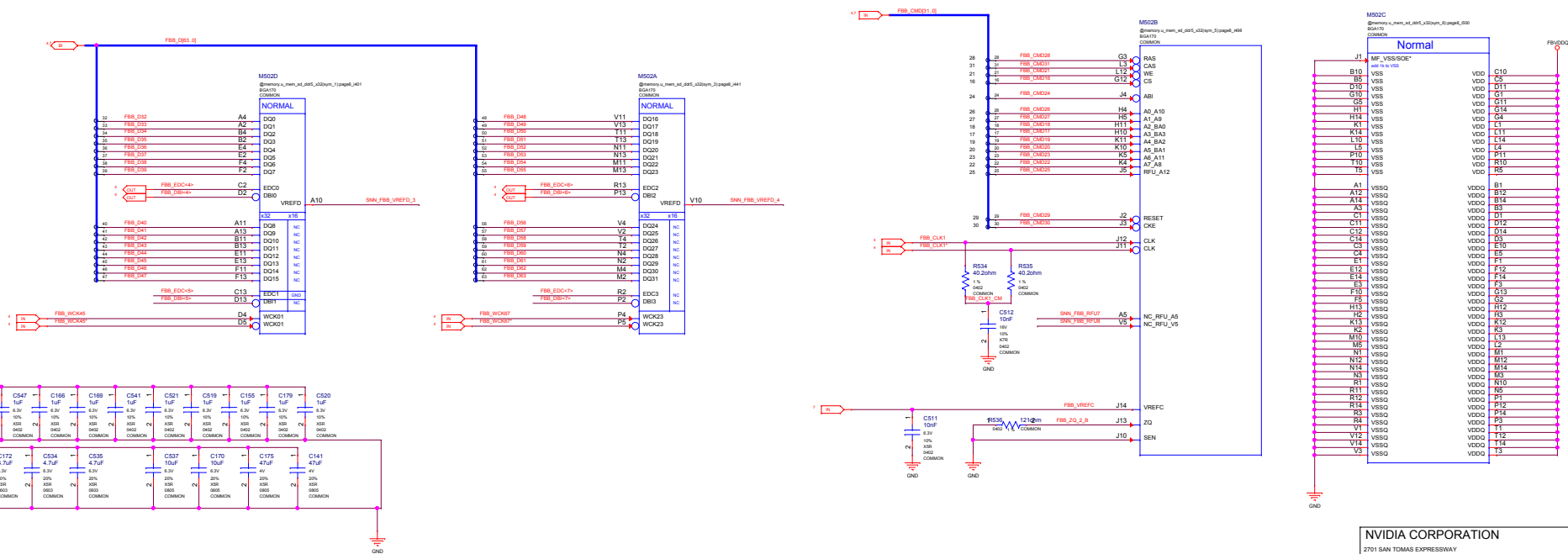


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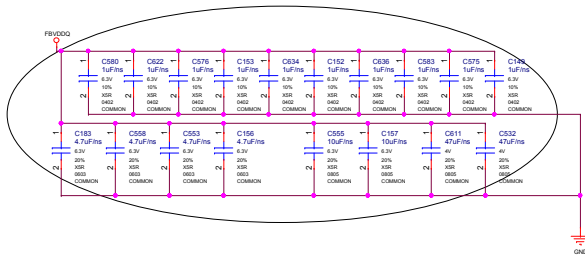
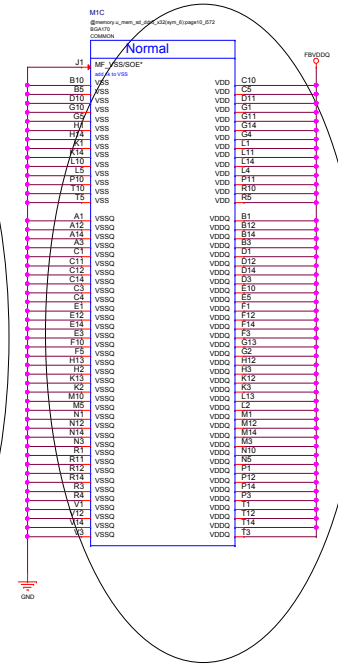
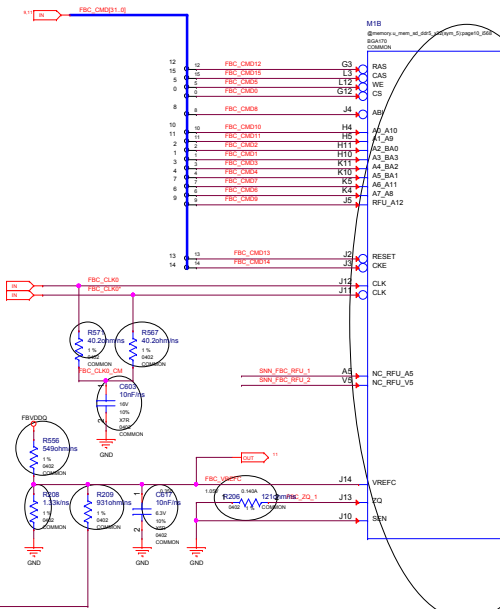
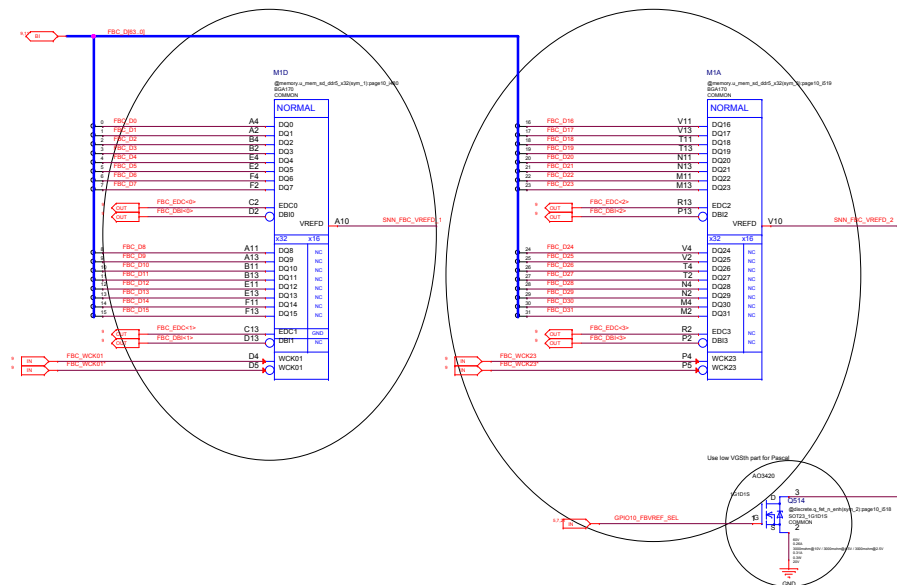
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MEMORY: FBC Partition 31..0



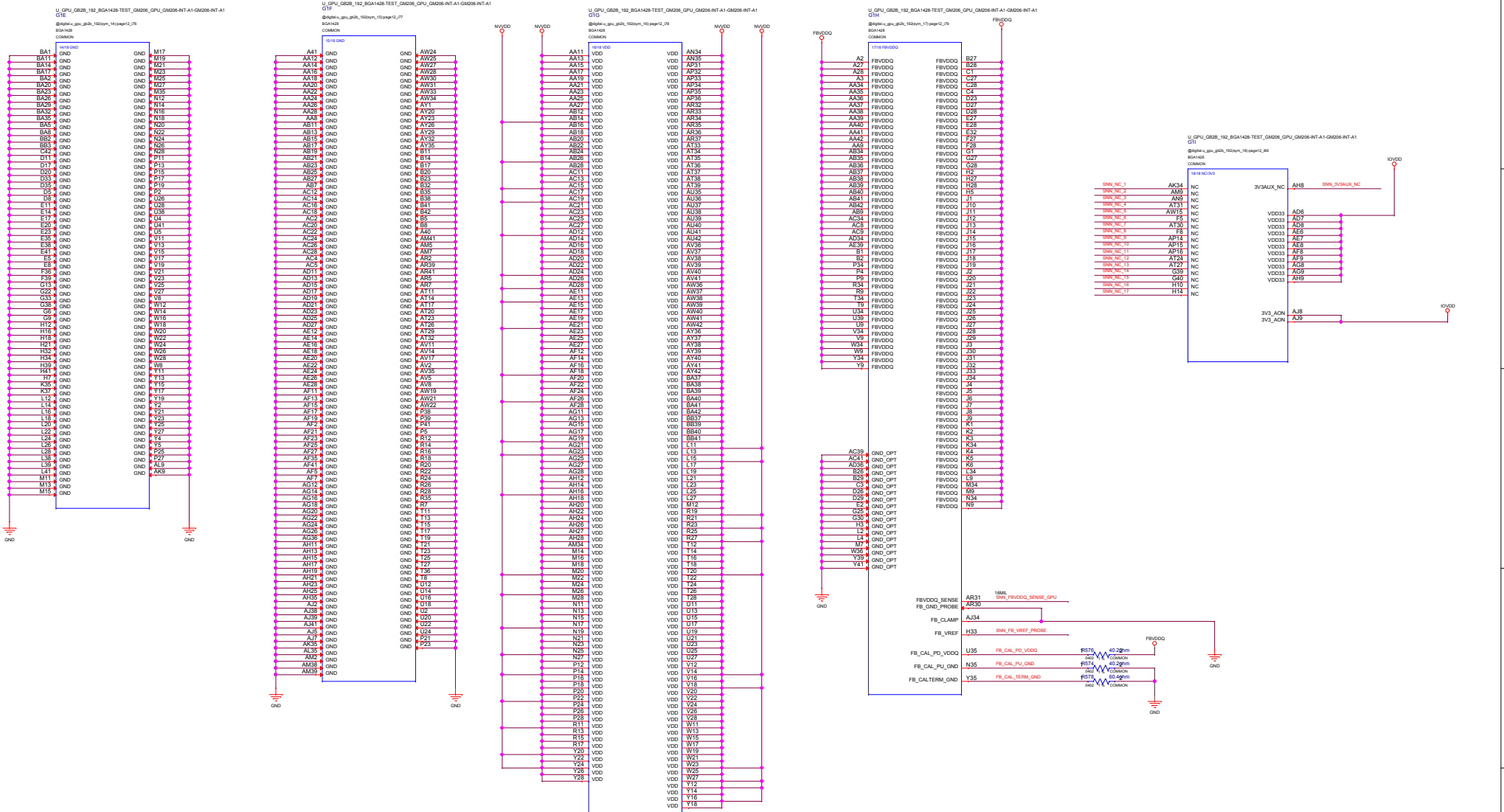
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GPU PWR and GND



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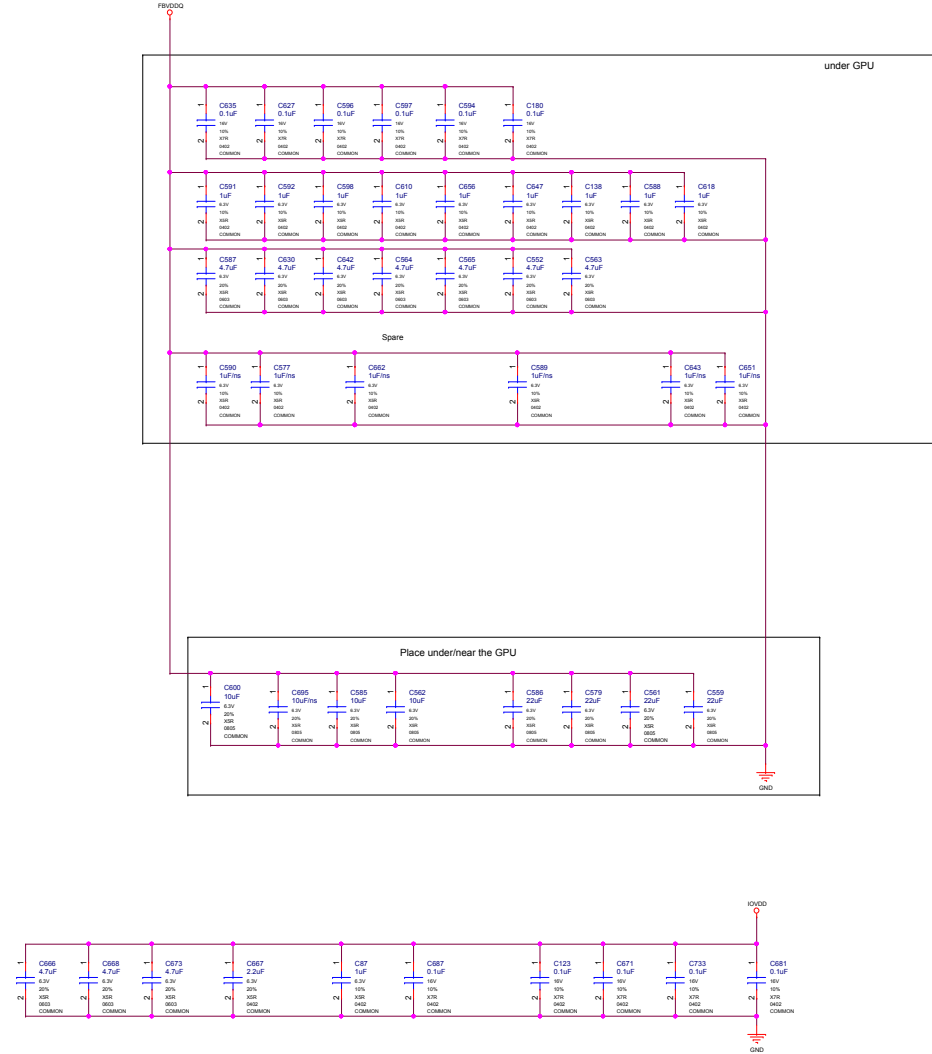
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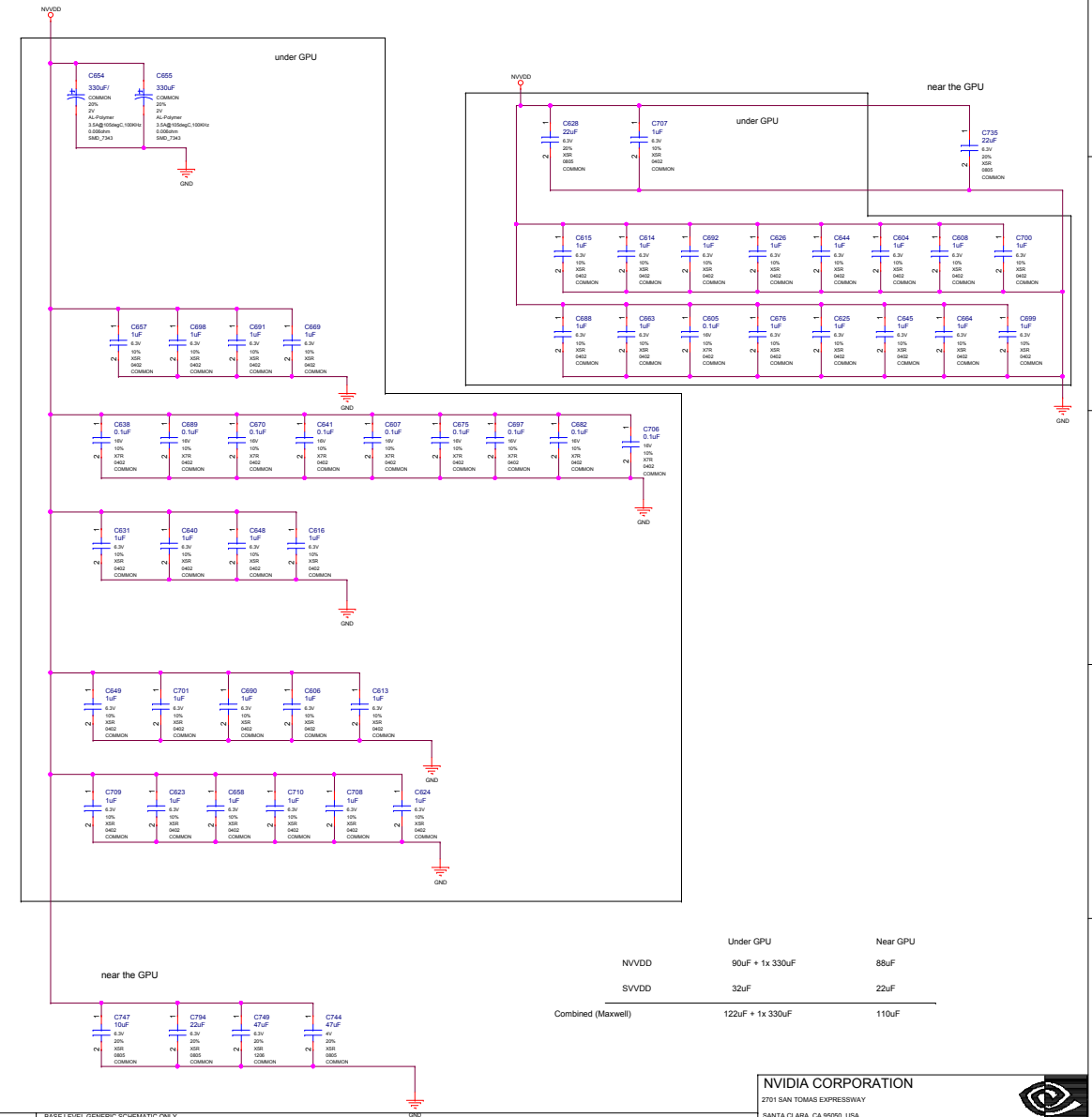
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GPU Decoupling

FBVDDQ



NVVDD



	Under GPU	Near GPU
NVDD	90uF + 1x 330uF	88uF
SVDD	32uF	22uF
Combined (Maxwell)	122uF + 1x 330uF	110uF

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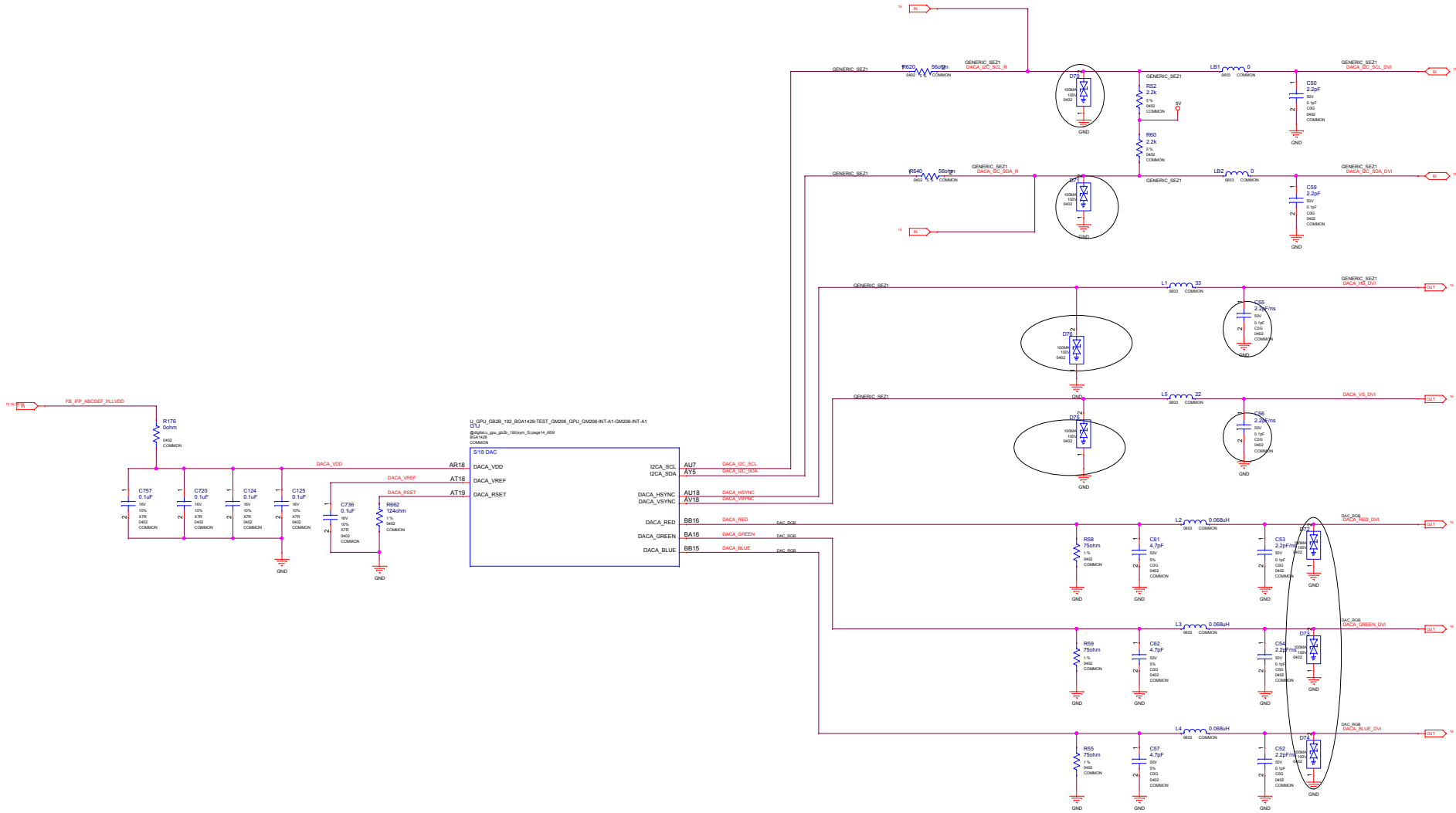
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PAGE DETAIL	GPU Decoupling

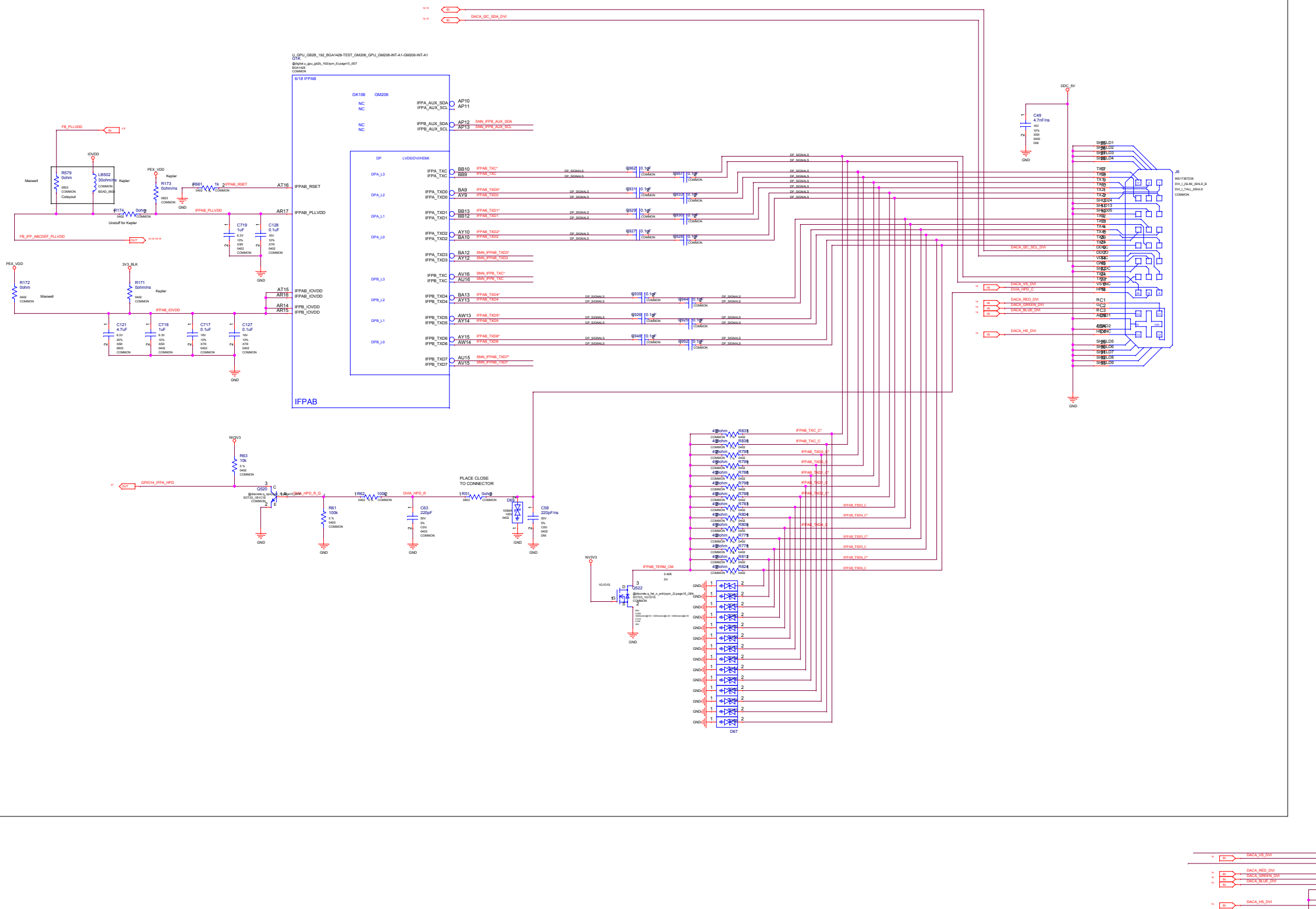
DACA Interface

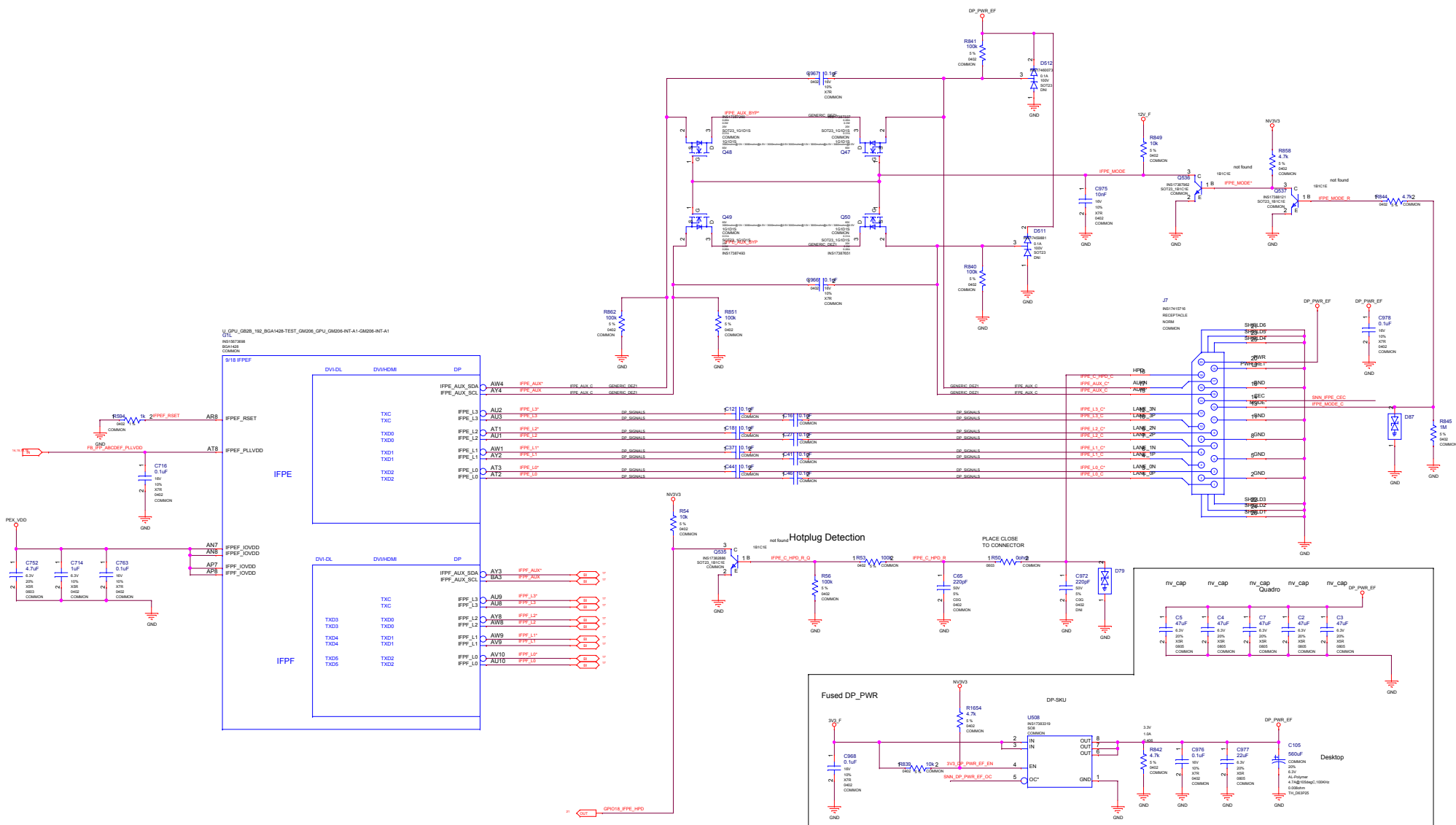


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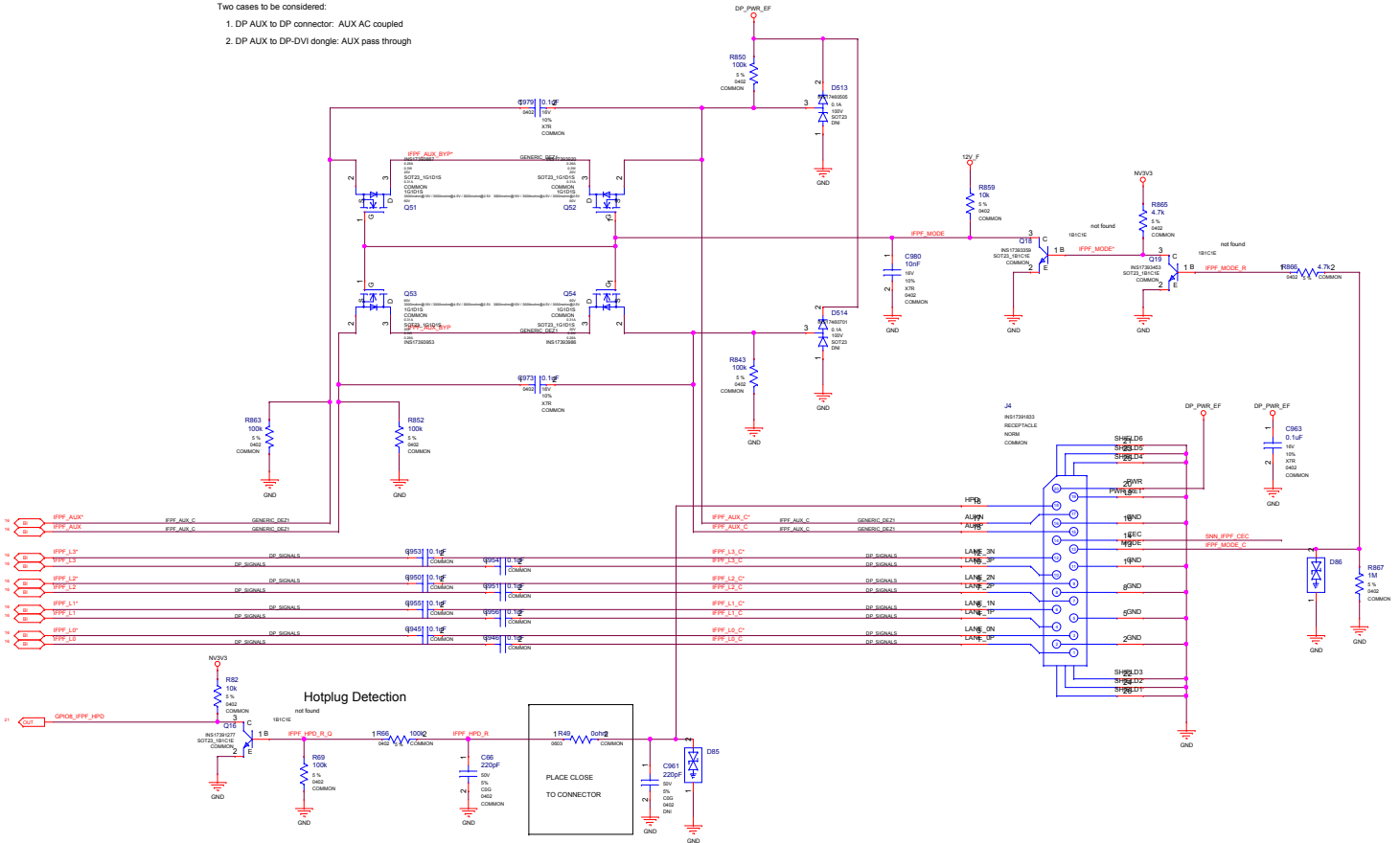
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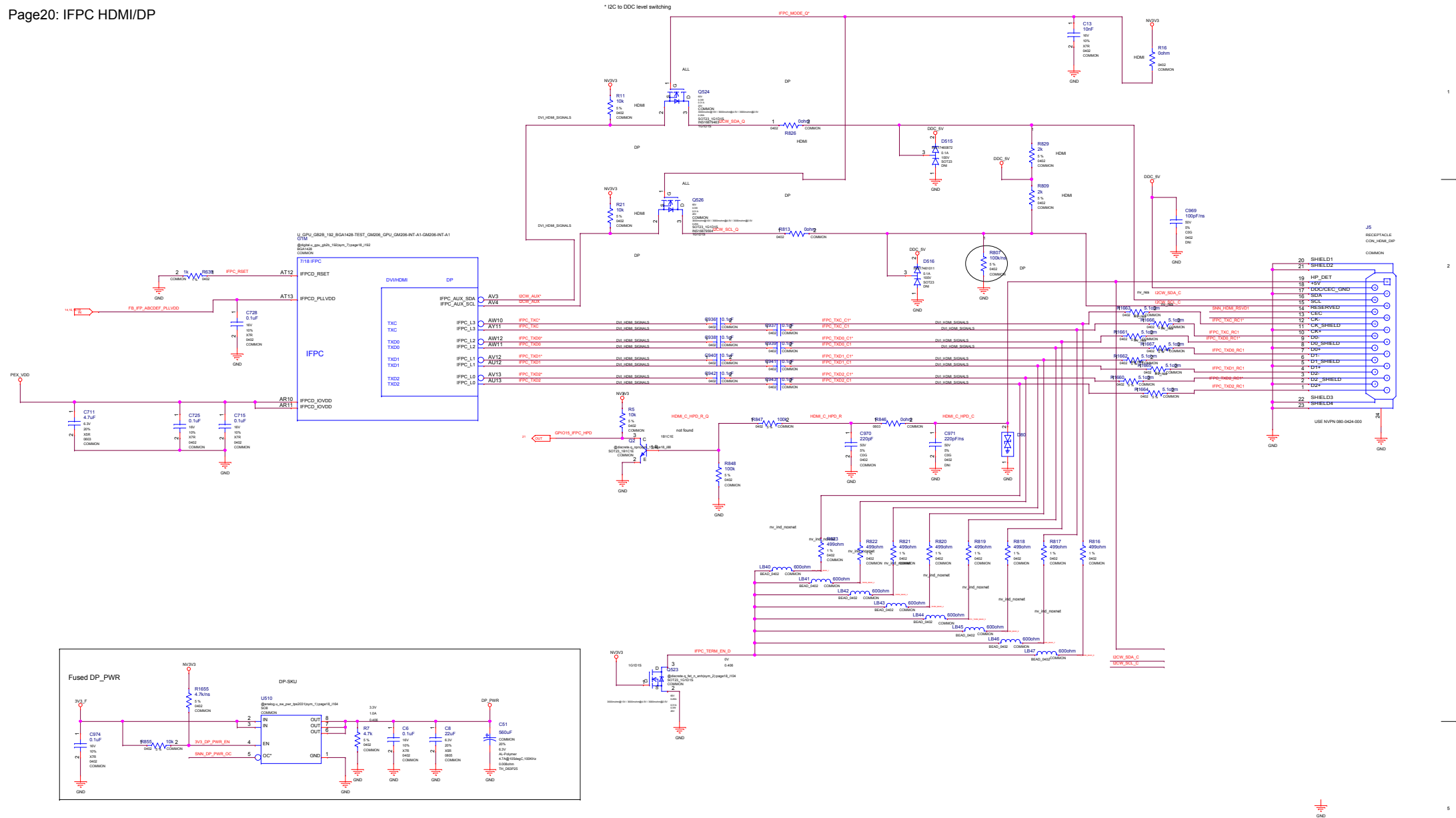




- Two cases to be considered:
1. DP AUX to DP connector: AUX AC coupled
 2. DP AUX to DP-DVI dongle: AUX pass through



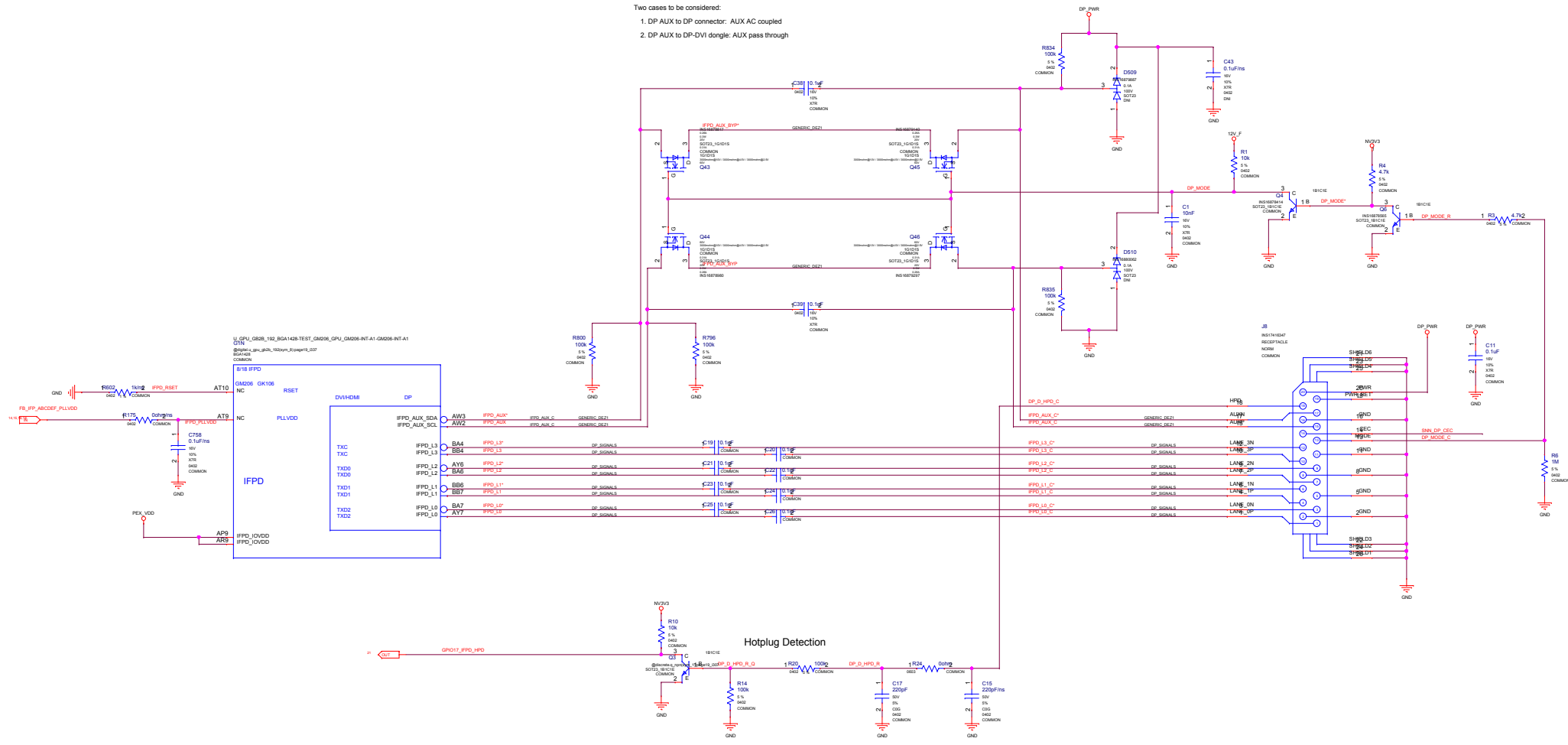
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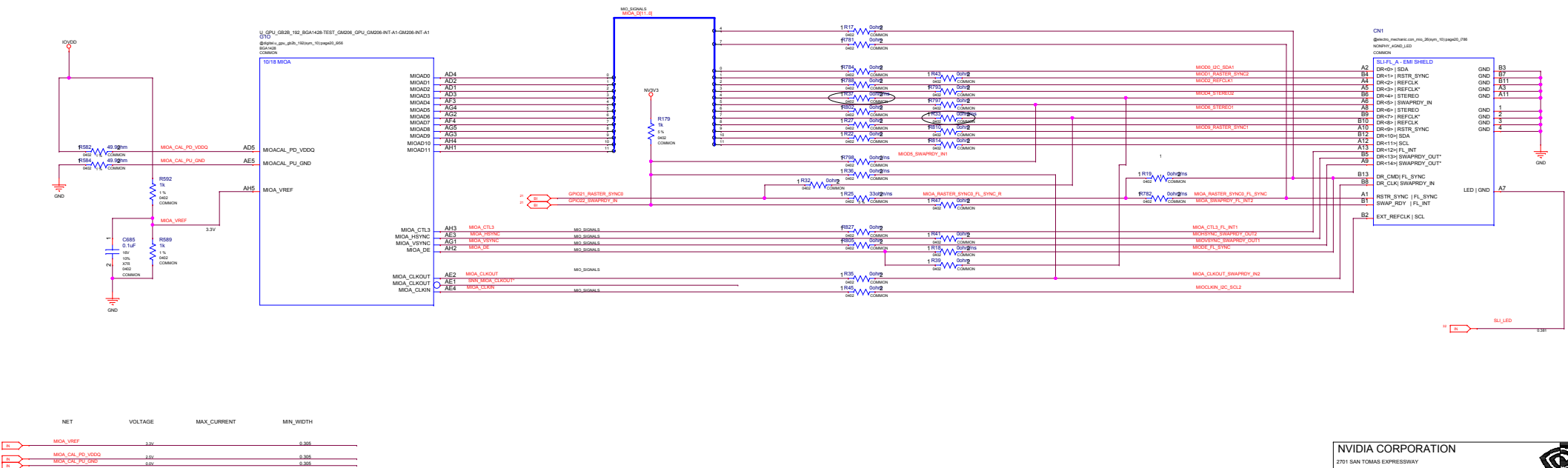
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MIOA/SLI Interface



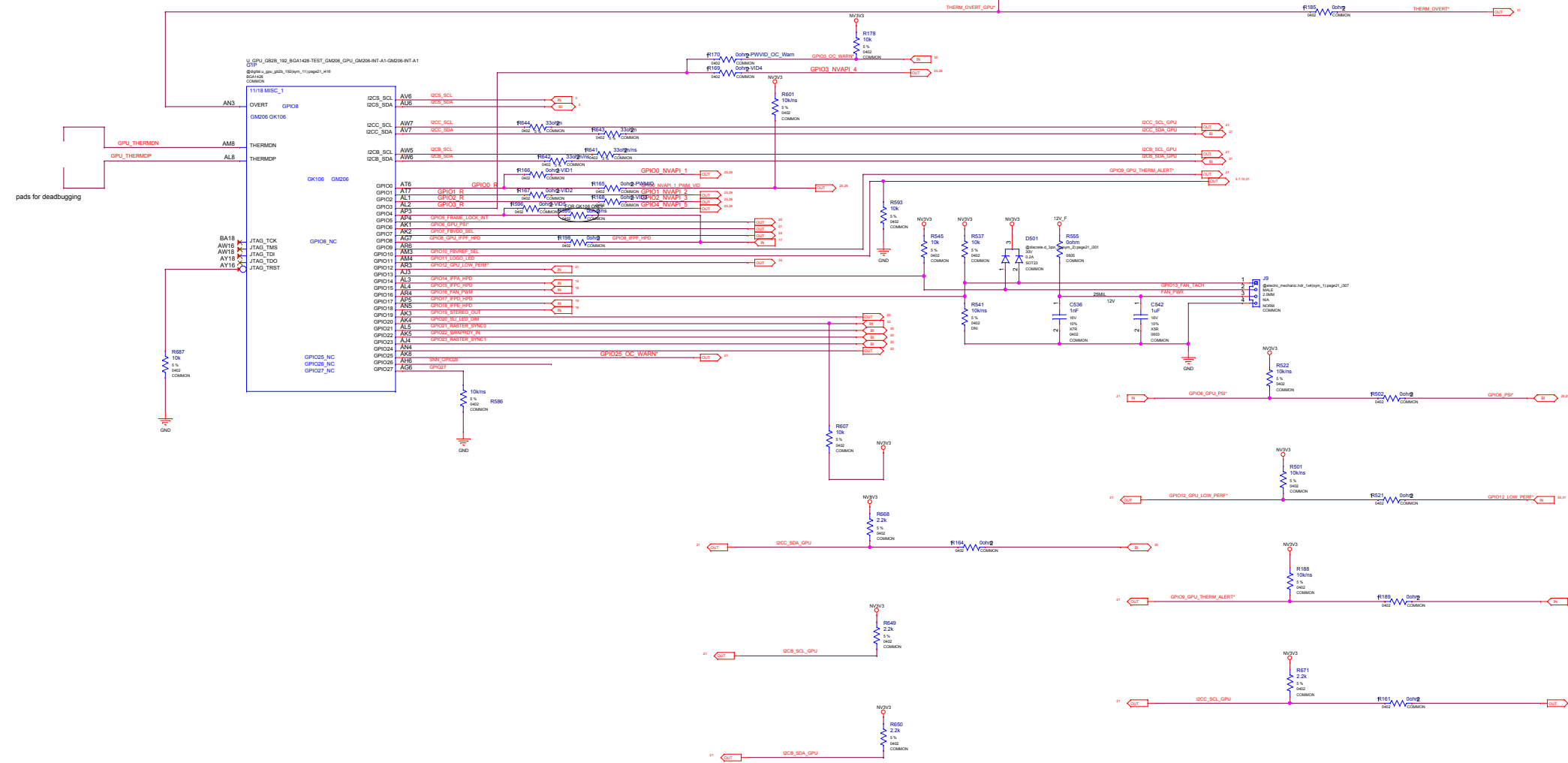
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MISC1: Fan, Thermal, JTAG, GPIO



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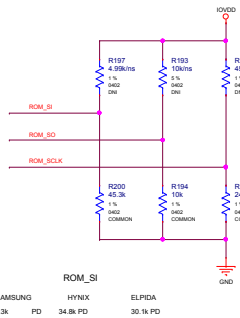
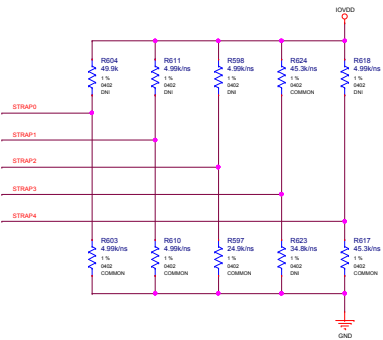
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MISC2: ROM, XTAL, Straps

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STRAP1	3GIO_PADCFG_LUT_ADR*	
STRAP2	PCI_DEVID [3:0]*	
STRAP3	SOR_EXPOSED [3:0]*	
STRAP4	DP_PLL_VDD_33V*	
	PEX_MAX_SPEED*	
	PEX_SPD_CHANGE_GEN3*	
	*	
ROM_SI	RAMCFG[0]*	RAMCFG[0]*
	RAMCFG[1]*	RAMCFG[1]*
	RAMCFG[2]*	RAMCFG[2]*
	RAMCFG[3]*	RAMCFG[3]*
ROM_SO	VGA_DEVICE*	VGA_DEVICE*
	SMB_ALT_ADDR*	SMB_ALT_ADDR*
	FB[0]_APERTURE_SIZE*	PCIE_CFG*
	FB[1]_APERTURE_SIZE*	DEVID_SEL*
	PEX_PLL_EN_TERM100*	SOR0_EXPOSED*
ROM_SCLK	PCI_DEVID_EXT[5]*	SOR1_EXPOSED*
	SUB_VENDOR*	SOR2_EXPOSED*
	PCI_DEVID_EXT[4]*	SOR3_EXPOSED*

MULTI_STRAP_REF0_GND	
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BINARY BRINGUP	NC
MULTI-LEVEL	40.2k 1% TO GND

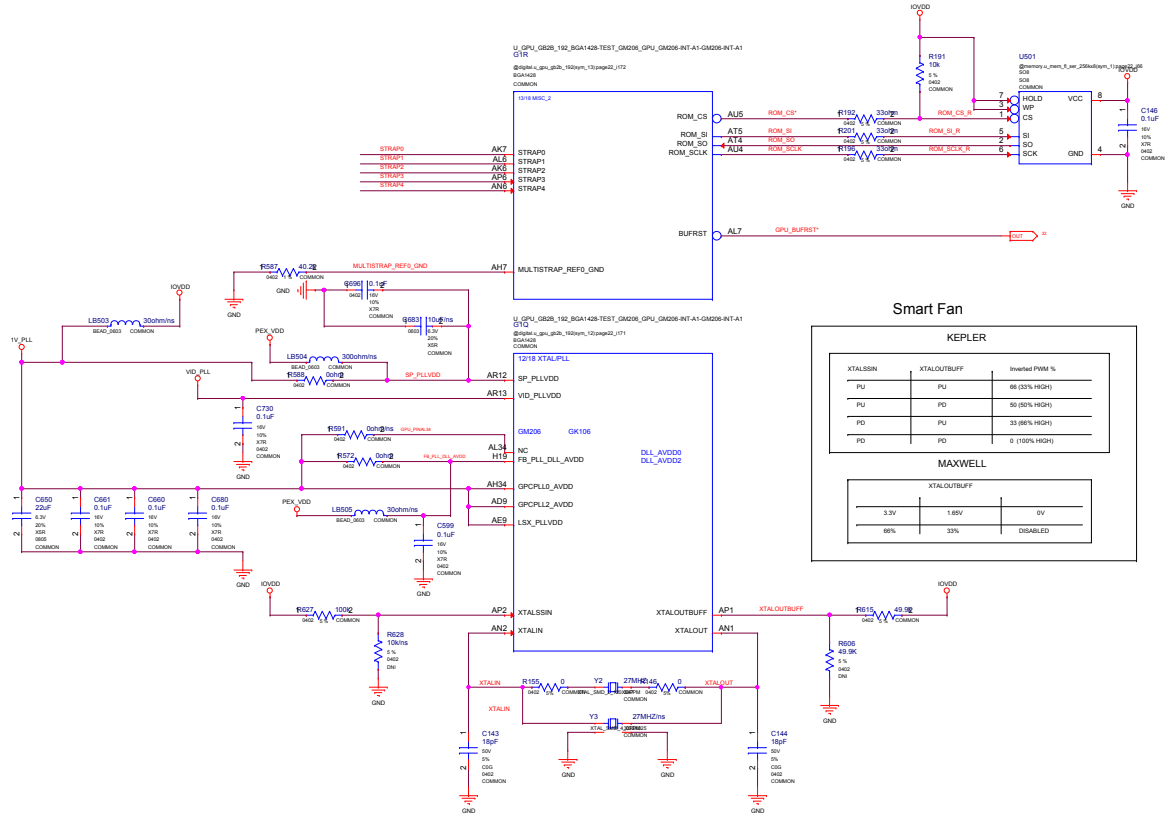
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3.3V	1.65V	0V
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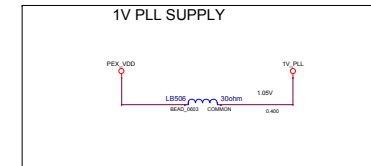
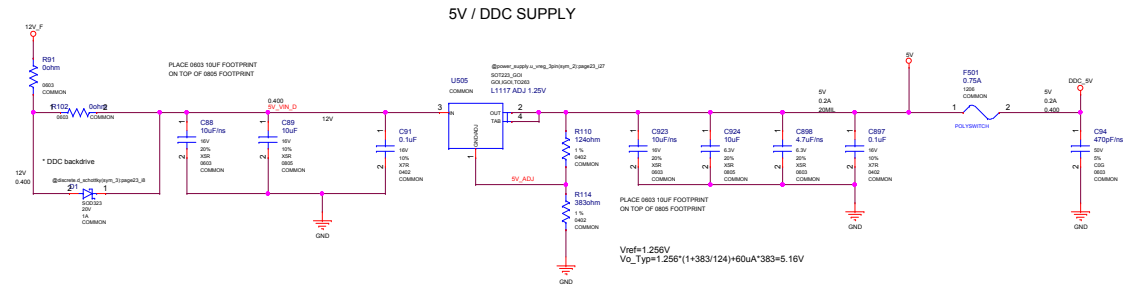
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PAGE DETAIL	MISC2: ROM, XTAL, Straps

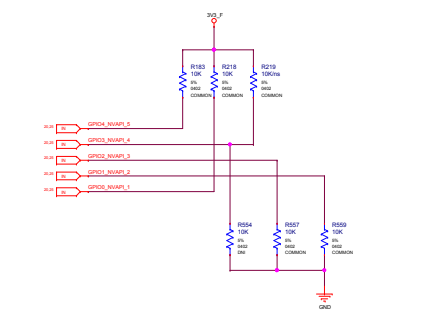
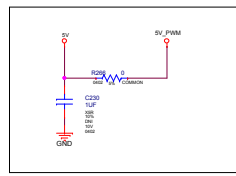
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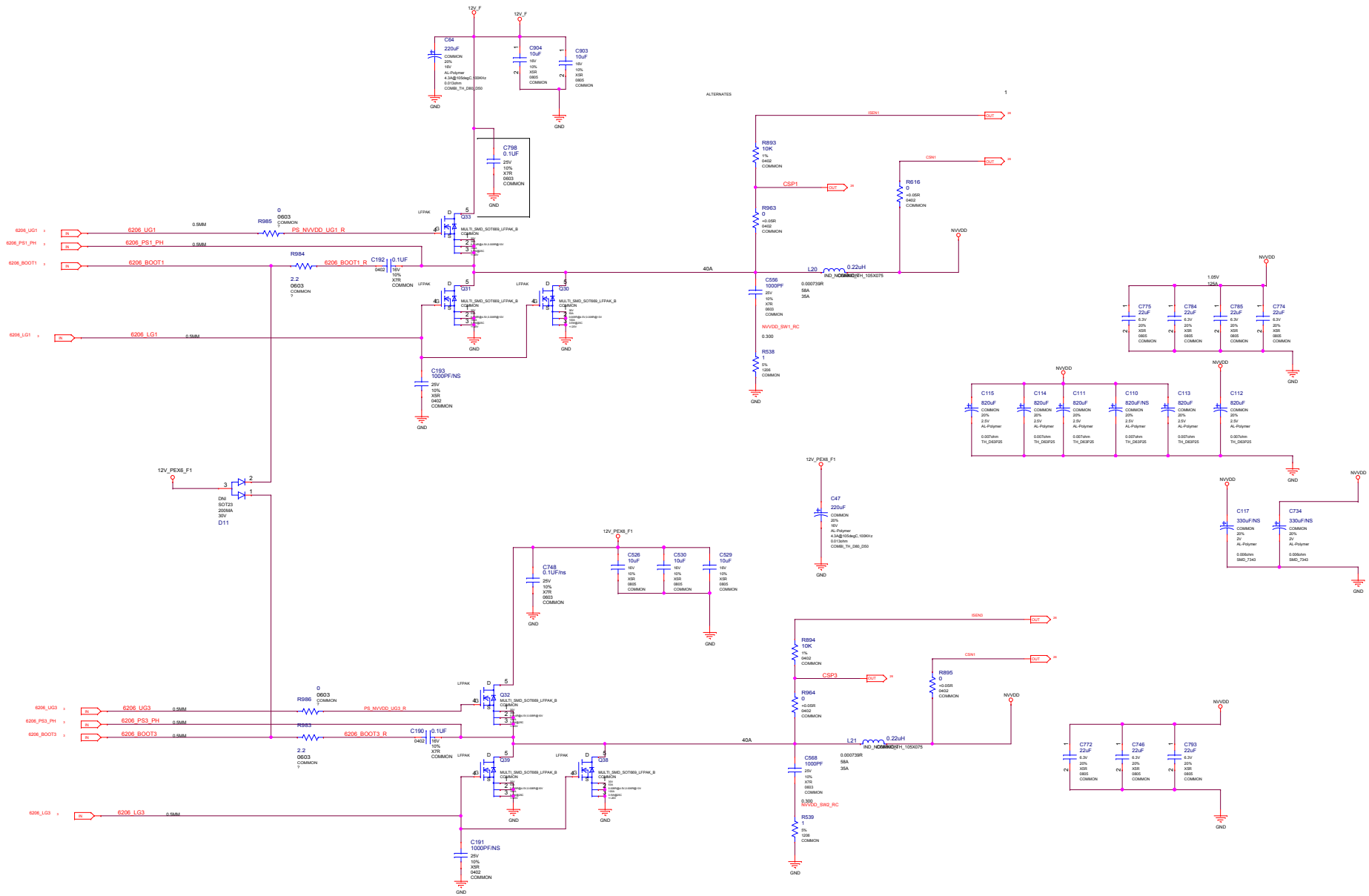
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10k	0001	1001
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20k	0011	1011
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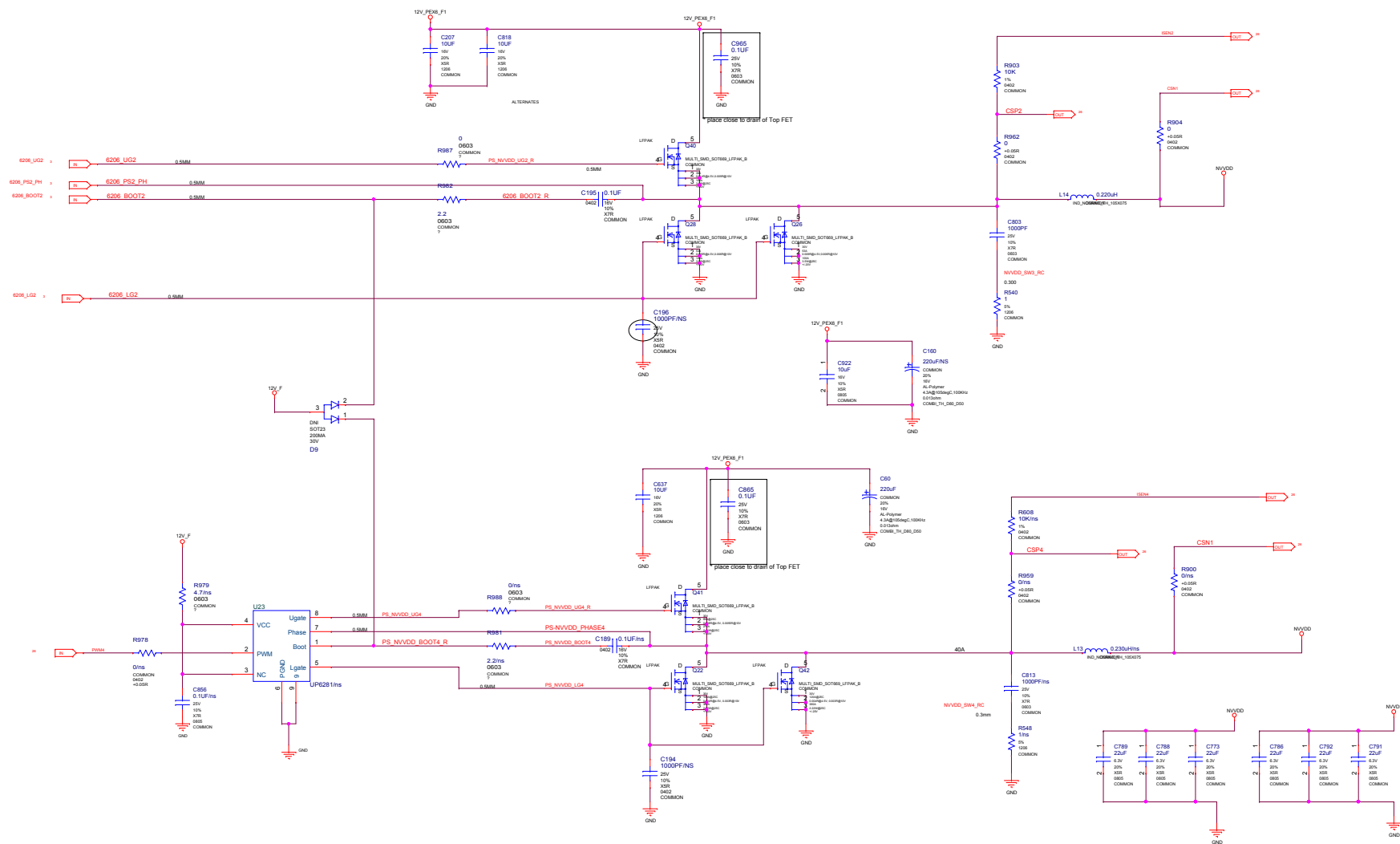


PS: 5V, PEX_VDD



[illegible]





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[RESERVED]

ASSEMBLY

PCB DETAIL

BASE LEVEL GENERIC SCHEMATIC ONLY

[RESERVED]

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PCB REV

BOOM REV

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PG301-AD2

A

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PS: NVVDD OVR2+1 POWER


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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY		
PAGE DETAIL	PS: NVVDD OVR2+1 option		

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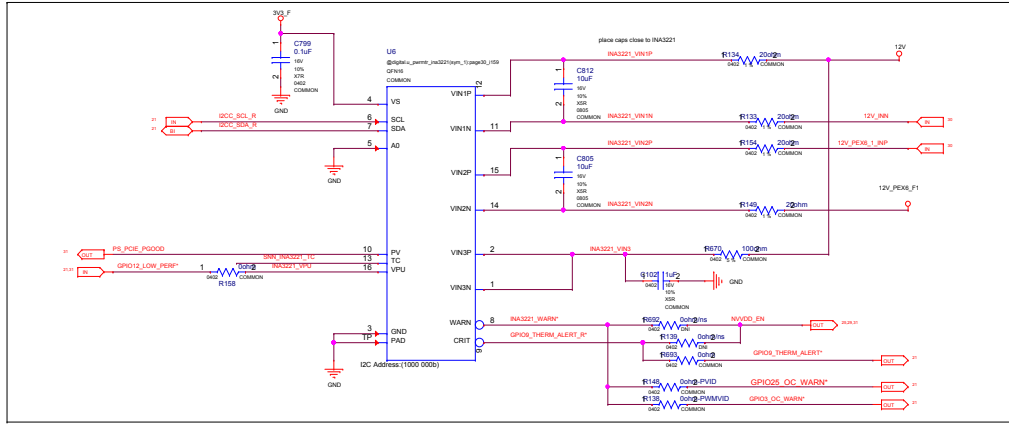
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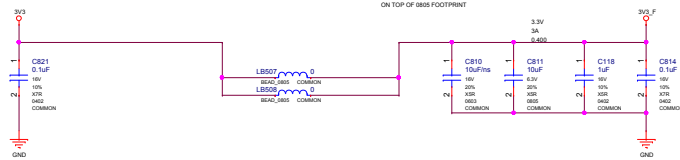


NV_PN	600-1G301-BASE-200		
PCB REV	PG301-A02	PAGE	29 OF 34
BCM REV	A	DATE	09/OCT/2014

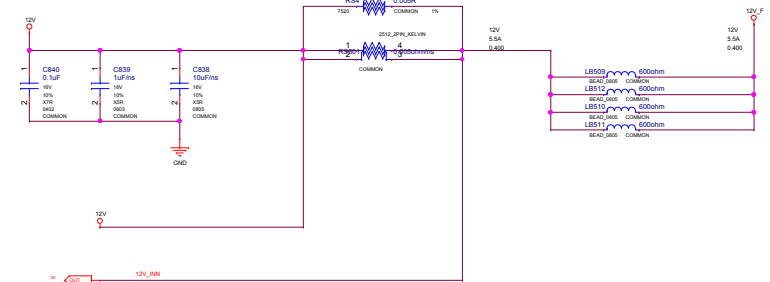
PS: Inputs, Filtering, and Monitoring



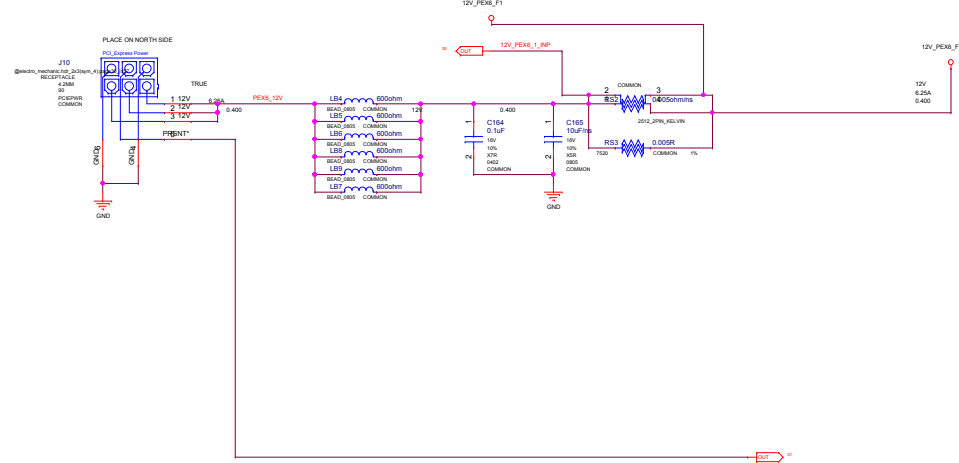
PEX 3V3 INPUT - 10W



PEX_12V INPUT - 66W



PEX6 INPUT - 2x3 PCIe CON 75W



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PAGE DETAIL	PS: Inputs, Filtering, and Monitoring

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NV_PN 600-1G301-BASE-200

PCB REV P0301A02

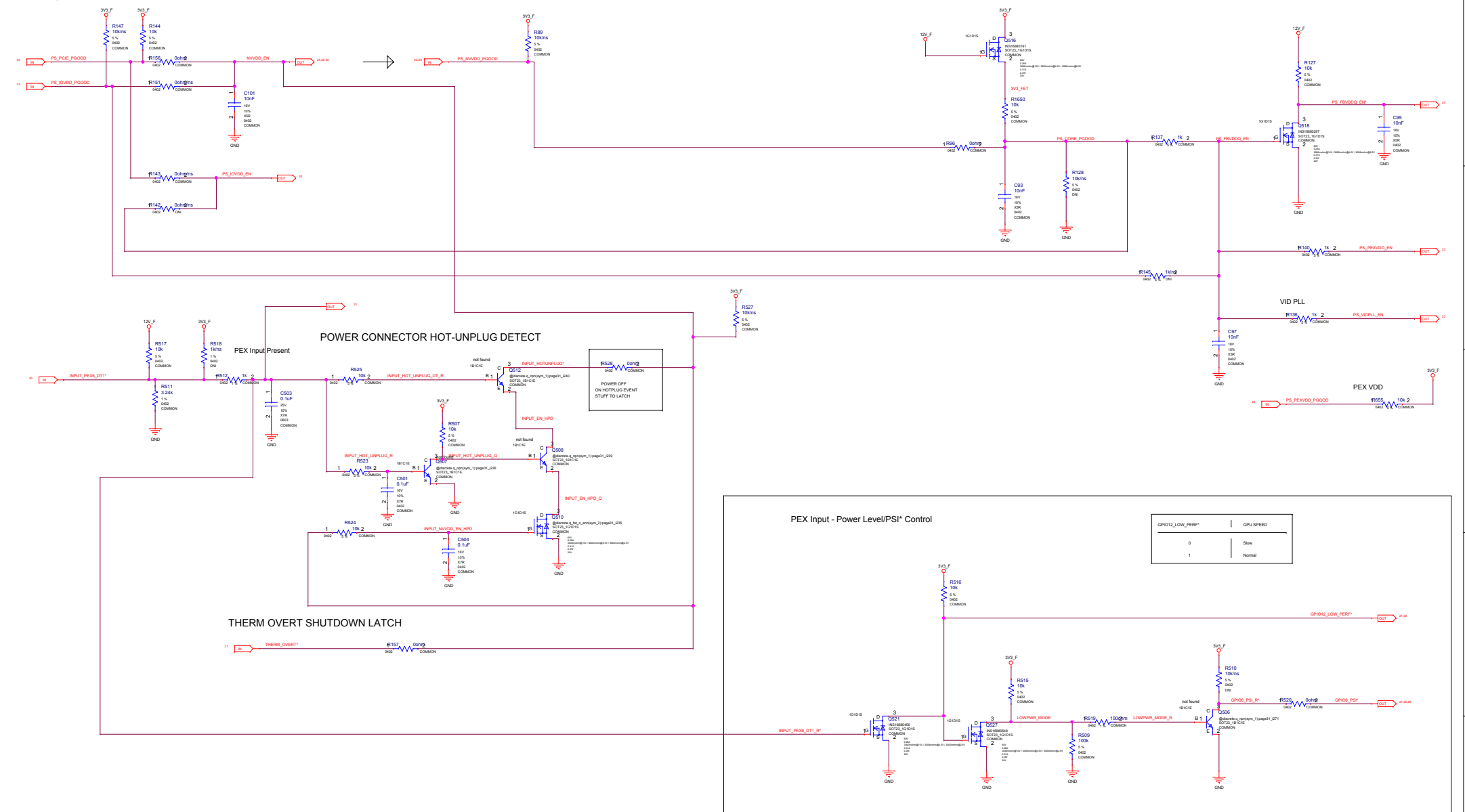
BOM REV A

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PS: Sequence and Shutdown



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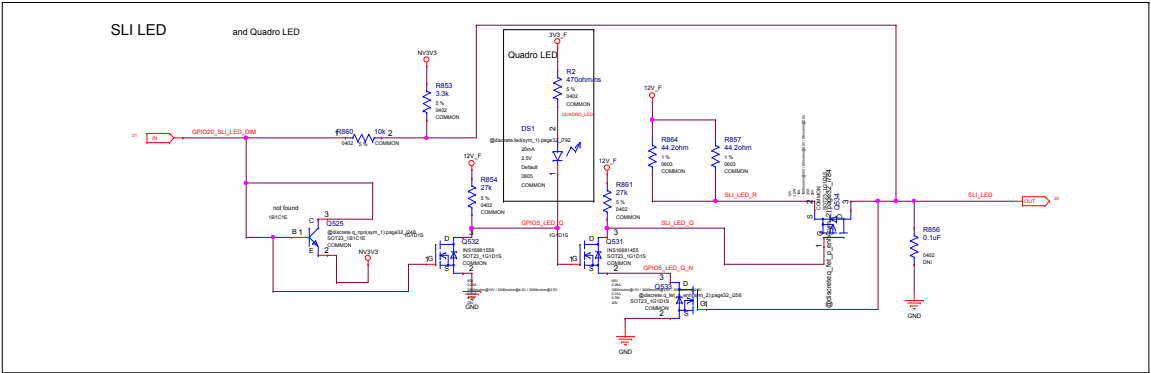
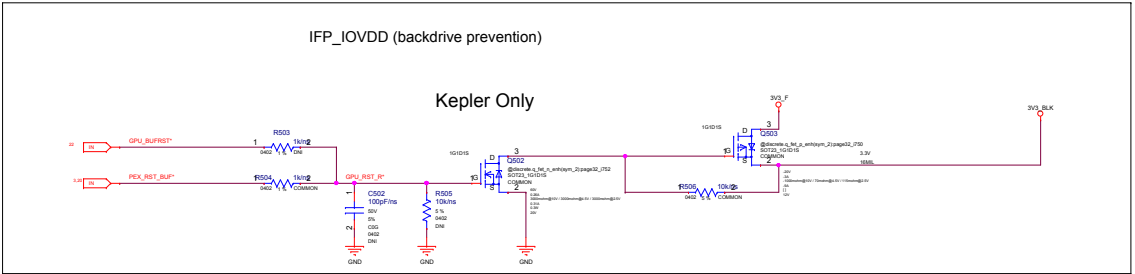


ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	PS: Sequence and Shutdown

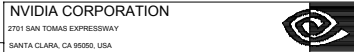
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LEDs

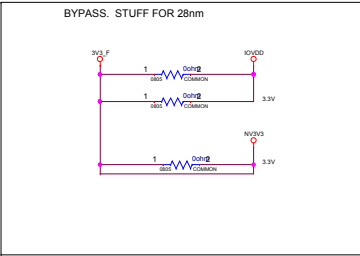


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PS: IOVDD, NV3V3, NV12V

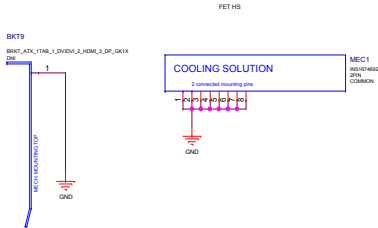


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Mechanical: Bracket/Thermal Solution

Brackets:



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