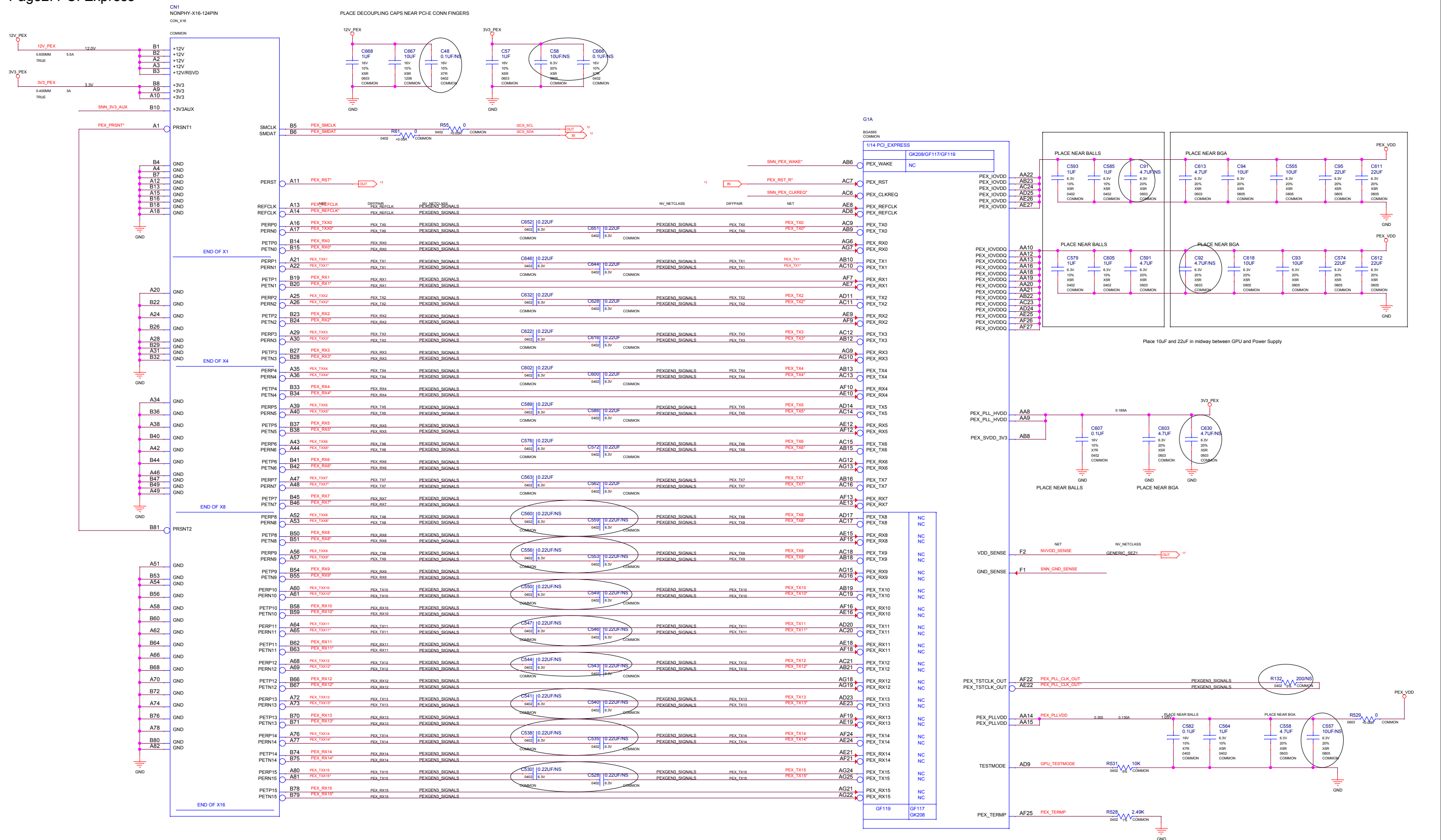
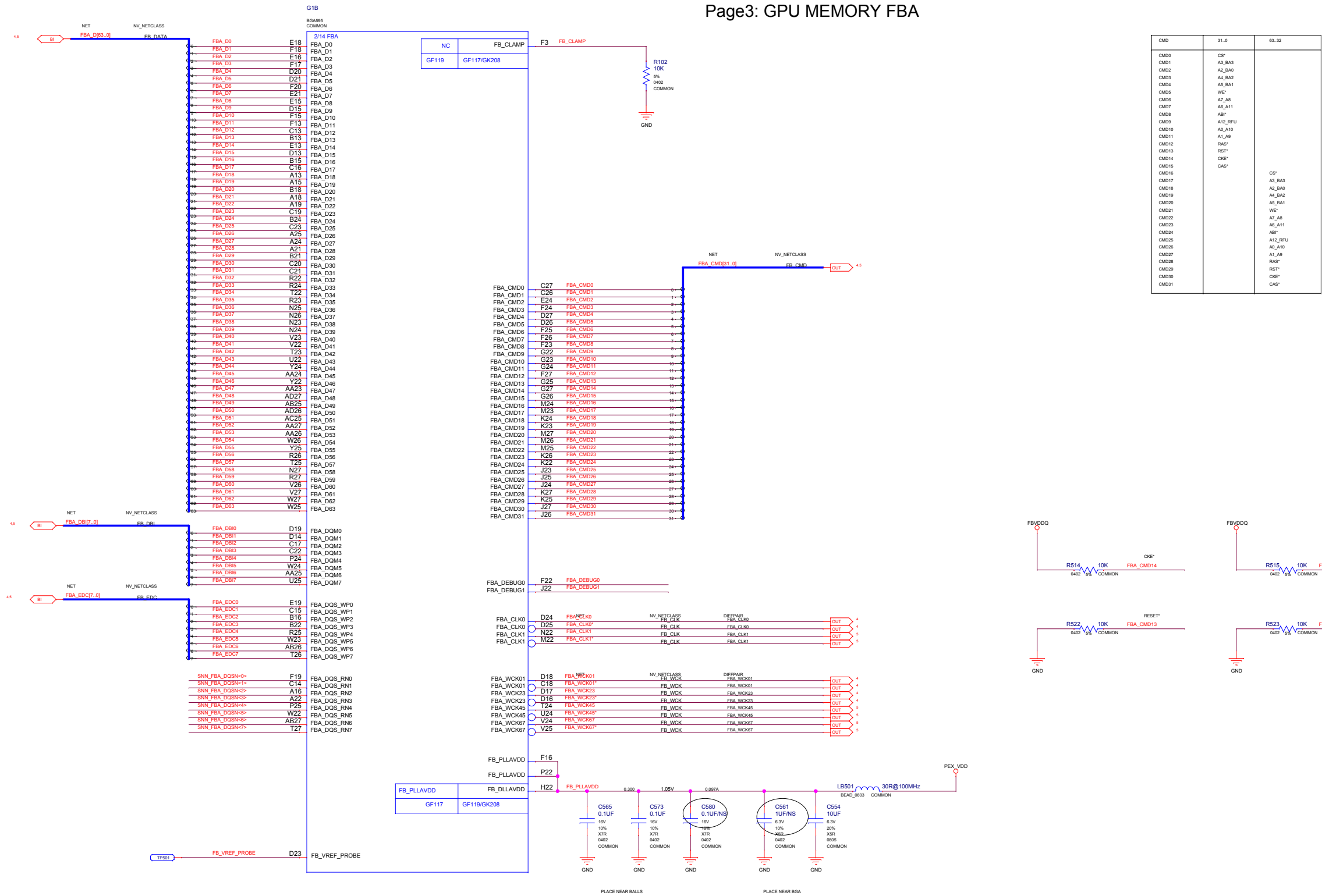


P2131_A00

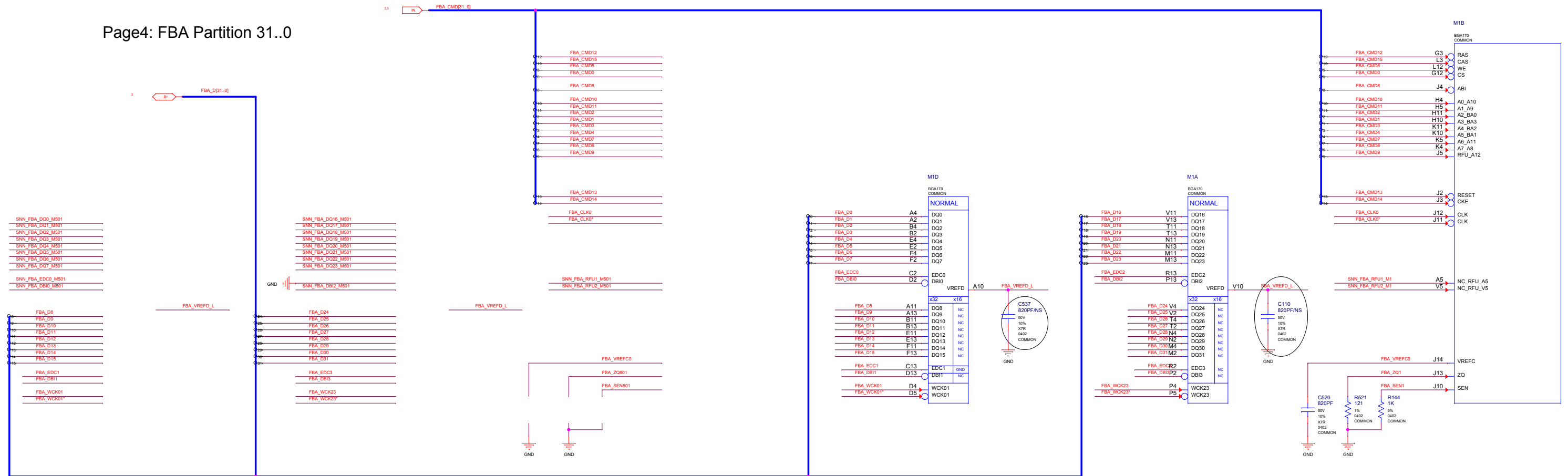
GK208 GDDR5 X16 64BIT
DVI-I/DVI-D/SlimVGA + HDMI/DP + VGA

1)R150 Place inside U503

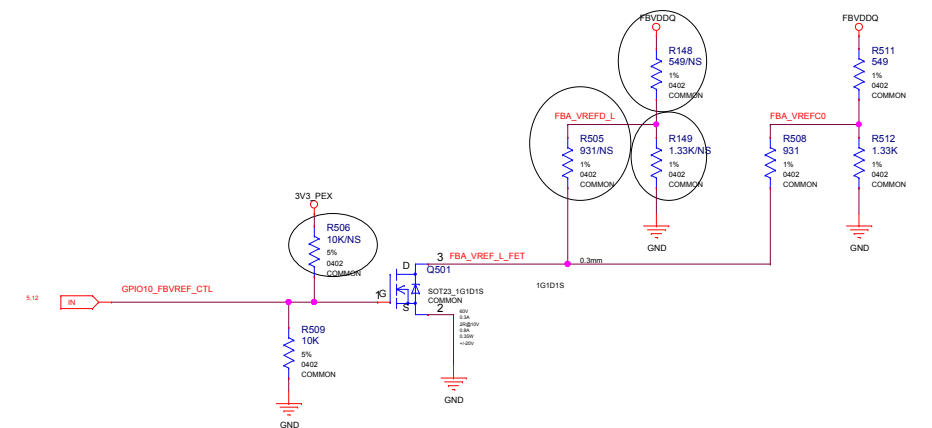
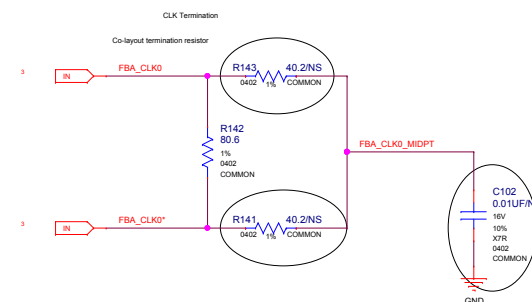
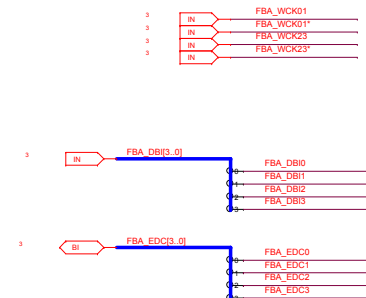


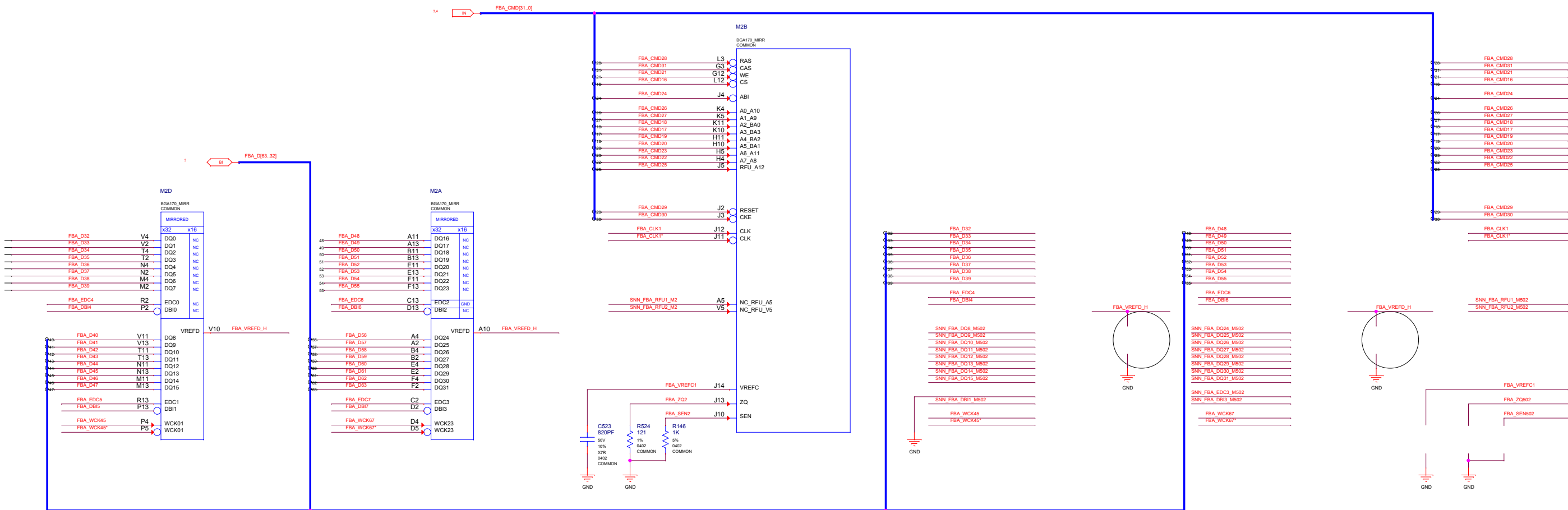


Page4: FBA Partition 31..0

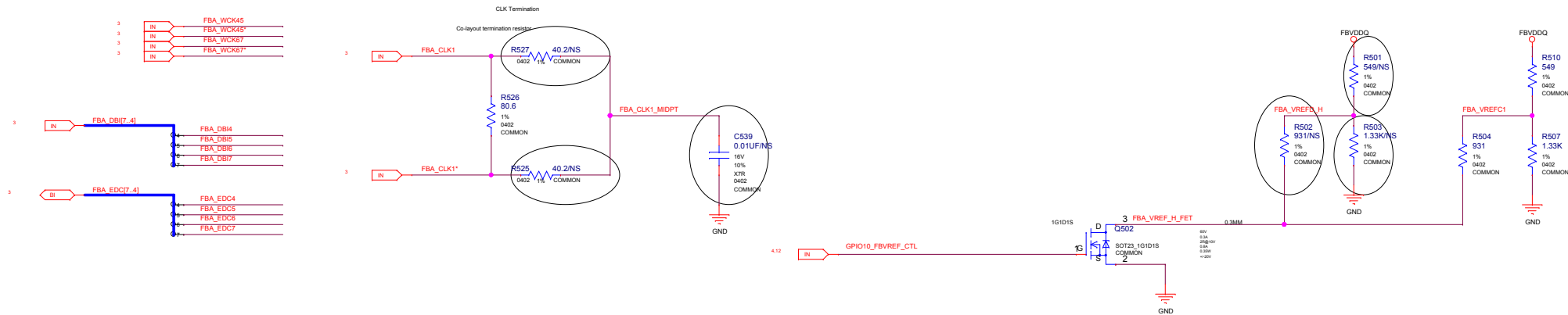


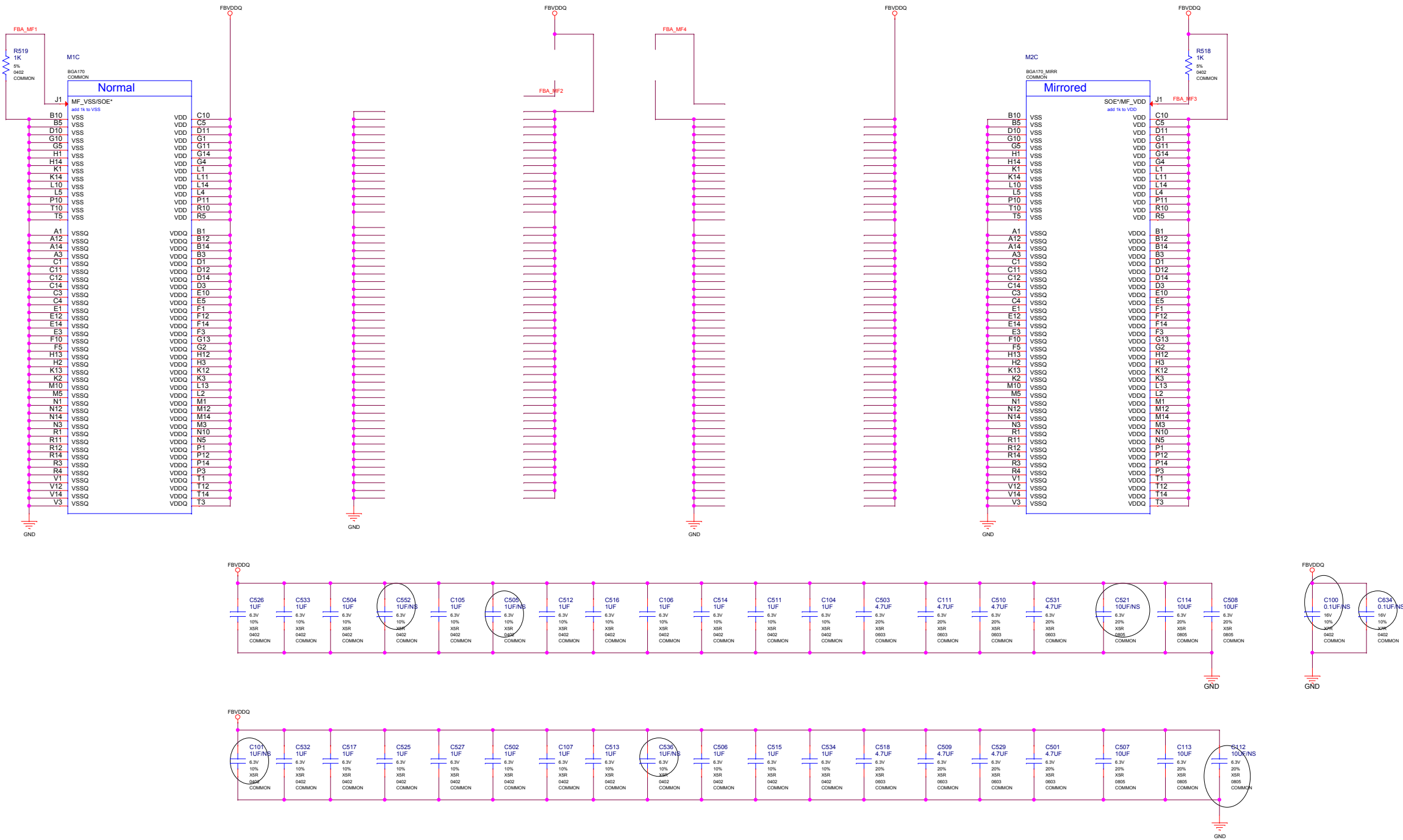
CMO	31.0	83.32
CM00	C5*	
CM01	A3_BA3	
CM02	A2_BA0	
CM03	A4_BA2	
CM04	A0_BA1	
CM05	W6*	
CM06	A7_A8	
CM07	A6_A11	
CM08	AB1*	
CM09	A12_RFU	
CM10	A0_A10	
CM11	A1_A9	
CM12	RA5*	
CM13	RS1*	
CM14	CKE*	
CM15	CAS*	
CM16		C0*
CM17		A3_BA3
CM18		A2_BA0
CM19		A4_BA2
CM20		A0_BA1
CM21		W6*
CM22		A7_A8
CM23		A6_A11
CM24		AB1*
CM25		A12_RFU
CM26		A0_A10
CM27		A1_A9
CM28		RA5*
CM29		RS1*
CM30		CKE*
CM31		CAS*

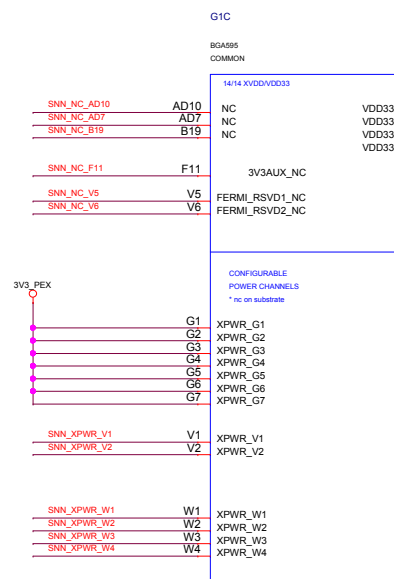
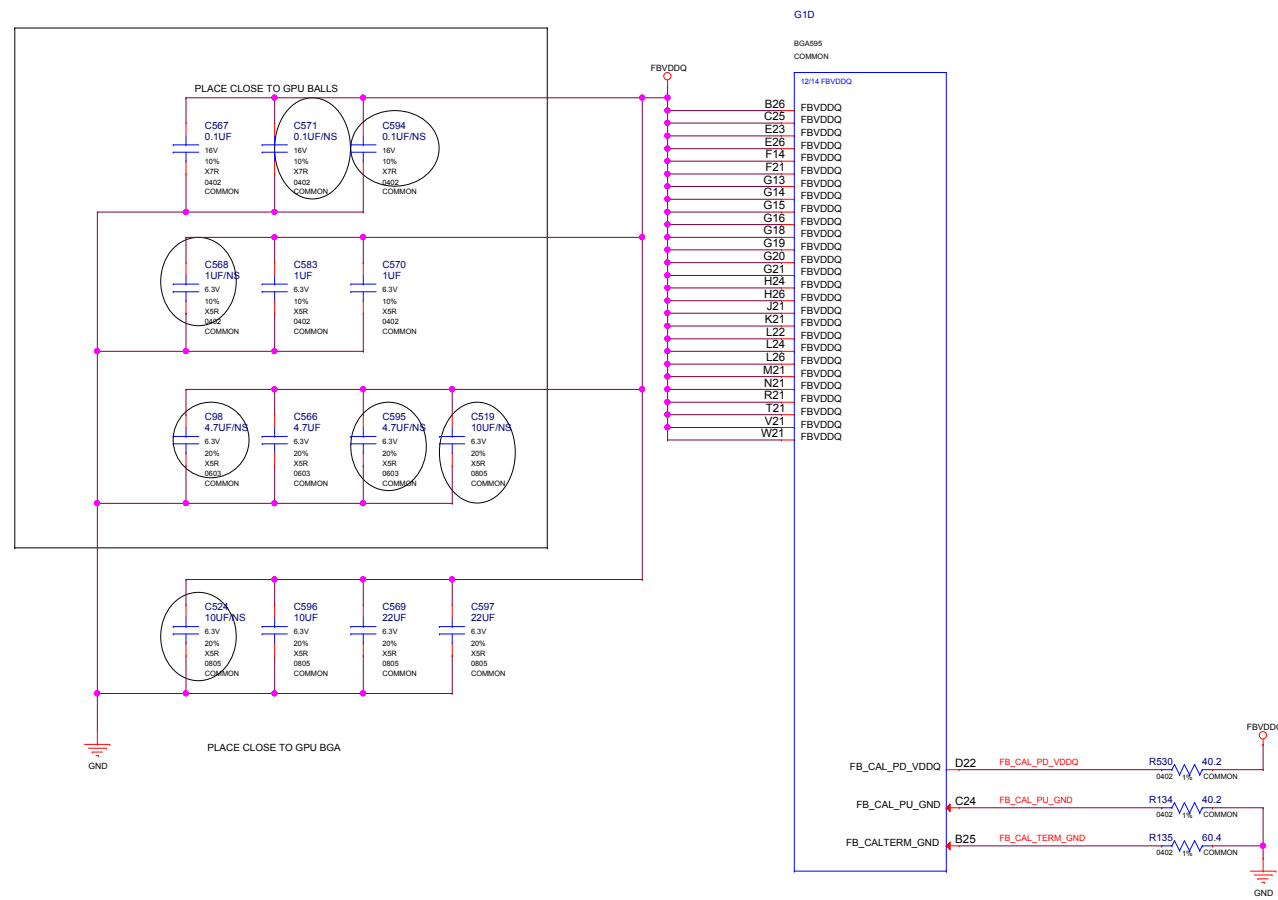




CMD	31..0	63..32
CMD0	CS*	
CMD1	A3_BA3	
CMD2	A2_BA0	
CMD3	A4_BA2	
CMD4	A5_BA1	
CMD5	WE*	
CMD6	A7_A8	
CMD7	A6_A11	
CMD8	AB*	
CMD9	A12_RFU	
CMD10	A0_A10	
CMD11	A1_A9	
CMD12	RAS*	
CMD13	RST*	
CMD14	CKE*	
CMD15	CAS*	
CMD16		CS*
CMD17		A3_BA3
CMD18		A2_BA0
CMD19		A4_BA2
CMD20		A5_BA1
CMD21		WE*
CMD22		A7_A8
CMD23		A6_A11
CMD24		AB*
CMD25		A12_RFU
CMD26		A0_A10
CMD27		A1_A9
CMD28		RAS*
CMD29		RST*
CMD30		CKE*
CMD31		CAS*







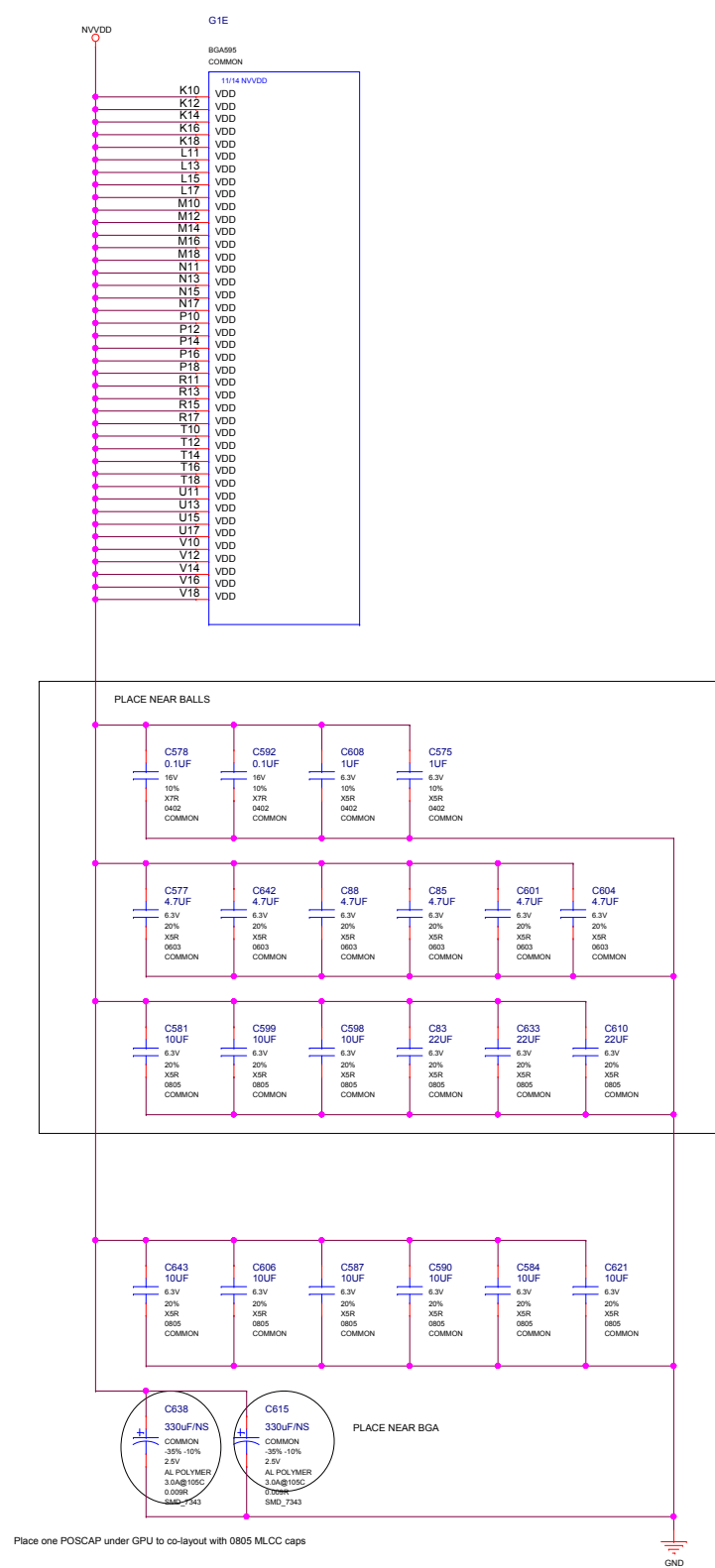
** XPWR pins are configurable.

These pins are not connected on the substrate.

Therefore, XPWR pins can be assigned as needed,

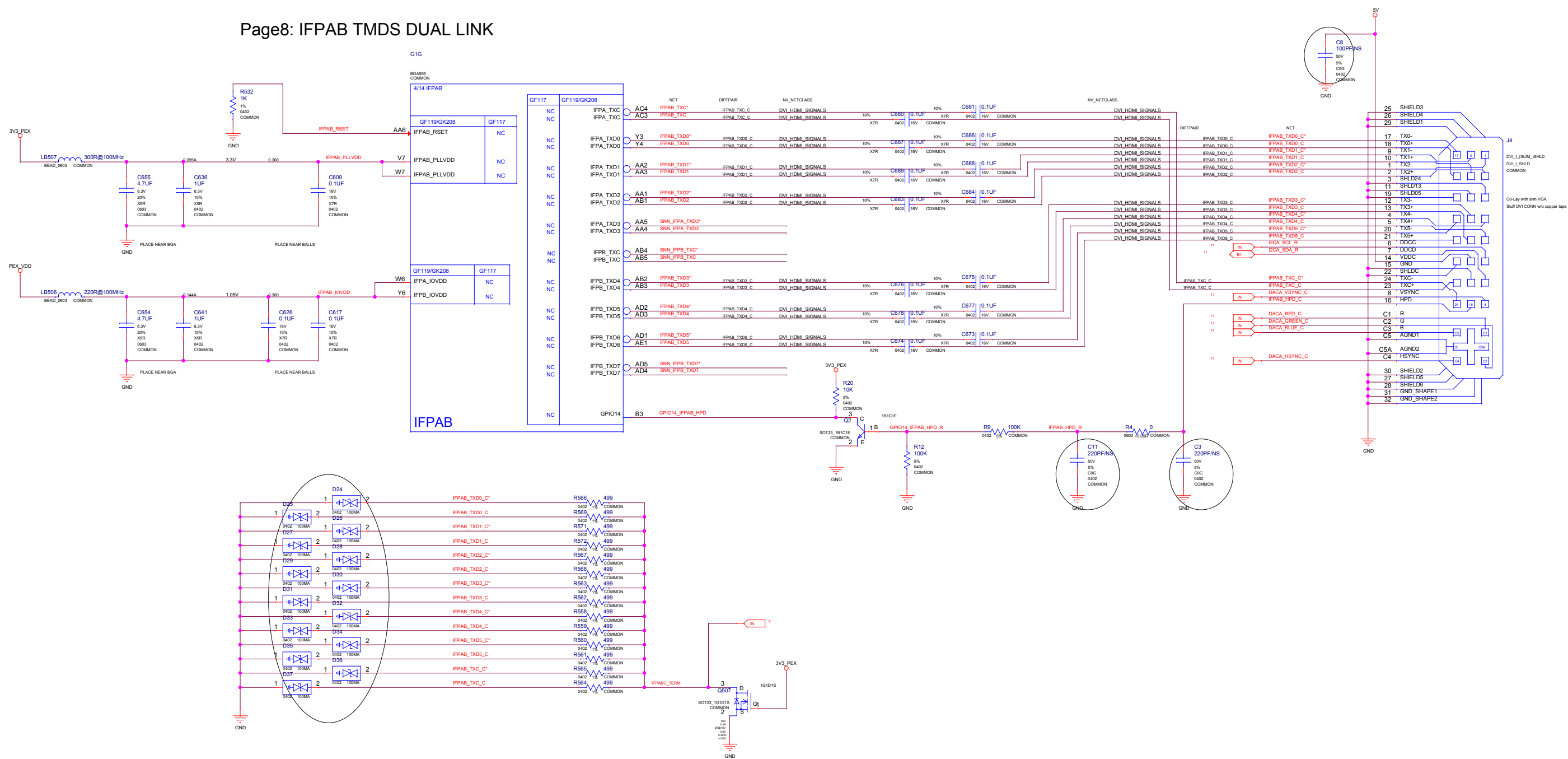
to improve Top layer routing, power delivery.

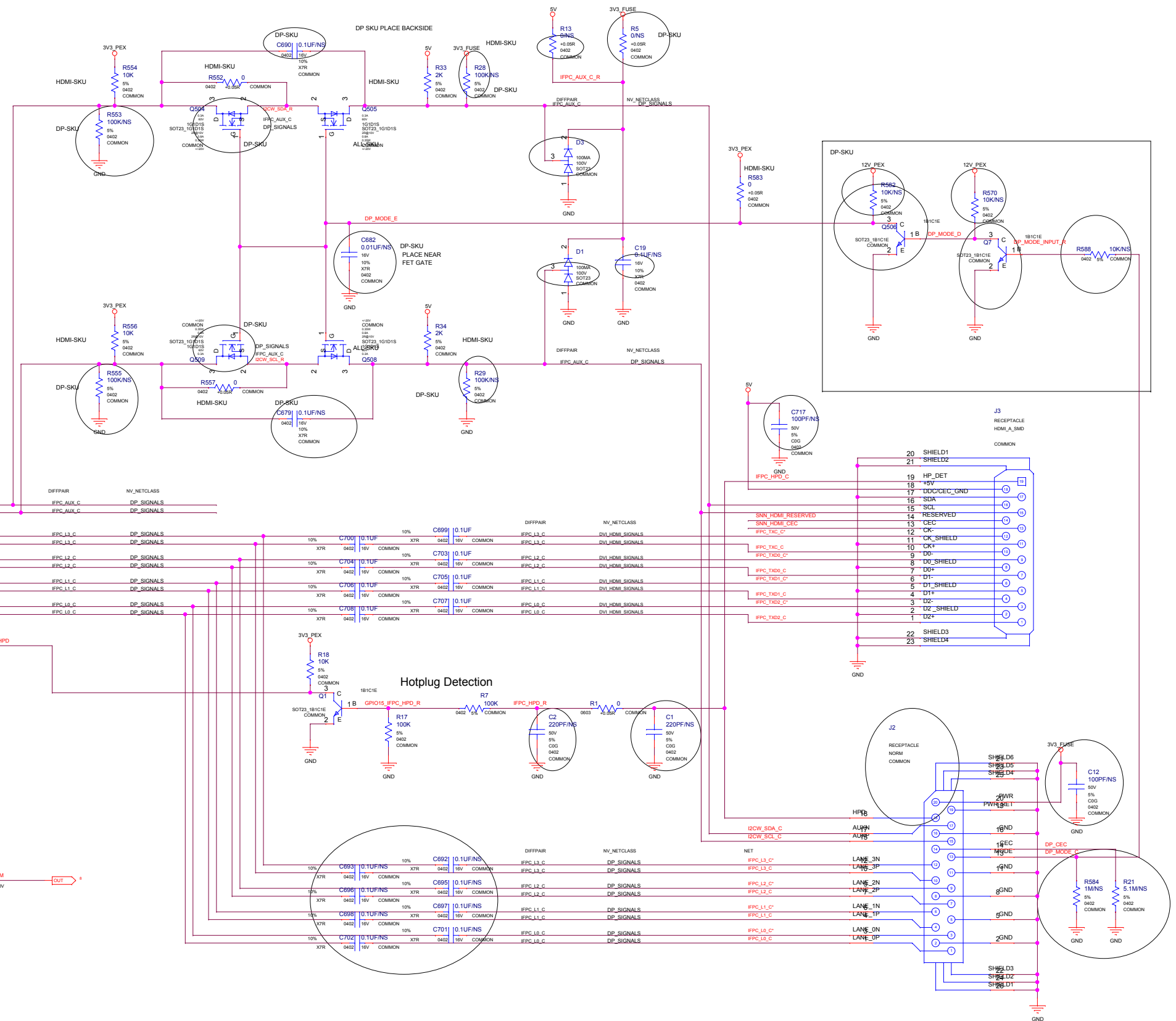
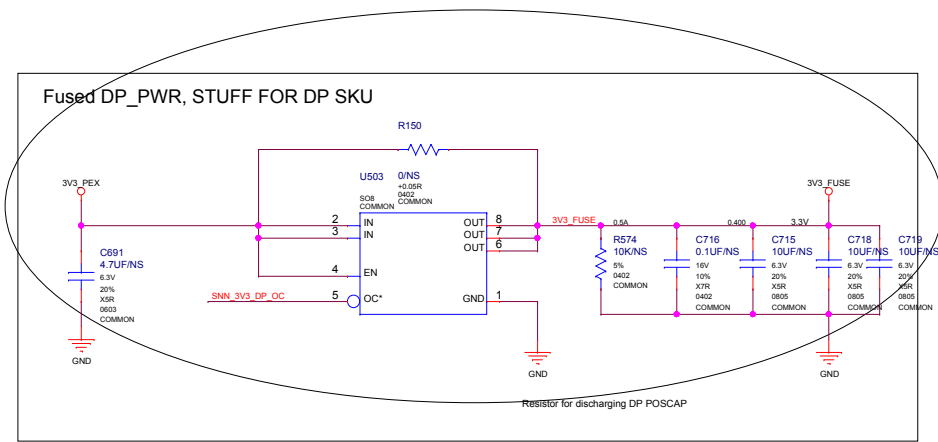
Connection will be modified according to Layout.



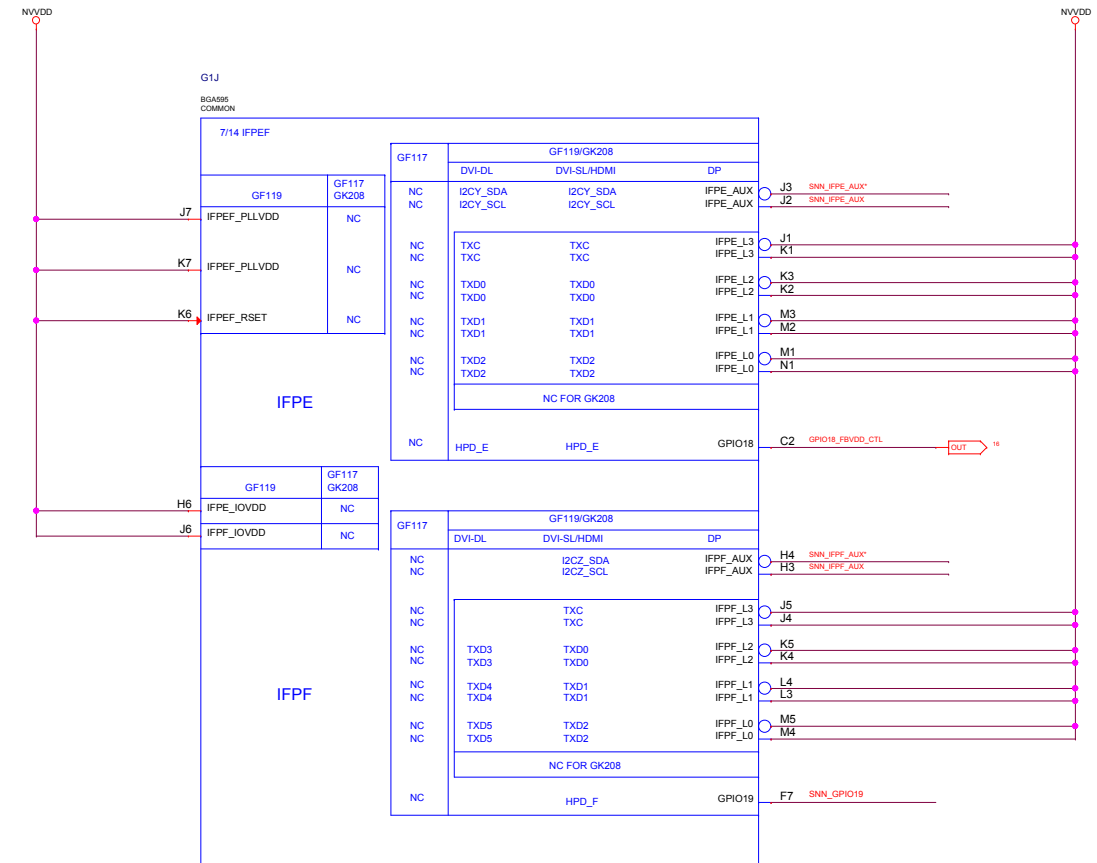
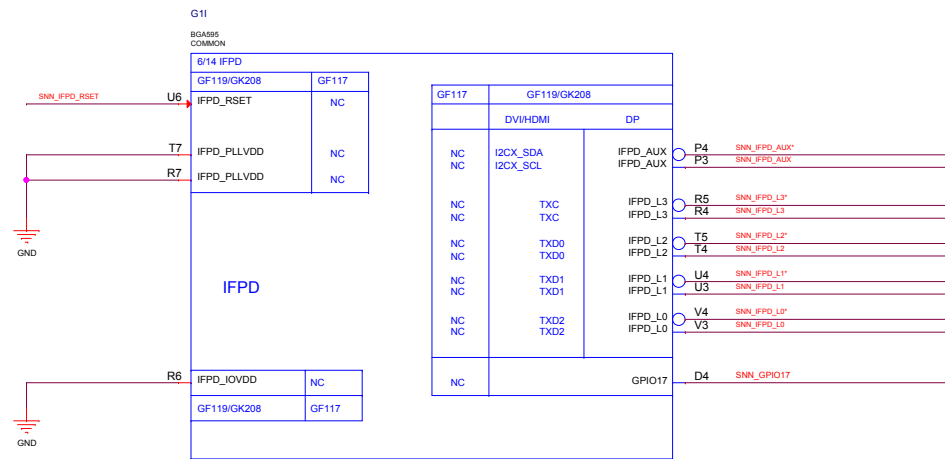
Place one POSCAP under GPU to co-layout with 0805 MLCC caps

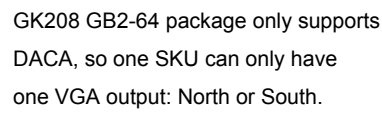
Page8: IFPAB TMDS DUAL LINK

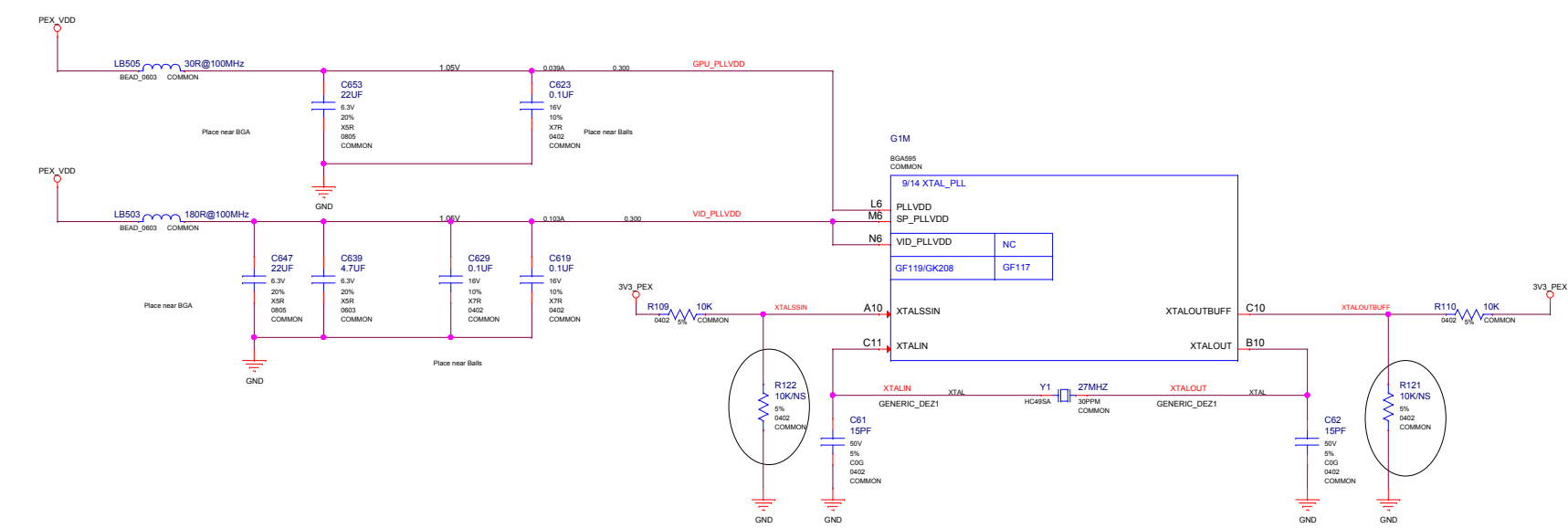




Page10: UNUSED IFPD,IFPE & IFPF

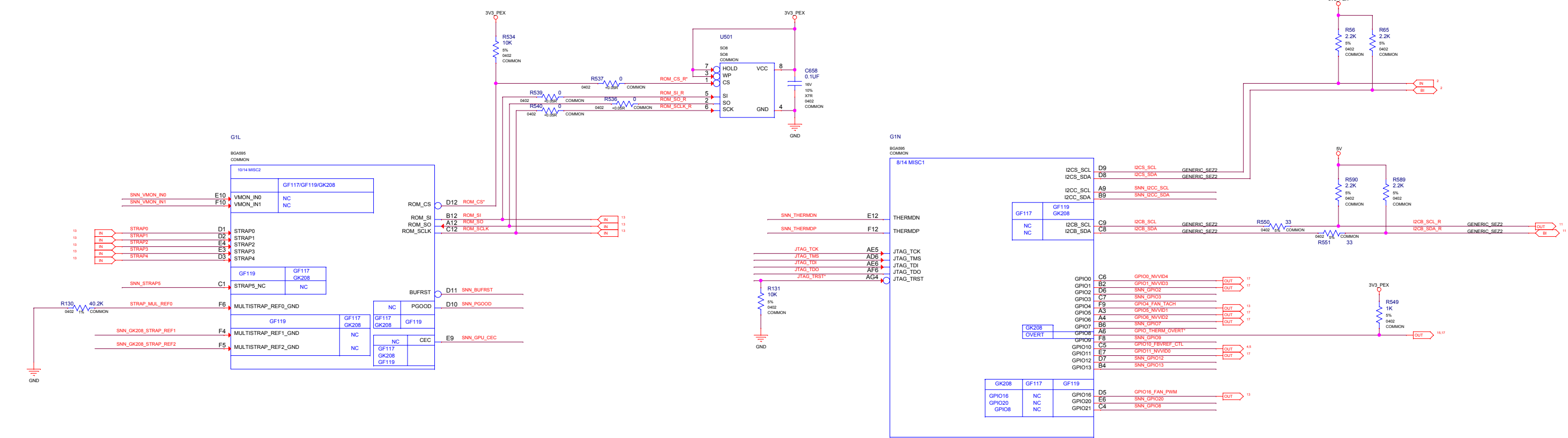






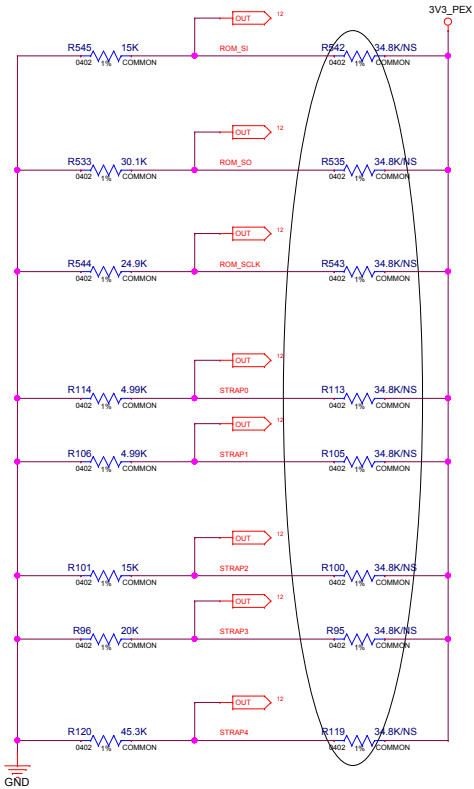
Smart Fan

XTALSSIN	XTALOUTBUFF	Inverted PWM %
PU	PU	66 (33% HIGH)
PU	PD	50 (50% HIGH)
PD	PU	33 (66% HIGH)
PD	PD	0 (100% HIGH)



Multilevel Straps			
0000	5.1K to GND	1000	5.1K to VCC
0001	10K to GND	1001	10K to VCC
0010	15K to GND	1010	15K to VCC
0011	20K to GND	1011	20K to VCC
0100	24.9K to GND	1100	24.9K to VCC
0101	30.1K to GND	1101	30.1K to VCC
0110	34.8K to GND	1110	34.8K to VCC
0111	45.3K to GND	1111	45.3K to VCC

GK208 Straps



Bit Signal	Values			
03: RAMCFG[0]				
02: RAMCFG[2]	0000	Reserved		
01: RAMCFG[1]	0001	128Mx16 GDDR5 X16 64bit Elpida		ROM_SI
	0010	128Mx16 GDDR5 X16 64bit Hynix A-die		
00: RAMCFG[3]	0011	128Mx16 GDDR5 X16 64bit Samsung D-die		
03: FR[1]	01	128MB		
02: FR[0]	10	256MB		
01: SMB_ALT_ADDR	0	DISABLED	1	ENABLED
00: VGA_DEVICE	0	DISABLED	1	ENABLED
03: PCI_DEVID_EXT	SKU0	0	SKU1	TBD
02: SUB_VENDOR	0	NO_BIOS	1	BIOS
01: PCI_DEVID_EXT_BITS	SKU0	0	SKU1	TBD
00: PEX_PLL_EN_TERM100	0	DISABLED	1	ENABLED
03: USER[3]	0000	INIT		Strap0
02: USER[2]				
01: USER[1]				
00: USER[0]				
03: 3GIO_PADCFG_LUT_ADR[3]	0000	DISKTOP_DEFAULT	1000	DISKTOP_JTHRS
02: 3GIO_PADCFG_LUT_ADR[2]	0001	MOBILE_DEFAULT	1001	MOBILE_JTHRS_HAMP
01: 3GIO_PADCFG_LUT_ADR[1]	0010	MOBILE_JTHRS_LLAMP	1010	MOBILE_JTHRS_LLAMP
00: 3GIO_PADCFG_LUT_ADR[0]	0011	MOBILE_JTHRS_LLAMP	1011	MOBILE_JTHRS_LLAMP
	0100	MOBILE_JTHRS_HAMP	1100	MOBILE_JTHRS_HAMP
	0101	MOBILE_JTHRS_HAMP	1101	MOBILE_JTHRS_HAMP
	0110	MOBILE_JTHRS_HAMP	1110	MOBILE_JTHRS_HAMP
	0111	MOBILE_JTHRS_HAMP	1111	MOBILE_JTHRS_HAMP
03: PCI_DEVID[3]	SKU0	0010		Strap2
02: PCI_DEVID[2]				
01: PCI_DEVID[1]	SKU1	TBD		
00: PCI_DEVID[0]				
03: SOR3_EXPOSED	0	DISABLED	1	ENABLED
02: SOR2_EXPOSED	0	DISABLED	1	ENABLED
01: SOR1_EXPOSED	0	DISABLED	1	ENABLED
00: SOR0_EXPOSED	0	DISABLED	1	ENABLED
02: PCIE_SPEED_CHANGE_GEN3	0	DISABLED	1	ENABLED
01: PCIE_MAX_SPEED	0	GEN1	1	GEN3
00: DP_PLL_VDD_3V3	0	USE1V8	1	USE3V3

