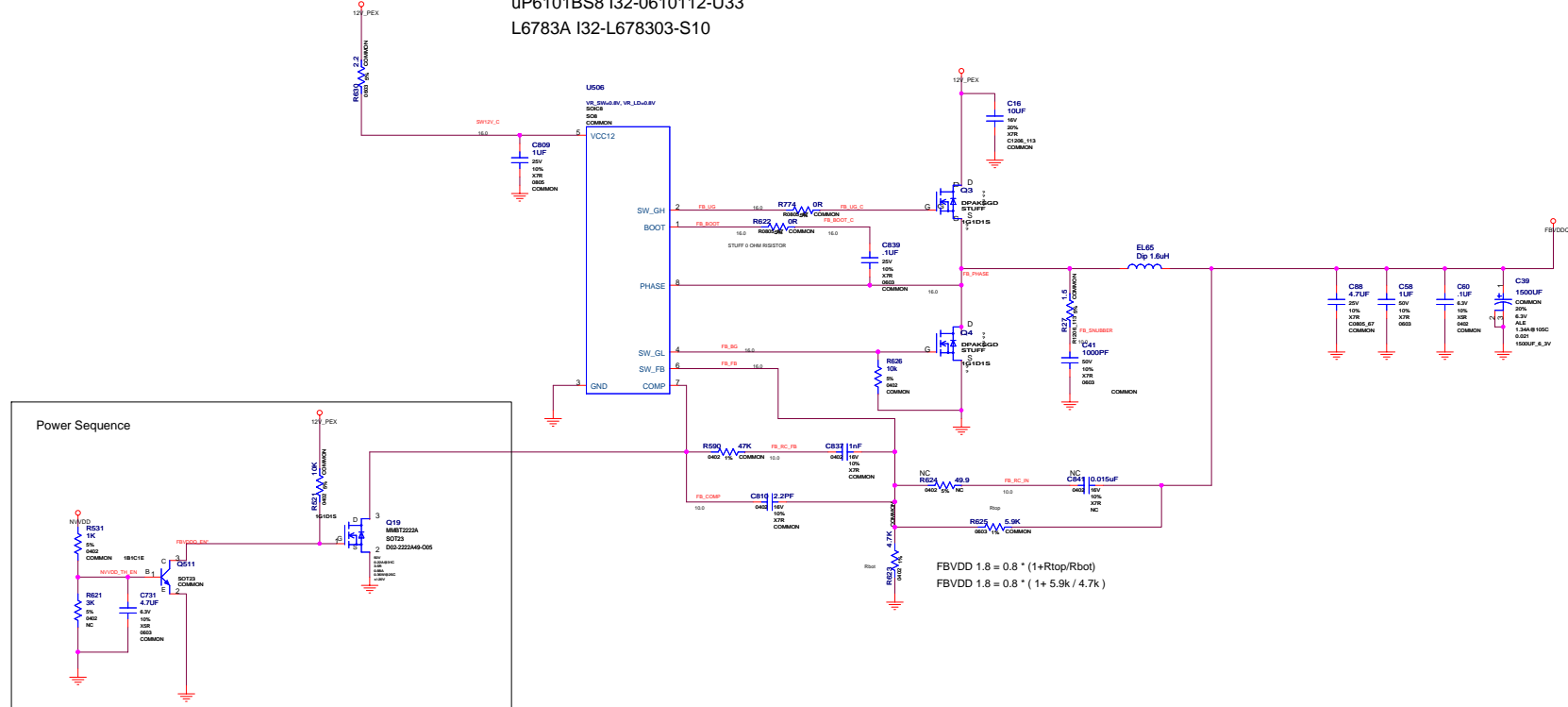


## PowerSupplyII: FBVDDQ

APW7065 I32-0706502-A30  
uP6101BS8 I32-0610112-U33  
L6783A I32-L678303-S10



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| PAGE DETAIL | PowerSupply: NVVDD  |

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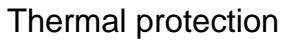


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## thermal protection and dynamic NVVDD



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| PAGE DETAIL | thermal protection and dynamic NVDD   |

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| A | B | C | D | E | F | G | H |
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Power down option  
A3V3 Rtop 15K Rbot 10K  
NVVDD Rtop 1K Rbot 3K

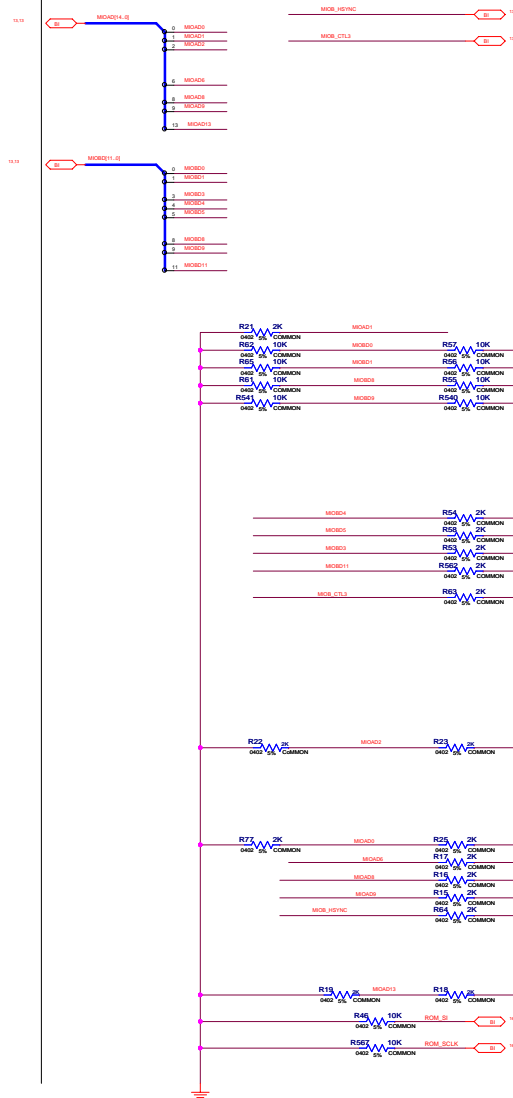
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## BIOS, Straps, Misc

## Straps



Assembly: BIOS

REG: NV\_PEXTDEV\_BOOT\_0[illegible]

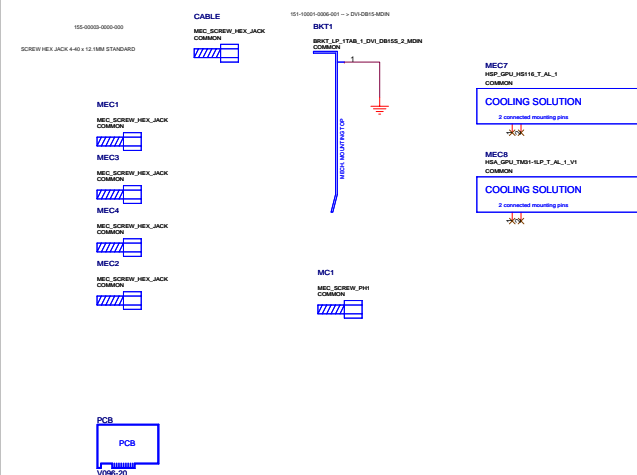
|                  |                      |                    |
|------------------|----------------------|--------------------|
| 10: FC1_DEV0_0   | FC1_DEV0[0]          |                    |
| 11: FC1_DEV0_1   |                      |                    |
| 12: FC1_DEV0_2   |                      |                    |
| 13: FC1_DEV0_3   |                      |                    |
| 28: FC1_DEV0_EXT | FC1_DEV0[63:0]       |                    |
| <hr/>            |                      |                    |
| 14: FRQ[0]       | Power-Redn Rate Size | 0000-0000          |
|                  |                      | 0011-0000          |
| 15: FRQ[1]       |                      | 0111-0000          |
|                  |                      | 1000-0000          |
| 16: FRQ[2]       |                      | 1010-0000          |
|                  |                      | 1011-0000          |
|                  |                      | 1100-0000          |
|                  |                      | 1101-0000          |
| <hr/>            |                      |                    |
| 22: ROM_TYPE_0   | ROM_TYPE[0]          | 00: Parallel       |
|                  |                      | 01: Serial_A222    |
| 23: ROM_TYPE_1   |                      | 10: Serial_A222-0f |

|                        | 0 (default - internal term only) |                        |
|------------------------|----------------------------------|------------------------|
| 17 PEK_FALL_EN_TERM10  | 0000-Creatap                     |                        |
| 18 3G0_PACQGT_LUT_AOR0 | 0001-Abatac                      | 1001-Abatac HTW05 NAMP |
|                        | 0002-Abatac HTW05 LAMP           | 1002-Abatac HTW05 LAMP |
| 19 3G0_PACQGT_LUT_AOR1 | 0003-Abatac HTW05 LAMP           | 1003-Abatac HTW05 LAMP |
|                        | 0004-Abatac HTW05 LAMP           | 1100-Mioha HTW05 NAMP  |
| 20 3G0_PACQGT_LUT_AOR2 | 0005-Abatac HTW05 NAMP           | 1004-Abatac HTW05 NAMP |
|                        | 0006-Abatac HTW05 NAMP           | 1101-Abatac HTW05 NAMP |
| 21 3G0_PACQGT_LUT_AOR3 | 0007-Abatac HTW05 NAMP           | 1102-Abatac HTW05 NAMP |
|                        | 0008-Abatac HTW05 NAMP           |                        |

31: StrapOverride per SW

| REG: NV_STRAP_1             |                   | VALUEs                |
|-----------------------------|-------------------|-----------------------|
| Bit Signal                  | VALUE_ID          |                       |
| 15: Set Clock Configuration |                   | 0=Disable<br>1=Enable |
| 12: MIO_EN_33V_0            | MIOA_VDDO Voltage | 0=2.5V<br>1=3.3V      |
| 13: MIO_EN_33V_1            | MIOB_VDDO Voltage | 0=2.5V<br>1=3.3V      |
| 16: PCI_JOBAR               |                   | 0=Disable<br>1=Enable |

## Mechanical parts



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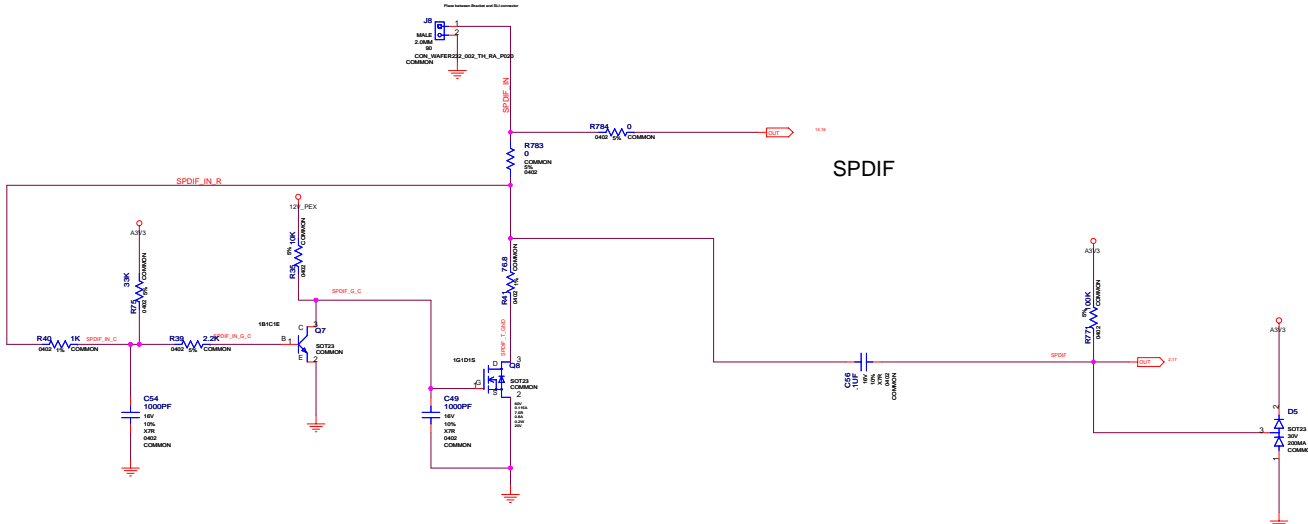



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| PAGE DETAIL | BIOS STRAPS & MECHANICALS   |

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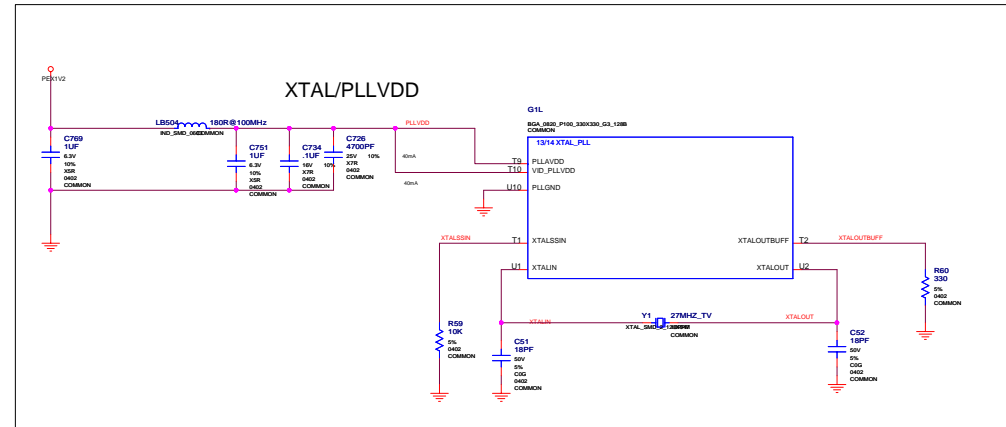
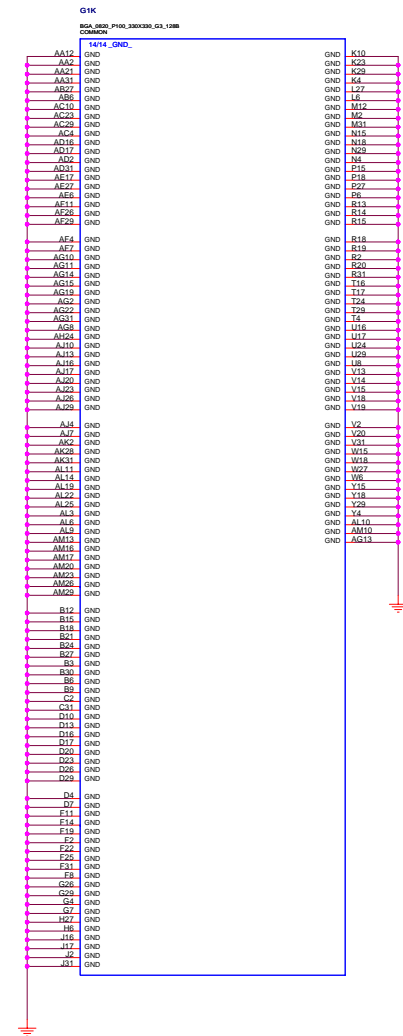
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## GPU GND CONNECTION, XTAL



|    | NET     | NV_CRITICAL | NV_IMPEDANCE | DIFFPAIR  |
|----|---------|-------------|--------------|-----------|
| IN | VTALIN  | 1           | 500M         |           |
| IN | VTALOUT | 1           | 500M         |           |
|    | NET     | VOLTAGE     | MAX_CURRENT  | MIN_WIDTH |
| IN | PLVDD   | 1.2V        | 0.3A         | 120M      |

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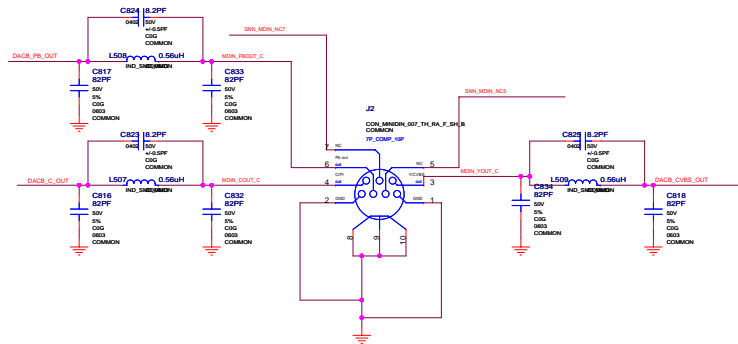
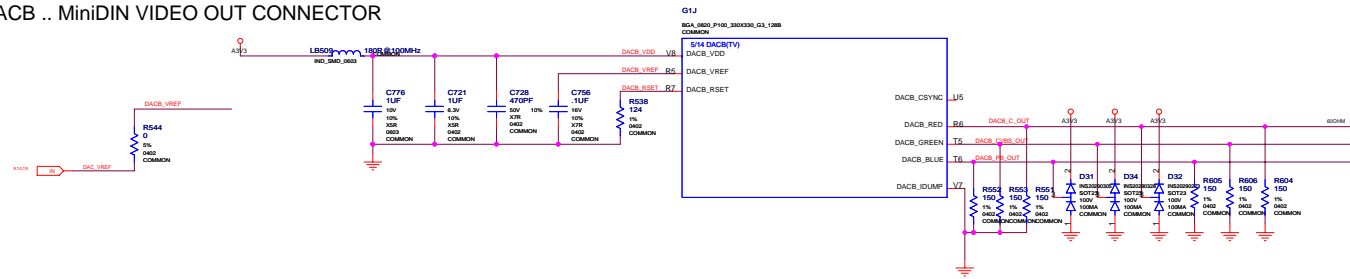
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| ASSEMBLY    | BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| PAGE DETAIL | GPU GND CONNECTION, XTAL  |

## DACB .. MiniDIN VIDEO OUT CONNECTOR



## DACB NET RULES

|    | NET           | NV_CRITICAL | NV_IMPEDANCE | DIFFPAIR |
|----|---------------|-------------|--------------|----------|
| IN | DACS_C_OUT    | 1           | NOISE        |          |
| IN | MDIN_COUT_C   | 1           | NOISE        |          |
| IN | DACS_CVBS_OUT | 1           | NOISE        |          |
| IN | MDIN_YOUT_C   | 1           | NOISE        |          |
| IN | DACS_PB_OUT   | 1           | NOISE        |          |
| IN | MDIN_PROUT_C  | 1           | NOISE        |          |

|    |            |   |       |
|----|------------|---|-------|
| IN | MDIN_SCL_C | 2 | 500HM |
| IN | MDIN_SDA_C | 2 | 500HM |

| NET | VOLTAGE    | MAX_CURRENT | MIN_WIDTH |
|-----|------------|-------------|-----------|
| IN  | DACIS_VDD  | 3.30000V    | 0.07A     |
| IN  | DACIS_VREF |             | 16.0      |
| IN  | DACIS_RSET |             | 16.0      |
| IN  |            |             | 16.0      |

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| ASSEMBLY    | BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| PAGE DETAIL | DACB FILTERS, SYNC STRIPPER, MINIDIN CONNECTOR NORTH HDTV HEADER                      |

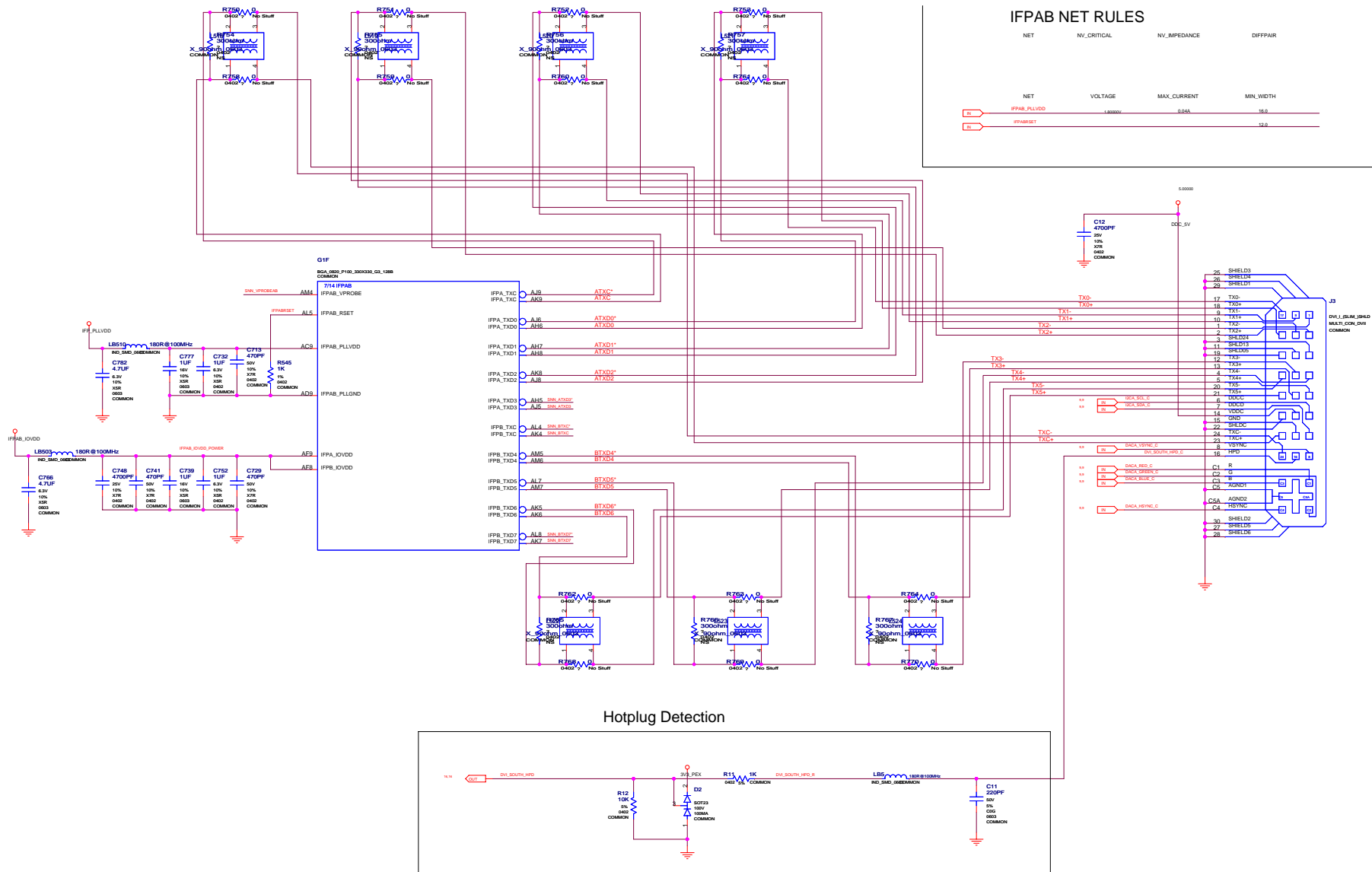




## IFPAB NET RULES



|    | NET          | VOLTAGE  | MAX_CURRENT | MIN_WIDTH |
|----|--------------|----------|-------------|-----------|
| 19 | IFPCD_PLLVDD | 3.300000 | 0.04        | 16.0      |
| 19 | IFPD_JOYVDD  | 0.000000 | 0           | 10.0      |
| 19 | IFPC_JOYVDD  | 3.300000 | 0.15        | 16.0      |



## Hotplug Detection

|             |   |
|-------------|---|
| ASSEMBLY    | BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| PAGE DETAIL | TMD5 LINK A & P's, DVI CONNECTOR SOUTH  |

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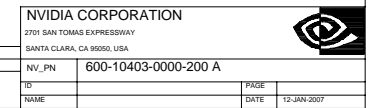
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Secondary Display (DACC), DB15

## DACC RGB-FILTER



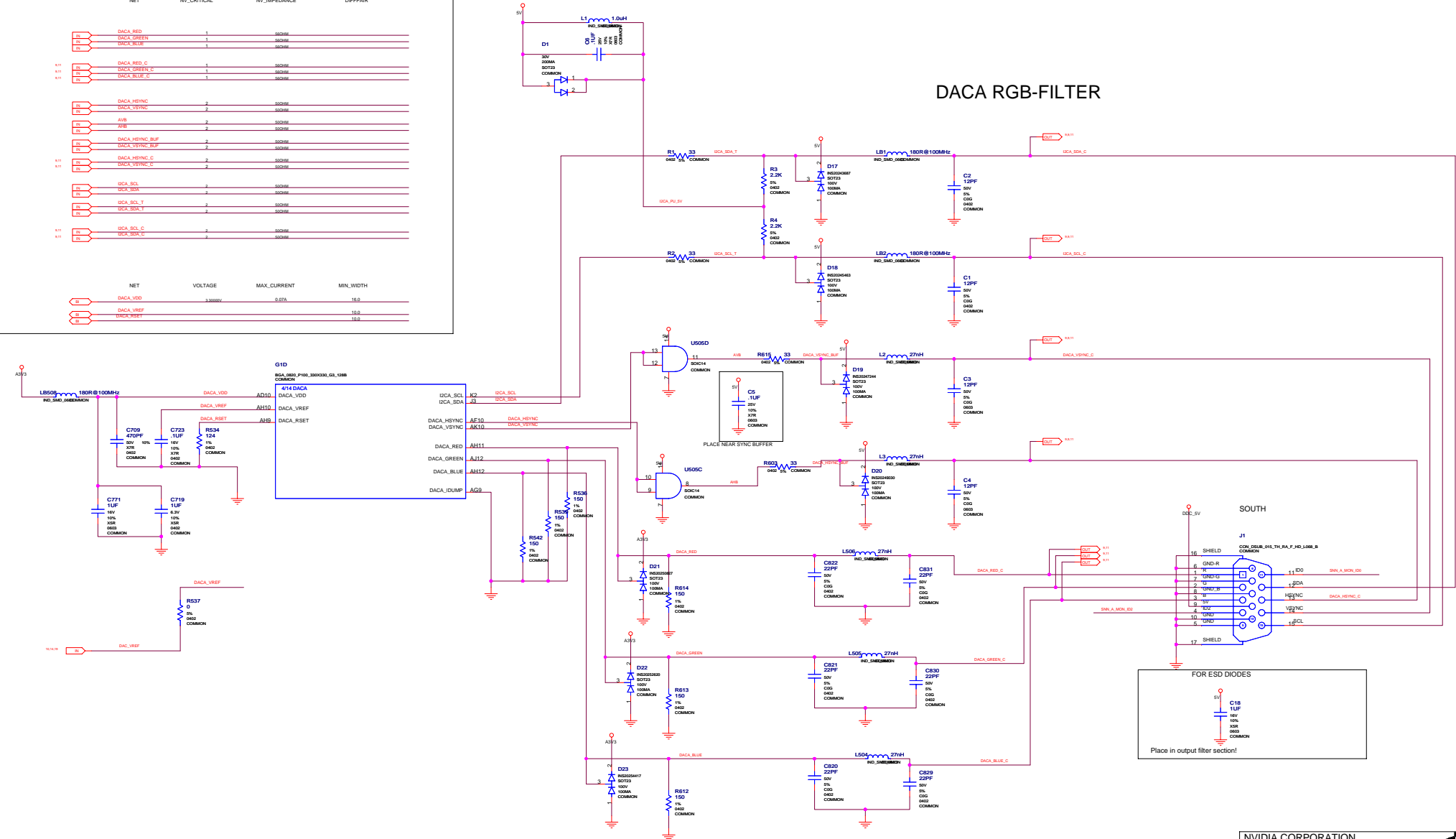
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# DACA NET RULES

| NET            | NV_CRITICAL | NV_IMPEDANCE | DIFFPAIR |
|----------------|-------------|--------------|----------|
| DACA_RED       | 1           | 50OHM        |          |
| DACA_GREEN     | 1           | 50OHM        |          |
| DACA_BLUE      | 1           | 50OHM        |          |
| DACA_RED_C     | 1           | 50OHM        |          |
| DACA_GREEN_C   | 1           | 50OHM        |          |
| DACA_BLUE_C    | 1           | 50OHM        |          |
| DACA_HSYNC     | 2           | 50OHM        |          |
| DACA_VSYNC     | 2           | 50OHM        |          |
| AUX            | 2           | 50OHM        |          |
| DACA_HSYNC_BUF | 2           | 50OHM        |          |
| DACA_VSYNC_BUF | 2           | 50OHM        |          |
| DACA_HSYNC_C   | 2           | 50OHM        |          |
| DACA_VSYNC_C   | 2           | 50OHM        |          |
| DACA_SCL       | 2           | 50OHM        |          |
| DACA_SDA       | 2           | 50OHM        |          |
| DACA_SCL_T     | 2           | 50OHM        |          |
| DACA_SDA_T     | 2           | 50OHM        |          |
| DACA_SCL_C     | 2           | 50OHM        |          |
| DACA_SDA_C     | 2           | 50OHM        |          |
| DACA_VDD       | 33000V      | 0.07A        | 15.0     |
| DACA_VREF      |             |              | 10.0     |
| DACA_VSET      |             |              | 10.0     |

## Primary Display (DACA), Slim DB15

## DACA RGB-FILTER



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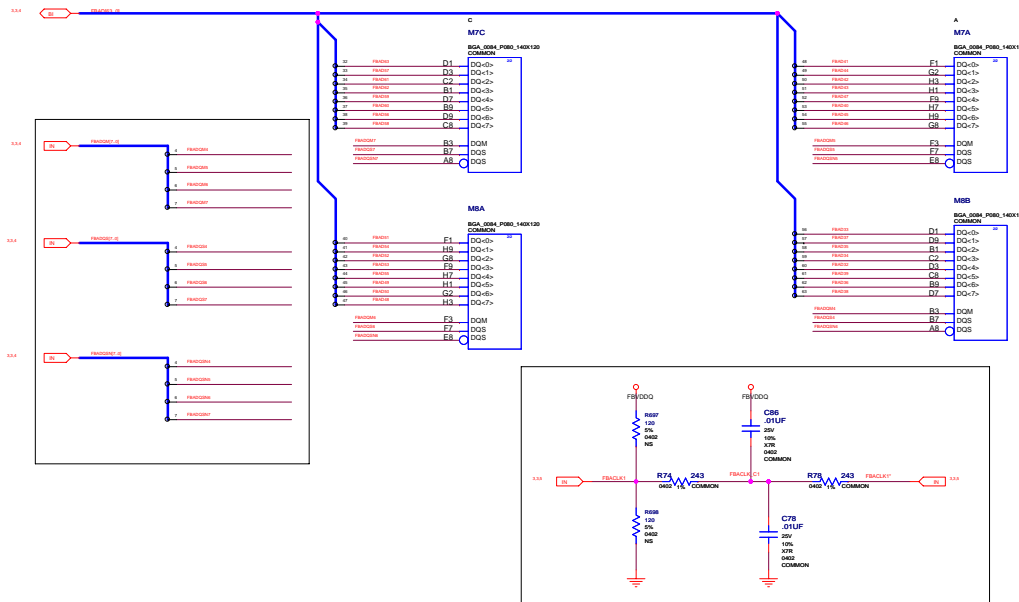
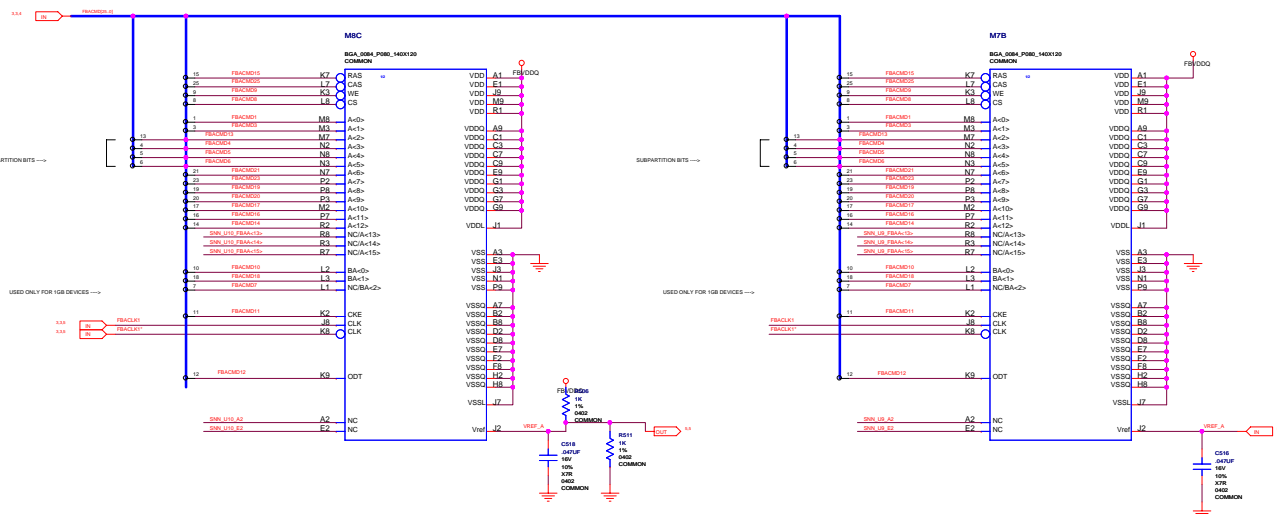
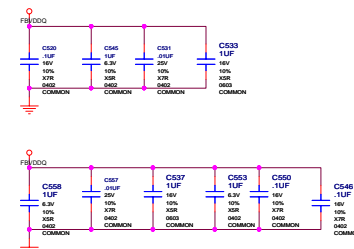
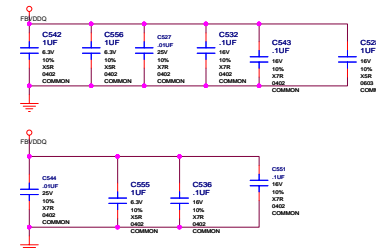
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ASSEMBLY BOARD LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO\_STUFF ASSEMBLY NOTES AND BOM NOT FINAL  
PAGE DETAIL DACA FILTER, DACA SYNC BUFFER & DB15 SOUTH

G1C  
RGA\_0001\_F00L\_000100\_03\_1388  
COMMON

|    |         |       |      |             |
|----|---------|-------|------|-------------|
| D3 | FBCD1   | FBVTT | A23  | SNV_FVTT_2  |
| A3 | FBCD1   | FBVTT | AB23 | SNV_FVTT_2  |
| C3 | FBCD2   | FBVTT | H16  | SNV_FVTT_3  |
| A3 | FBCD3   | FBVTT | H17  | SNV_FVTT_3  |
| D3 | FBCD4   | FBVTT | J10  | SNV_FVTT_4  |
| C3 | FBCD5   | FBVTT | J23  | SNV_FVTT_5  |
| A3 | FBCD6   | FBVTT | J24  | SNV_FVTT_5  |
| D3 | FBCD7   | FBVTT | B3   | SNV_FVTT_6  |
| C3 | FBCD8   | FBVTT | K11  | SNV_FVTT_7  |
| F3 | FBCD9   | FBVTT | K12  | SNV_FVTT_7  |
| D3 | FBCD10  | FBVTT | K21  | SNV_FVTT_10 |
| D3 | FBCD11  | FBVTT | K22  | SNV_FVTT_10 |
| E3 | FBCD12  | FBVTT | K24  | SNV_FVTT_12 |
| D3 | FBCD13  | FBVTT | K8   | SNV_FVTT_13 |
| E3 | FBCD14  | FBVTT | L23  | SNV_FVTT_14 |
| D3 | FBCD15  | FBVTT | M23  | SNV_FVTT_15 |
| D3 | FBCD16  | FBVTT | L25  | SNV_FVTT_16 |
| F3 | FBCD17  | FBVTT | L26  | SNV_FVTT_17 |
| D3 | FBCD18  |       |      |             |
| D3 | FBCD19  |       |      |             |
| D3 | FBCD20  |       |      |             |
| E3 | FBCD21  |       |      |             |
| C3 | FBCD22  |       |      |             |
| E3 | FBCD23  |       |      |             |
| C3 | FBCD24  |       |      |             |
| E3 | FBCD25  |       |      |             |
| C3 | FBCD26  |       |      |             |
| C3 | FBCD27  |       |      |             |
| C3 | FBCD28  |       |      |             |
| C3 | FBCD29  |       |      |             |
| A3 | FBCD30  |       |      |             |
| B3 | FBCD31  |       |      |             |
| B3 | FBCD32  |       |      |             |
| C3 | FBCD33  |       |      |             |
| C3 | FBCD34  |       |      |             |
| B3 | FBCD35  |       |      |             |
| C3 | FBCD36  |       |      |             |
| B3 | FBCD37  |       |      |             |
| C3 | FBCD38  |       |      |             |
| A3 | FBCD39  |       |      |             |
| D3 | FBCD40  |       |      |             |
| D3 | FBCD41  |       |      |             |
| F3 | FBCD42  |       |      |             |
| D3 | FBCD43  |       |      |             |
| E3 | FBCD44  |       |      |             |
| E3 | FBCD45  |       |      |             |
| F3 | FBCD46  |       |      |             |
| F3 | FBCD47  |       |      |             |
| E3 | FBCD48  |       |      |             |
| A3 | FBCD49  |       |      |             |
| C3 | FBCD50  |       |      |             |
| C3 | FBCD51  |       |      |             |
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PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY



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| PAGE DETAIL | FBA 16Mx16 DDR2 MEMORIES, 1ST BANK 32..63   |

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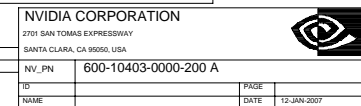


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PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY









P403-B00 DESIGN -- G84-200, 256 MB DDR2,DVI-I, VGA, HDTV

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Page 3: FBA MEMORY INTERFACE, GPU FBVDD/Q DECOUPLING CAPS, FBVTT TERMINATION

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Page 16: JTAG, BIOS ROM, HDCP ROM, FAN CONTROL, GPIO

Page 17: SPDIF

Page 18: BIOS STRAPS & MECHANICALS

Page 19: POWER SUPPLY: TMDS IOVDD,SV,ASV3

Page 20: Thermal protection and dynamic NVVDD

Page 21: PowerSupplyII: NVVDD

Page 22: PowerSupplyIII: FBVDDQ, PLLVDD

REV HISTORY

- 96/10/30 Design change to G84-G53 Large size 8400GS circuit PCB LP costdown version
1. Remove FBC 2ND 0..63
  2. Remove TMDS C HDMI circuit
  3. Remove SCART function form DACB
  4. Change Fancontrol func to Direct driver
  5. Change FBVDDQ PWM power solution to 431 LDO solution

| REV | VARIANT     | NPVN               | ASSEMBLY  |
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| 0   | BASE        | 600-10403-0000-200 | BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| 1   | SKU0020     | 600-10403-0020-200 | P403-B00 086-300 459/400MHz 256MB 18Mx16 BGAS4 GDDR2 DVI-I-CL+VGA+HDTV-CL             |
| 2   | SKU0021     | 600-10403-0021-200 | P403-B00 084-200 459/400MHz 256MB 18Mx16 BGAS4 GDDR2 DVI-I-CL+VGA+HDTV-CL             |
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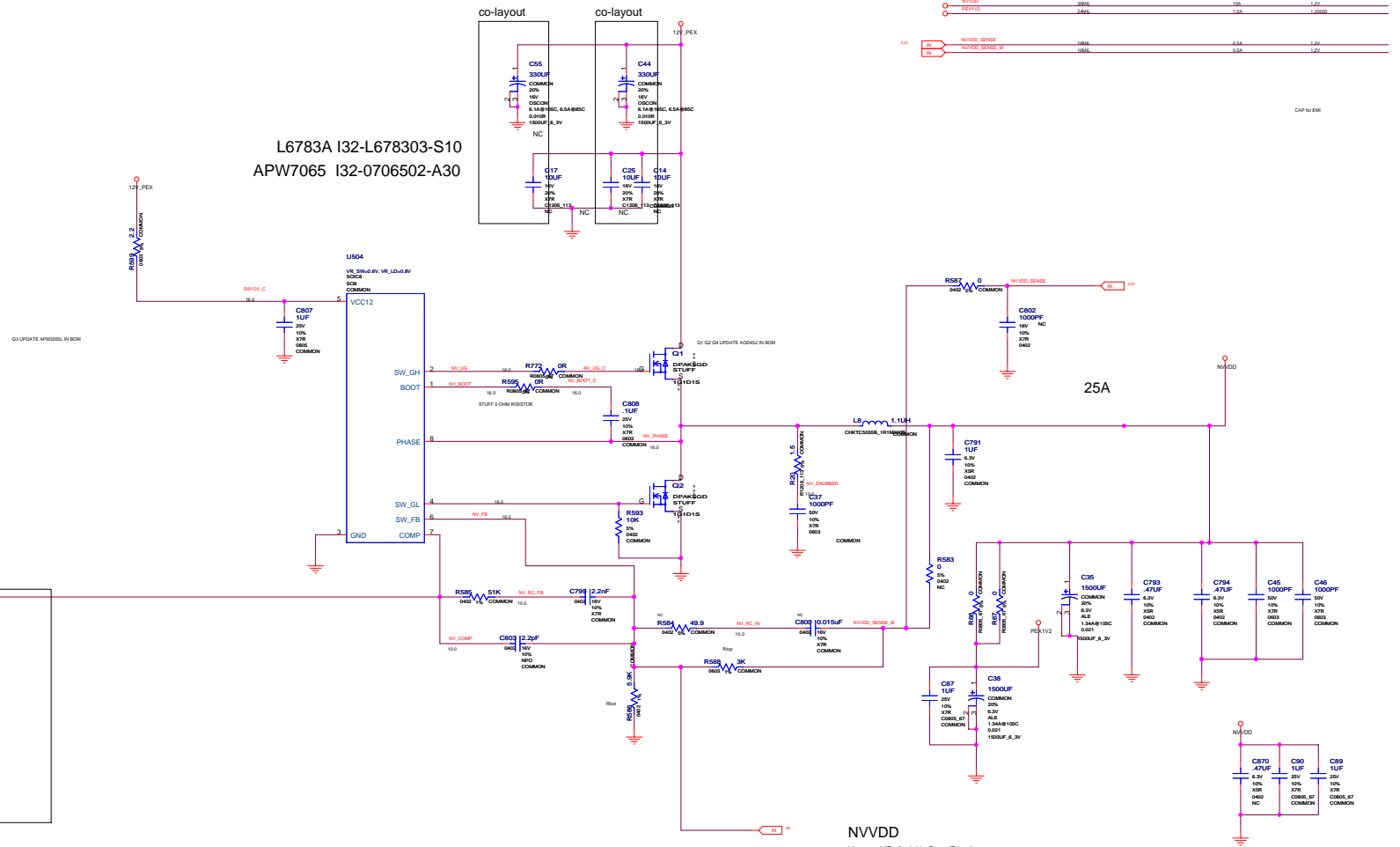
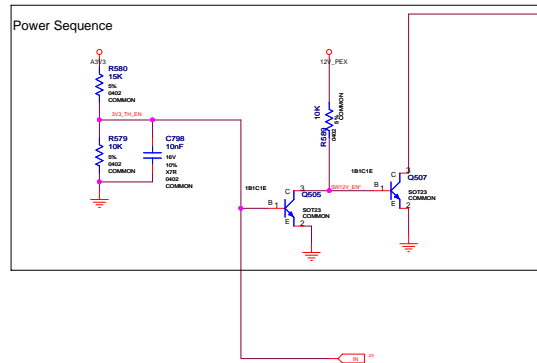
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## PowerSupplyII: NVVDD

L6783A I32-L678303-S10  
APW7065 I32-0706502-A30




NVVDD

$$V_{out} = V_{Ref} * (1 + R_{top}/R_{bot})$$

$$1.1V = 0.8V * (1 + (1.54k/4.02k)) \quad (ISL6549)$$

$$1.2V = 0.8V * (1 + (1.54k/3.09k)) \quad (ISL6549)$$

$$1.2V = 0.8V * (1 + (10k/20k)) \quad (APW7065)$$

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