PG160-B02

8GB GDDR6, 256b, X16
Tall DVI-D + USBC/DP + DP + HDMI/DP + DP

TABLE OF CONTENTS

Page	Description
1	Table of Contents
2	Block Diagram
3	PCI Express
4	PCIE RC TERM
5	MEMORY: GPU FB_AB
6	MEMORY: FBA[31:0]
7	MEMORY: FBA[63:32]
8	MEMORY: FBB[31:0]
9	MEMORY: FBB[63:32]
10	MEMORY: GPU FB_CD
11	MEMORY: FBC[31:0]
12	MEMORY: FBC[63:32]
13	MEMORY: FBD[31:0]
14	MEMORY: FBD[63:32]
15	GPU PWR & GND
16	GPU Decoupling
17	GPU Decoupling2
18	IO: IFPAB DVI-D-DL
19	IO: IFPE DP
20	IO: IFPF USBC
21	IO: IFPC HDMI/DP
22	IO: IFPD DP
23	IO: NVHS Interface and Frame Lock
24	MISC1: Thermal, JTAG, GPIO,STEREO
25	MISC2: ROM, XTAL,STRAPS
26	PS: 1V8_PLL, 1V8_AON
27	PS: 5V
28	PS: PEXVDD
29	PS: FBVDDQ
30	PS: FBVDDQ PH2
31	PS: NVVDD Controller
32	PS: NVVDD Controller OVR3i
33	PS: NVVDD Phase 1,2
34	PS: NVVDD Phase 3,4
35	PS: NVVDD Phase 5,6
36	PS: Input Power Balancing Switcher
37	PS: Input, Filtering, and Monitoring
38	PS: STEERING, UPB & HOT-UNPLUG
39	PS: Type-C BuckBoost
40	PD PPC
41	PS: 12V & 3V3_A SWITCHER
42	SEQ: 5V, 1V8, NV3V3 ENABLE
43	SEQ: NV, PEX, FB ENABLE
44	SEQ: VOLTAGE MONITOR
45	SEQ: DISCHARGE
46	SEQ: MISC
47	LOGO LED
48	LED DRIVER & BOOST
49	DUAL FAN
50	PS: Pre-filter
51	MECH
52	PTC

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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	Table of Contents

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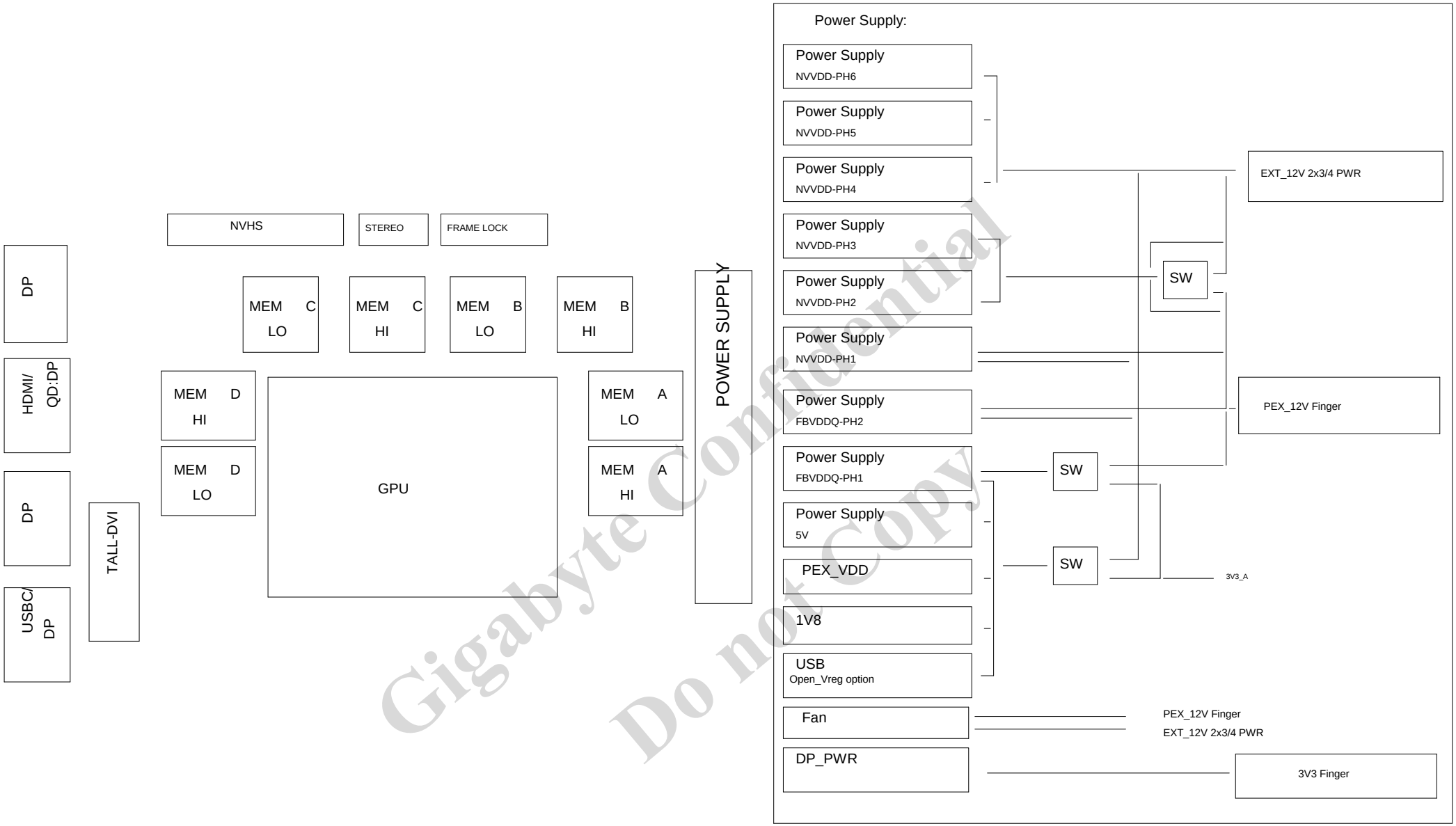
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Sunday, November 11, 2018	Sheet 1 of 52	

Page2: Block Diagram



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Block Diagram

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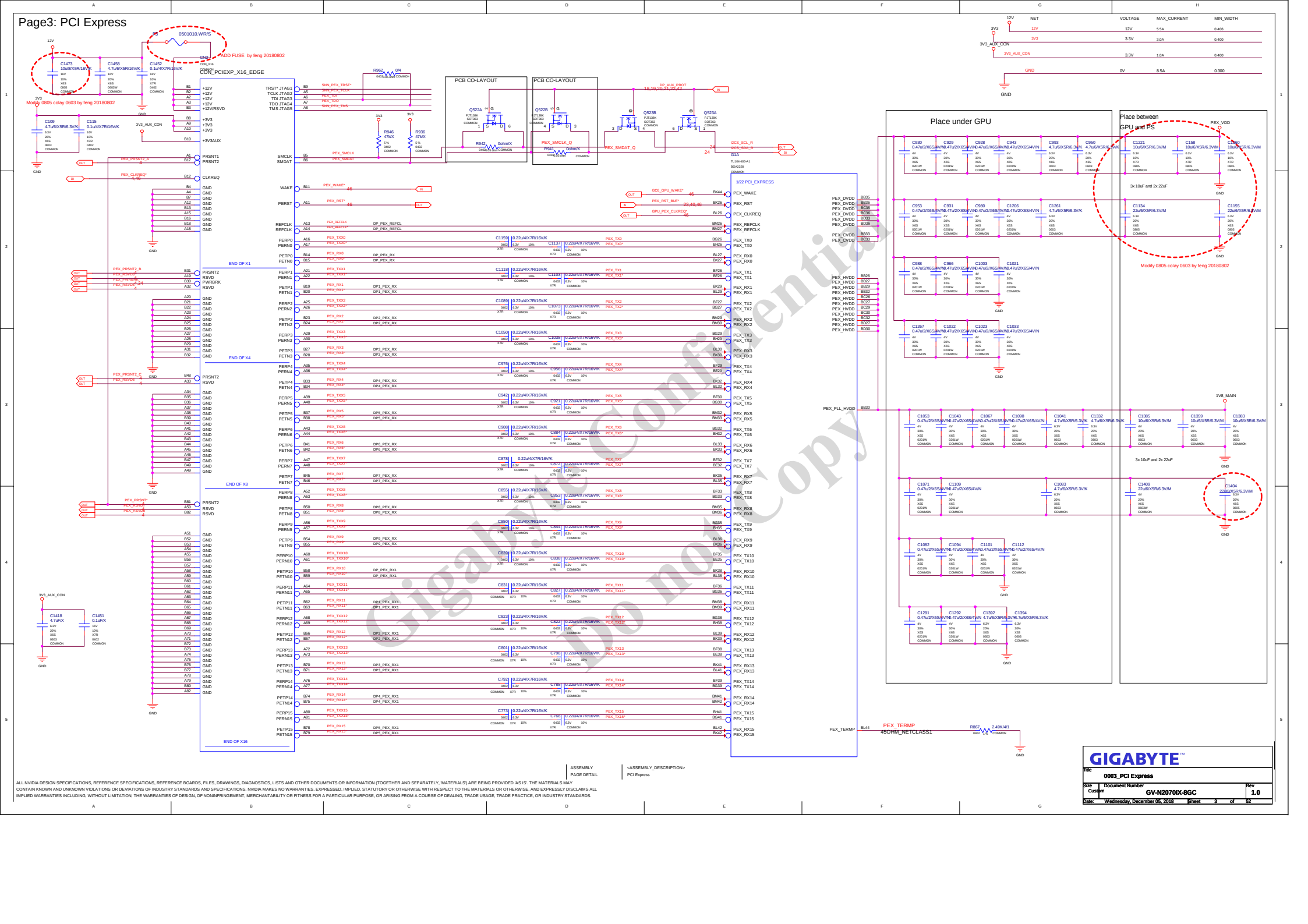
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Sunday, November 11, 2018	Sheet 2 of 52	

Page3: PCI Express



NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
12V	12V	5.5A	0.406
3V3	3.3V	3.0A	0.400
3V3_AUX_CON	3.3V	1.0A	0.400
GND	0V	8.5A	0.300

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PAGE DETAIL

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PCI Express

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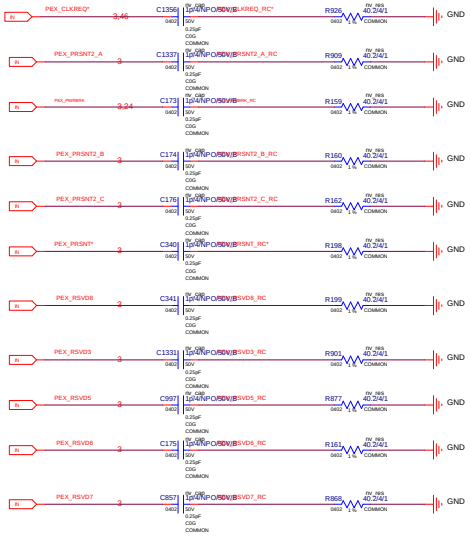
Title	0003_PCI Express	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 3 of 52	

Page4: PCIE RC TERM



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PCIE RC TERM

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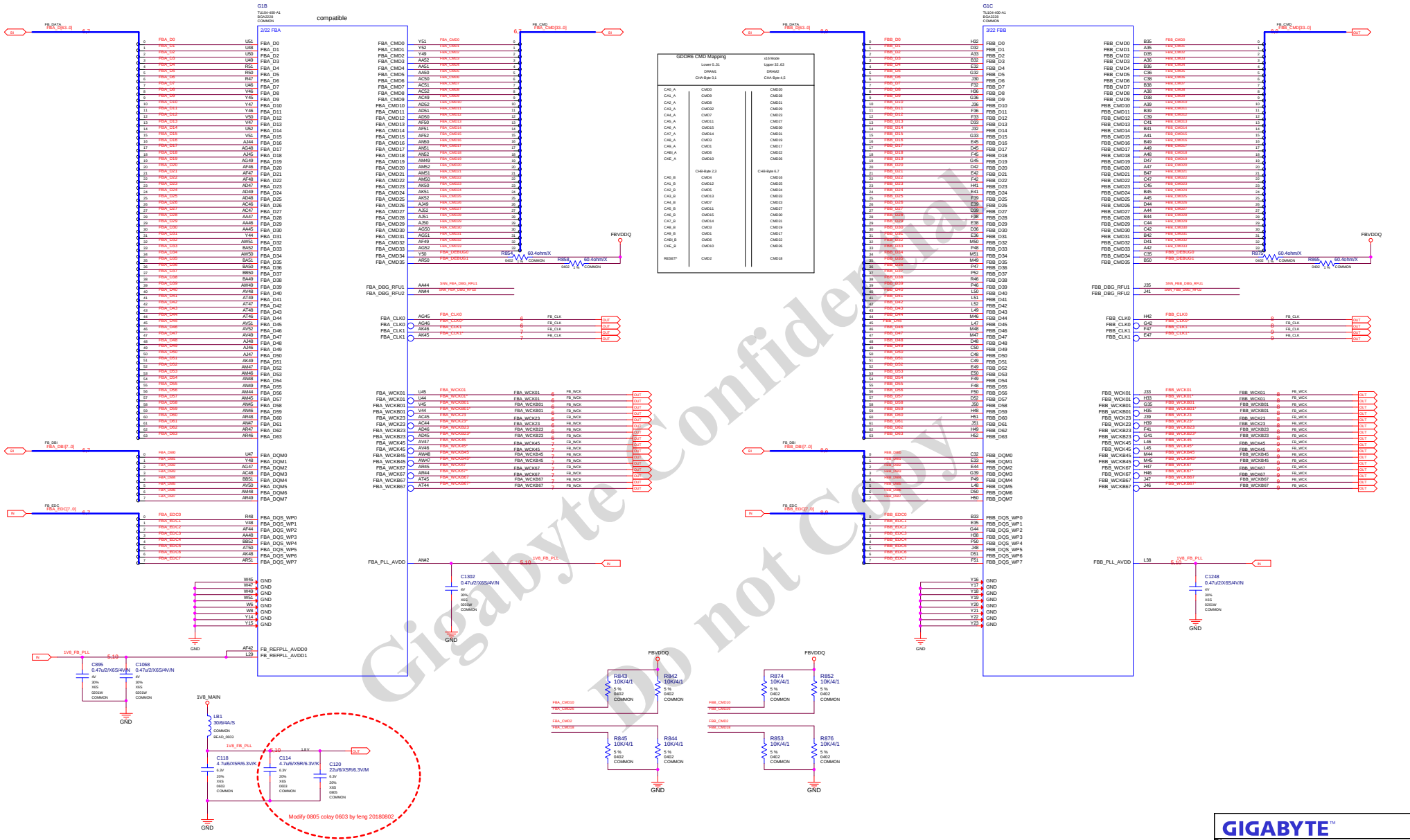
Title	0004_PCIE RC TERM	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 4 of 52	

Page5: MEMORY: GPU Partition A/B



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	MEMORY: GPU FB_AB

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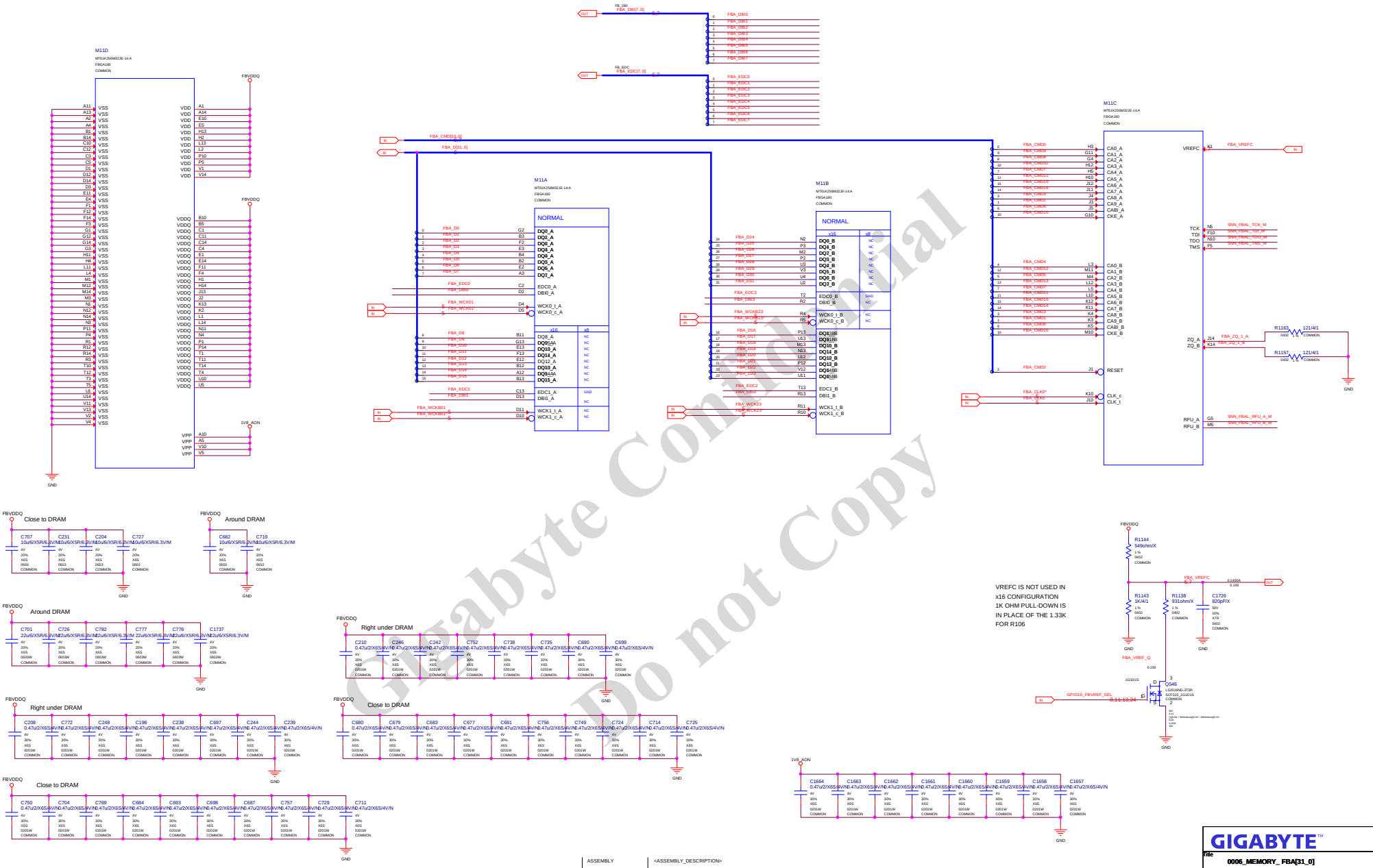
Title	0005_MEMORY_ GPU FB_AB	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 5 of 52	

Page6: MEMORY: FBA Partition 31..0



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PAGE DETAIL	MEMORY: FBA[31:0]

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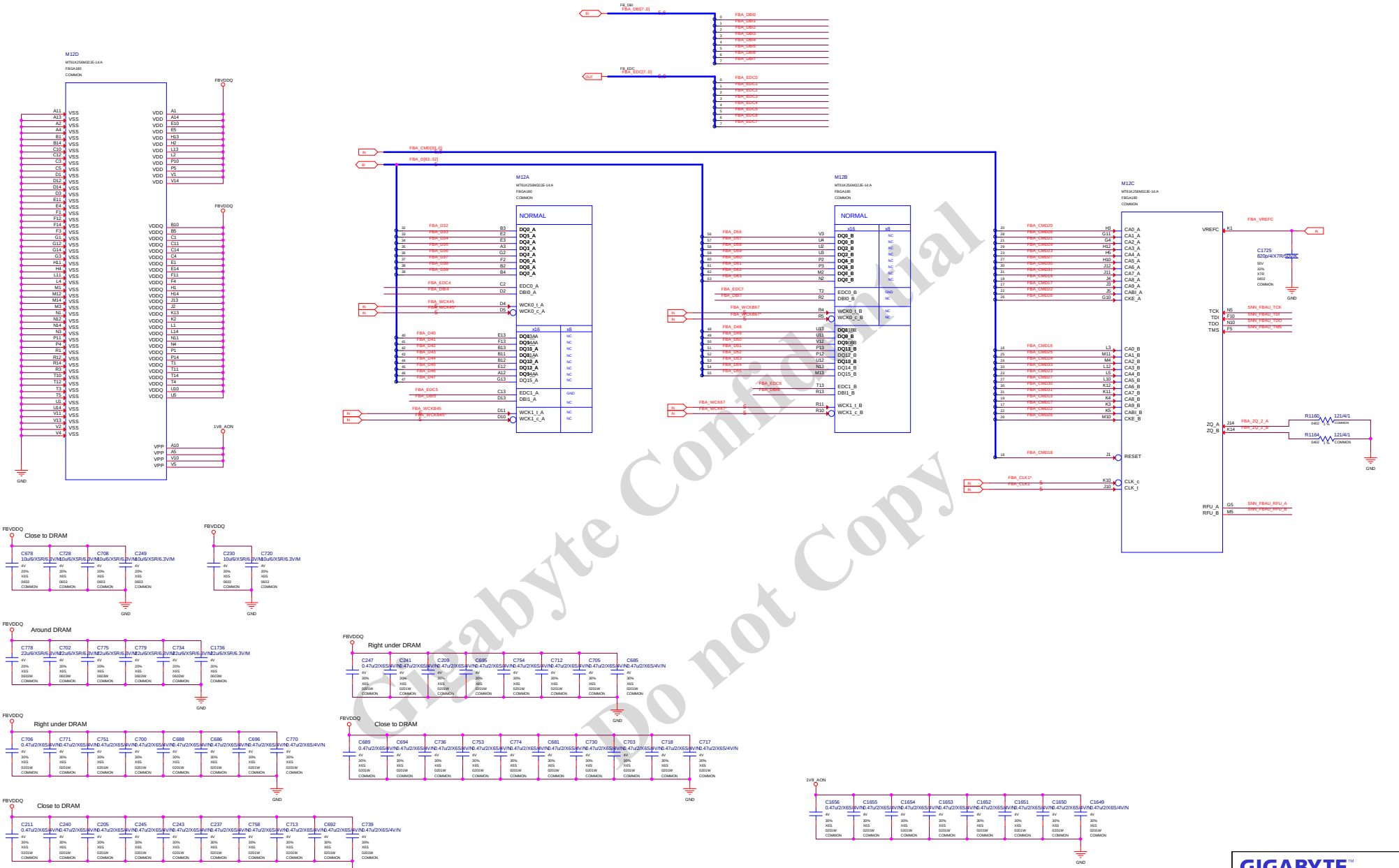
Title	0006_MEMORY_ FBA[31_0]	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 6 of 52	

Page7: MEMORY: FBA Partition 63..32



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MEMORY: FBA[63:32]

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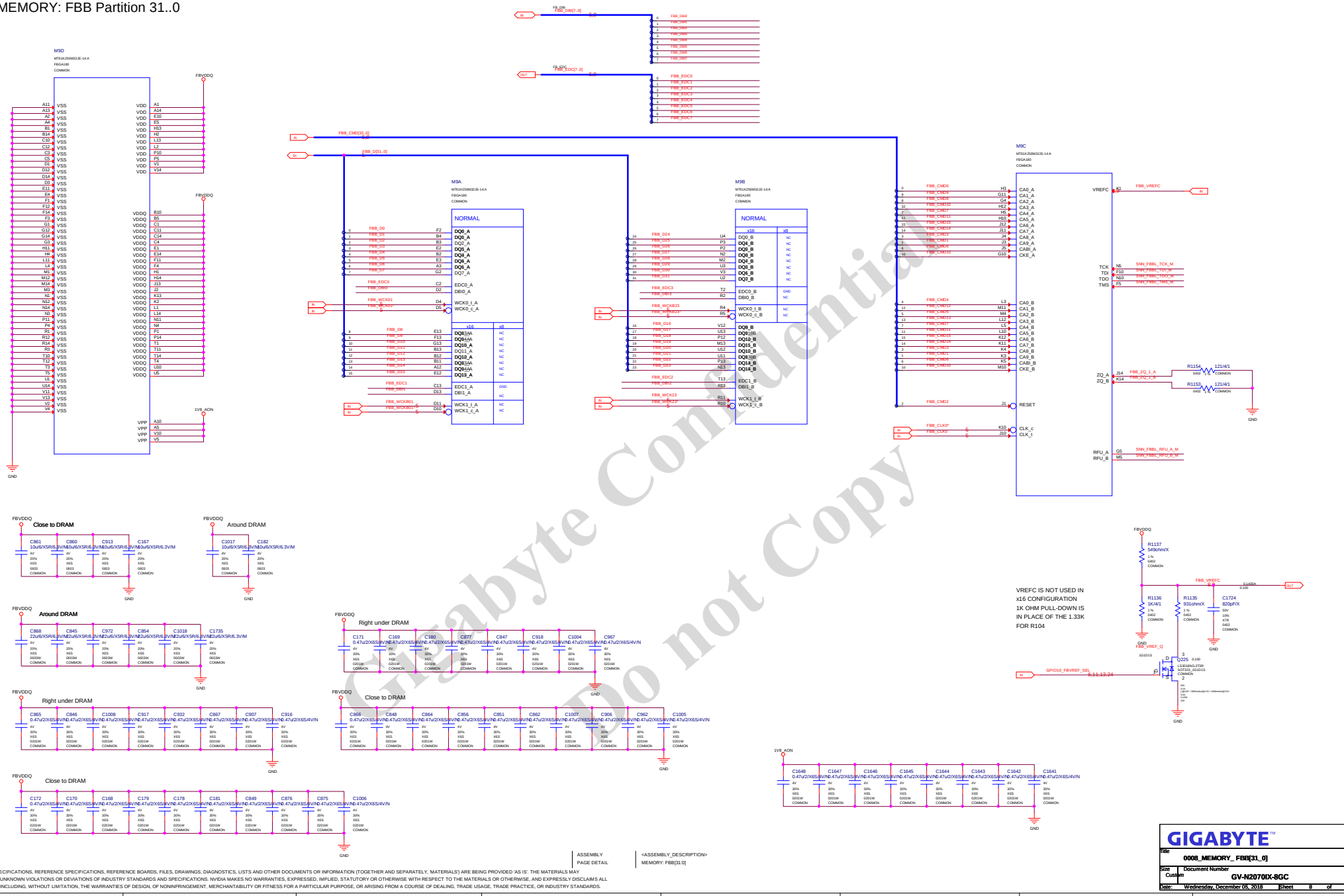
Title	0007_MEMORY_ FBA[63_32]	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 7 of 52	

Page8: MEMORY: FBB Partition 31..0



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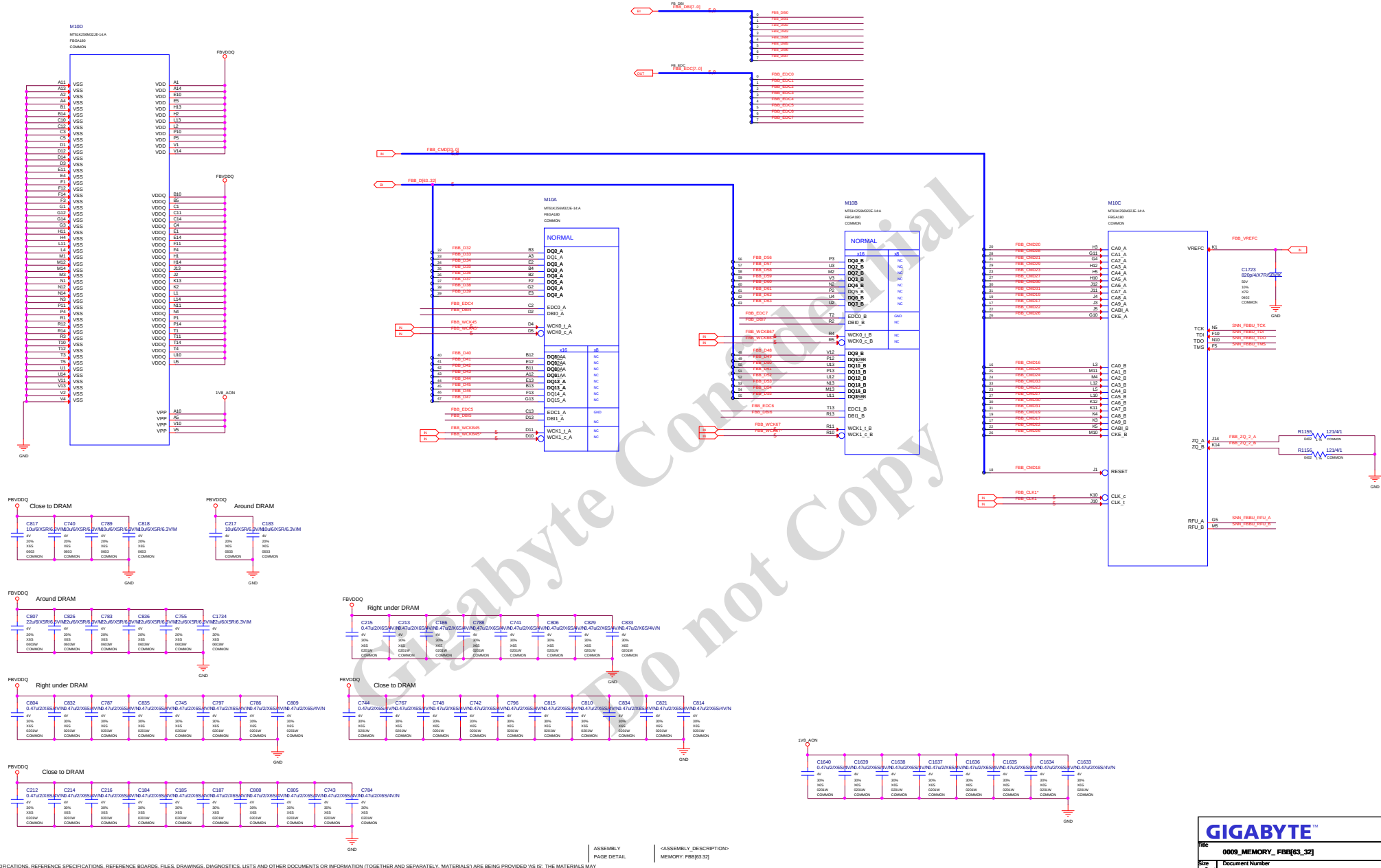
Title	0008_MEMORY_ FBB[31_0]	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 8 of 52	

Page9: MEMORY: FBB Partition 63..32



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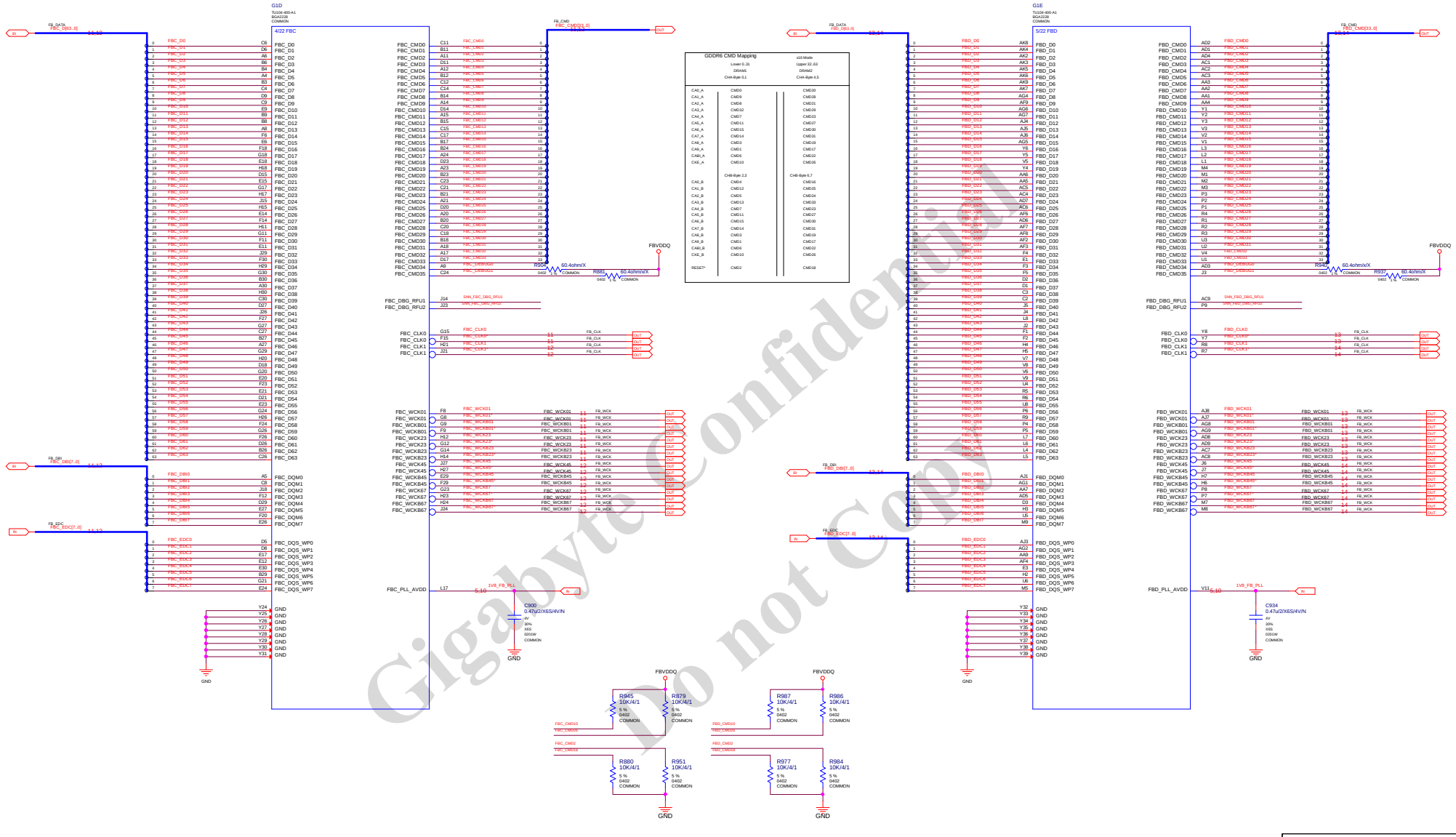
Title	0009_MEMORY_ FBB[63_32]	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 9 of 52	

Page10: MEMORY: GPU Partition C/D



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	MEMORY: GPU FB_CD

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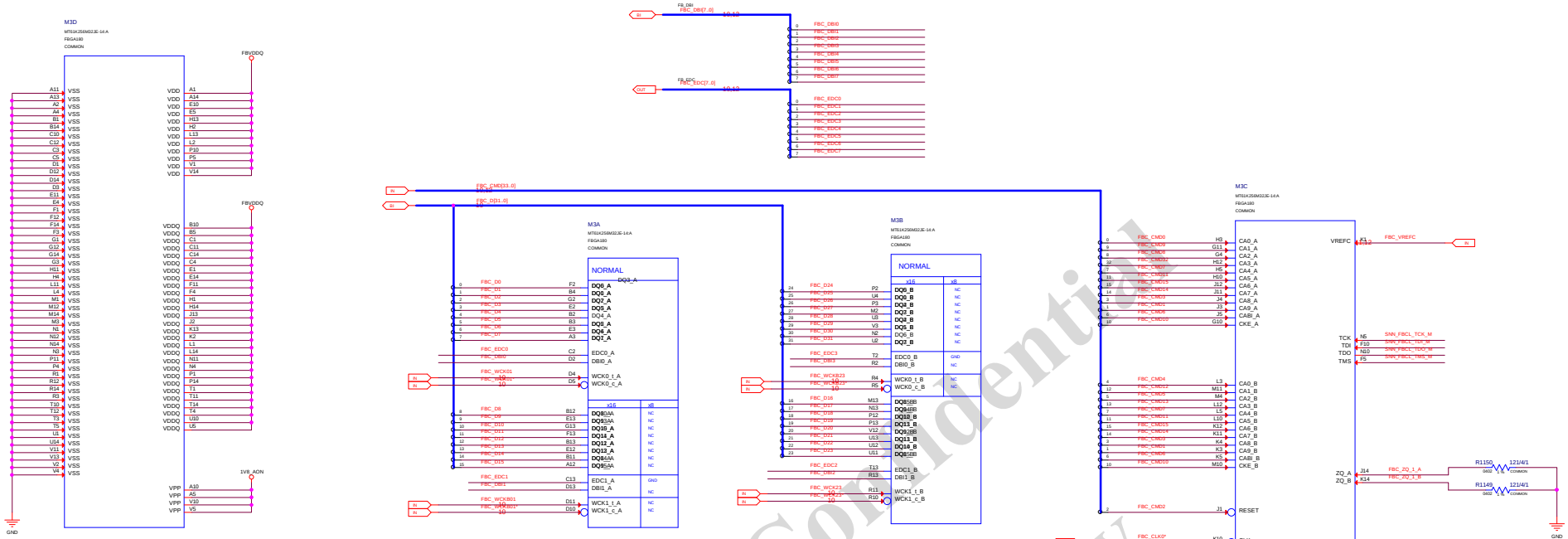
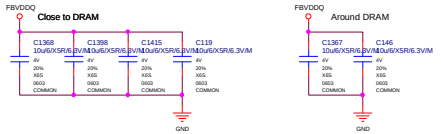
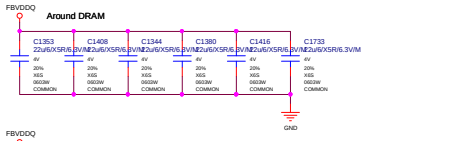
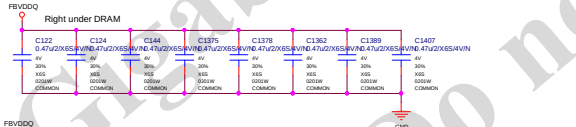
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 10 of 52	

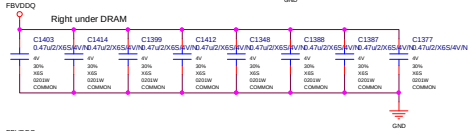
Page11: MEMORY: FBC Partition 31..0

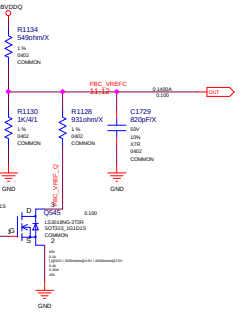












VREFC IS NOT USED IN
x16 CONFIGURATION
1K OHM PULL-DOWN IS
IN PLACE OF THE 1.33K
FOR R75

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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	MEMORY: FBC[31:0]

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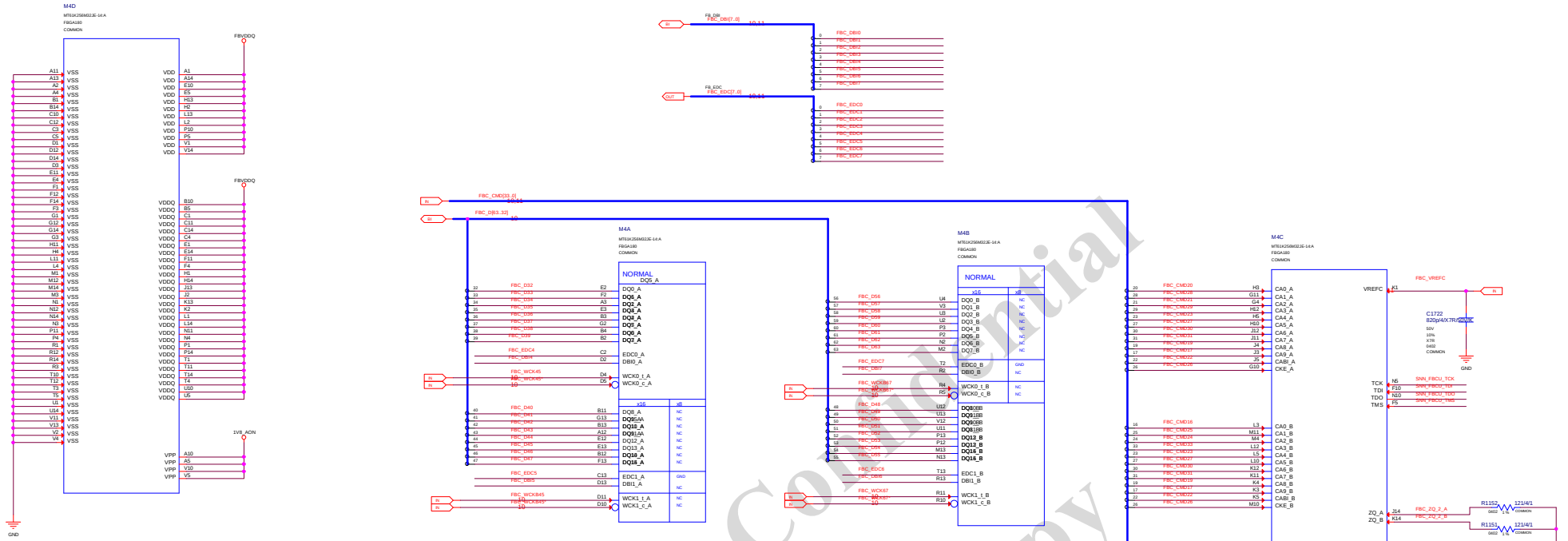
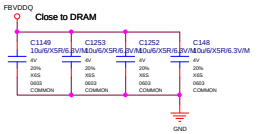
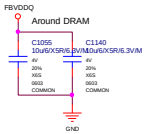
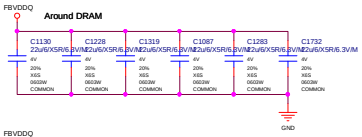
Title	0011_MEMORY_ FBC[31_0]	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 11 of 52	

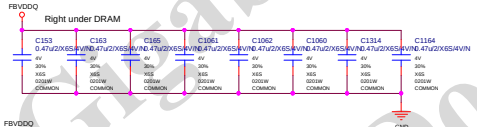
Page12: MEMORY: FBC Partition 63..32

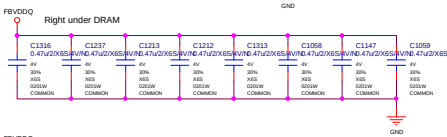


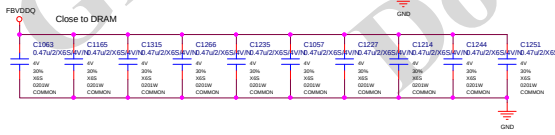


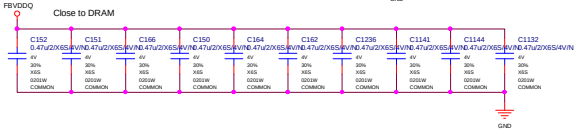












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PAGE DETAIL	MEMORY: FBC[63:32]

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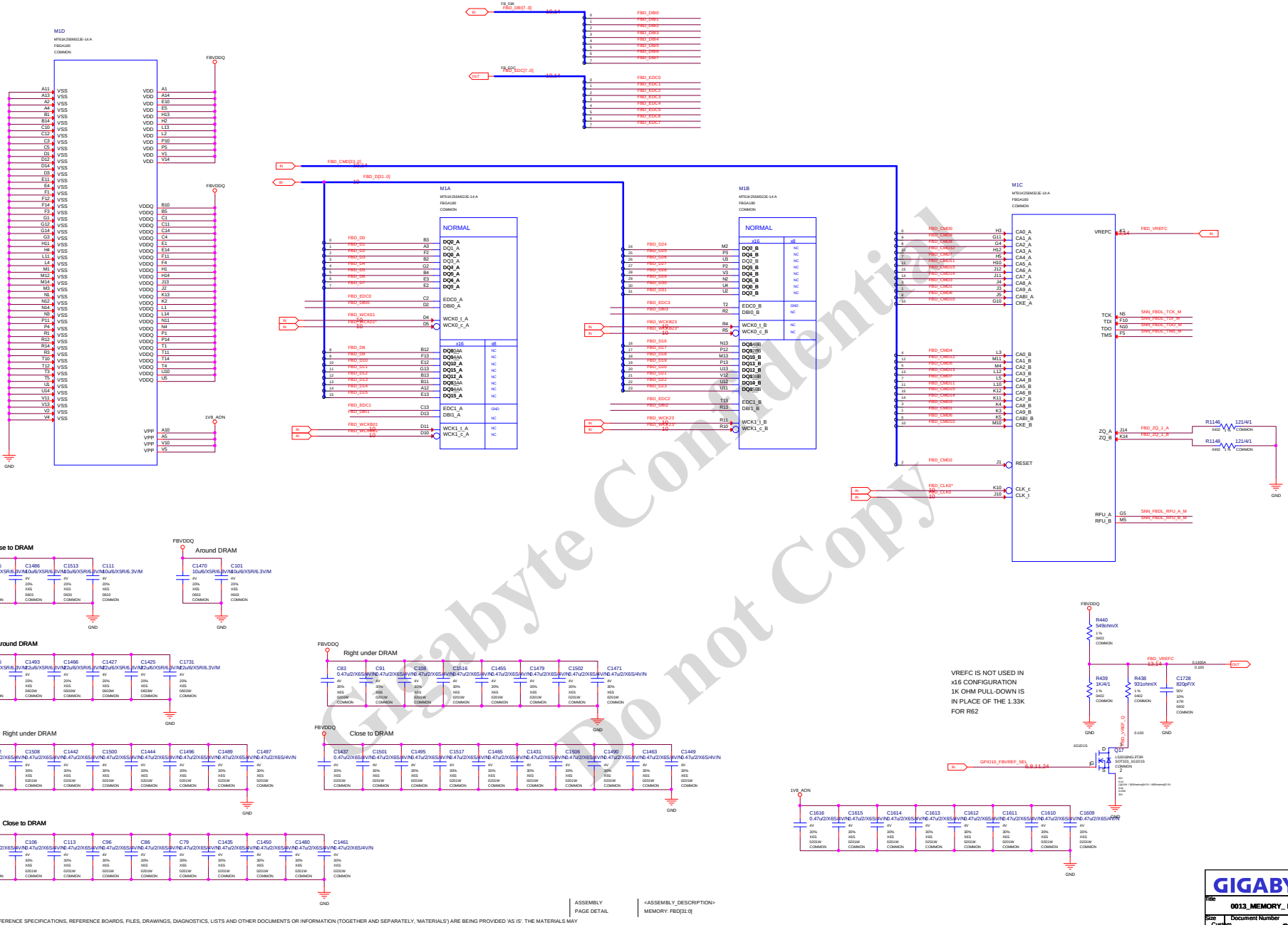
Title	0012_MEMORY_ FBC[63_32]	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 12 of 52	

Page13: MEMORY: FBD Partition 31..0



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	MEMORY: FBD[31:0]

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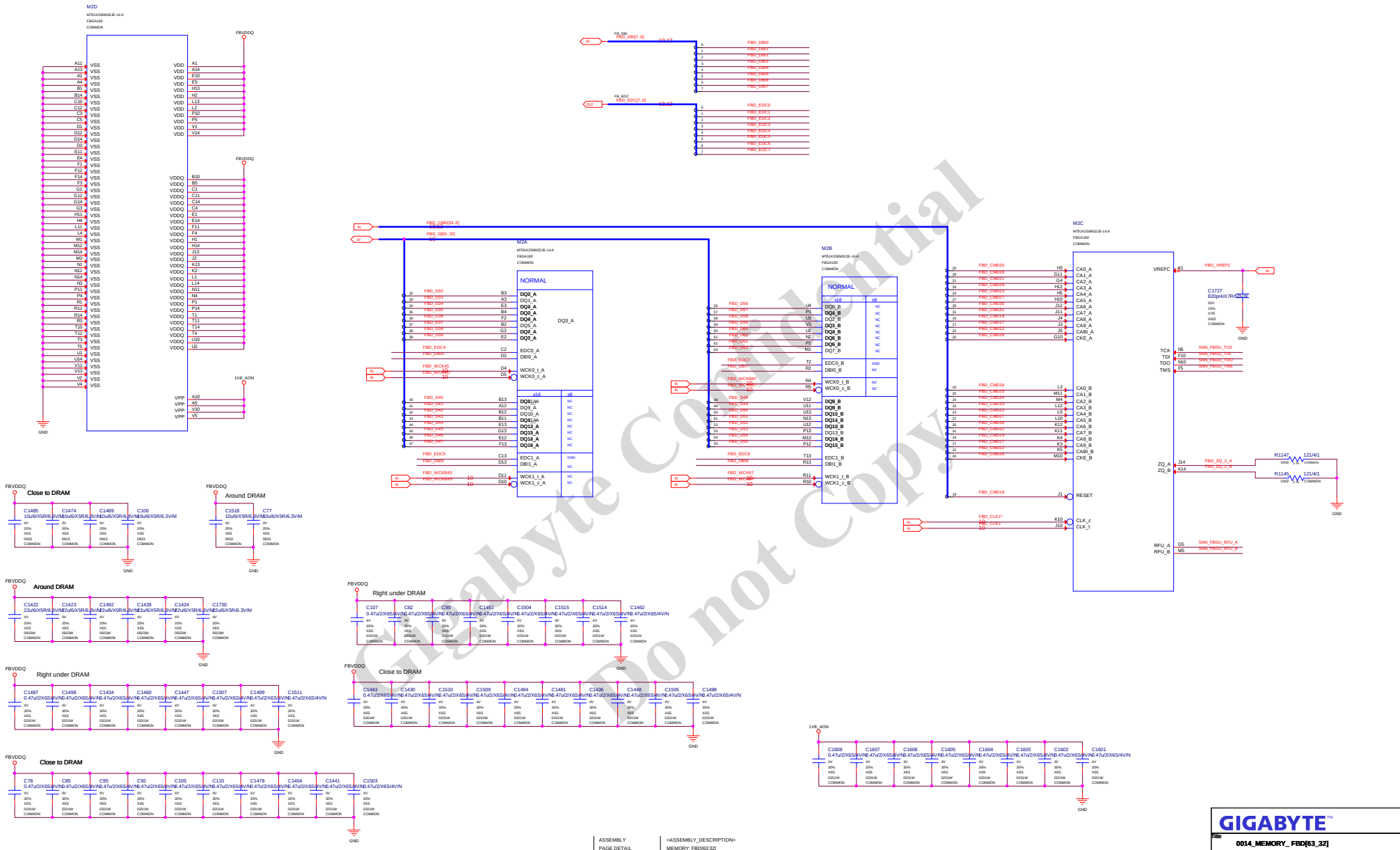
Title	0013_MEMORY_ FBD[31_0]	
Size: Custom	Document Number: GV-N20700IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 13 of 52	



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	MEMORY: FBD[63:32]

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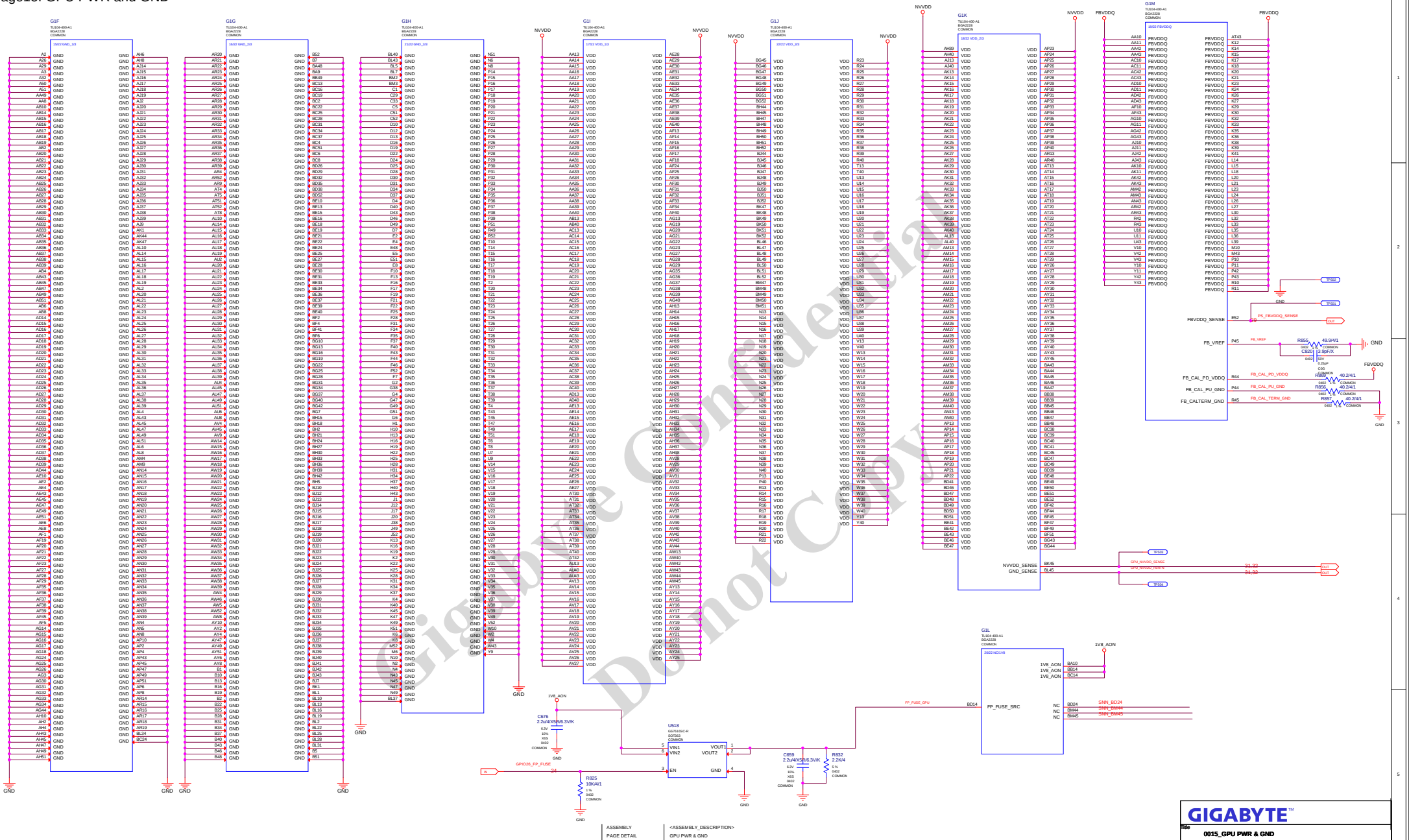
Title	0014_MEMORY_ FBD[63_32]	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 14 of 52	

Page15: GPU PWR and GND



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	GPU PWR & GND

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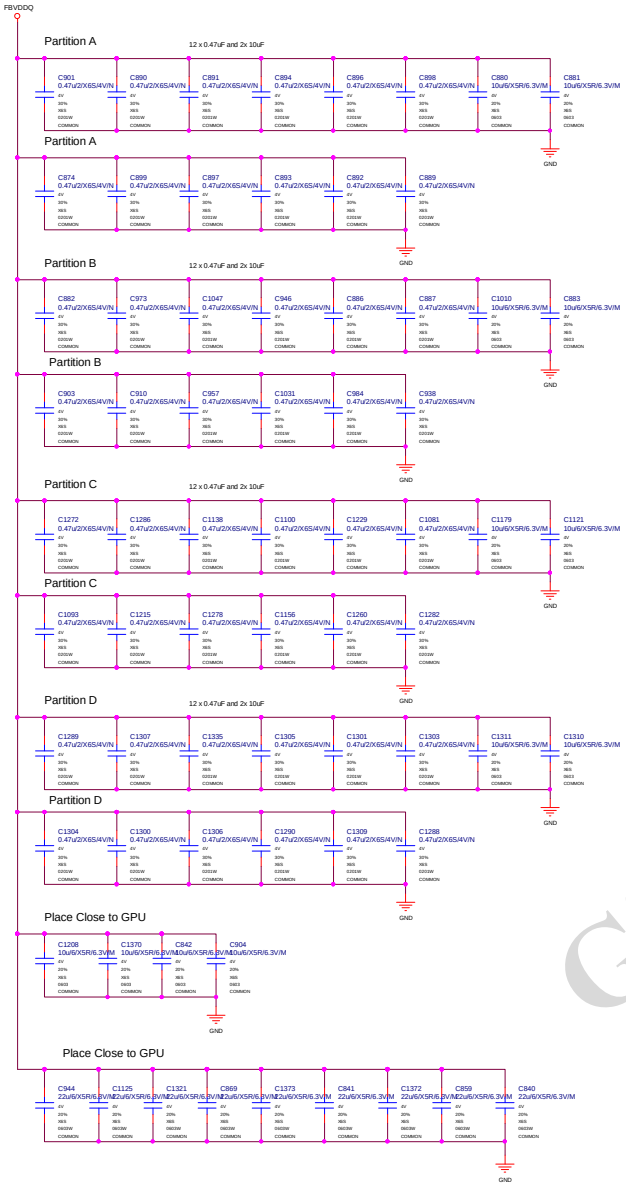
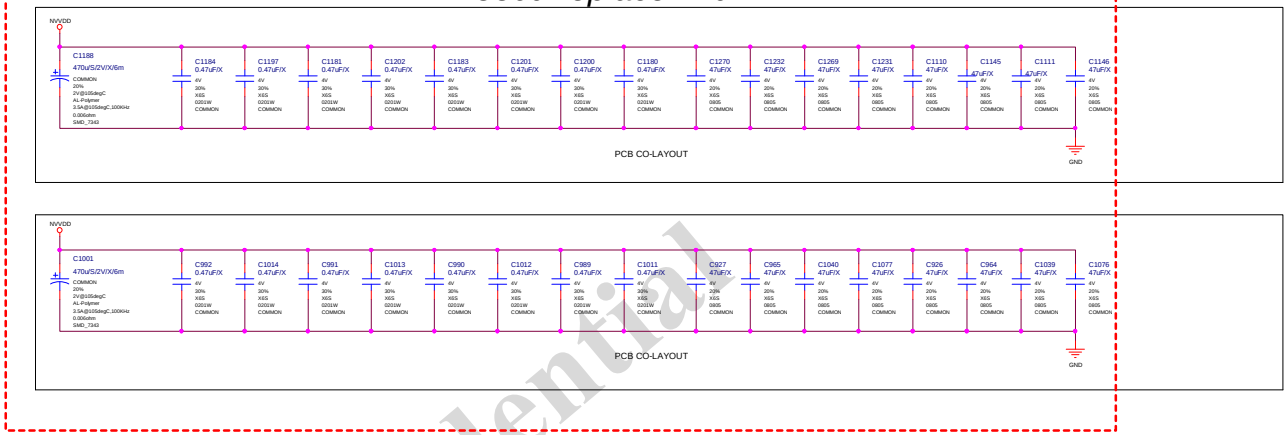
Title	0015_GPU PWR & GND	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 15 of 52	

Page16: GPU Decoupling 1

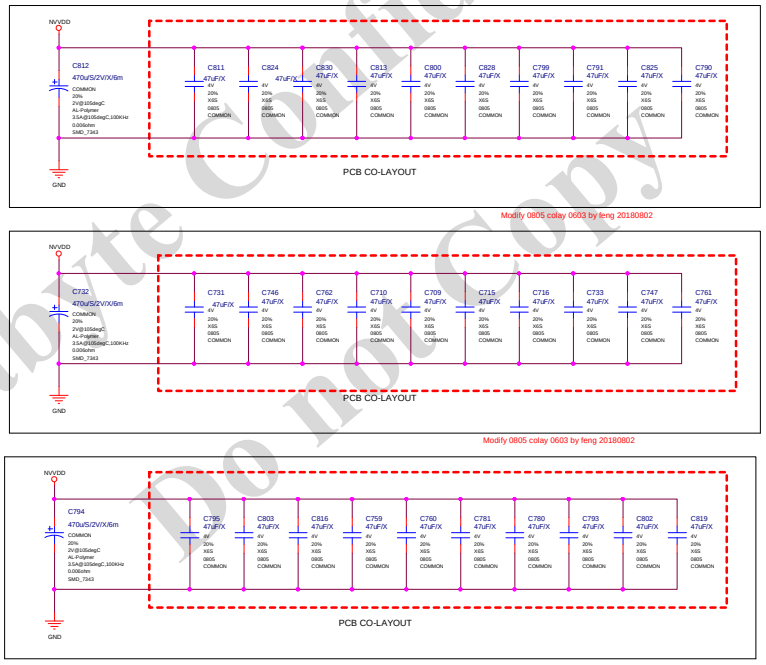
FBVDDQ



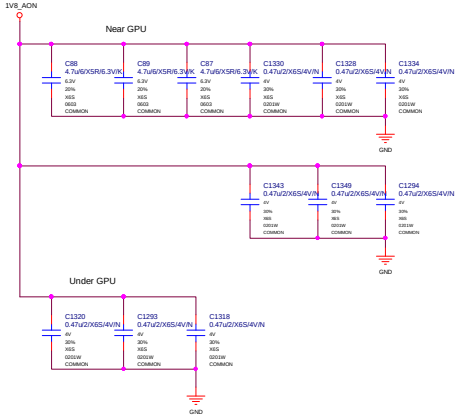
Under GPU



Near GPU



1V8_AON



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	GPU Decoupling

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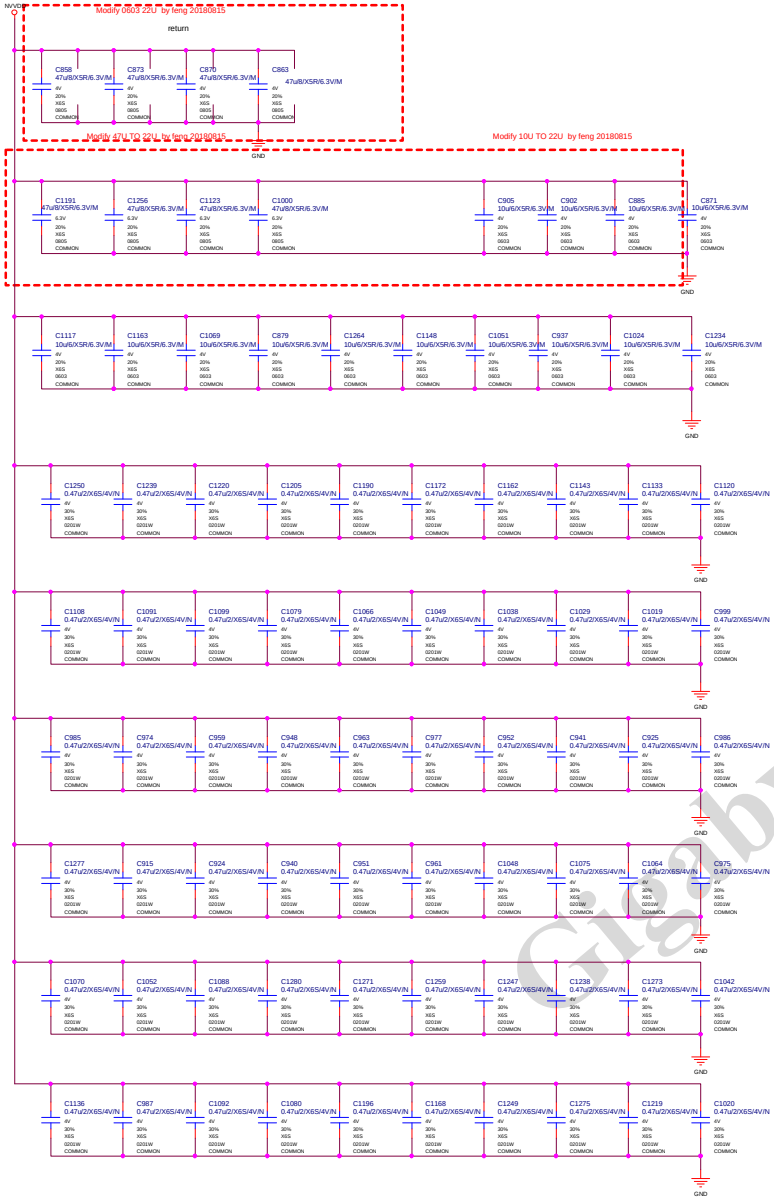
Title	0016_GPU Decoupling	
Size: Custom	Document Number: GV-N20700X-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 16 of 52	

Page17: GPU Decoupling 2

NVVDD



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	GPU Decoupling2

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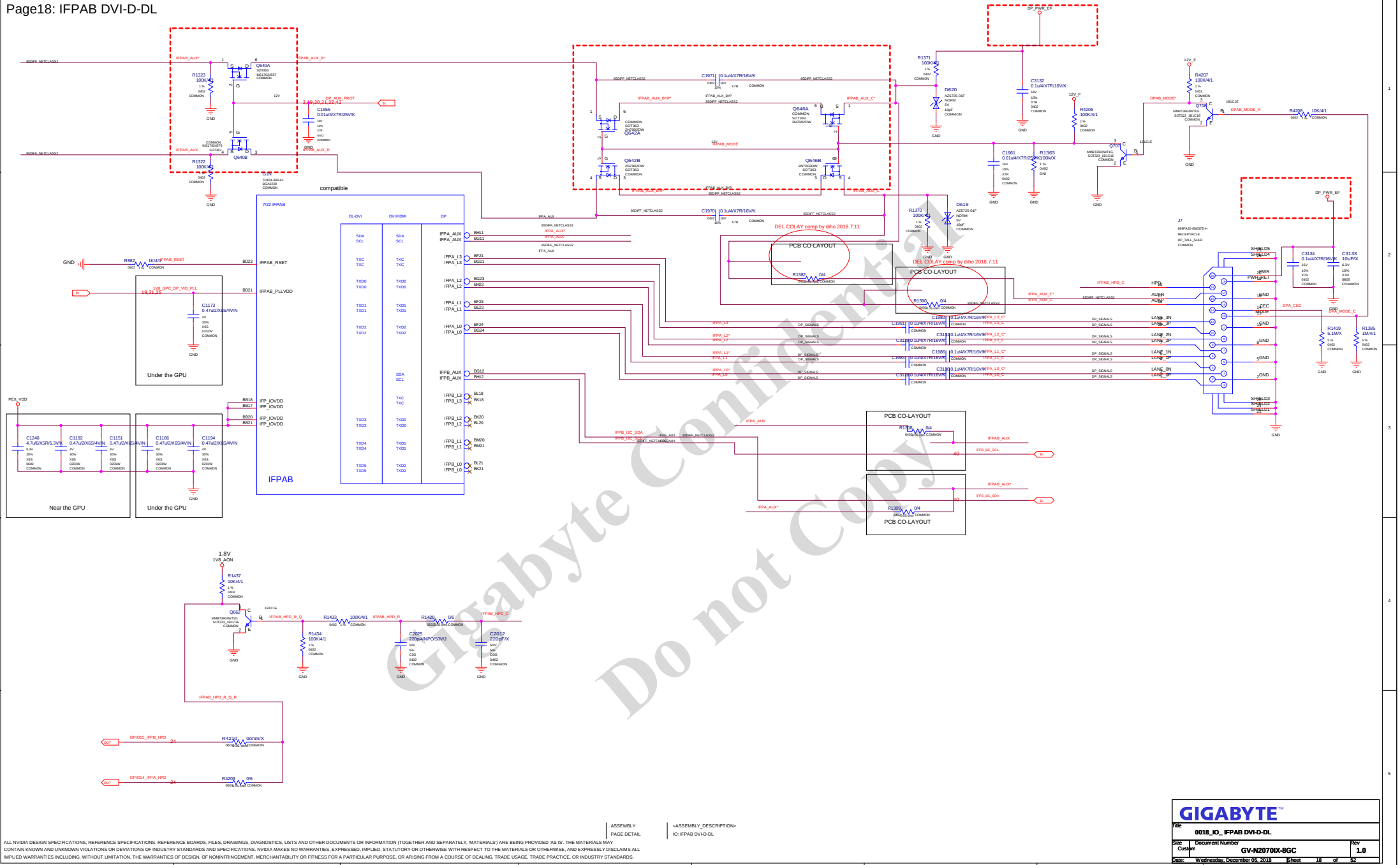
Title	0017_GPU Decoupling2	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 17 of 52	

Page18: IFPAB DVI-D-DL



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	IO: IFPAB DVI-D-DL

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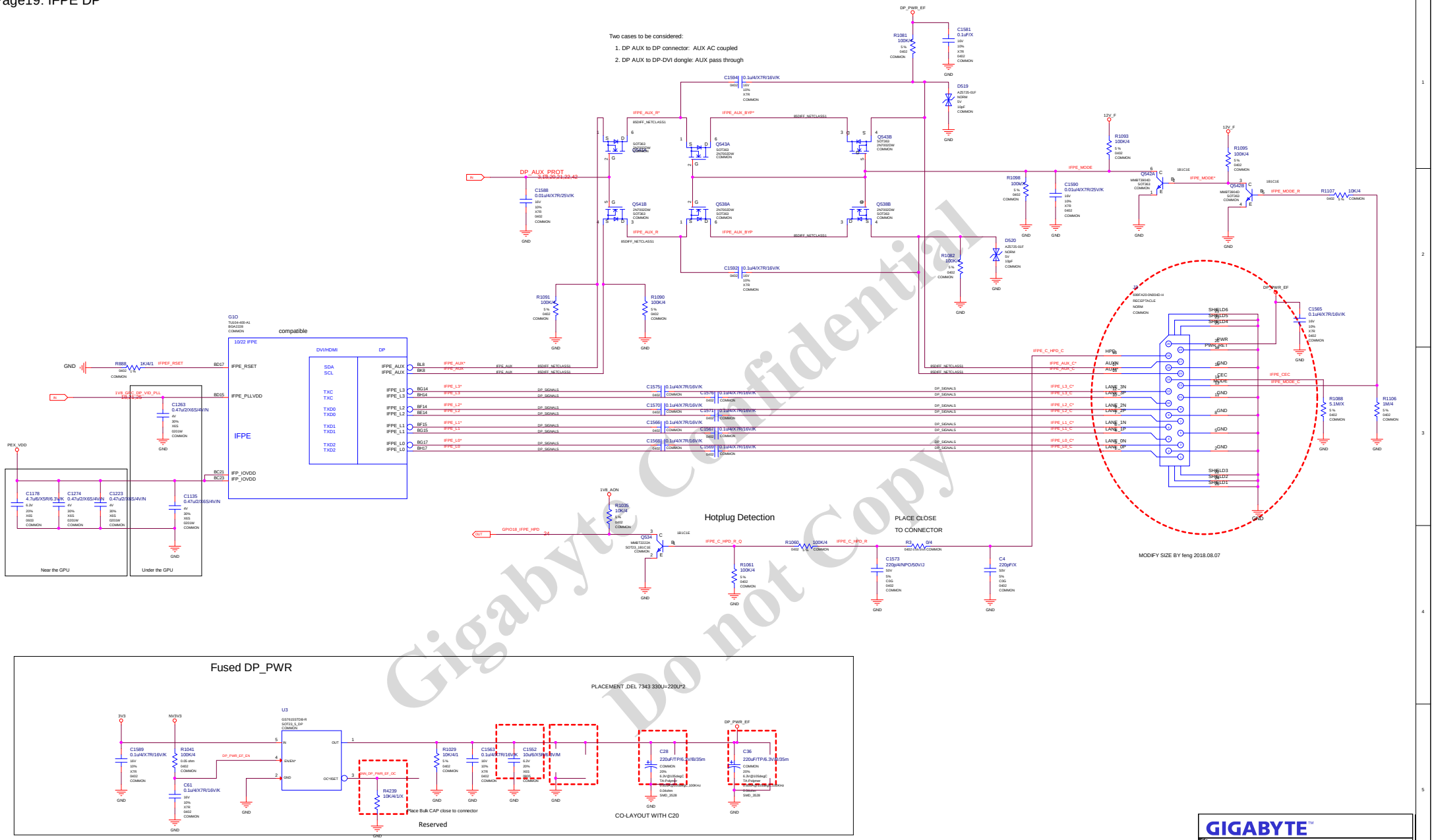
Title	0018_IO_ IFPAB DVI-D-DL	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 18 of 52	

Page19: IFPE DP



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	IO: IFPE DP

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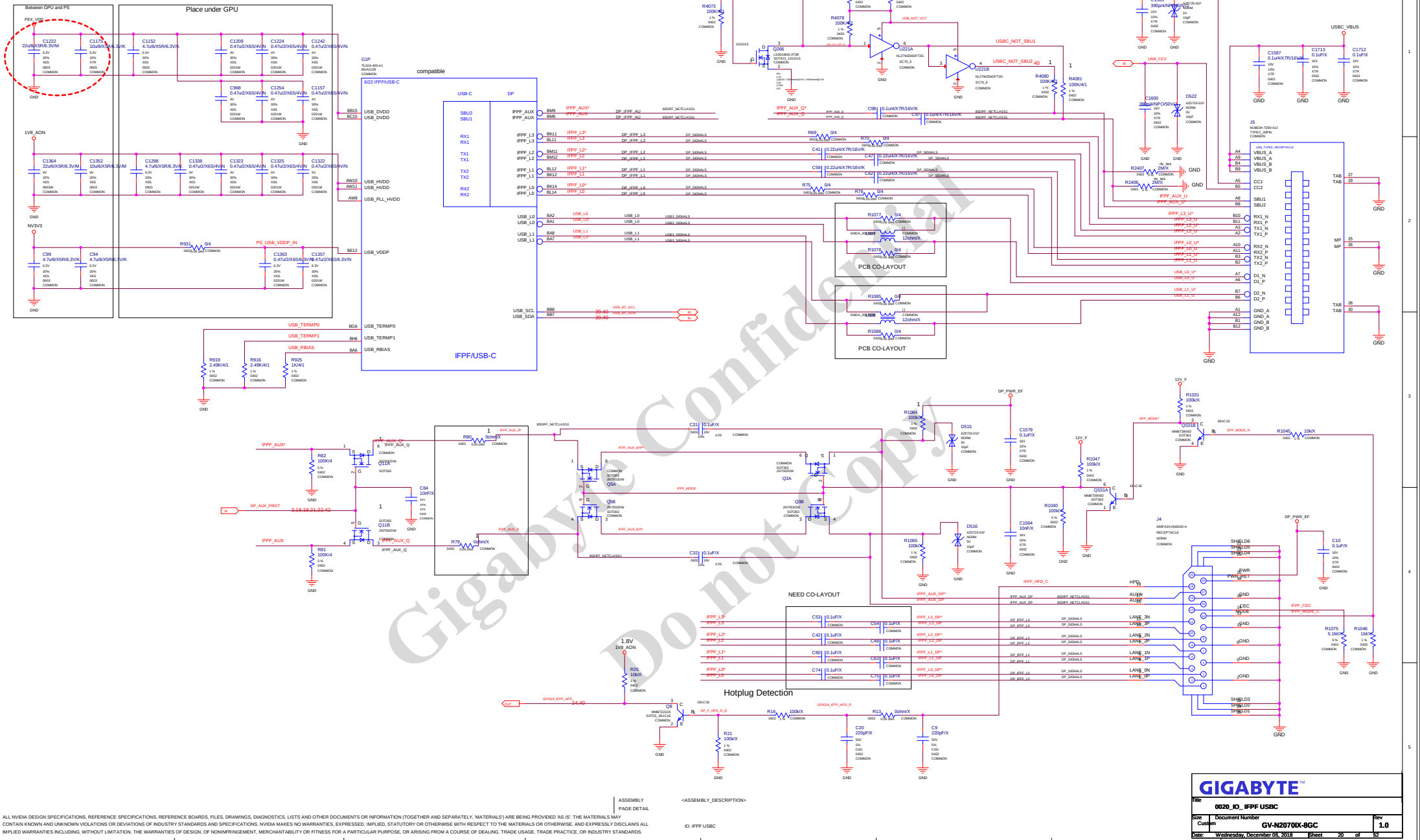
Title	0019_IO_ IFPE DP	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 19 of 52	

Page20: IFPF USB-C



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PAGE DETAIL	<ASSEMBLY_DESCRIPTION>
IO: IFPF USBC	

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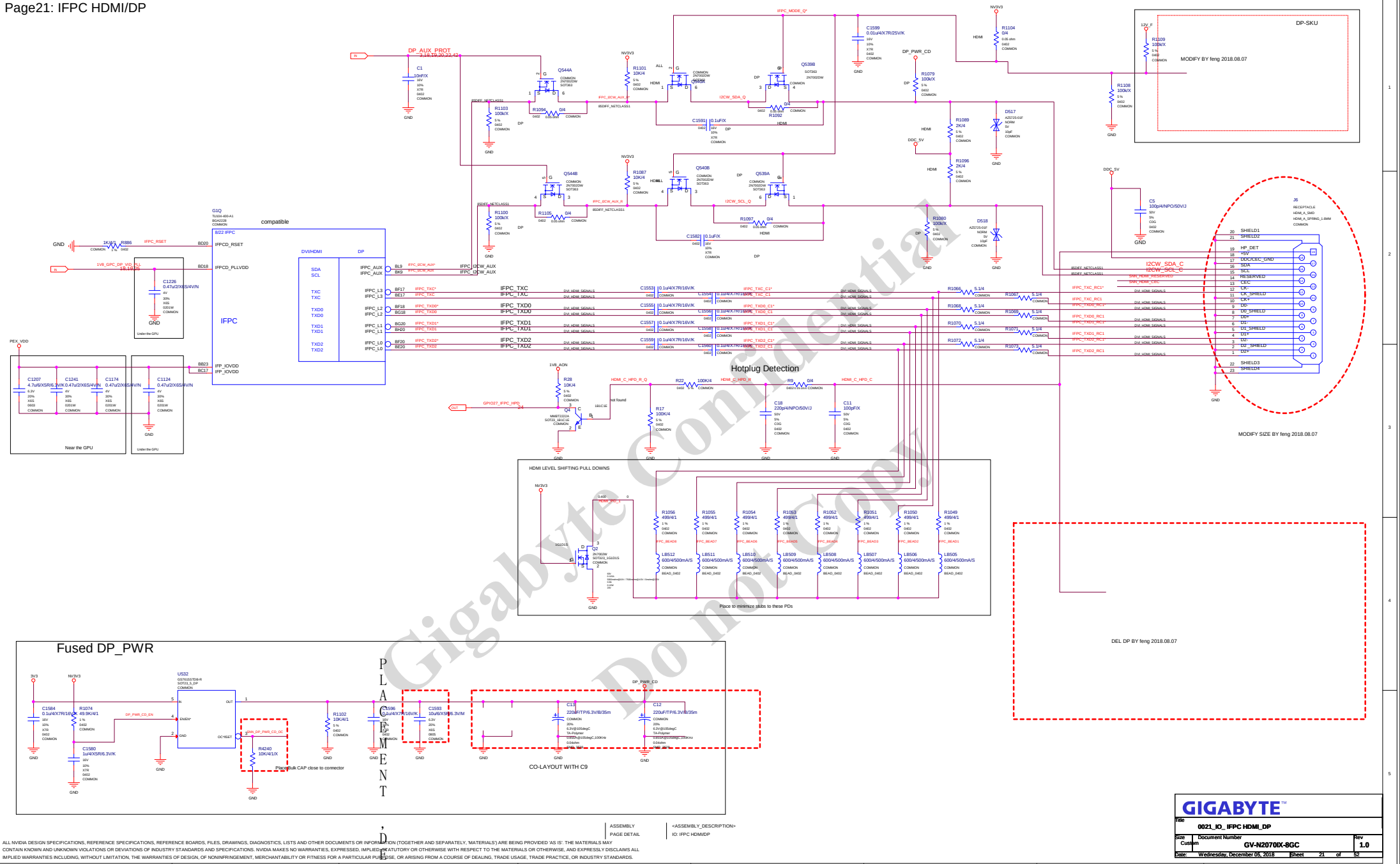
Title	0020_IO_ IFPF USBC	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 20 of 52	

21-JUN-2018



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PLACEMENT, DL 7343, 2

ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	IO: IFPC HDMI/DP

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Title	0021_IO_ IFPC HDMI_DP	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 21 of 52	

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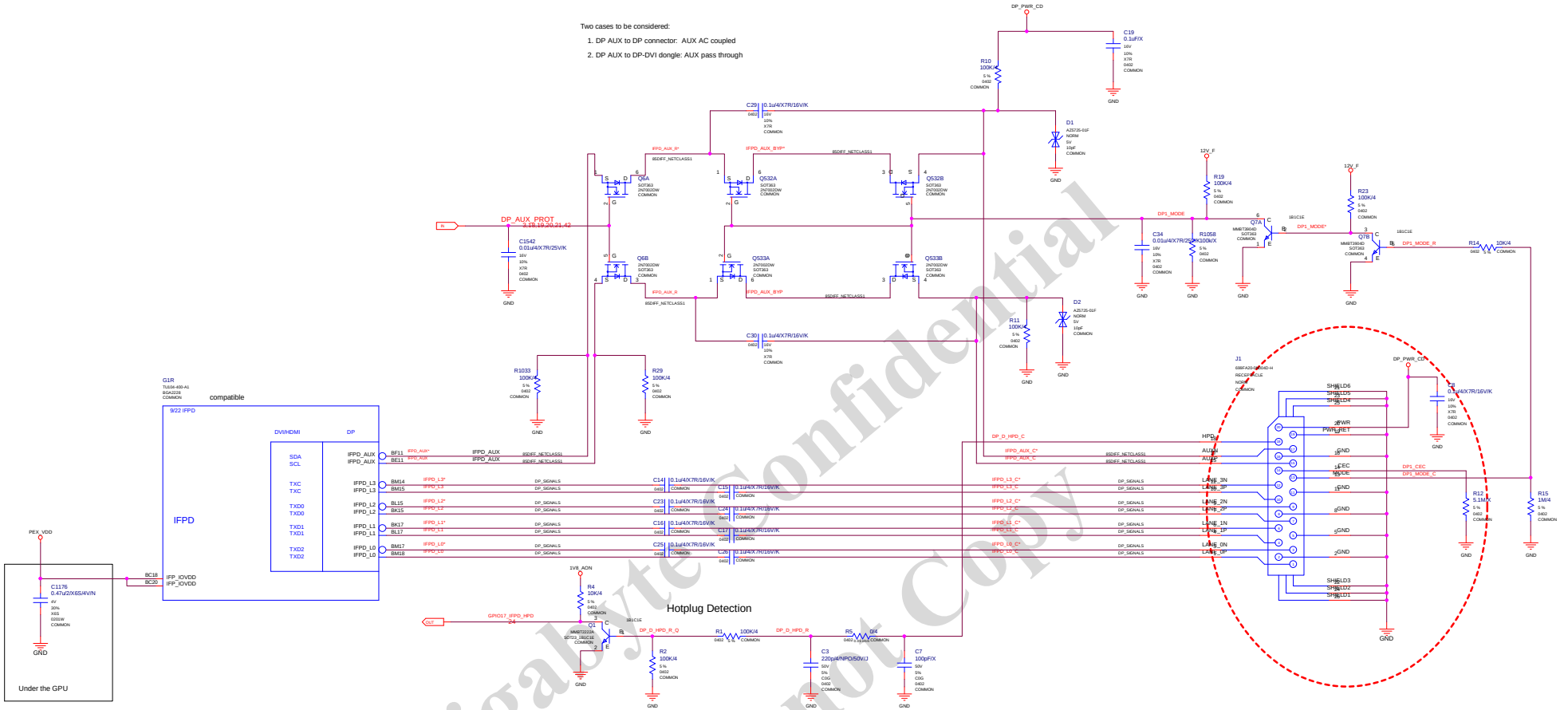
Page22: IFPD DP



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ASSEMBLY <ASSEMBLY_DESCRIPTION>
PAGE DETAIL IO: IFPD DP

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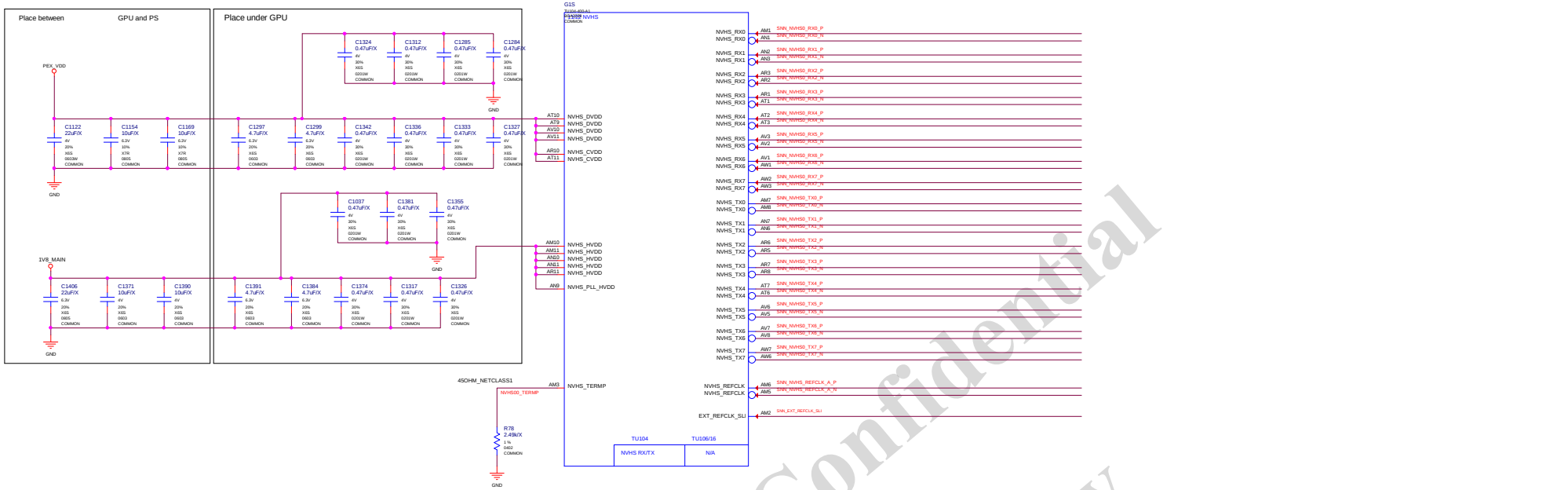
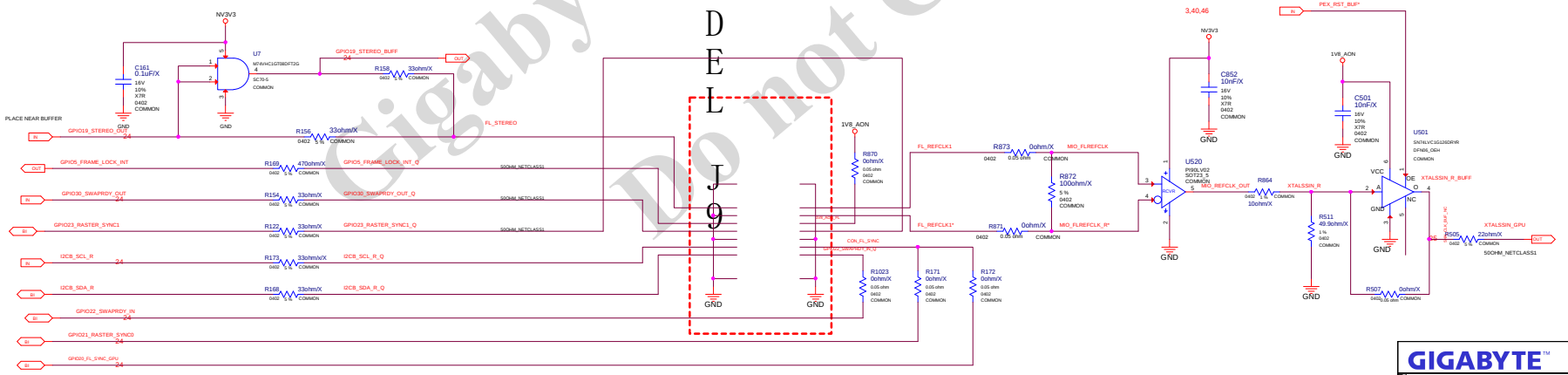
Title	0022_IO_ IFPD DP	
Size: Custom	Document Number: GV-N20700X-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 22 of 52	

Page23: NVHS Interface and Frame Lock





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ASSEMBLY
PAGE DETAIL

<ASSEMBLY_DESCRIPTION>
IO: NVHS Interface and Frame Lock

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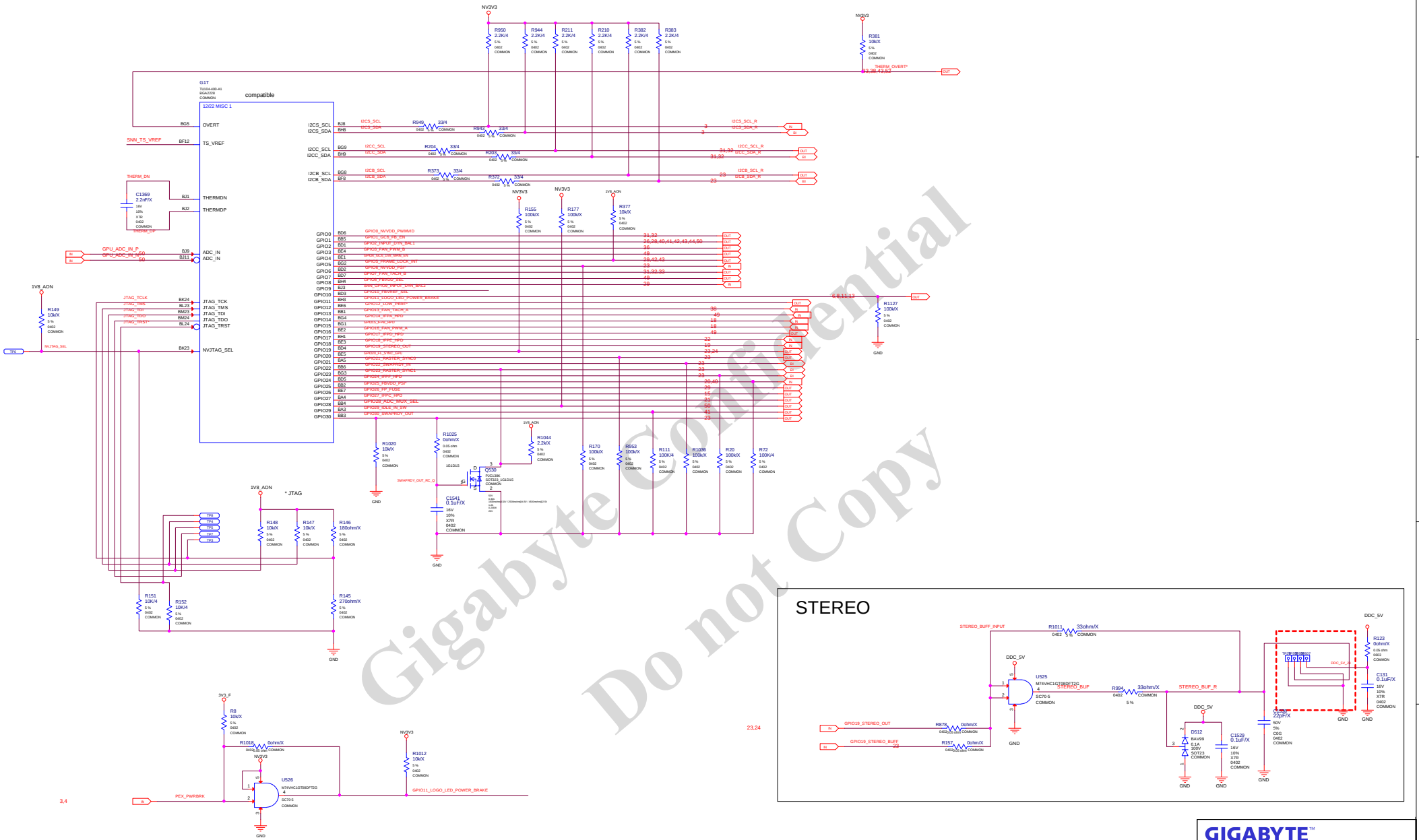
Title	0023_IO_ NVHS Interface and Fra	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 23 of 52	

Page24: MISC1: Fan, Thermal, JTAG, GPIO



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ASSEMBLY
PAGE DETAIL

<ASSEMBLY_DESCRIPTION>
MISC1: Thermal, JTAG, GPIO,STEREO

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Title	0024_MISC1_ Thermal, JTAG, GPIO	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 24 of 52	

Page25: MISC2: ROM, XTAL, Straps

STRAP2	STRAP1	STRAP0	RAMCFG[4:0]
L	L	L	00000
L	L	H	00001
L	H	L	00010
L	H	H	00011
H	H	L	00110
H	H	H	00111
L	L	M	01000

RAMCFG[4:0]	DENSITY	WIDTH	VENDOR	DIE
00000	8Gb	256-bit	Samsung	C
00001	8Gb	256-bit	Micron	A
00010	8Gb	256-bit	Hynix	M
00011	8Gb	256-bit	Samsung	C
00100	8Gb	256-bit	Micron	A
00101	8Gb	256-bit	Hynix	M
00110	16Gb	256-bit	Samsung	M
00111				

ROM_SO	ROM_SI	ROM_SCLK	DUMMY[2:0],FS_OVERT	1:ENABLE 0:DISABLE	
L	L	L	XXX1	FS_OVERT ENABLE	DEFAULT
L	L	M	XXX0	FS_OVERT DISABLE	

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1 DEFAULT
L	L	L	0	0	0	0

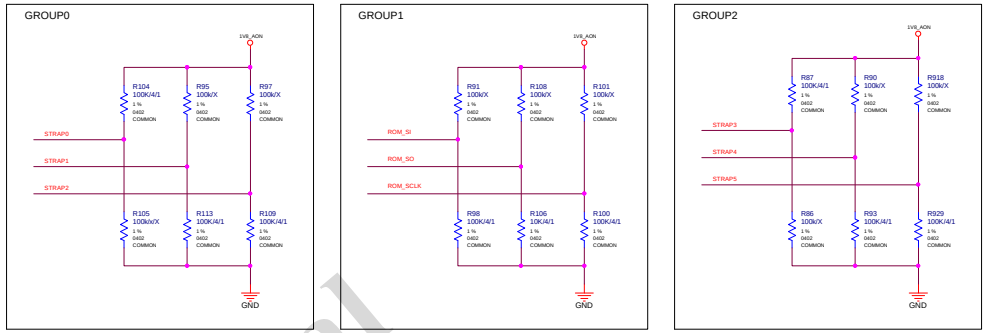
H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V

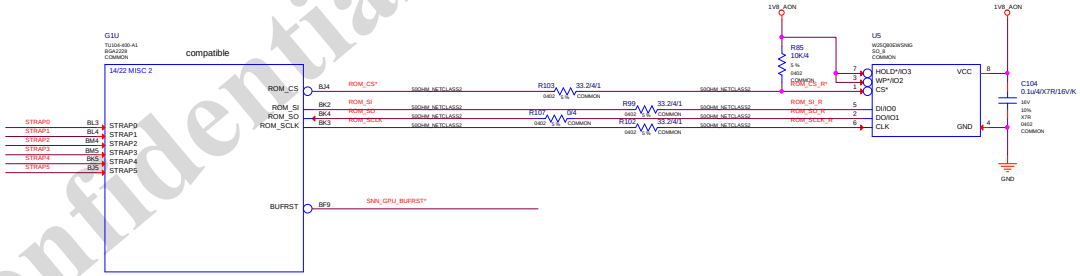
1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE

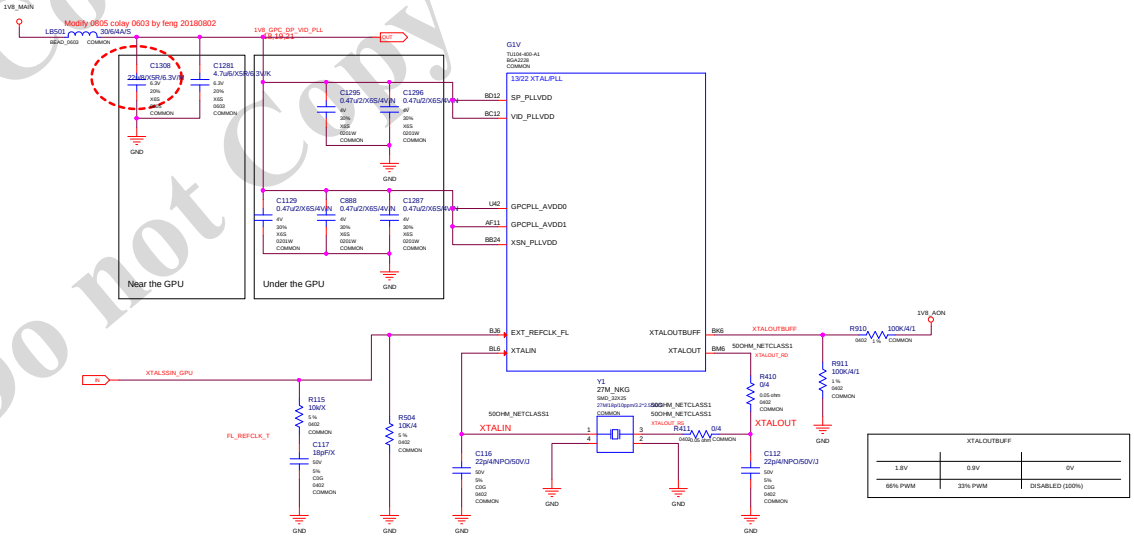
1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGNAL

1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER

1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE







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ASSEMBLY
PAGE DETAIL
<ASSEMBLY_DESCRIPTION>
MISC2: ROM, XTAL,STRAPS

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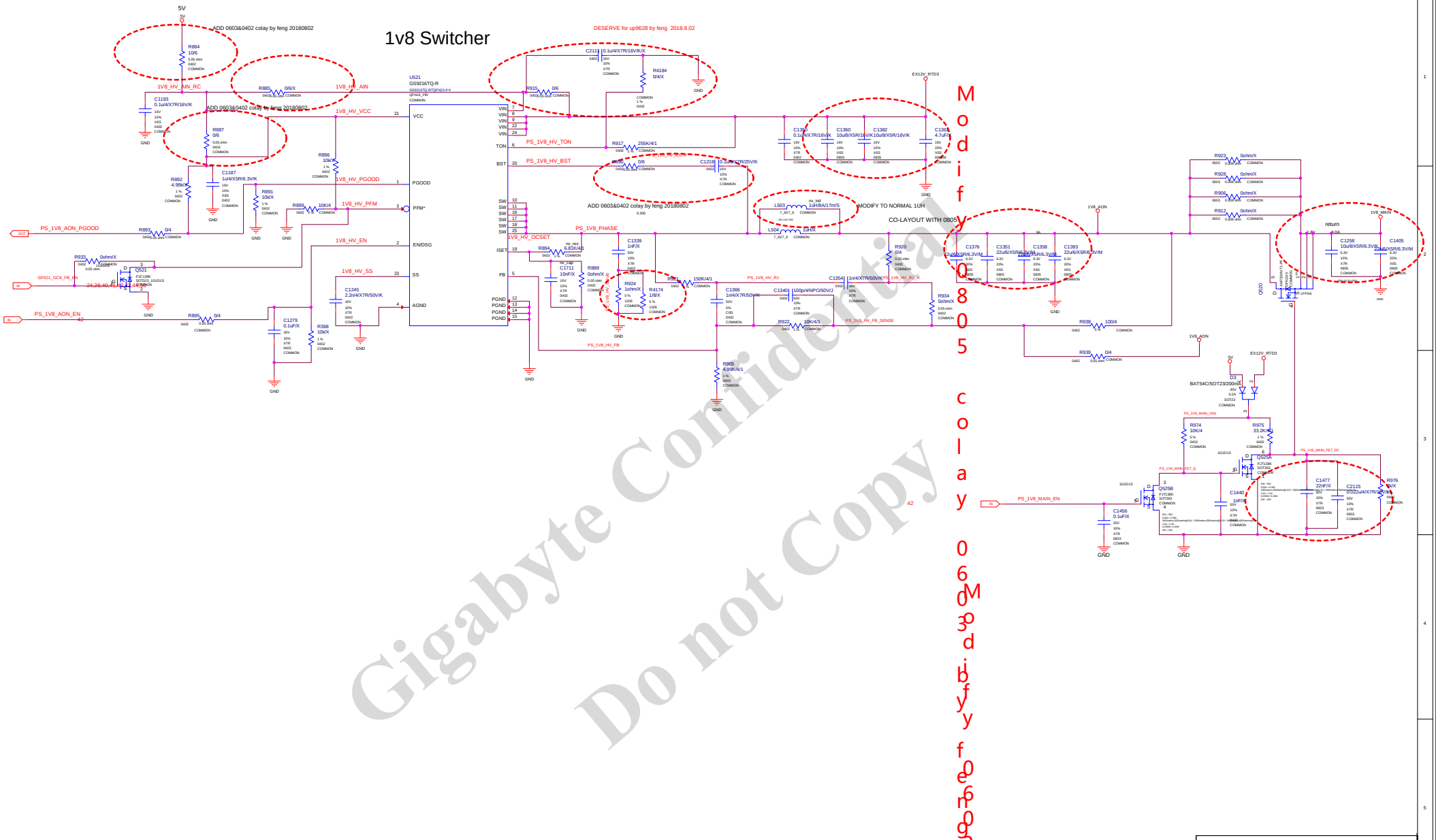
Title	0025_MISC2_ ROM, XTAL,STRAPS	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 25 of 52	



ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: 1V8_PLL, 1V8_AON

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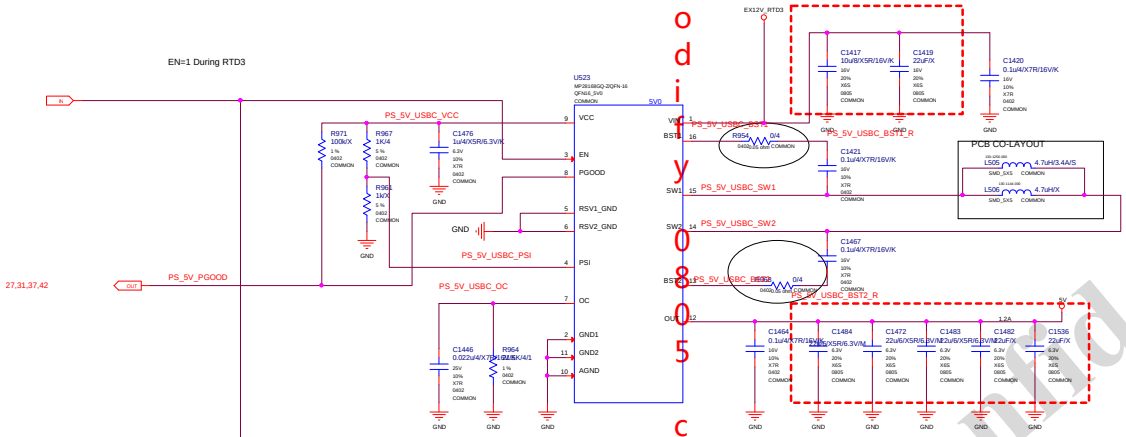
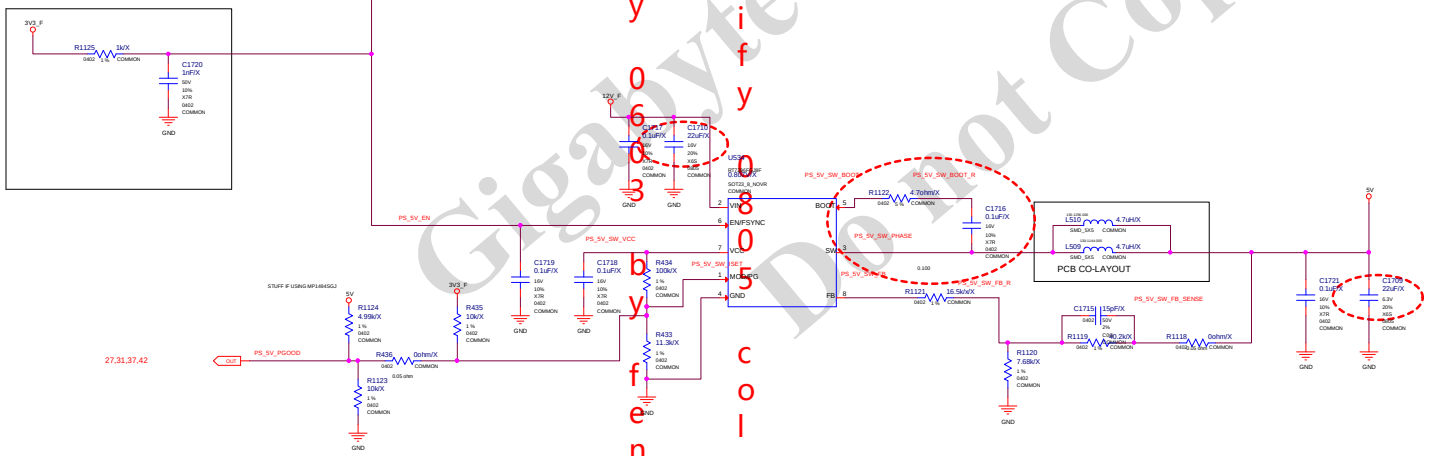
Title	0026_PS_ 1V8_PLL, 1V8_AON	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 26 of 52	





Modify 0805 colay 0603 by Feng Modify 0805 colay 0603 by 20180808

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ASSEMBLY | <ASSEMBLY_DESCRIPTION>
PAGE DETAIL | PS: 5V

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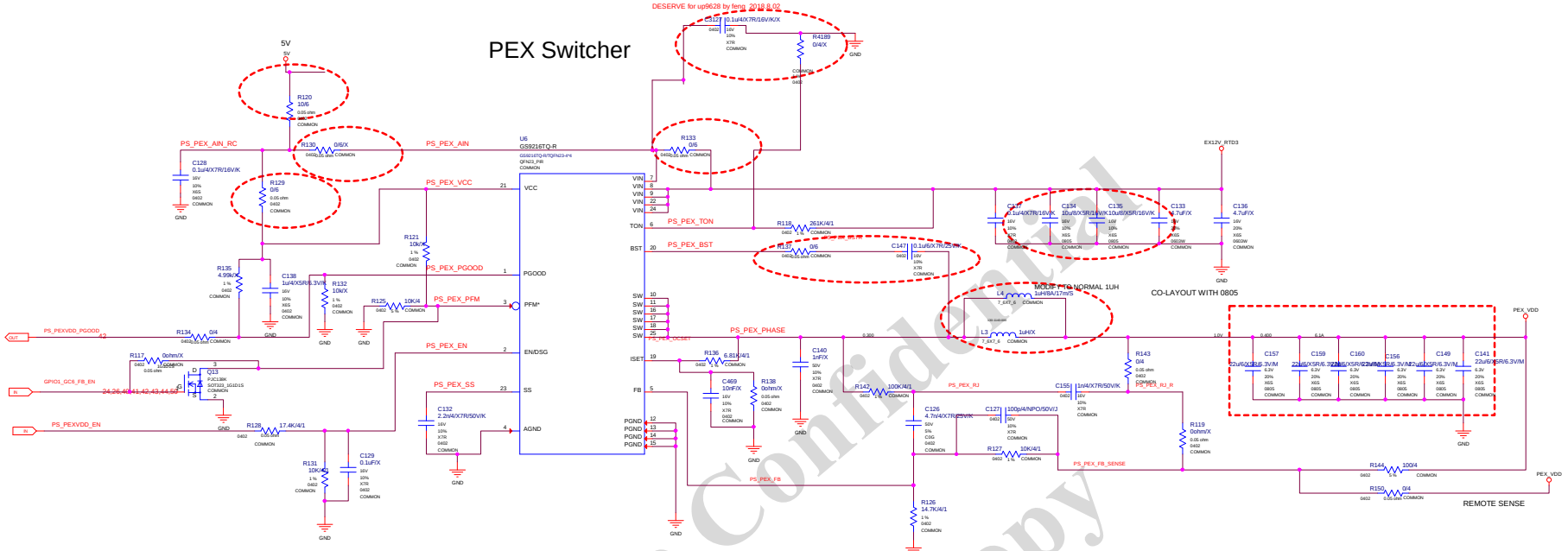
Title	0027_PS_ 5V	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 27 of 52	



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ASSEMBLY <ASSEMBLY_DESCRIPTION>
PAGE DETAIL PS: PEXVDD

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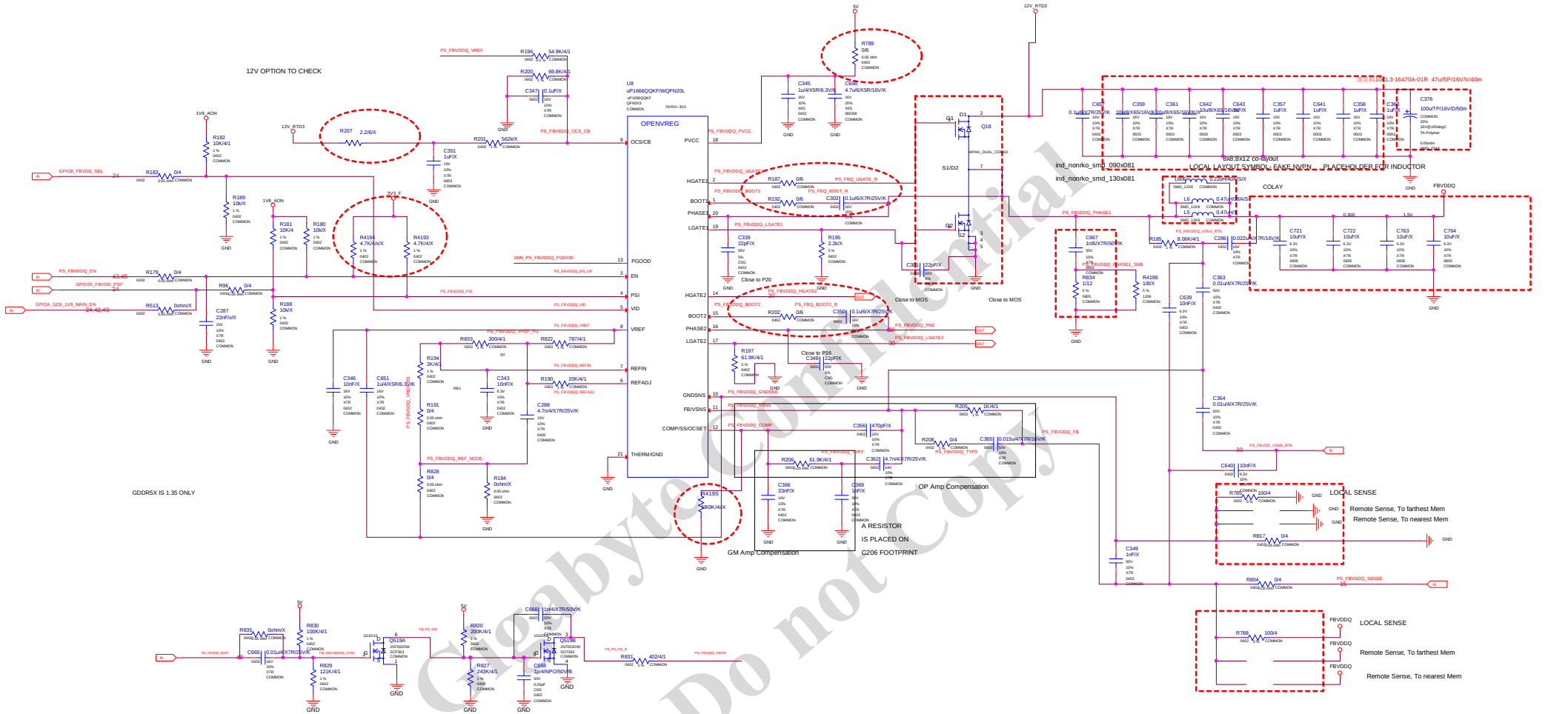
Title	0028_PS_ PEXVDD		
Size Custom	Document Number GV-N2070IX-8GC		Rev 1.0
Date: Wednesday, December 05, 2018	Sheet 28	of 52	



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: FBVDDQ

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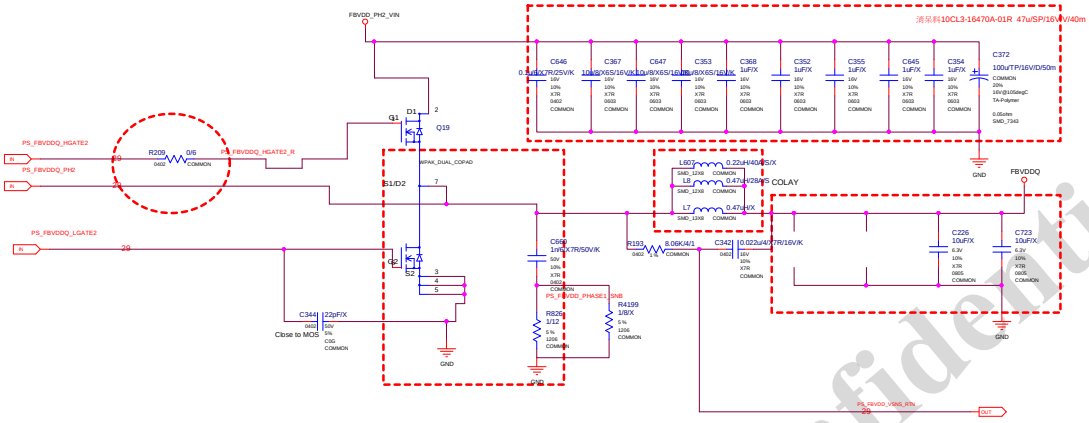
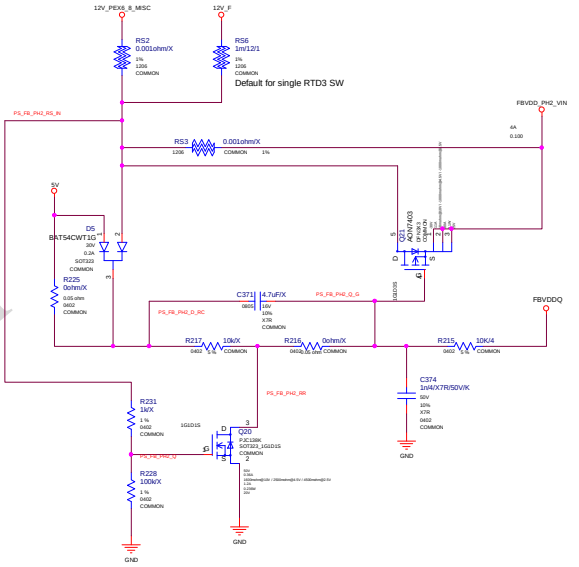
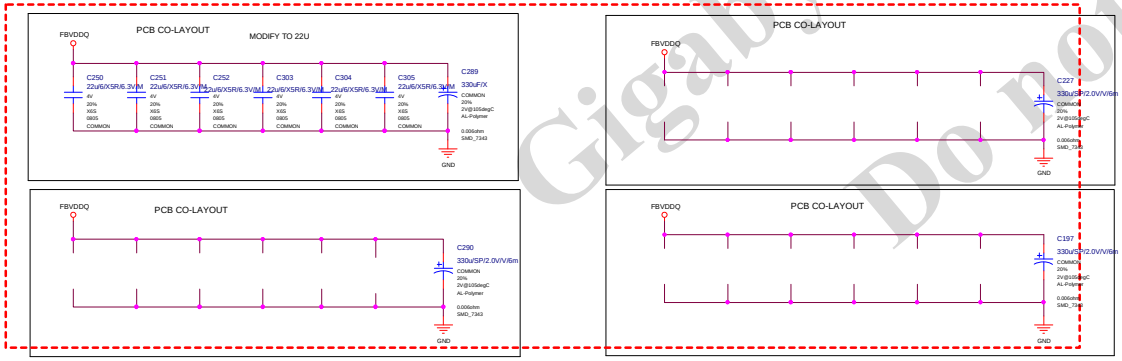
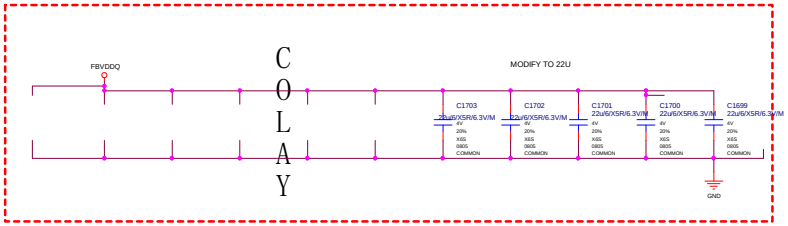
Title	0029_PS_ FBVDDQ	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 29 of 52	

Page30: PS: FBVDDQ PH2









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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: FBVDDQ PH2

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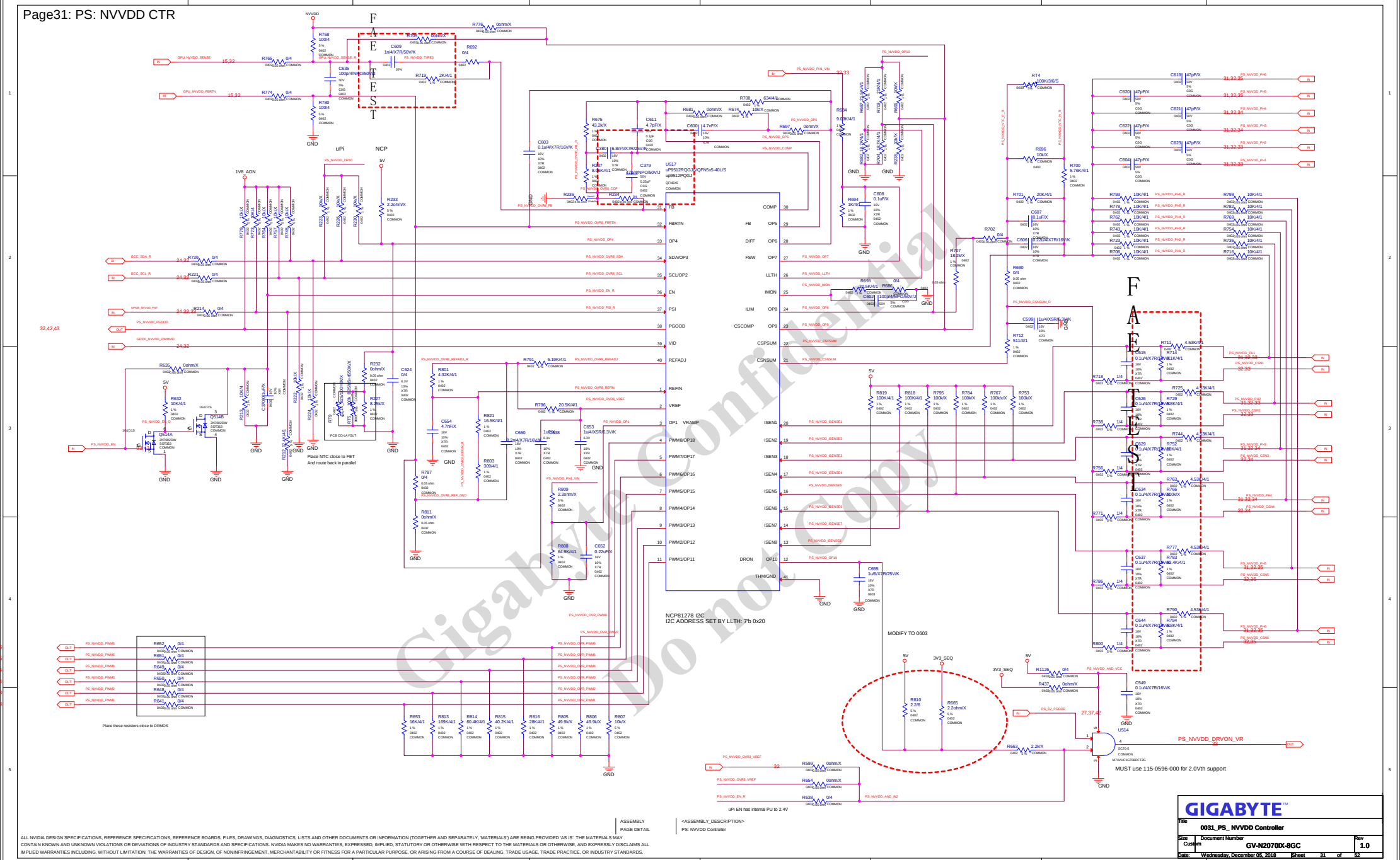
Title	0030_PS_ FBVDDQ PH2	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 30 of 52	



ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: NVVDD Controller

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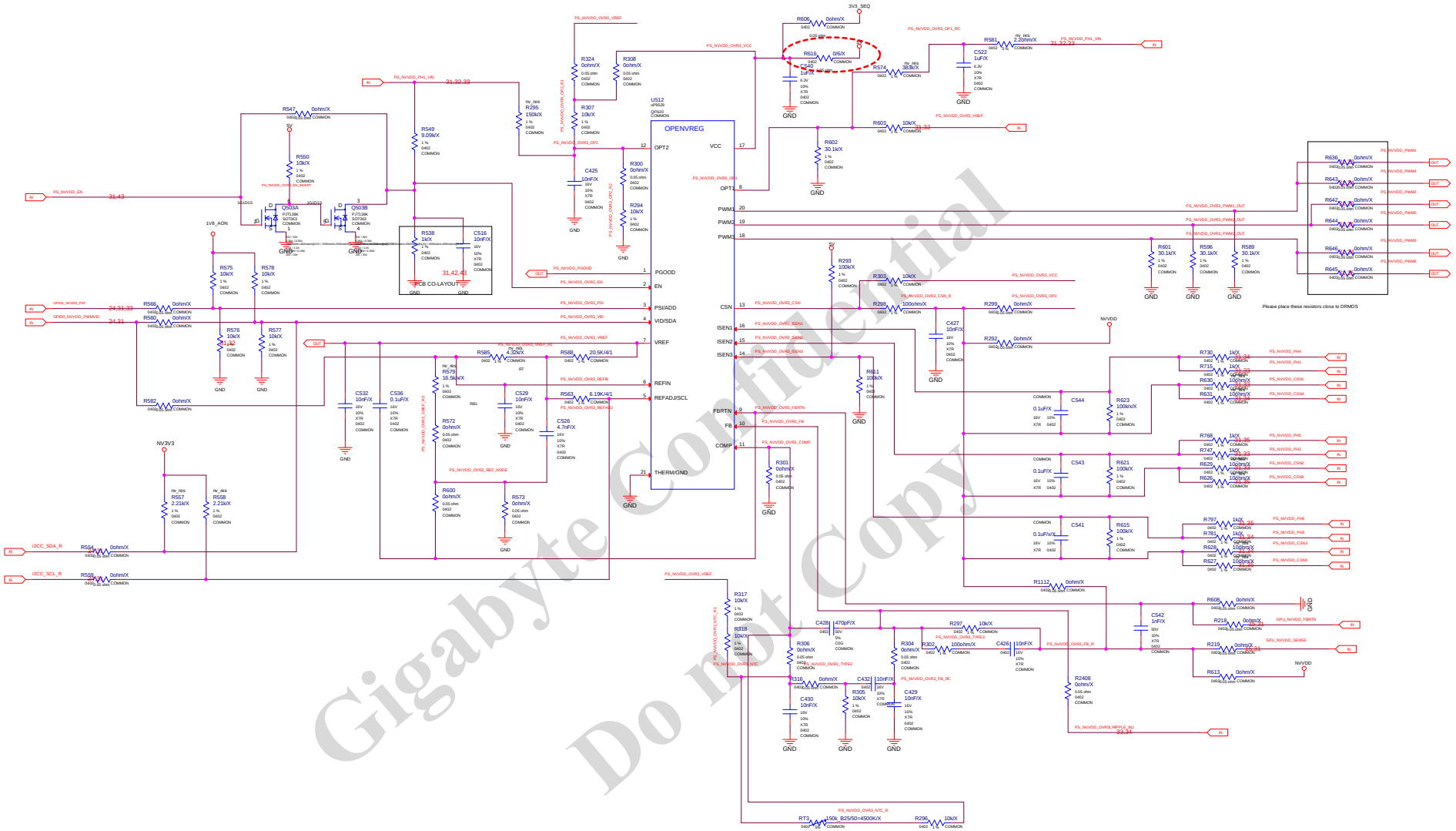
Title	0031_PS_ NVVDD Controller	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 31 of 52	



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: NVVDD Controller OVR3i

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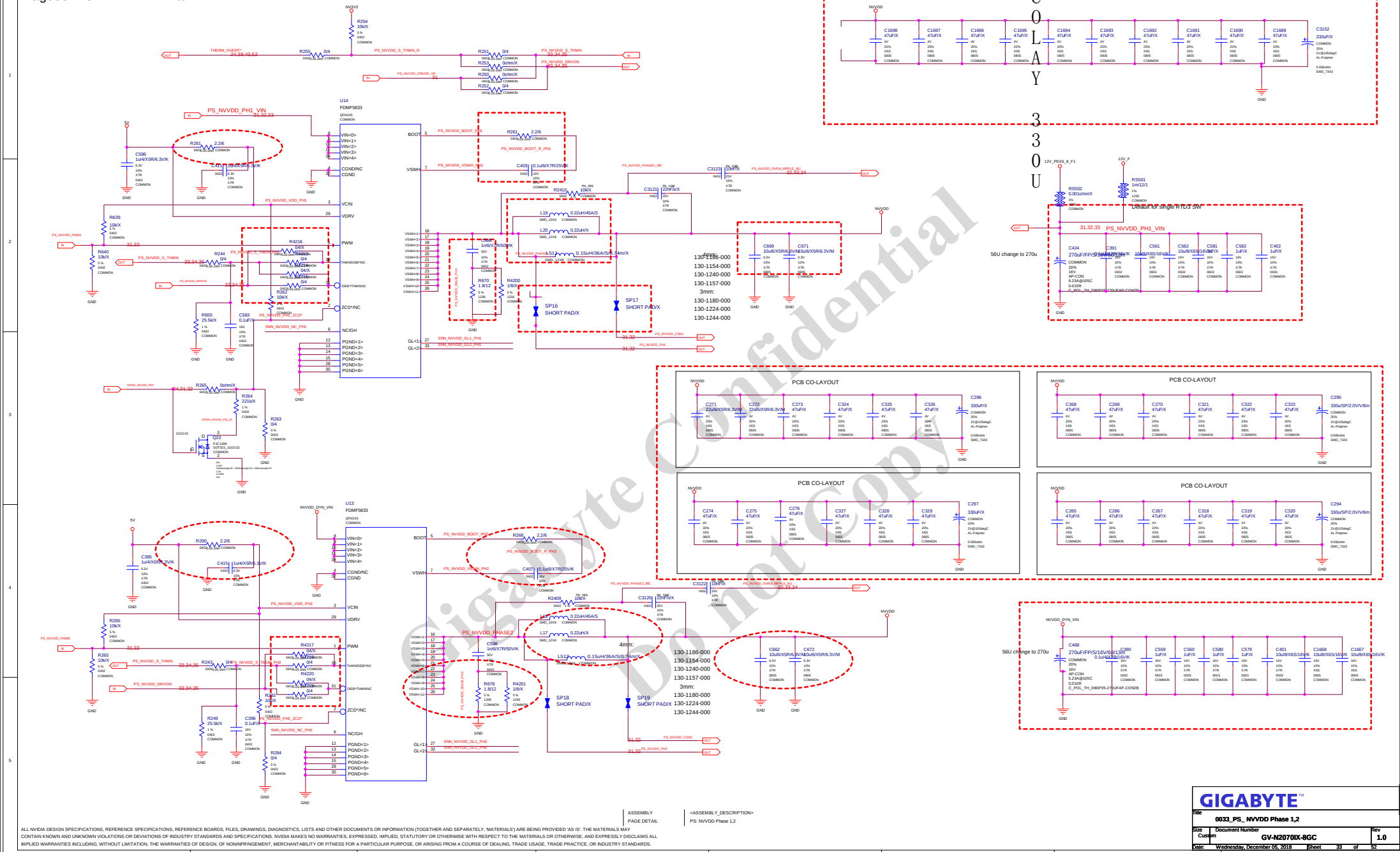
Title	0032_PS_ NVVDD Controller OVR3i	
Size: Custom	Document Number: GV-N20700X-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 32 of 52	

Page33: PS: NVVDD PH1&2



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ASSEMBLY
PAGE DETAIL
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PS: NVVDD Phase 1,2

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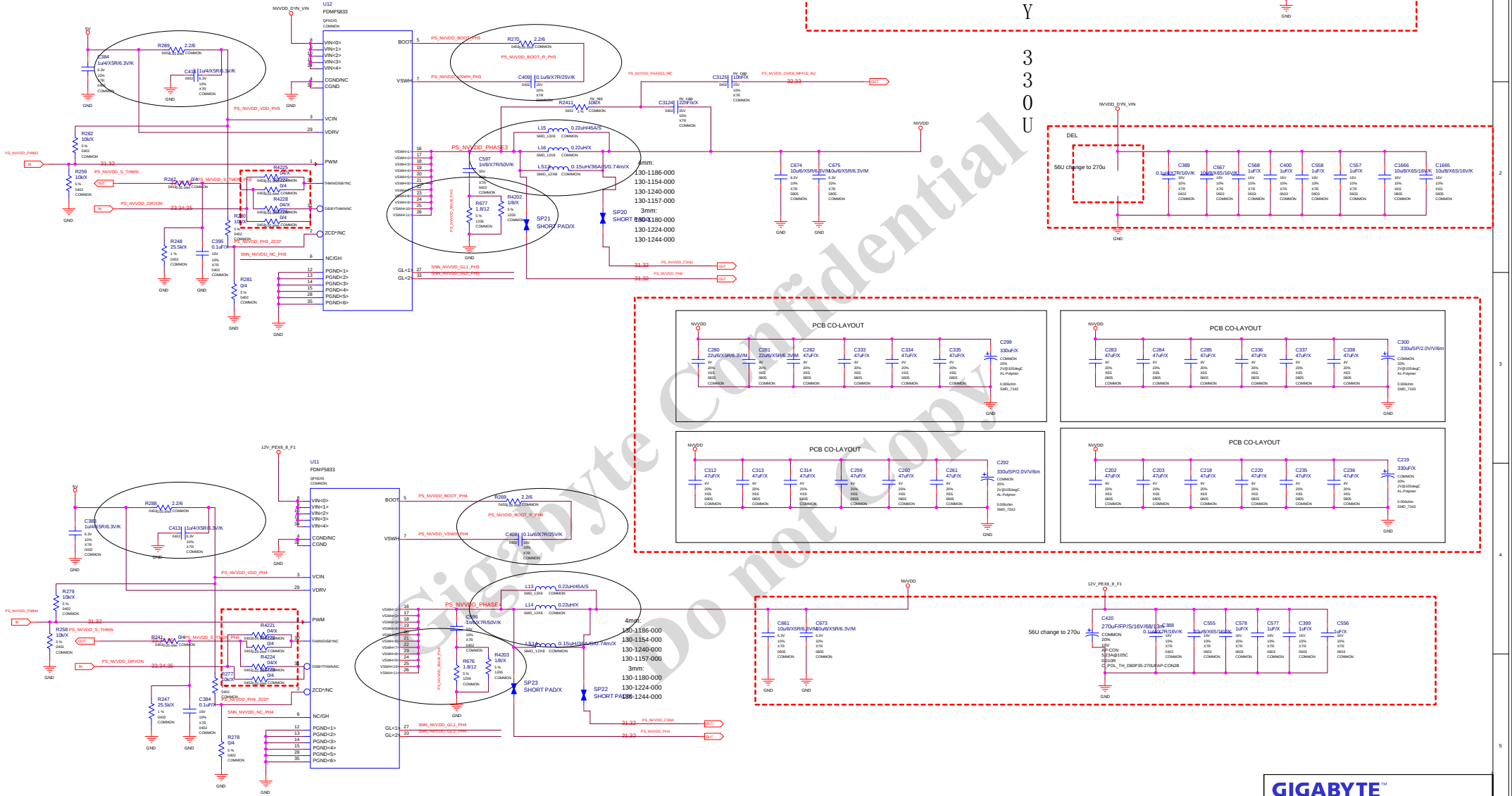
Title	0033_PS_ NVVDD Phase 1,2	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 33 of 52	



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PAGE DETAIL	PS: NVVDD Phase 3,4

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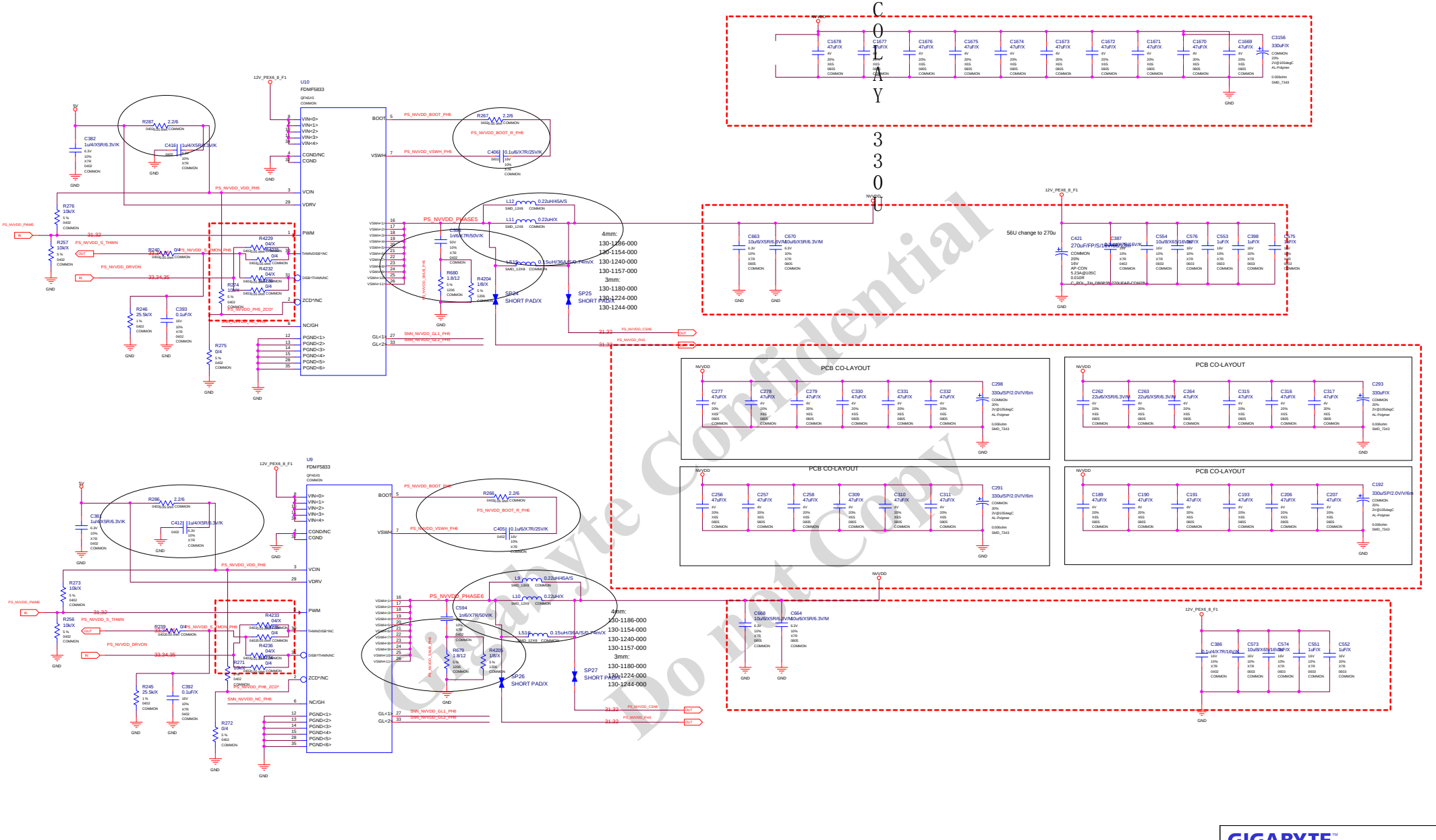
Title	0034_PS_ NVVDD Phase 3,4	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 34 of 52	



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PAGE DETAIL | PS: NVVDD Phase 5,6

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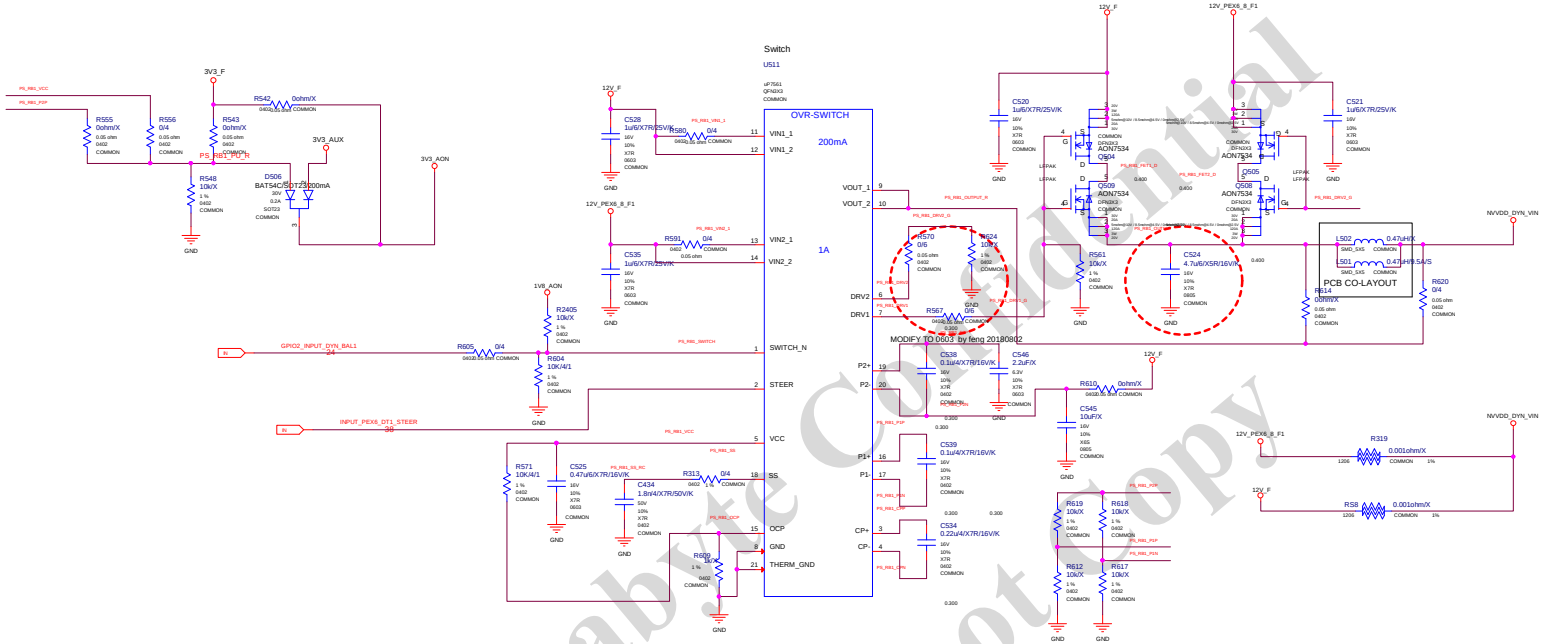
Title	0035_PS_ NVVDD Phase 5,6	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 35 of 52	

Page36: Input Power Balancing Switcher



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: Input Power Balancing Switcher

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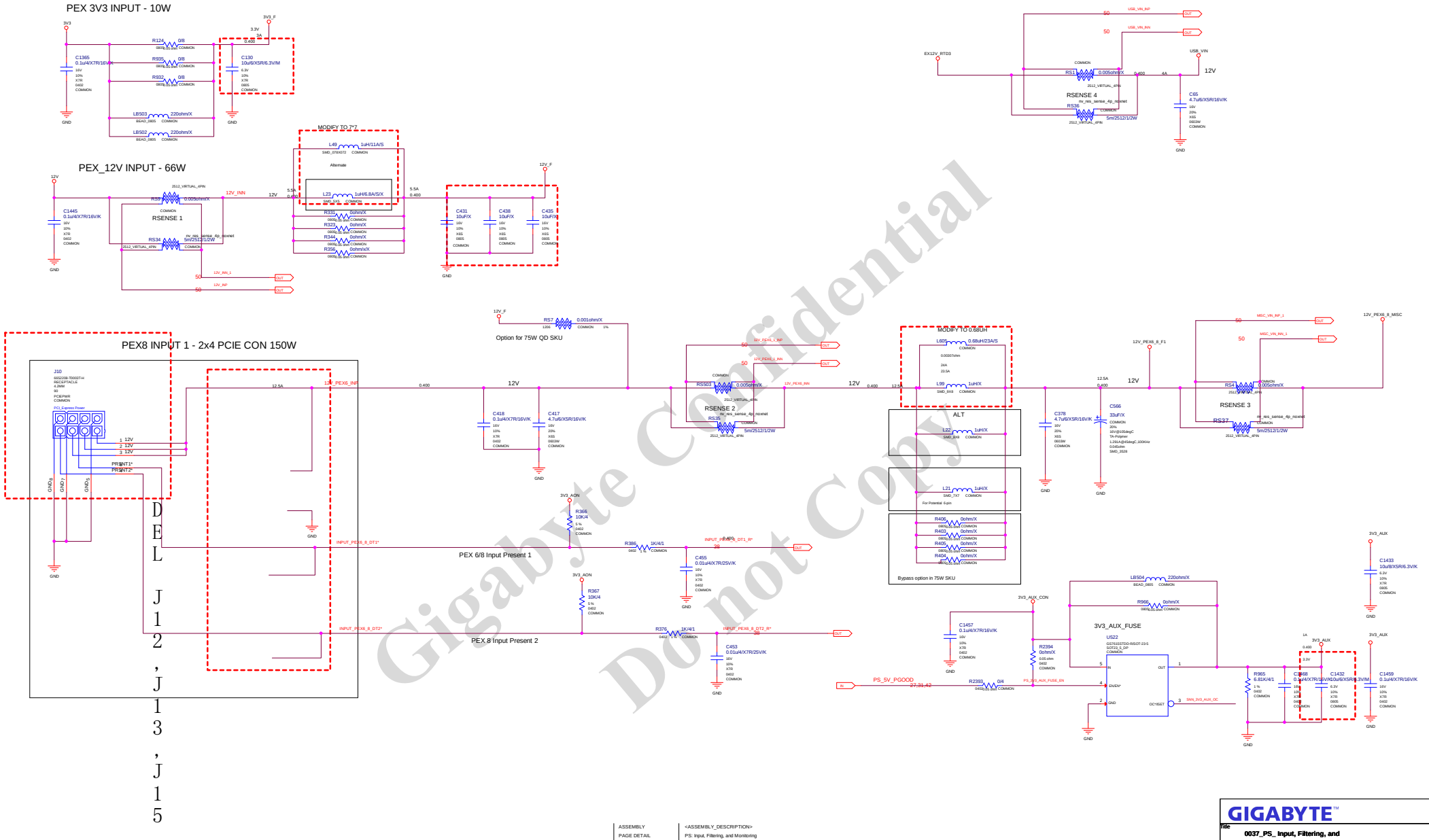
Title	0036_PS_ Input Power Balancing	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 36 of 52	



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: Input, Filtering, and Monitoring

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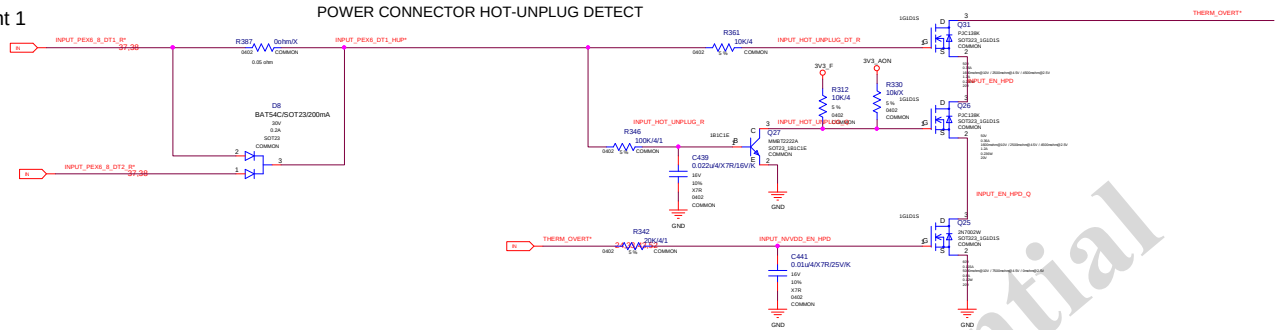
Title	0037_PS_ Input, Filtering, and	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 37 of 52	

Page38: PS: STEERING, UNDER POWER BOOT & HOT-UNPLUG

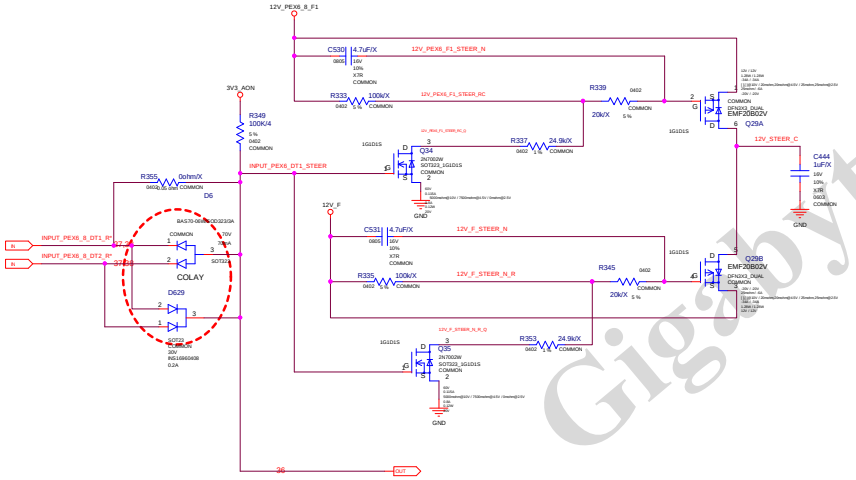
PEX Input Present 1

POWER CONNECTOR HOT-UNPLUG DETECT

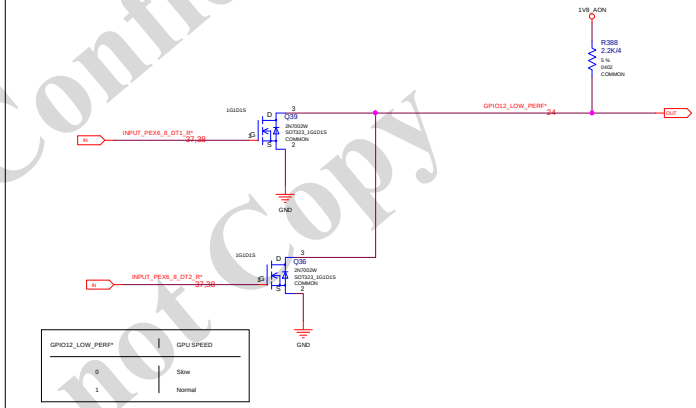


12V CURRENT STEERING (UNDER POWER BOOT):

GUIDES CURRENT FROM PEX EDGE TO PEX 6 PIN INPUT AREA



PEX Input - Power Level/PSI* Control



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ASSEMBLY | <ASSEMBLY_DESCRIPTION>
PAGE DETAIL | PS: STEERING, UPB & HOT-UNPLUG

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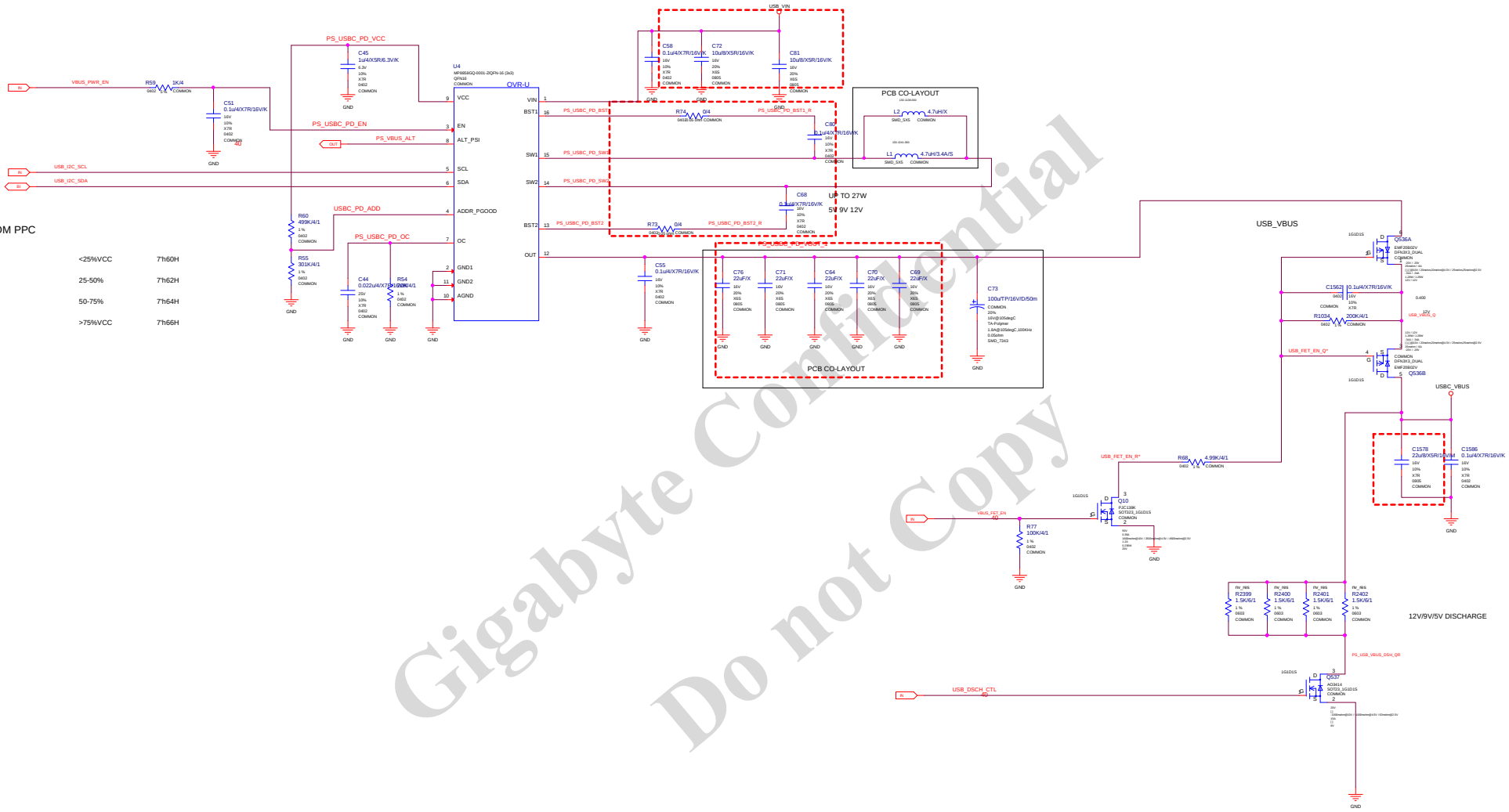
Title	0038_PS_ STEERING, UPB & HOT-UN	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 38 of 52	

Page39: BUCKBOOST



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: Type-C BuckBoost

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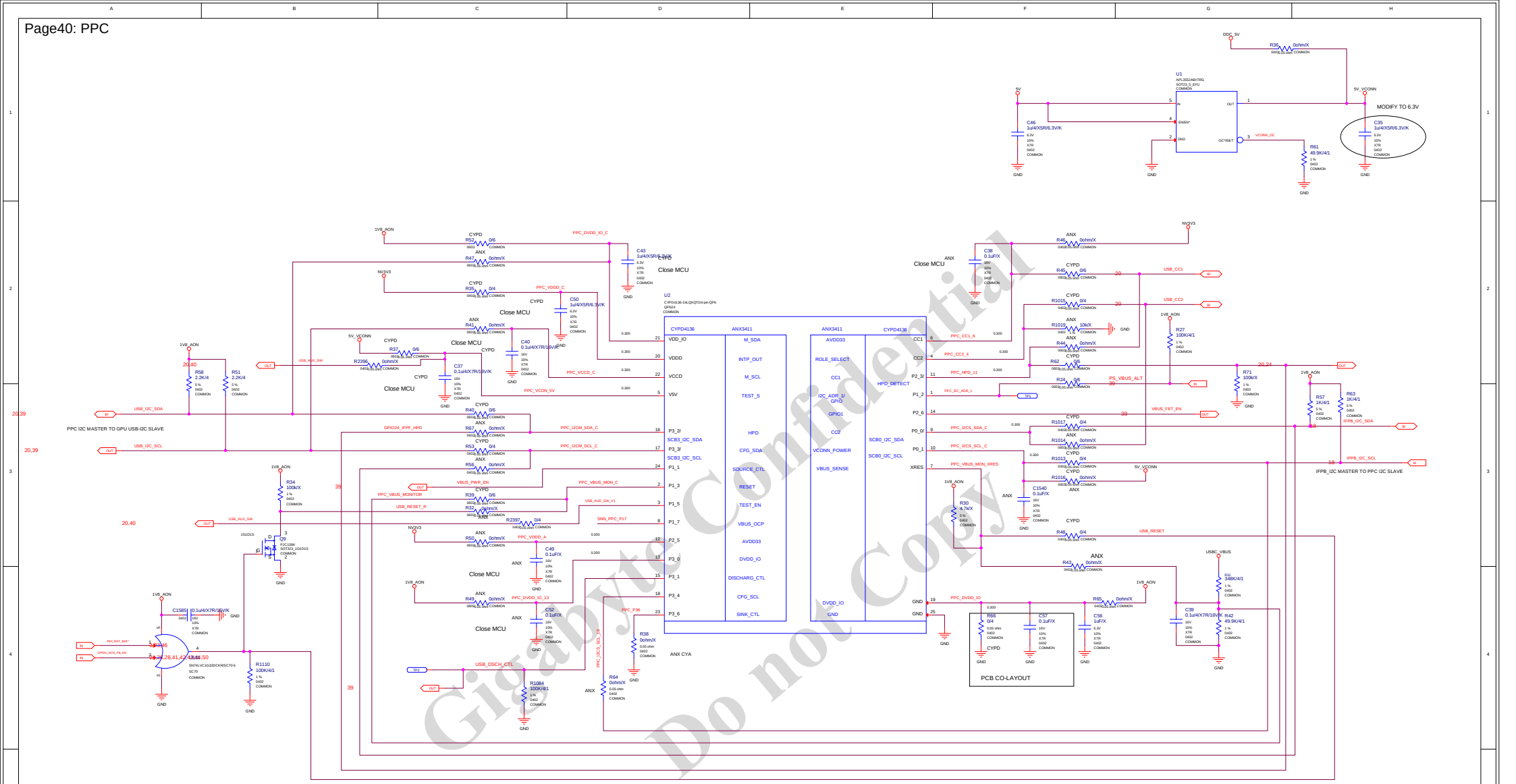
Title	0039_PS_ Type-C BuckBoost	
Size: Custom	Document Number: GV-N20700X-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 39 of 52	

Page40: PPC



	SCL	SDA
CYPD I2C SLAVE	PIN 10	PIN 9
CYPD I2C MASTER	PIN 17	PIN 16
ANX I2C SLAVE	PIN 18	PIN 17
ANX I2C MASTER	PIN 22	PIN 21

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ASSEMBLY <ASSEMBLY_DESCRIPTION>
PAGE DETAIL PD PPC

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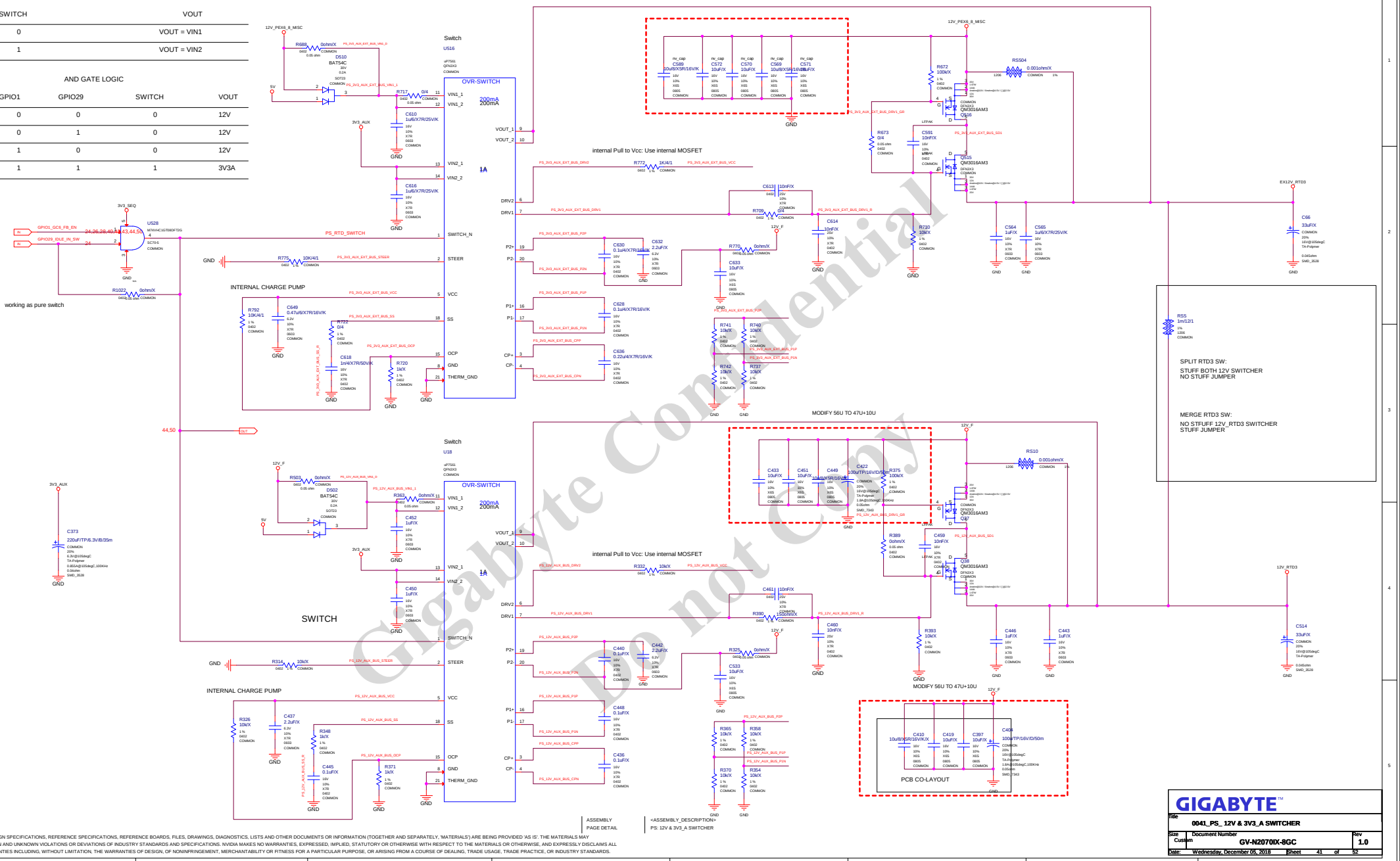
Title	0040_PD PPC	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 40 of 52	

Page41: 12V & 3V3_A SWITCHER

SWITCH	VOUT
0	VOUT = VIN1
1	VOUT = VIN2

AND GATE LOGIC

GPIO1	GPIO29	SWITCH	VOUT
0	0	0	12V
0	1	0	12V
1	0	0	12V
1	1	1	3V3A



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: 12V & 3V3_A SWITCHER

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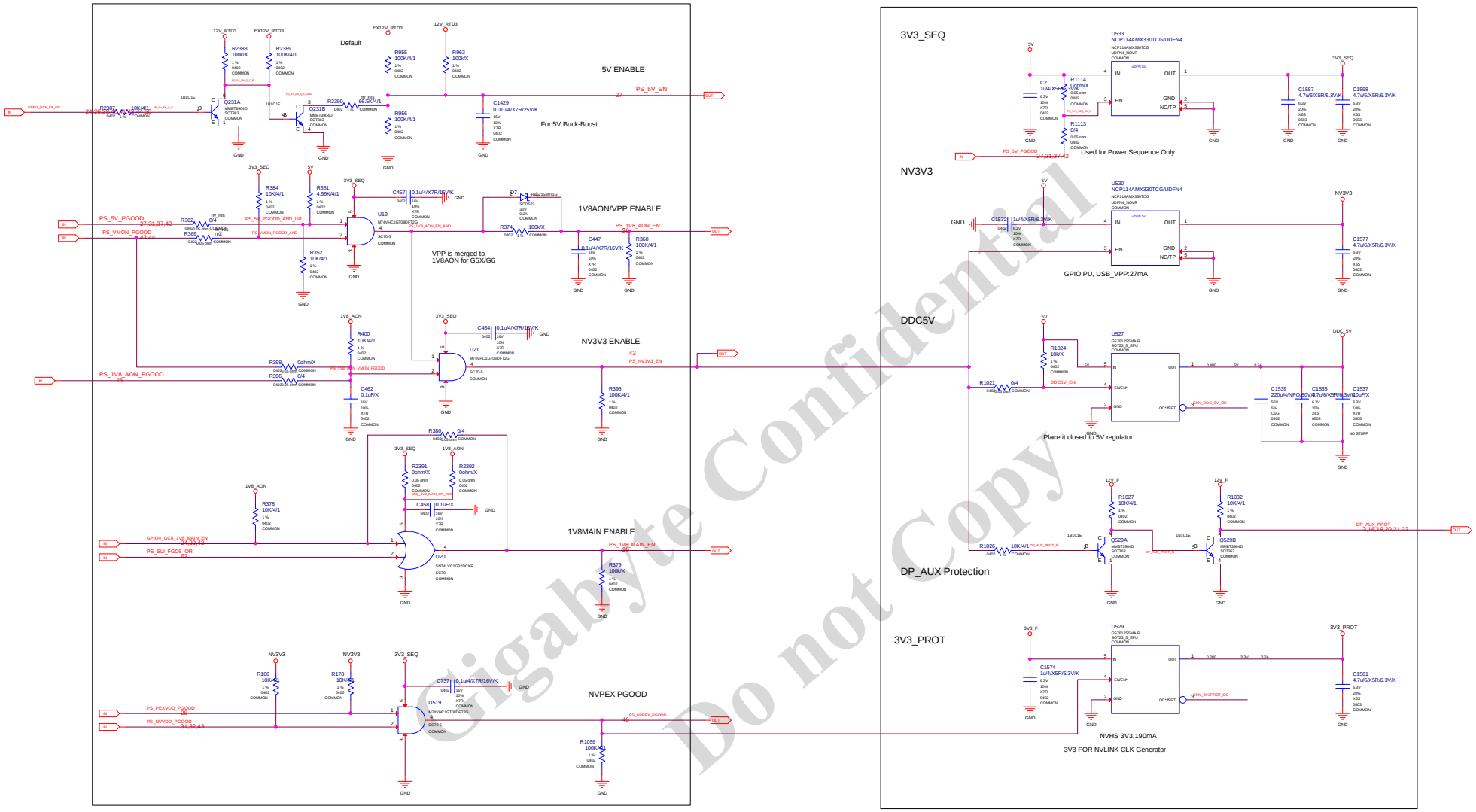
Title	0041_PS_ 12V & 3V3_A SWITCHER	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 41 of 52	

Page42: SEQ:5V,1V8,NV3V3 ENABLE



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	SEQ: 5V, 1V8, NV3V3 ENABLE

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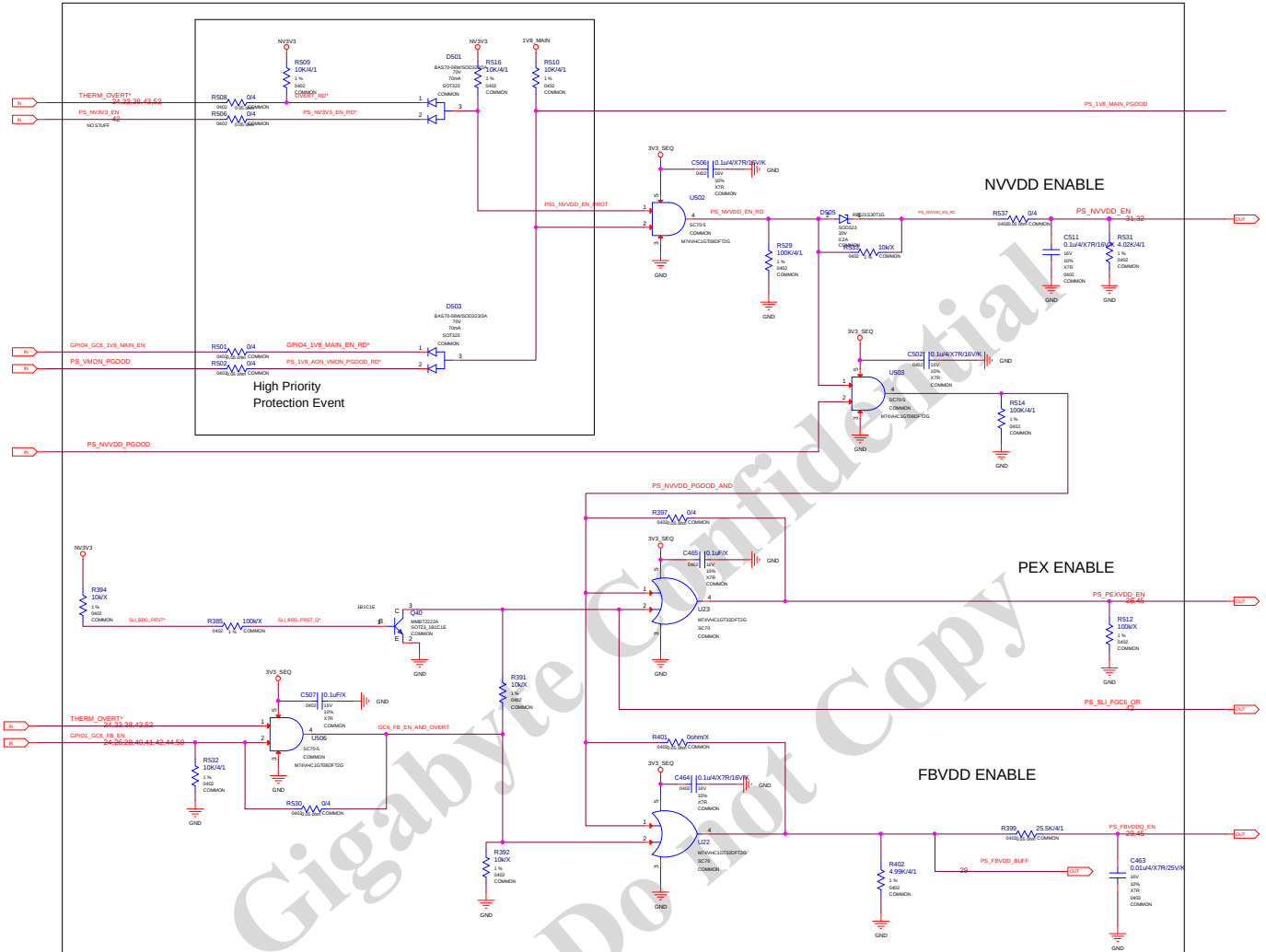
Title	0042_SEQ_ 5V, 1V8, NV3V3 ENABLE	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 42 of 52	

Page43: SEQ:NV,PEX,FB ENABLE



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	SEQ: NV, PEX, FB ENABLE

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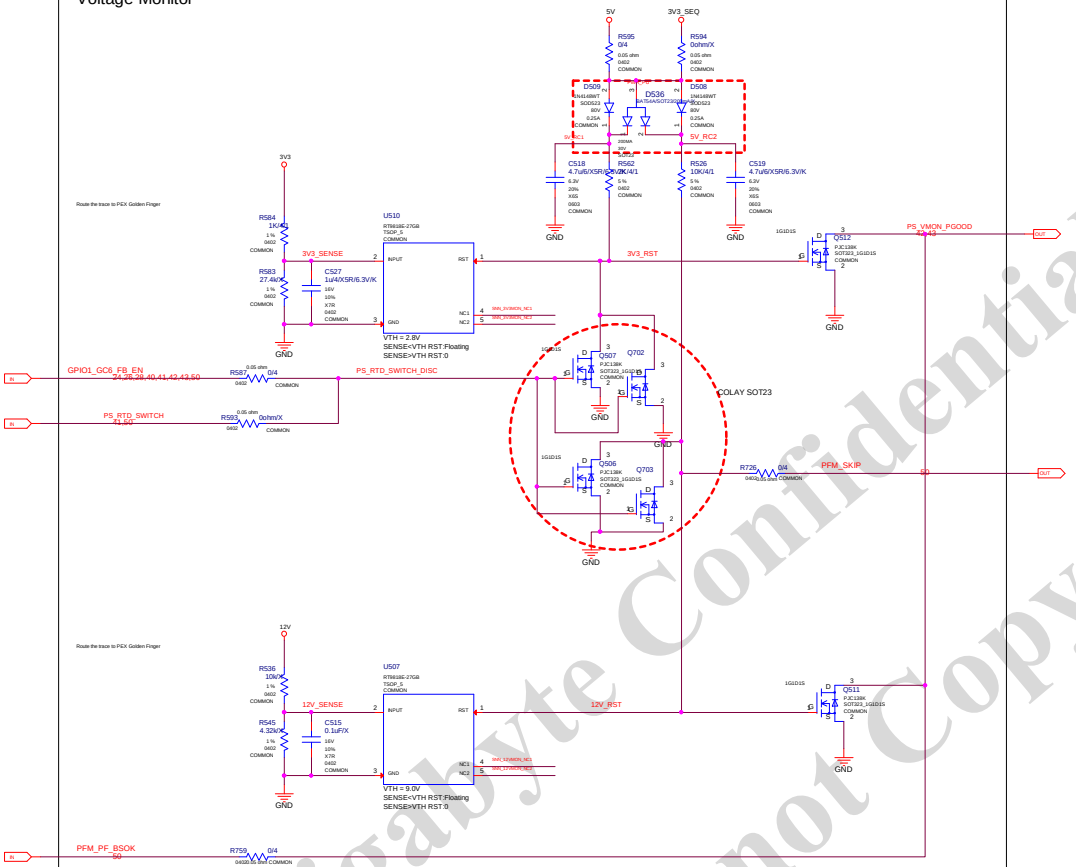
Title	0043_SEQ_ NV, PEX, FB ENABLE	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 43 of 52	

Voltage Monitor



Signal	Direction	Function
3V3	INPUT	Sense the 3V3 Voltage from PCIE golden finger
12V	INPUT	Sense the 12V Voltage from PCIE golden finger
PS_VMON_PGOOD	OPEN-DRAIN	Floating(H) once both 3V3 and 12V reach Vth
GC6_FB_EN	INPUT	Indicator for RTD3/GC6 residence,Use to Mask the VMON_PGOOD
PS_PF_SKIP	INPUT	From INA3221(VPU) or Pre-filter(SKIP)
PS_PF_BSOK	INPUT	From INA3221(PV) or Pre-filter(BS_OK)

BOM Configuration

OPTIONS	PEX3V3_SENSE	PEX12V_SENSE	OTHER_12V_SENSE
Use Pre-Filter	Pre-Filter	Pre-Filter	Pre-Filter
Use INA3221	Voltage_Monitor	INA3221	INA3221
NO INA3221 NO Pre-Filter	Voltage_Monitor	Voltage_Monitor	N/A

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ASSEMBLY <ASSEMBLY_DESCRIPTION>
PAGE DETAIL SEQ: VOLTAGE MONITOR

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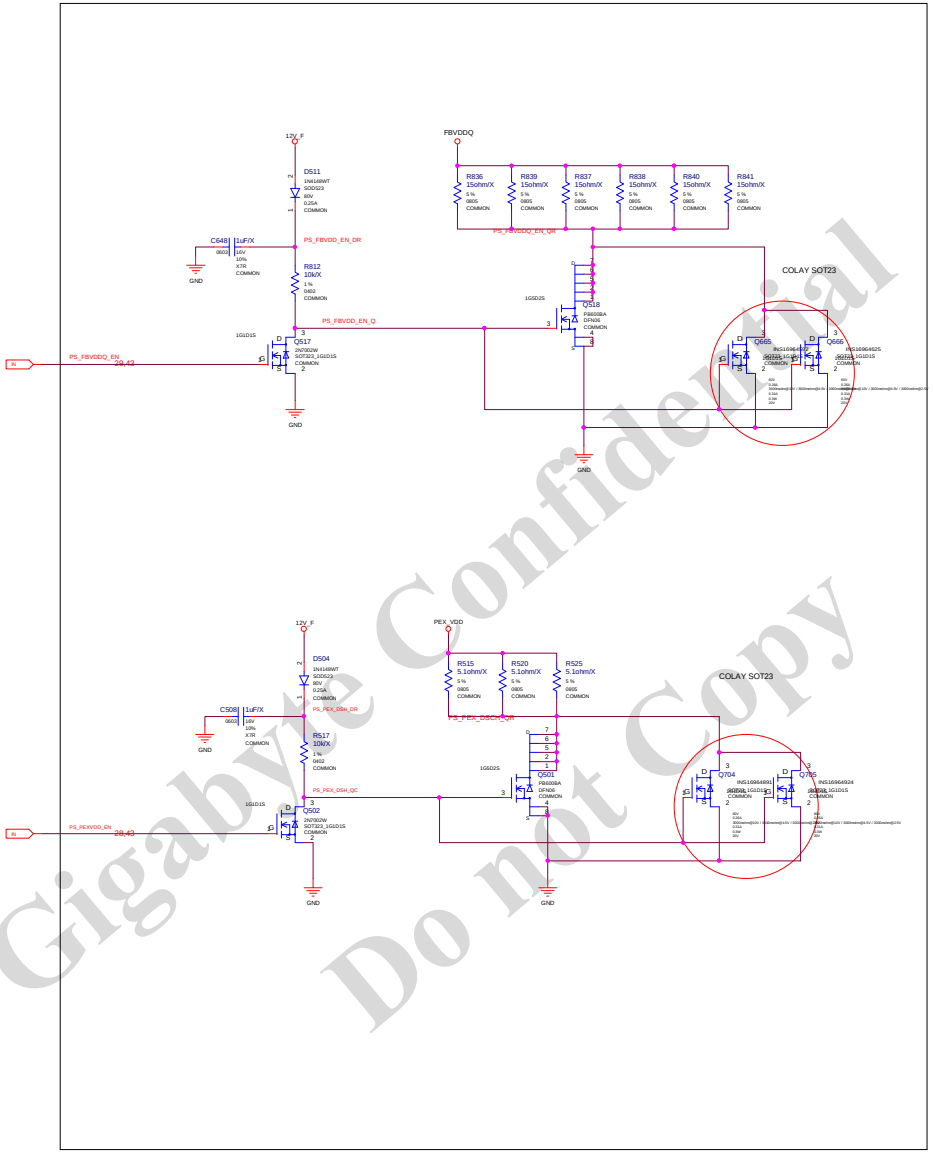
Title: 0044_SEQ_ VOLTAGE MONITOR

Size: Custom | Document Number: GV-N2070IX-8GC | Rev: 1.0

Date: Wednesday, December 05, 2018 | Sheet 44 of 52

Page45: SEQUENCE:Discharge



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	SEQ: DISCHARGE

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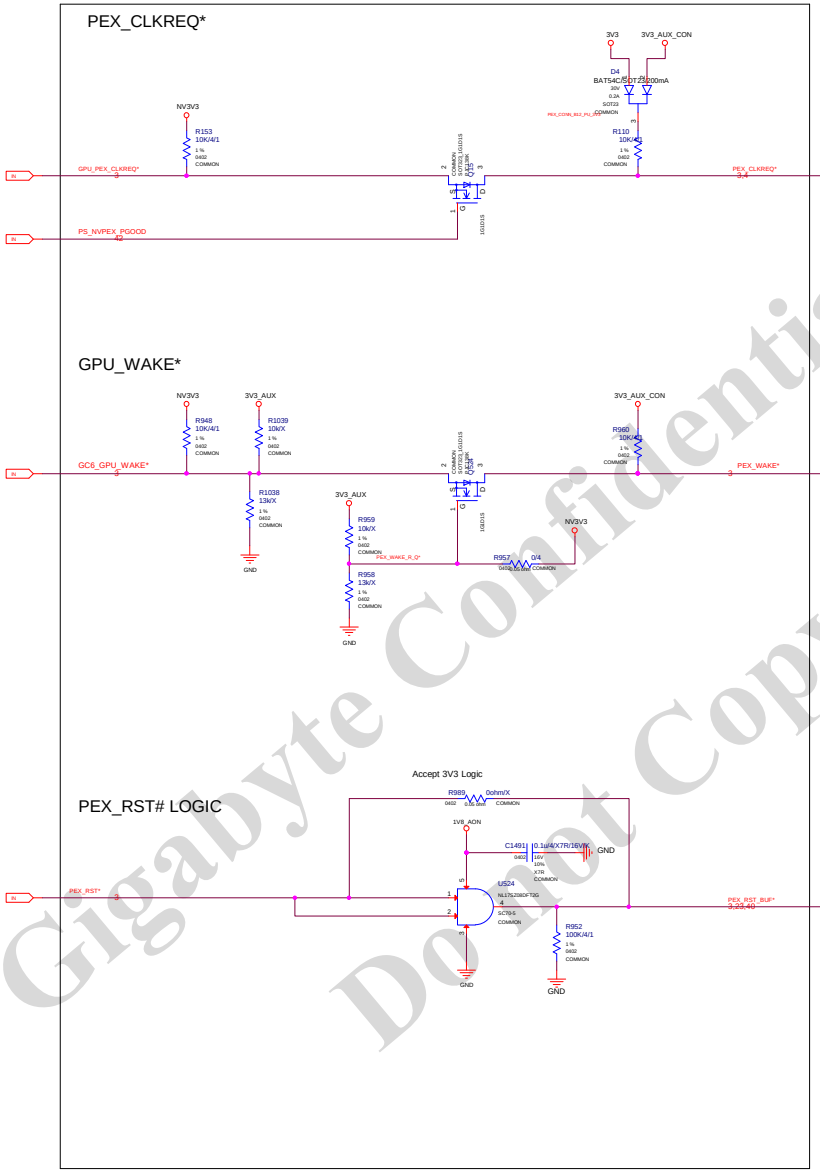
Title	0045_SEQ_ DISCHARGE	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Sunday, November 11, 2018	Sheet 45 of 52	



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	SEQ: MISC

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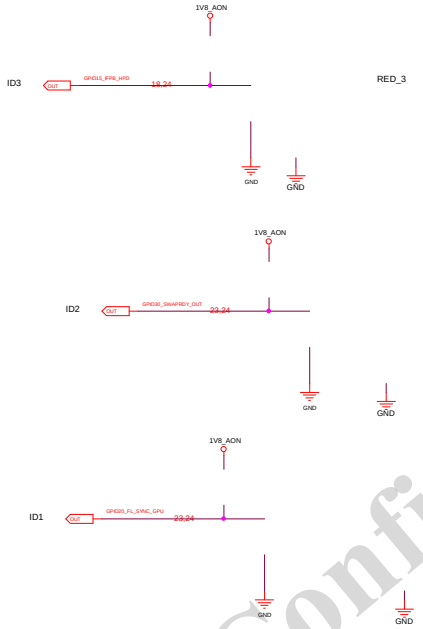
Title	0046_SEQ_ MISC	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 46 of 52	

Page47: LOGO LED



RED_3

12V FROM FAN PAGE

PLACE CAPS CLOSE TO CONNECTOR

FOR ONE ZONE RGB SOLUTION

15-PIN RIGHT ANGLE: 080-1618-000 $0.108

$CDS_IMAGE|clipboard_0_0.jpg|2278|4285

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ASSEMBLY <ASSEMBLY_DESCRIPTION>
PAGE DETAIL LOGO LED

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Title	0047_LOGO LED	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Sunday, November 11, 2018	Sheet 47 of 52	

Page48: LED DRIVER & Boost

INPUT LEAKAGE CURRENT OF ADC_INx PINS: -0.2uA~0.2uA(max)

8-bit I2C Address of MCU on GPU BOARD: 0x92

NV3V3

I2CB_SCL_R 23,24

I2CB_SDA_R 23,24

GND

BOOT0	BOOT MODE
0	Main Flash Memory
1	System Memory

Blue#2_2
Blue#3

Green#2
Red#2_2
Green#2_2

GPIO1_FRAME_LOCK_INT 23,24

PCB CO-LAYOUT

GPIO11_LOGO_LED_POWER_BRAKE 24

PCB CO-LAYOUT

GPIO19_STEREO_OUT 23,24

PCB CO-LAYOUT

PS_NVPEX_PGOOD 42,46

3V3_PROT

PEX_RST_BUF 3,23,40,46

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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	LED DRIVER & BOOST

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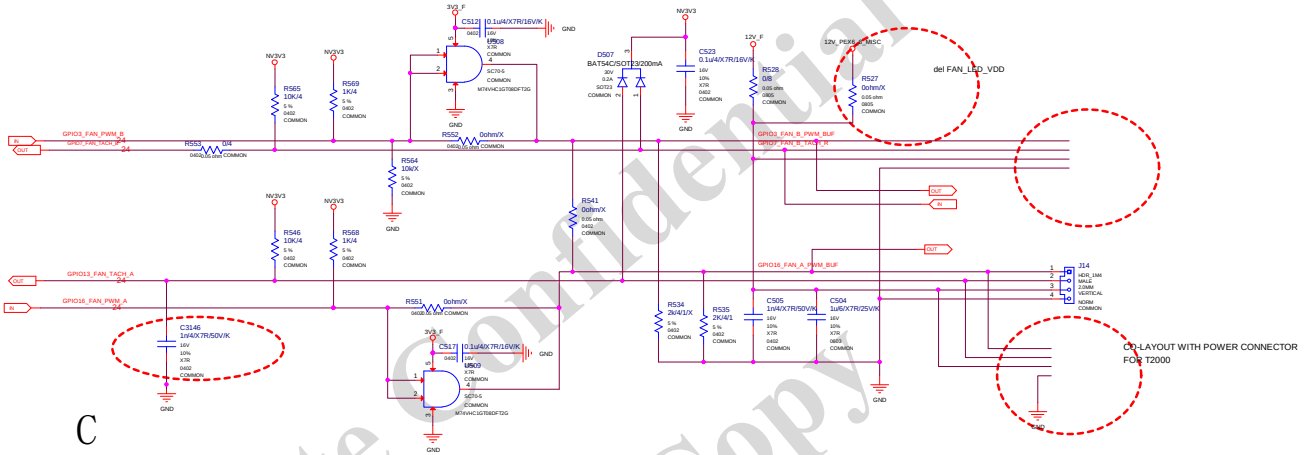
Title	0048_LED DRIVER & BOOST	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Sunday, November 11, 2018	Sheet 48 of 52	



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ASSEMBLY <ASSEMBLY_DESCRIPTION>
PAGE DETAIL DUAL FAN

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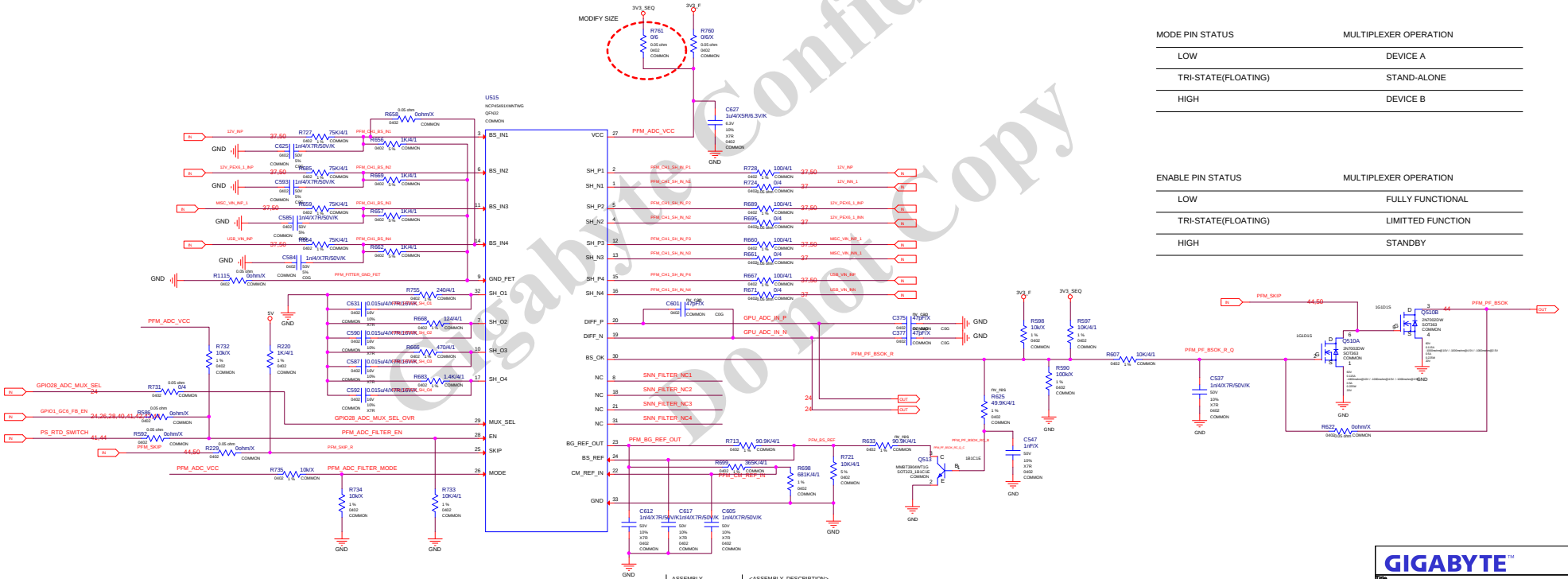
Title	0049_DUAL FAN	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 49 of 52	



MODE PIN STATUS	MULTIPLEXER OPERATION
LOW	DEVICE A
TRI-STATE(FLOATING)	STAND-ALONE
HIGH	DEVICE B

ENABLE PIN STATUS	MULTIPLEXER OPERATION
LOW	FULLY FUNCTIONAL
TRI-STATE(FLOATING)	LIMITTED FUNCTION
HIGH	STANDBY

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ASSEMBLY <ASSEMBLY_DESCRIPTION>
PAGE DETAIL PS: Pre-filter

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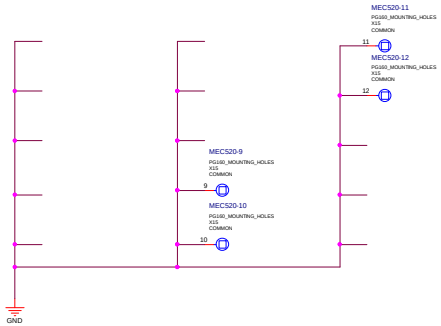
Title	0050_PS_ Pre-filter	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 50 of 52	

Page51: MECH

Brackets:

MOUNTING HOLES FOR HS:



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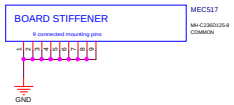

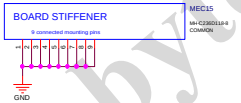

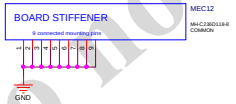

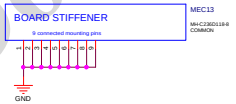

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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	MECH

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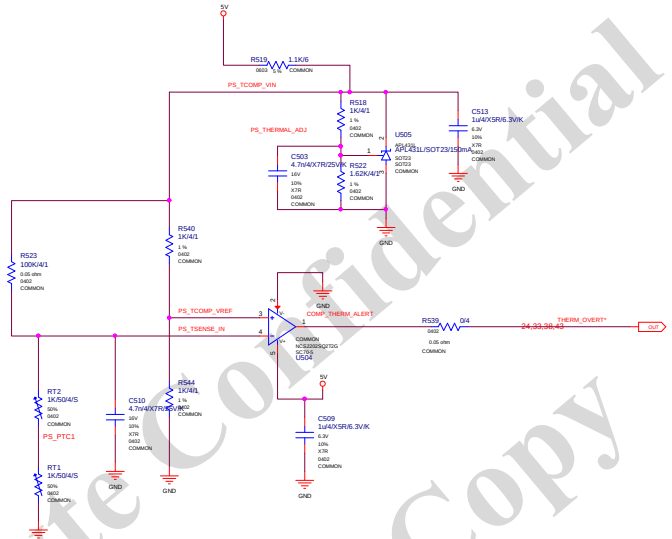
Title	0051_MECH	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 51 of 52	

Page52: PTC



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PTC

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Title	0052_PTC	
Size: Custom	Document Number: GV-N2070IX-8GC	Rev: 1.0
Date: Wednesday, December 05, 2018	Sheet 52 of 52	