

P561: G98, DDR2 MEMORY 32MX16/16Mx16/64MX16

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REV HISTORY


History 10		
96/09/27	page 08	add AV_OUT
	page 13	change Q1, Q2 to TO252
	page 14	add FBVDDQ-linear block, change U2 footprint
96/09/28	page 07	change J4 footprint
	page 12	add J7 (co-lay J6)
	page 13	add R572 for RT9259A, R570 footprint change to 0805, change L11, C11, C12, C31 footprint
	page 14	remove PWM block
		add D20, D21, C211, C212, C213, C214
96/10/01	page 14	add R210, R211
	page 09	add R75~R88, R63~R69, L15~L21 for DVI (EMI_solution)
96/10/02	page 12	add FAN Control Function
96/10/03	page 09	and netname (Between common Choke and DVI connector)
96/10/05	page 12	cnage Y501 (4 pin to 2 pin)
96/10/12	page 12	add L30, remove L10
96/10/12	baseon	PCB:2.0 CIRCUIT DSN CAHNGE to PCB :1.0
	ONLY CHANGE	page 13 remove PWM solution, ADD linear solutin
		remove C301~~C308 (EMI solution for FBVDDQ)
96/10/17	page 11	add CABLE and PCB
96/10/18	page 14	add C301~~C308 (EMI solution for FBVDDQ)
History 20		
96/10/03	page 14	remove FBVDDQ-LINNEAR block, add FBVDDQ-PWM function
		change L15 footprint as CHK4417C_3R3S01, change C35 footprint
96/10/05	page 12	cahngue Y501 (4pin to 2 pin)
96/10/09	page 11	add FM1~~ FM6 for Fiducial Point
		add U301~~U306 for EMI
	page 13	add C309 for EMI
	page 14	add C301~~C308, C310~~C312 for EMI
96/10/10	page 13	add L30
96/10/11	page 13	remove L10
96/10/17	page 11	add cable

REV	VARIANT	NVPN	ASSEMBLY
B	BASE	600-10561-0000-100	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO. STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU0000	600-10561-0000-100	P561: G98-300, 64 BIT DDR2 180A/16 MEMORY, VGA+DVI+HDCP
2	SKU0001	600-10561-0001-100	P561: G98-300, 64 BIT DDR2 320A/16 MEMORY, VGA+DVI+HDCP
3	SKU0007	600-10561-0007-100	P561: G98-300, 64 BIT DDR2 320M/16 MEMORY, VGA+DVI+HDCP
4	SKU0007	600-10561-0007-200	P561: G98-300, 64 BIT DDR2 320M/16 MEMORY, VGA+DVI+HDCP
5	SKU0001	600-10561-0001-200	P561: G98-300, 64 BIT DDR2 320M/16 MEMORY, VGA+DVI+HDCP
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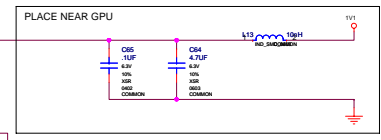
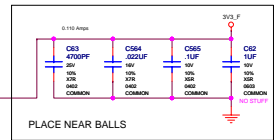
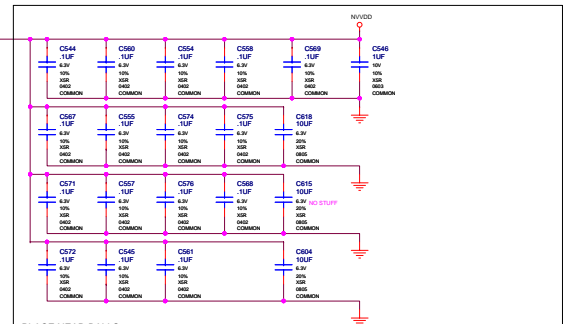
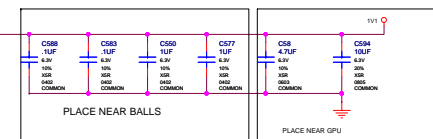
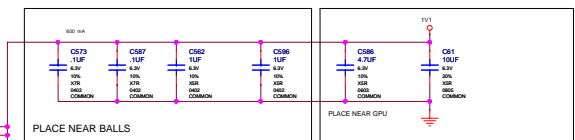
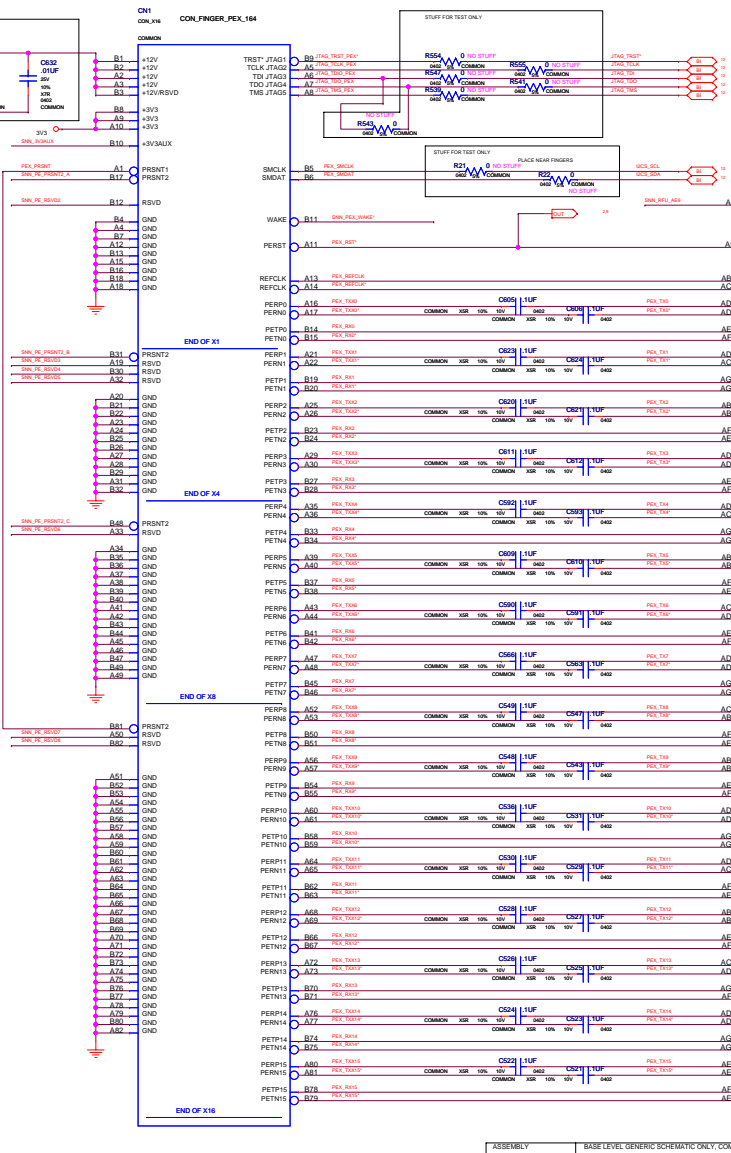
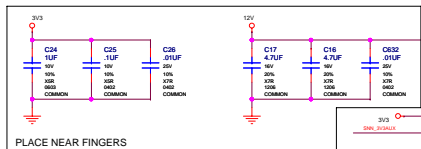
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NAME

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO. STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	P561 Overview

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PCI Express Interface



	Net Name	DIFF_PAIR	CRITICAL	IMPEDANCE
10	PC1-1000A	PC1-1000A	+	H000Z
11	PC1-1000B	PC1-1000B	+	H000Z
12	PC1-700	PC1-700A	+	H000Z
13	PC1-700	PC1-700B	+	H000Z
14	PC1-700	PC1-700C	+	H000Z
15	PC1-700	PC1-700D	+	H000Z
16	PC1-700	PC1-700E	+	H000Z
17	PC1-700	PC1-700F	+	H000Z
18	PC1-700	PC1-700G	+	H000Z
19	PC1-700	PC1-700H	+	H000Z
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33	PC1-700	PC1-700V	+	H000Z
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39	PC1-700	PC1-700B	+	H000Z
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262	PC1-700	PC1-700Q	+	H000Z
263	PC1-700	PC1-700R		

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PCI EXPRESS INTERFACE

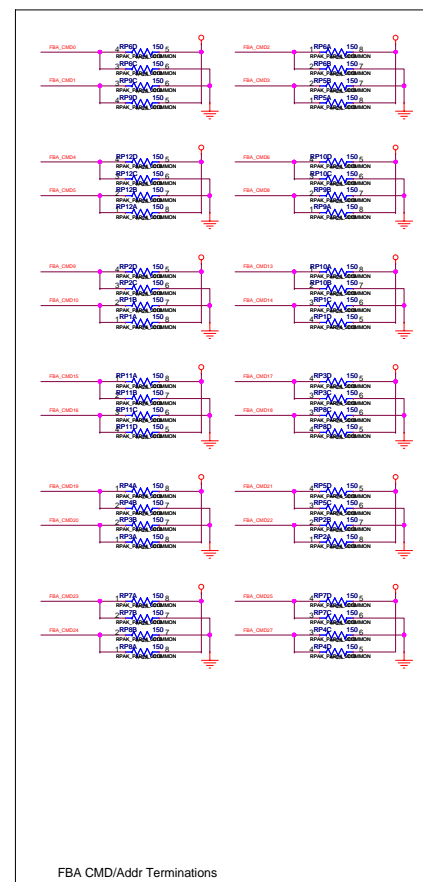
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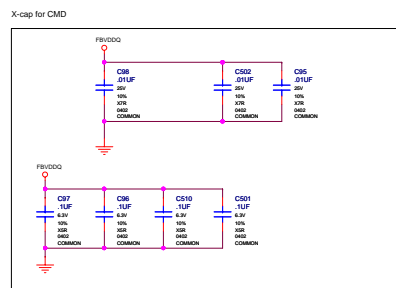
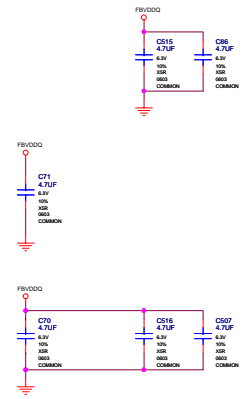
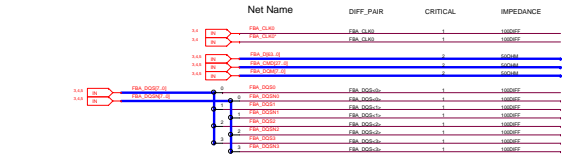
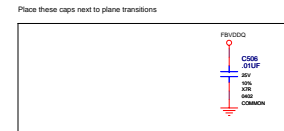
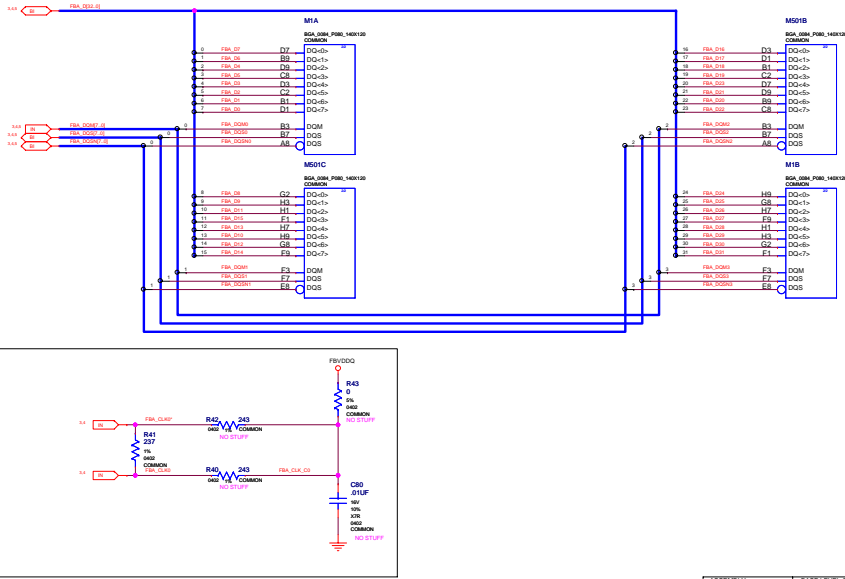
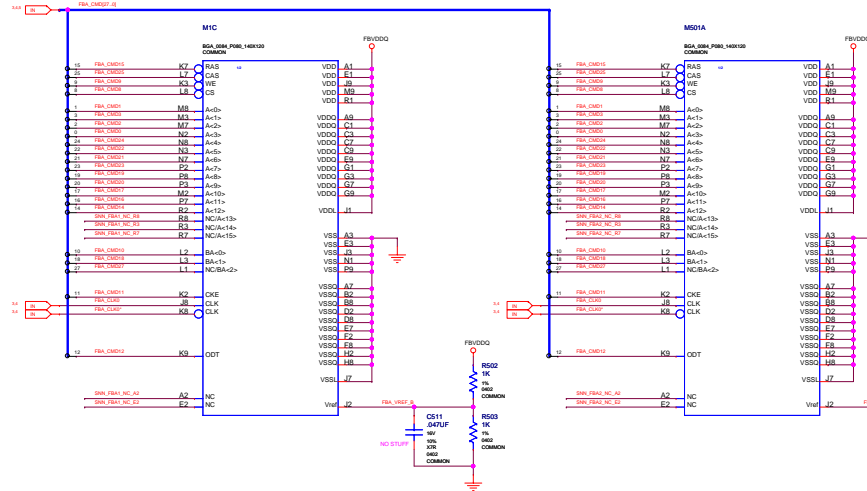
G		H	
SIGNAL NAME	HV CRITICAL NET	REFERENCE	
IN → FBA_DEBUG	1	SD00M	




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CMD-ADDR	
CMD00	A3
CMD01	A0
CMD02	A2
CMD03	A1
CMD04	NC
CMD05	NC
CMD06	NC
CMD07	CS1*
CMD08	CS0*
CMD09	W0*
CMD10	BA0
CMD11	CAS
CMD12	BS1000†
CMD13	NC
CMD14	A13
CMD15	BA3*
CMD16	A11
CMD17	A13
CMD18	BA1
CMD19	A6
CMD20	A8
CMD21	A8
CMD22	A6
CMD23	A7
CMD24	A4
CMD25	CAS*
CMD26	A13
CMD27	BA2
CMD28	RFU0
CMD29	RFU1
CMD30	RFU2



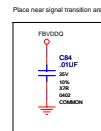
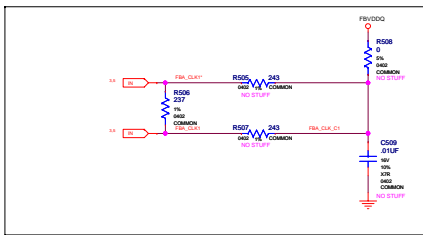
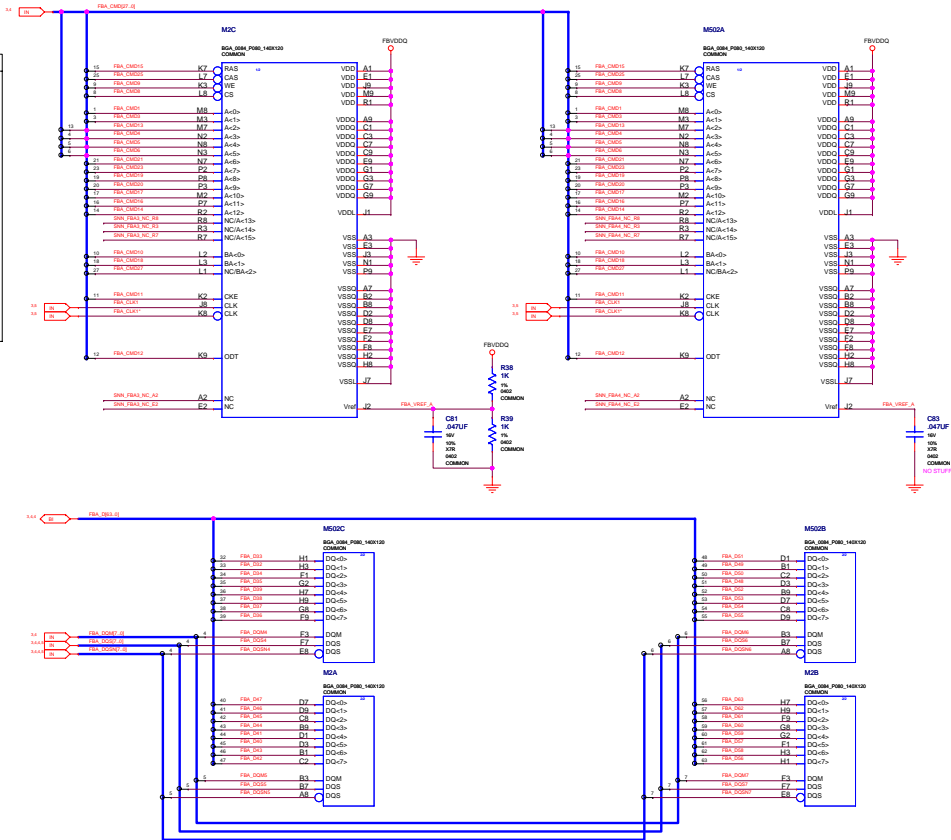
Place these caps next to plane transitions

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NAME		DATE	31-July-2007

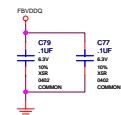
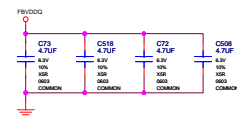
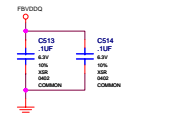
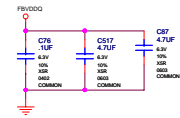
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Memory 1st Bank 32..63

CMD-AORR	
CMD01	NC
CMD1	AD
CMD2	AI
CMD4	A3
CMD5	AD
CMD6	AE
CMD7	CS1*
CMD8	W3*
CMD10	BAQ
CMD11	CRK
CMD12	REF100T
CMD13	AJ
CMD14	A12
CMD15	RA5*
CMD16	A11
CMD17	A10
CMD18	BA1
CMD19	AE
CMD20	AE
CMD21	AE
CMD22	NC
CMD23	A7
CMD24	NC
CMD25	CA5*
CMD26	A13
CMD27	BA2
CMD28	REF10
CMD29	REF11
CMD30	REF2



Net Name		OFF_PAIR	CRITICAL	IMPEDANCE
10	FBA_0243	1	1	10000
10	FBA_0243	1	1	10000
0.01	FBA_0205B(1)	1	1	10000
0.01	FBA_0205B(1)	1	1	10000
0	FBA_0205A	1	1	10000
0	FBA_0205A	1	1	10000
0	FBA_0205B	1	1	10000
0	FBA_0205B	1	1	10000
0	FBA_0205C	1	1	10000
0	FBA_0205C	1	1	10000
0	FBA_0205D	1	1	10000
0	FBA_0205D	1	1	10000
0	FBA_0205E	1	1	10000
0	FBA_0205E	1	1	10000
0	FBA_0205F	1	1	10000
0	FBA_0205F	1	1	10000
0	FBA_0205G	1	1	10000
0	FBA_0205G	1	1	10000
0	FBA_0205H	1	1	10000
0	FBA_0205H	1	1	10000



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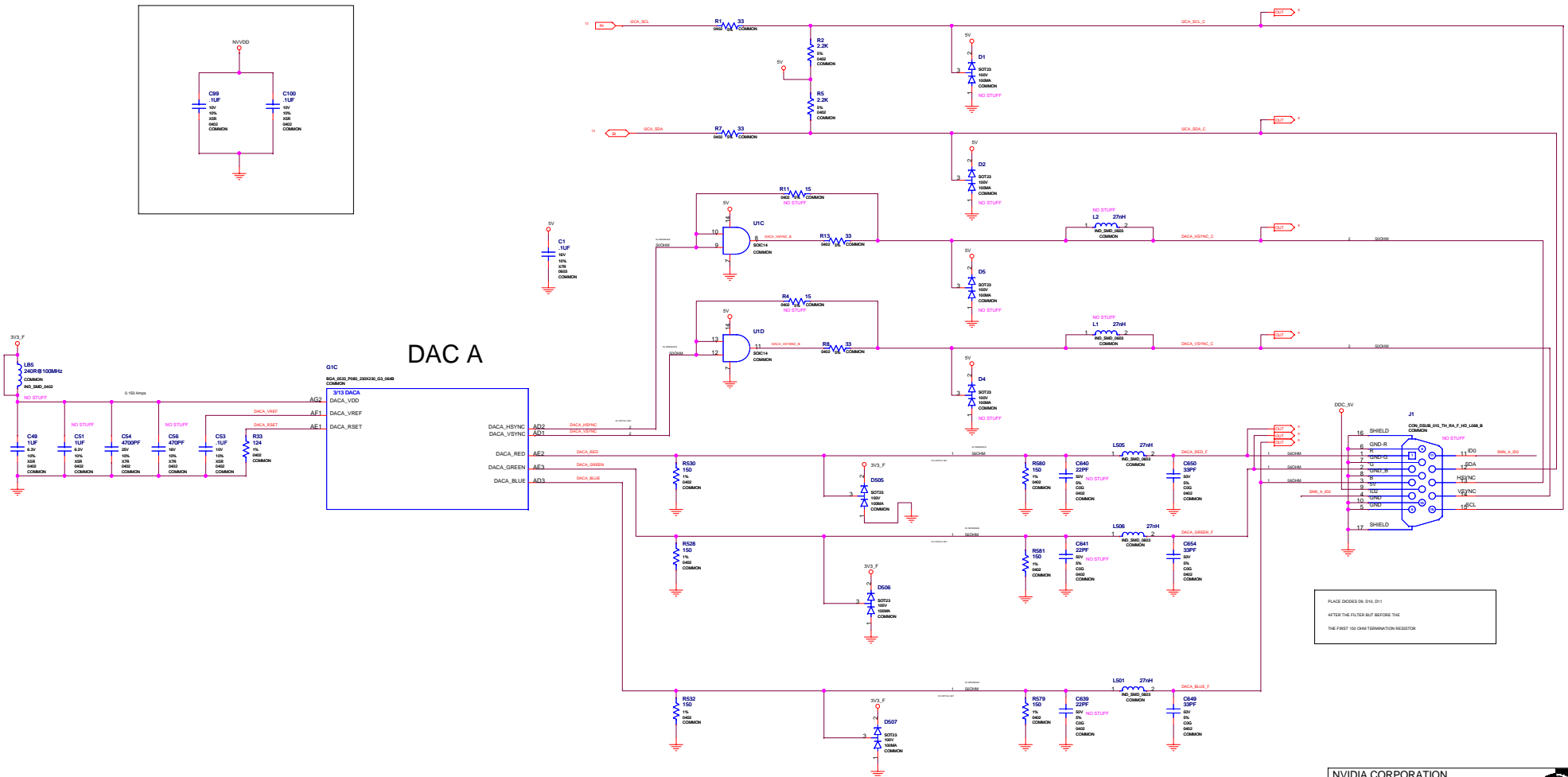
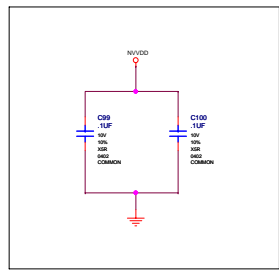
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NAME	DATE 31-AUG-2007

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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Memory 1st Bank 32..63

DACA, Slim DB15 Connector



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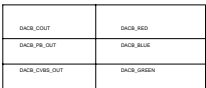
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ID	PAGE
NAME	DATE 31-AUG-2007

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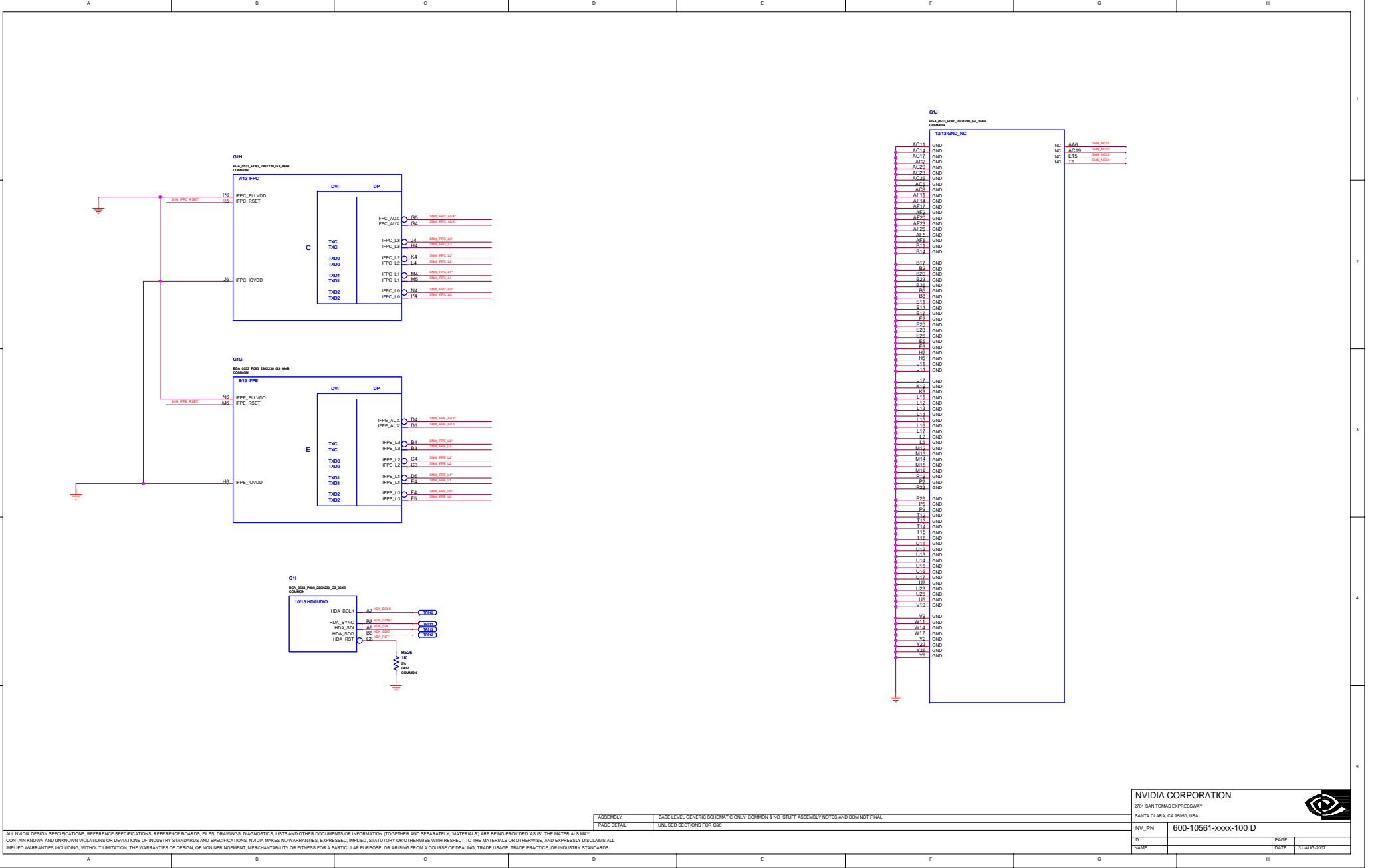
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACA, Slim DB15 Connector

A	B	C	D	E	F	G
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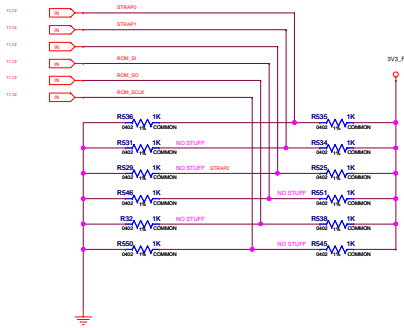


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Straps, Mechanical Parts



Each strap pin represents a 4 bit value
Pullup or Pulldown configures the MSB
Resistor Value determines the 3 LSBs
Resistor range is $R \times n$
where n is 0-9 and R is 5K ohm.

Resistor Value	Multiview	Tied to VCC	Tied to Ground
3 Kohms	Y	0000	0000
6 Kohms	Y	0001	0001
12 Kohms	Y	0010	0010
24 Kohms	Y	0011	0011
48 Kohms	Y	0100	0100
96 Kohms	Y	0101	0101
192 Kohms	Y	0110	0110
284 Kohms	Y	0111	0111
2 Kohms**	N	xxxx	xxxx

STRAPS

- ```

1. SUB VENDOR = 1 (ROM PRESENT)
 = 0 (NO ROM PRESENT)

2. DEVICE ID FOR GR464 = 0x0622
 A) PCI_DEV_ID_ID = 0x1558 = 0x010
 B) PCI_DEV_VENDOR_ID = 0x1404 = 0x010

3. XGID_PADCFG_LUT_ADDR_0 = 0x000

4. TUNING_CFG_ID = 0x01

5. RAMCFG_CFG_ID = 0x0000 DOR2 64 BIT ELPHDA
 = 0x0100 10MAH4 DOR2 64 BIT DSAUSING, WIDCON
 = 0x0110 10MAH4 DOR2 64 BIT INFTRN
 = 0x0120 10MAH4 DOR2 64 BIT INFTRN
 = 0x0130 10MAH4 DOR2 64 BIT INFTRN
 = 0x0140 10MAH4 DOR2 64 BIT INFTRN
 = 0x0150 10MAH4 DOR2 64 BIT INFTRN

6. USER_CFG_ID = 0x000

```

| MULTI-LEVEL MODE |                                                        |
|------------------|--------------------------------------------------------|
| SIGNAL           | FUNCTION                                               |
| STRAP[0]         | USER0.[0]                                              |
| STRAP[1]         | SSIO_PADCFG_LUT_ADDR[0]                                |
| STRAP[2]         | PC0_DEV0[0]                                            |
| ROM_SCLK         | PC0_DEV0_EXT_SUB_VENDOR_SLOT_CLK_CFG_PCK_PLN_EN_TERRM0 |
| ROM_S0           | RAMCFG[0]                                              |
| ROM_S1           | KICK_J77_TVMODE[0]                                     |

| BRINGUP BINARY MODE |                           |
|---------------------|---------------------------|
| SIGNAL              | FUNCTION                  |
| STRAP[0]            | IGNH, PADCFG, LUT_ADDR[0] |
| STRAP[1]            | IGNH, PADCFG, LUT_ADDR[1] |
| STRAP[2]            | IGNH, PADCFG, LUT_ADDR[2] |
| ROM_SELK            | IGNH, PADCFG, LUT_ADDR[3] |
| ROM_SI              | SUB_VENDOR                |
| ROM_SO              | KOLX_777                  |

| PRODUCTION BINARY MODE |              |
|------------------------|--------------|
| SIGNAL                 | FUNCTION     |
| STRAP[0]               | RAMCF_Q0[0]  |
| STRAP[1]               | RAMCF_Q0[1]  |
| STRAP[2]               | RAMCF_Q0[2]  |
| ROM_SELK               | PCI_DEV0[0]  |
| ROM_SI                 | PCI_DEV0_EXT |
| ROM_SE                 | SELK_377     |

STRAP SETTINGS FOR HYNIX 32Mx16 DDR2 500MHz ( MULTI LEVEL) R511= 40K, R512=40K

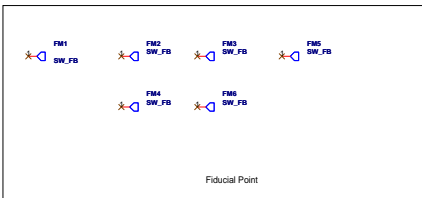
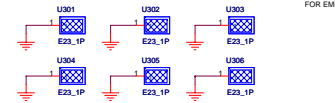
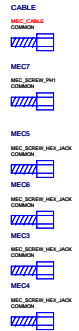
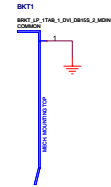
| FILE | END | PROGNAME | FUNCTION                                                     |
|------|-----|----------|--------------------------------------------------------------|
|      | 5K  | ETAN95   | USDPG-0                                                      |
|      | 5K  | ETAN95   | SUB=BARPG, LUT_ADRP-0                                        |
|      | 55K | ETAN95   | PGI-254545-0                                                 |
|      | 55K | ROM_LOAD | PGI-254545-EXT SUB=VENDOR, SUFF-COM, OFC=PGI_ROM_LOAD_TERMIN |
| 25K  | 45K | ROM_LOAD | BARPGPG-0                                                    |
|      |     | ROM_LOAD | ROM_LOAD_TERMIN-0                                            |

STRAP SETTINGS FOR HYNIX 32Mx16 DDR2 500MHz (BRINGUP BINARY) R511= NO STUFF, R512=NO STUFF

| ENVF | END | PROGNAME | FUNCTION            |
|------|-----|----------|---------------------|
|      | SK  | STRANSI  | 30H_PANOFI_L1F_ADRG |
|      | SK  | STRANSI  | 30H_PANOFI_L1F_ADRG |
|      | SK  | STRANSI  | 30H_PANOFI_L1F_ADRG |
|      | SK  | ROM_LOAD | 30H_PANOFI_L1F_ADRG |
| SK   |     | ROM_SI   | SUB_VNDORG          |
| SK   |     | ROM_SO   | ROM_ZIT             |

STRAP SETTINGS FOR HYNIX 32Mx16 DDR2 500MHz ( PRODUCTION BINARY) R511= 40K, R512= NO STUFF

| SIGNAL | GND | FUNCTION |
|--------|-----|----------|
| SK     |     | RAM0708  |
| SK     |     | RAM0709  |
| SK     |     | RAM0710  |
| SK     | SA  | RAM0711  |
| SK     | SA  | RAM0712  |
| SK     | SA  | RAM0713  |
| SK     | SA  | RAM0714  |
| SK     | SA  | RAM0715  |
| SK     | SA  | RAM0716  |
| SK     | SA  | RAM0717  |
| SK     | SA  | RAM0718  |
| SK     | SA  | RAM0719  |
| SK     | SA  | RAM0720  |
| SK     | SA  | RAM0721  |
| SK     | SA  | RAM0722  |
| SK     | SA  | RAM0723  |
| SK     | SA  | RAM0724  |
| SK     | SA  | RAM0725  |
| SK     | SA  | RAM0726  |
| SK     | SA  | RAM0727  |
| SK     | SA  | RAM0728  |
| SK     | SA  | RAM0729  |
| SK     | SA  | RAM0730  |
| SK     | SA  | RAM0731  |
| SK     | SA  | RAM0732  |
| SK     | SA  | RAM0733  |
| SK     | SA  | RAM0734  |
| SK     | SA  | RAM0735  |
| SK     | SA  | RAM0736  |
| SK     | SA  | RAM0737  |
| SK     | SA  | RAM0738  |
| SK     | SA  | RAM0739  |
| SK     | SA  | RAM0740  |
| SK     | SA  | RAM0741  |
| SK     | SA  | RAM0742  |
| SK     | SA  | RAM0743  |
| SK     | SA  | RAM0744  |
| SK     | SA  | RAM0745  |
| SK     | SA  | RAM0746  |
| SK     | SA  | RAM0747  |
| SK     | SA  | RAM0748  |
| SK     | SA  | RAM0749  |
| SK     | SA  | RAM0750  |
| SK     | SA  | RAM0751  |
| SK     | SA  | RAM0752  |
| SK     | SA  | RAM0753  |
| SK     | SA  | RAM0754  |
| SK     | SA  | RAM0755  |
| SK     | SA  | RAM0756  |
| SK     | SA  | RAM0757  |
| SK     | SA  | RAM0758  |
| SK     | SA  | RAM0759  |
| SK     | SA  | RAM0760  |
| SK     | SA  | RAM0761  |
| SK     | SA  | RAM0762  |
| SK     | SA  | RAM0763  |
| SK     | SA  | RAM0764  |
| SK     | SA  | RAM0765  |
| SK     | SA  | RAM0766  |
| SK     | SA  | RAM0767  |
| SK     | SA  | RAM0768  |
| SK     | SA  | RAM0769  |
| SK     | SA  | RAM0770  |
| SK     | SA  | RAM0771  |
| SK     | SA  | RAM0772  |
| SK     | SA  | RAM0773  |
| SK     | SA  | RAM0774  |
| SK     | SA  | RAM0775  |
| SK     | SA  | RAM0776  |
| SK     | SA  | RAM0777  |
| SK     | SA  | RAM0778  |
| SK     | SA  | RAM0779  |
| SK     | SA  | RAM0780  |
| SK     | SA  | RAM0781  |
| SK     | SA  | RAM0782  |
| SK     | SA  | RAM0783  |
| SK     | SA  | RAM0784  |
| SK     | SA  | RAM0785  |
| SK     | SA  | RAM0786  |
| SK     | SA  | RAM0787  |
| SK     | SA  | RAM0788  |
| SK     | SA  | RAM0789  |
| SK     | SA  | RAM0790  |
| SK     | SA  | RAM0791  |
| SK     | SA  | RAM0792  |
| SK     | SA  | RAM0793  |
| SK     | SA  | RAM0794  |
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| SK     | SA  | RAM0796  |
| SK     | SA  | RAM0797  |
| SK     | SA  | RAM0798  |
| SK     | SA  | RAM0799  |
| SK     | SA  | RAM0800  |
| SK     | SA  | RAM0801  |
| SK     | SA  | RAM0802  |
| SK     | SA  | RAM0803  |
| SK     | SA  | RAM0804  |
| SK     | SA  | RAM0805  |
| SK     | SA  | RAM0806  |
| SK     | SA  | RAM0807  |
| SK     | SA  | RAM0808  |
| SK     | SA  | RAM0809  |
| SK     | SA  | RAM0810  |
| SK     | SA  | RAM0811  |
| SK     | SA  | RAM0812  |
| SK     | SA  | RAM0813  |
| SK     | SA  | RAM0814  |
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| SK     | SA  | RAM0844  |
| SK     | SA  | RAM0845  |
| SK     | SA  | RAM0846  |
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| SK     | SA  | RAM0848  |
| SK     | SA  | RAM0849  |
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| SK     | SA  | RAM0853  |
| SK     | SA  | RAM0854  |
| SK     | SA  | RAM0855  |
| SK     | SA  | RAM0856  |
| SK     | SA  | RAM0857  |
| SK     | SA  | RAM0858  |
| SK     | SA  | RAM0859  |
| SK     | SA  | RAM0860  |
| SK     | SA  | RAM0861  |
| SK     | SA  | RAM0862  |
| SK     | SA  | RAM0863  |
| SK     | SA  | RAM0864  |
| SK     | SA  | RAM0865  |
| SK     | SA  | RAM0866  |
| SK     | SA  | RAM0867  |
| SK     | SA  | RAM0868  |
| SK     | SA  | RAM0869  |
| SK     | SA  | RAM0870  |
| SK     | SA  | RAM0871  |
| SK     | SA  | RAM0872  |
| SK     | SA  | RAM0873  |
| SK     | SA  | RAM0874  |
| SK     | SA  | RAM0875  |
| SK     | SA  | RAM0876  |
| SK     | SA  | RAM0877  |
| SK     | SA  | RAM0878  |
| SK     | SA  | RAM0879  |
| SK     | SA  | RAM0880  |
| SK     | SA  | RAM0881  |
| SK     | SA  | RAM0882  |
| SK     | SA  | RAM0883  |
| SK     | SA  | RAM0884  |
| SK     | SA  | RAM0885  |
| SK     | SA  | RAM0886  |
| SK     | SA  | RAM0887  |
| SK     | SA  | RAM0888  |
| SK     | SA  | RAM0889  |
| SK     | SA  | RAM0890  |
| SK     | SA  | RAM0891  |
| SK     | SA  | RAM0892  |
| SK     | SA  | RAM0893  |
| SK     | SA  | RAM0894  |
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| SK     | SA  | RAM0898  |
| SK     | SA  | RAM0899  |
| SK     | SA  | RAM0900  |
| SK     | SA  | RAM0901  |
| SK     | SA  | RAM0902  |
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| SK     | SA  |          |



NVIDIA CORPORATION

2701 SAN TOMAS EXPRESSWAY

SANTA CLARA, CA 95050, USA

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|-------|----------------------|
| NV_PN | 600-10561-xxxx-100 D |
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| NAME | DATE 31-AUG-2007 |

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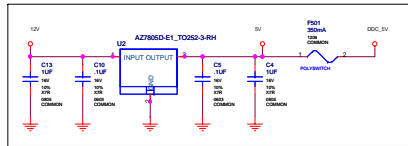
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|             |                                                                                       |
|-------------|---------------------------------------------------------------------------------------|
| ASSEMBLY    | BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| PAGE DETAIL | Straps, Mechanical Parts                                                              |

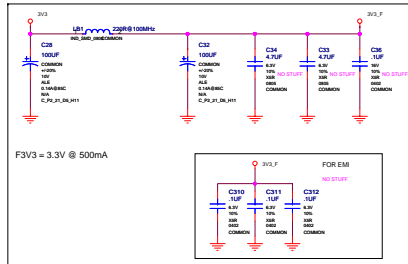


Power Supply II: 5V, DDC5V, F3V3, FBVDDQ

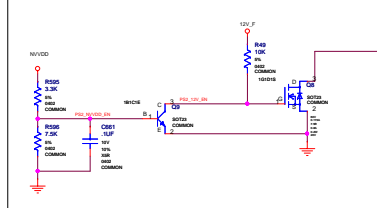
5V, DDC5V



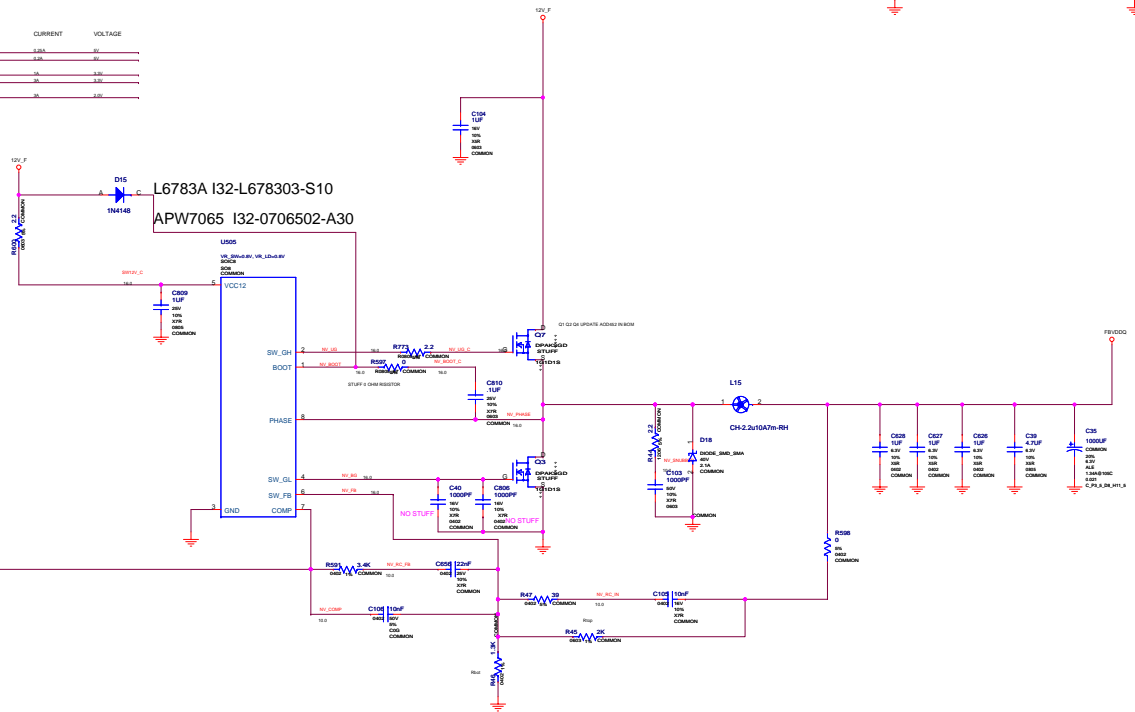
F3V3



### Power Sequence



| Net Name | MIN_LINE_WIDTH | CURRENT | VOLTAGE |
|----------|----------------|---------|---------|
| 0V       | 12MIL          | 1.20A   | 0V      |
| 500K_0V  | 12MIL          | 0.20A   | 0V      |
| 3V3L_F   | 10MIL          | 10A     | 3.3V    |
| 3V3      | 30MIL          | 30A     | 3.3V    |
| FW(0V2)  | 30MIL          | 30A     | 2.0V    |



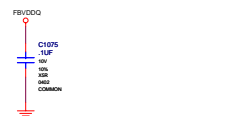
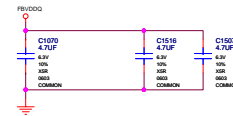
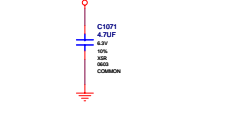
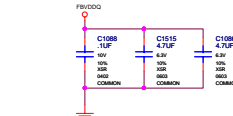
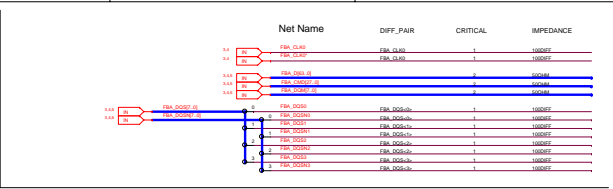
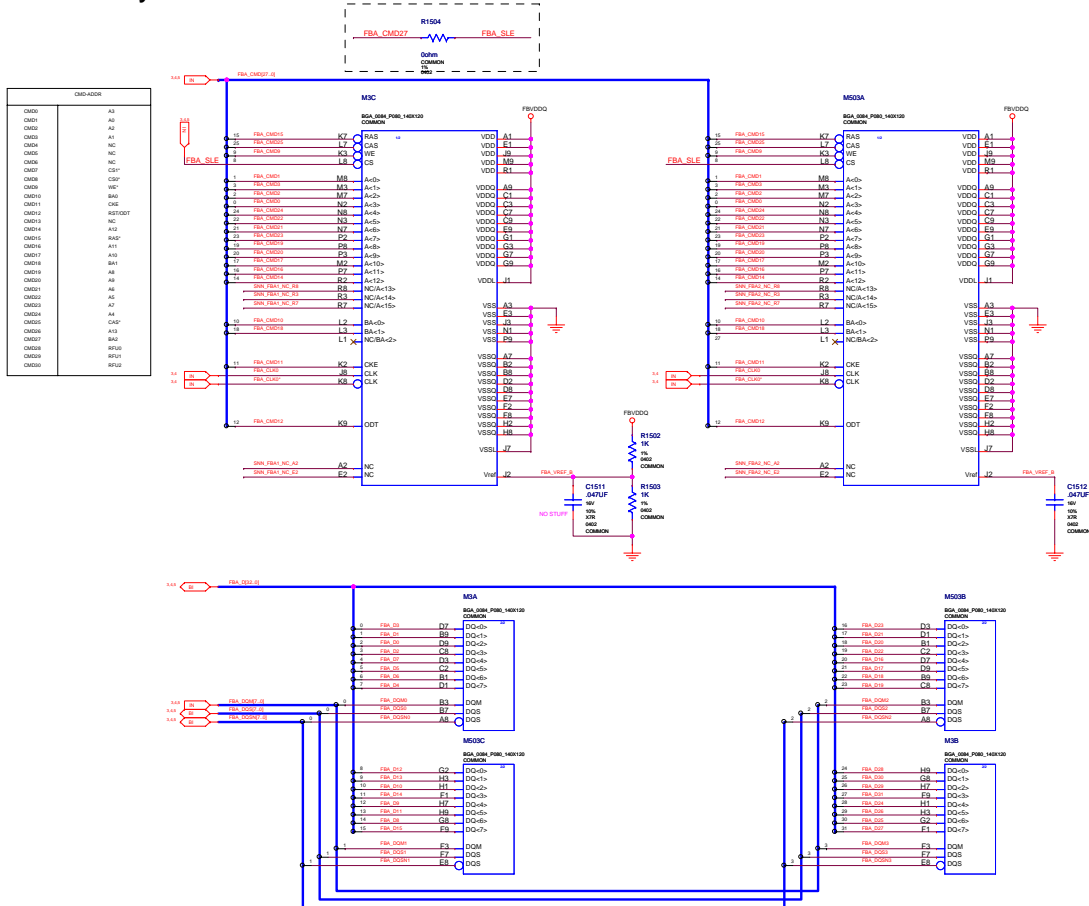
FBVDDQ

$$\begin{aligned} V_{out} &= V_{Ref} * (1 + R_{top}/R_{bot}) \\ 1.800V &= 0.8V * (1 + (5.9k/4.7k)) \quad (ISL6549) \\ 2.000V &= 0.8V * (1 + (5.9k/3.92k)) \quad (ISL6549) \\ 2.156V &= 0.8V * (1 + (5.9k/3.4k)) \quad (ISL6549) \end{aligned}$$

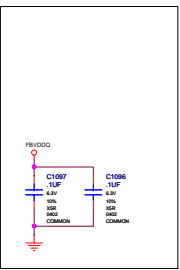




Memory 2nd Bank 0..31



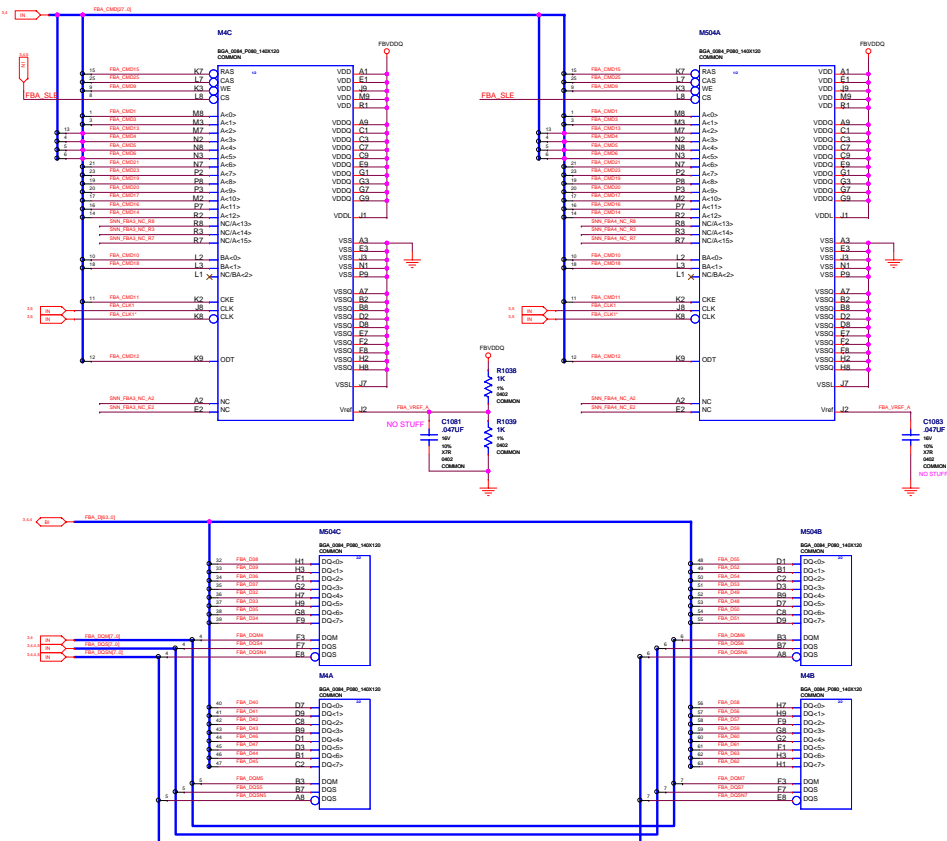
X-cap for CMD



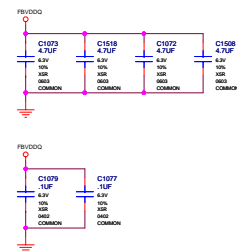
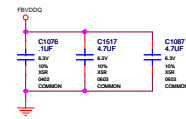



## Memory 2nd Bank 32..63

| CMD-ACOR |    |         |
|----------|----|---------|
| CMD00    |    | NC      |
| CMD01    | AI | AI      |
| CMD02    |    | NC      |
| CMD03    | AI | AI      |
| CMD04    | AI | A3      |
| CMD05    |    | AI      |
| CMD06    | AI | AI      |
| CMD07    |    | CS1*    |
| CMD08    |    | CS0*    |
| CMD09    |    | W0*     |
| CMD10    |    | BA3     |
| CMD11    |    | OK      |
| CMD12    |    | W0*F00T |
| CMD13    |    | AI      |
| CMD14    |    | A12     |
| CMD15    |    | BA0*    |
| CMD16    |    | A11     |
| CMD17    |    | A10     |
| CMD18    |    | BA1*    |
| CMD19    |    | AI      |
| CMD20    |    | AI      |
| CMD21    |    | AI      |
| CMD22    |    | NC      |
| CMD23    |    | AI      |
| CMD24    |    | NC      |
| CMD25    |    | CAS*    |
| CMD26    |    | A13     |
| CMD27    |    | BA2     |
| CMD28    |    | RFU0    |
| CMD29    |    | RFU1    |
| CMD30    |    | RFU2    |



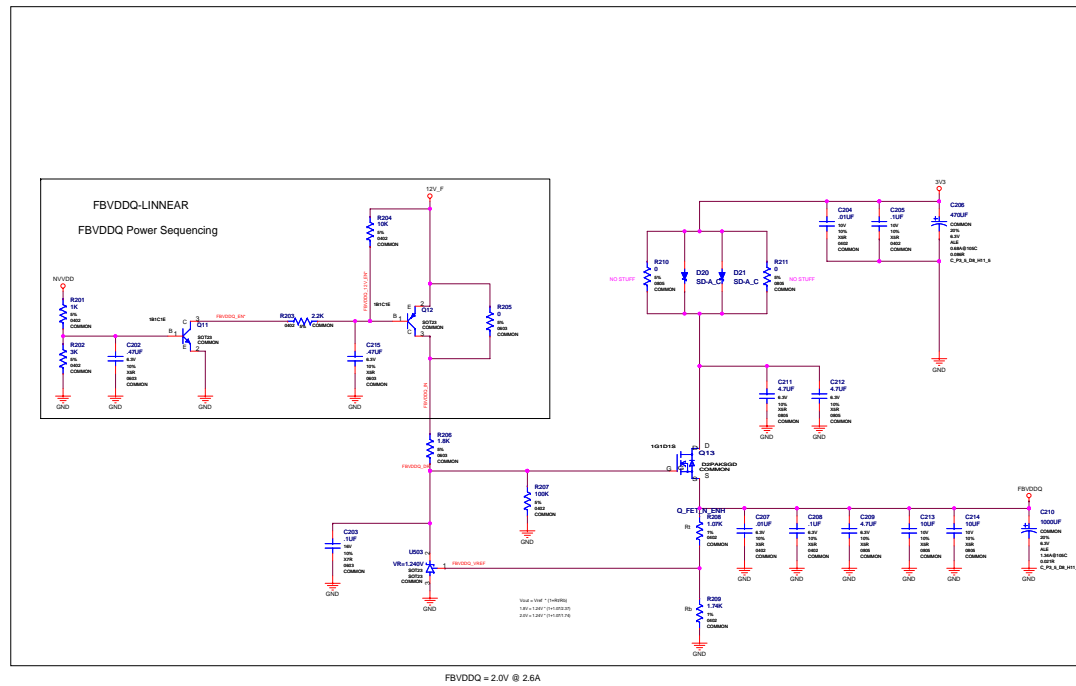
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|  | FBI_0413 | FBI_0413_1 | 1        | 100Ω      |
|  | FBI_0414 | FBI_0414_1 | 1        | 100Ω      |
|  | FBI_0415 | FBI_0415_1 | 1        | 100Ω      |
|  | FBI_0416 | FBI_0416_1 | 1        | 100Ω      |
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|  | FBI_0426 | FBI_0426_1 | 1        | 100Ω      |
|  | FBI_0427 | FBI_0427_1 | 1        | 100Ω      |
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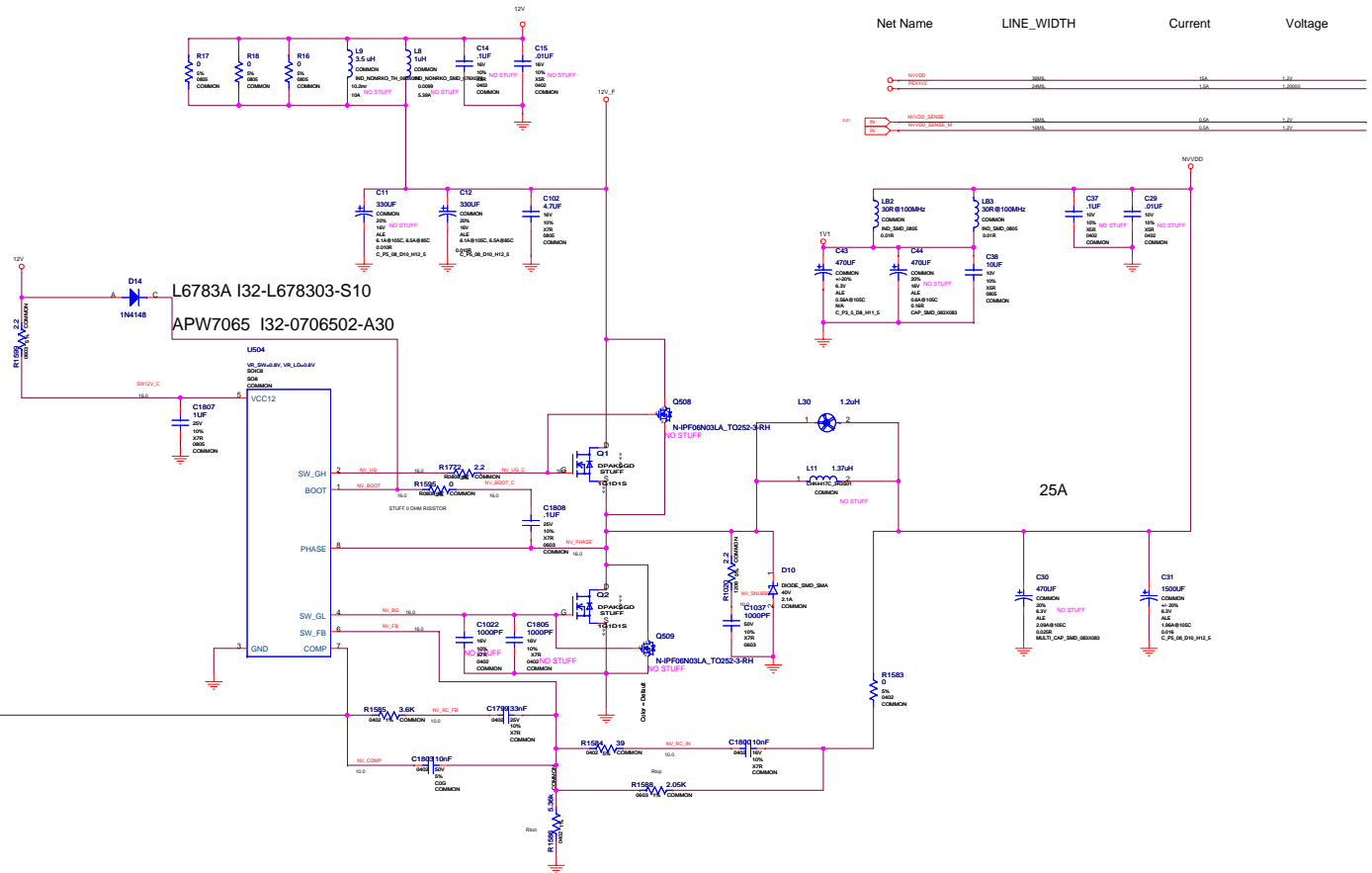
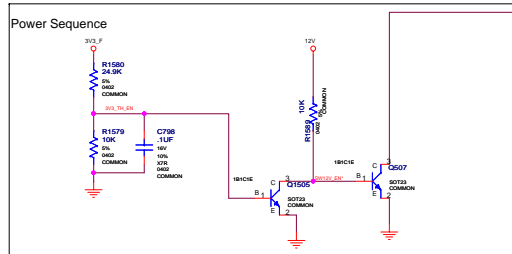
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| <b>NVIDIA CORPORATION</b><br>2701 SAN TOMAS EXPRESSWAY<br>SANTA CLARA, CA 95050, USA |  |  |             |
| <b>NV_PN</b>                                                                         |  | <b>600-10561-xxxx-100 D</b>                                                           |             |
| <b>ID</b>                                                                            |  | <b>PAGE</b>                                                                           |             |
| <b>NAME</b>                                                                          |  | <b>DATE</b>                                                                           | 31-AUG-2007 |



## Power Supply II: FBVDDQ



# PowerSupplyII: NVVDD



NVVDD

$$V_{out} = V_{Ref} \cdot (1 + R_{top}/R_{bot})$$

$$1.1V = 0.8V \cdot (1 + (1.54k/4.02k)) \quad (ISL6549)$$

$$1.2V = 0.8V \cdot (1 + (1.54k/3.09k)) \quad (ISL6549)$$

$$1.2V = 0.8V \cdot (1 + (10k/20k)) \quad (APW7065)$$

|                            |                      |
|----------------------------|----------------------|
| NVIDIA CORPORATION         |                      |
| 2701 SAN TOMAS EXPRESSWAY  |                      |
| SANTA CLARA, CA 95050, USA |                      |
| NV_PN                      | 600-10403-0000-200 A |
| ID                         |                      |
| NAME                       |                      |
| PAGE                       |                      |
| DATE                       | 12 JAN 2007          |

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| ASSEMBLY    | BASE LEVEL GENERIC SCHEMATIC ONLY. COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL. |
| PAGE DETAIL | PowerSupply: NVVDD                                                                     |

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