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ipad mini2

PDF	CSA	CONTENTS	SYNC MASTER	DATE
1	1	TABLE OF CONTENTS	N/A	N/A
2	2	BLOCK DIAGRAM: SYSTEM	J85_MLB_04	/02/2013
3	4	BOM TABLES	J85_MLB_04	/02/2013
4	6	SOC: MAIN	J85_MLB_04	/02/2013
5	7	SOC: I/OS	J85_MLB_04	/02/2013
6	8	SOC: NAND	J85_MLB_04	/02/2013
7	9	SOC: DP,MIPI	J85_MLB_04	/02/2013
8	10	SOC: SRAM, IO PWRS	J85_MLB_04	/02/2013
9	11	SOC: VDD, SRAM, CPU, GPU PWRS	J85_MLB_04	/02/2013
10	12	SOC: MISC & ALIASES	J85_MLB_04	/02/2013
11	13	IO: TRISTAR	J85_MLB_04	/02/2013
12	14	NAND STORAGE	J85_MLB_04	/02/2013
13	17	TOUCH: SUPPORT CKT & CONN	J85_MLB_04	/02/2013
14	18	AUDIO: HP FLEX CONN	J85_MLB_04	/02/2013
15	19	AUDIO: L81 CODEC	J85_MLB_04	/02/2013
16	20	AUDIO: CS35L19A AMPS	J85_MLB_04	/02/2013
17	21	BUTTON: CONN	J85_MLB_04	/02/2013
18	22	VIDEO: EDP SUPPORT & CONN	J85_MLB_04	/02/2013
19	24	SENSOR: OSCAR	J85_MLB_04	/02/2013
20	26	CAMERA: FF-ALS CONN & FILTERS	J85_MLB_04	/02/2013

PDF	CSA	CONTENTS	SYNC MASTER	DATE
21	27	SENSOR: ACCEL, COMPASS, GYRO	J85_MLB_04	/02/2013
22	29	CAMERA: REAR CONN & FILTERS	J85_MLB_04	/02/2013
23	57	IO: FILTERS & HOTBAR CONN	J85_MLB_04	/02/2013
24	58	WIFI/BT: MODULE	WIFI_DEV04	/04/2013
25	75	POWER: BATTERY CONNECTOR	J85_MLB_04	/02/2013
26	81	PMU: ANYA PAGE 1	J85_MLB_04	/02/2013
27	82	PMU: ANYA PAGE 2	J85_MLB_04	/02/2013
28	83	PMU: ANYA PAGE 3	J85_MLB_04	/02/2013
29	84	PMU: ANYA PAGE 4	J85_MLB_04	/02/2013
30	85	POWER: PP1V8_SW	J85_MLB_04	/02/2013
31	90	SEP: EEPROM & SOC DEBUG	J85_MLB_04	/02/2013
32	93	TEST: TP/HOLES/FIDUCIALS	J85_MLB_04	/02/2013
33	94	TEST: EE TP/PP	J85_MLB_04	/02/2013
34	121	POWER: ALIASES	J85_MLB_04	/02/2013

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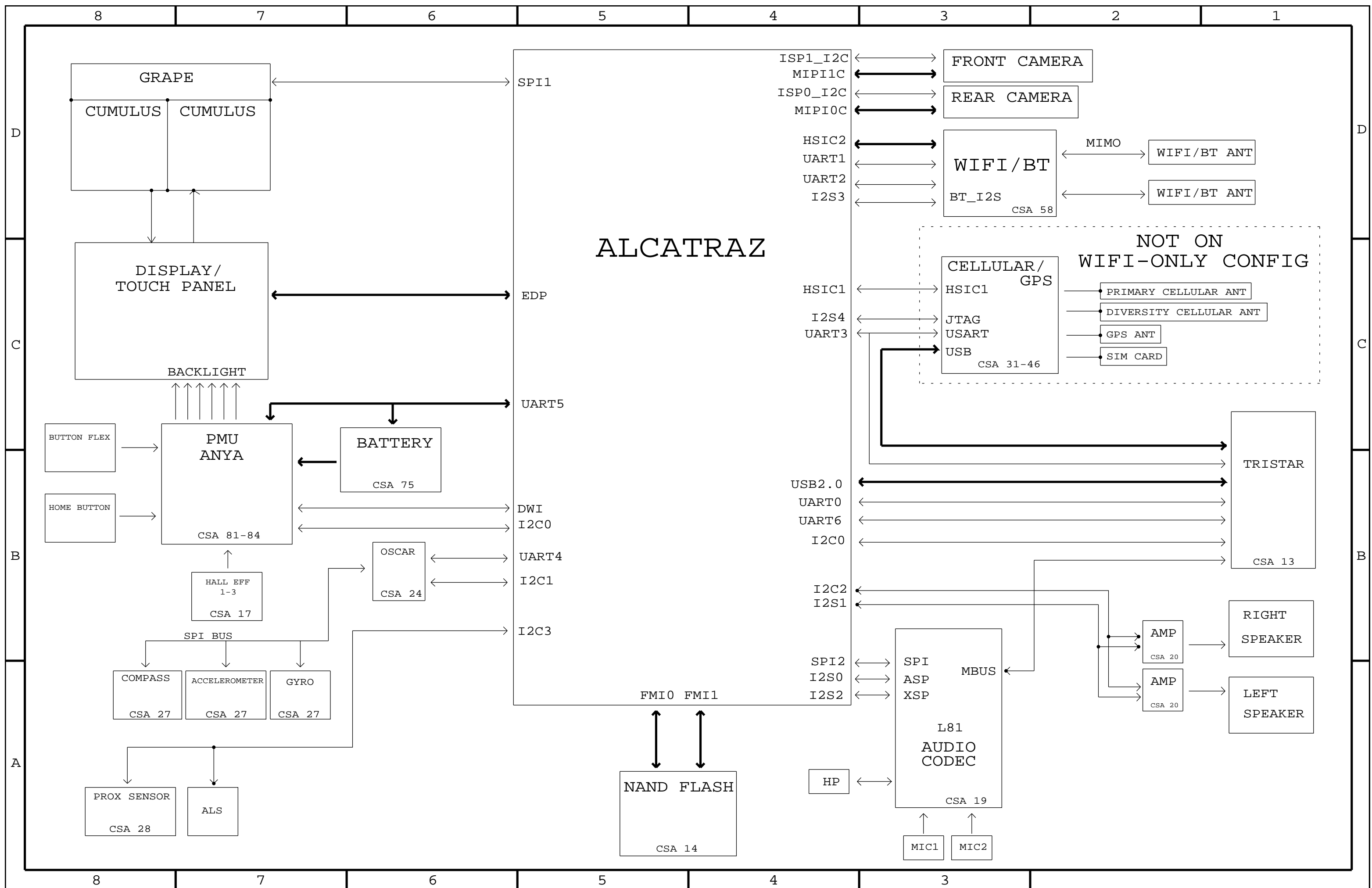
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DRAWING

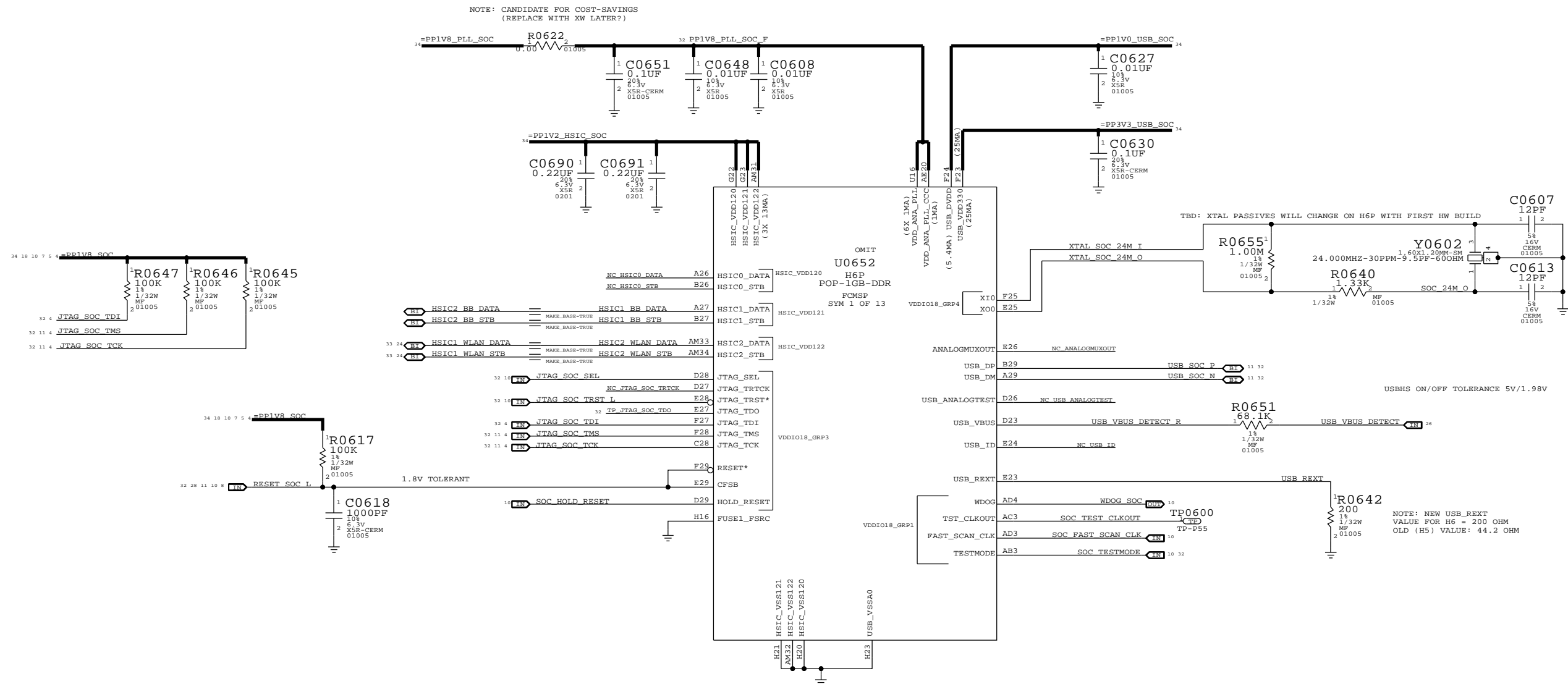
ipad mini2

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19	24	SENSOR: OSCAR	J85_MLB_04	02/2013
20	26	CAMERA: FF-ALS CONN & FILTERS	J85_MLB_04	02/2013

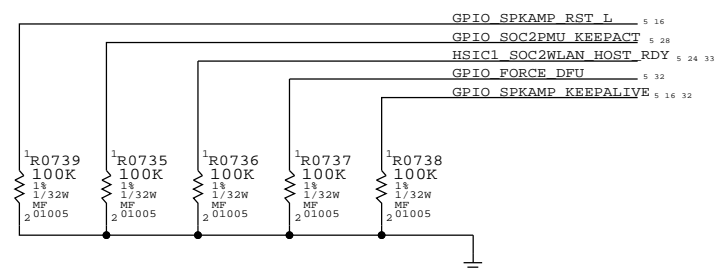
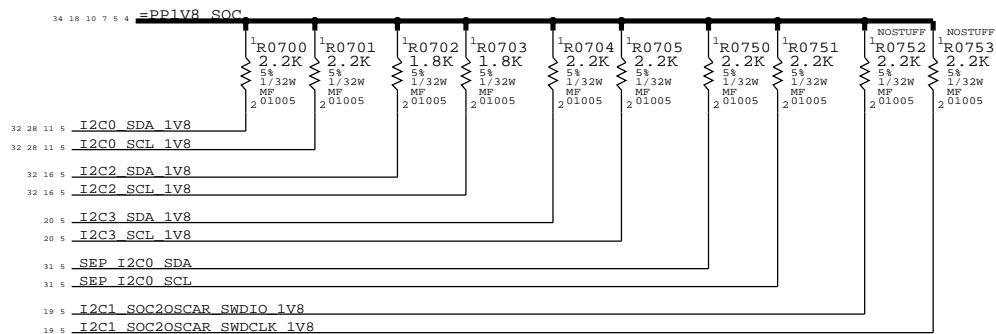
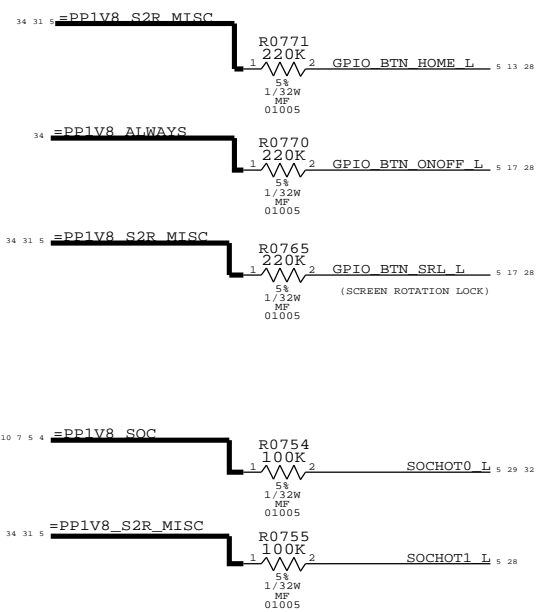
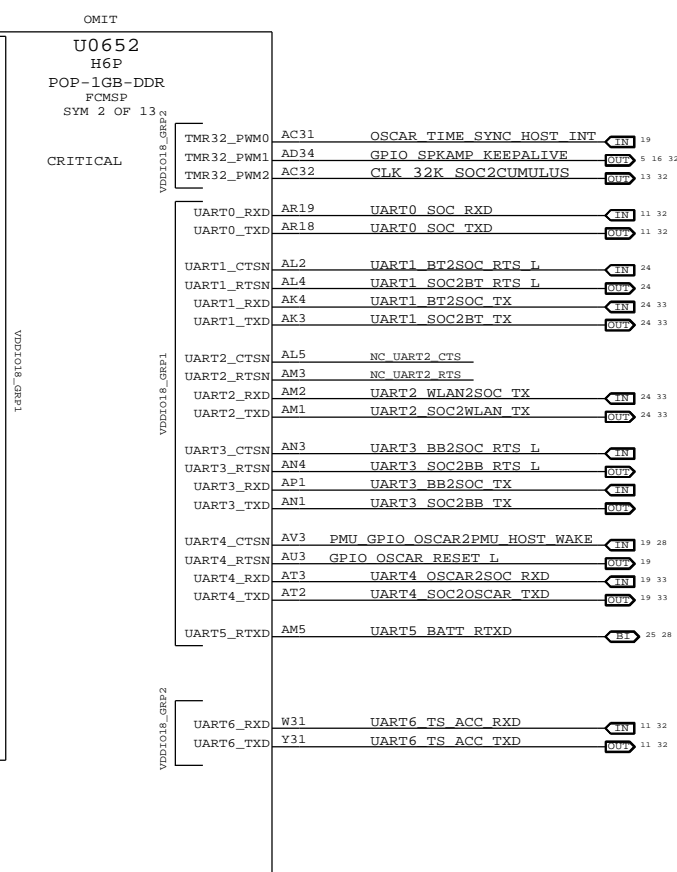
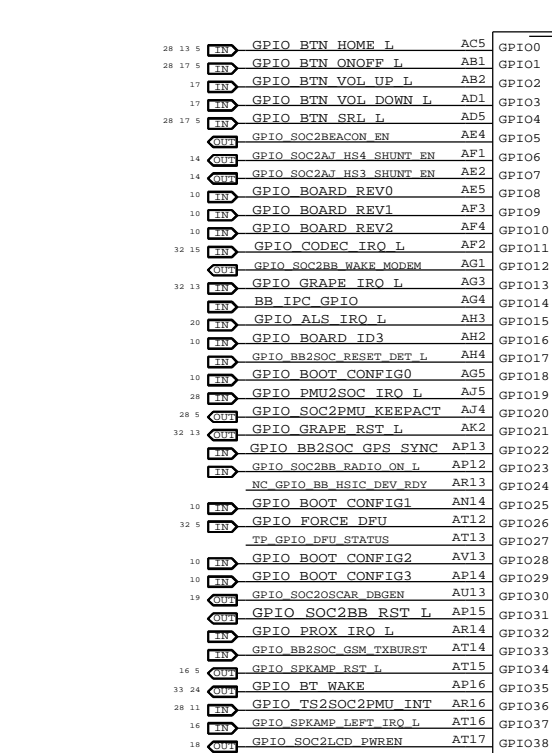
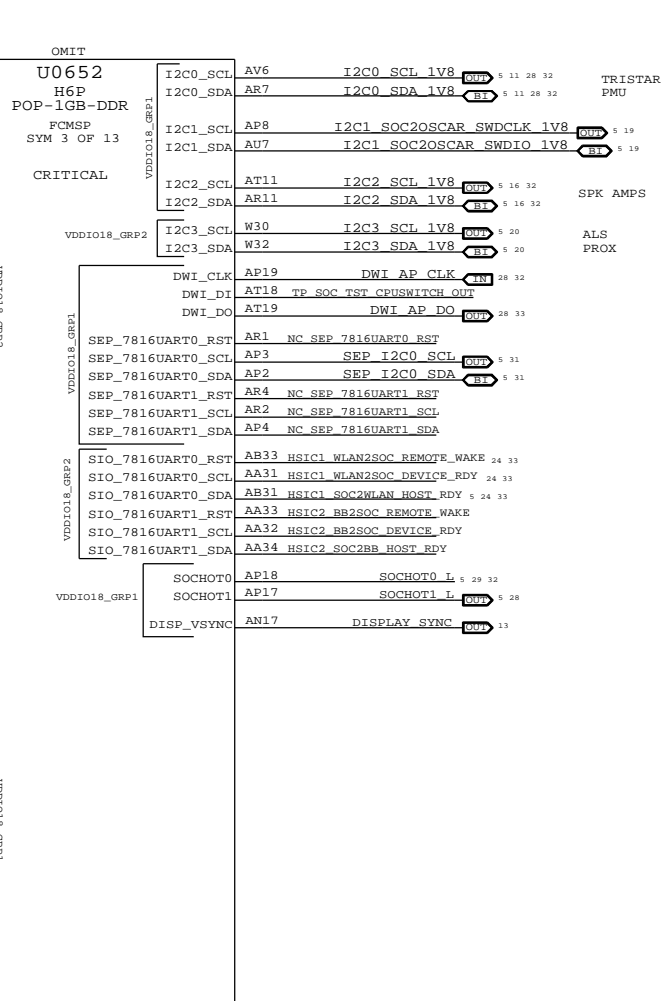
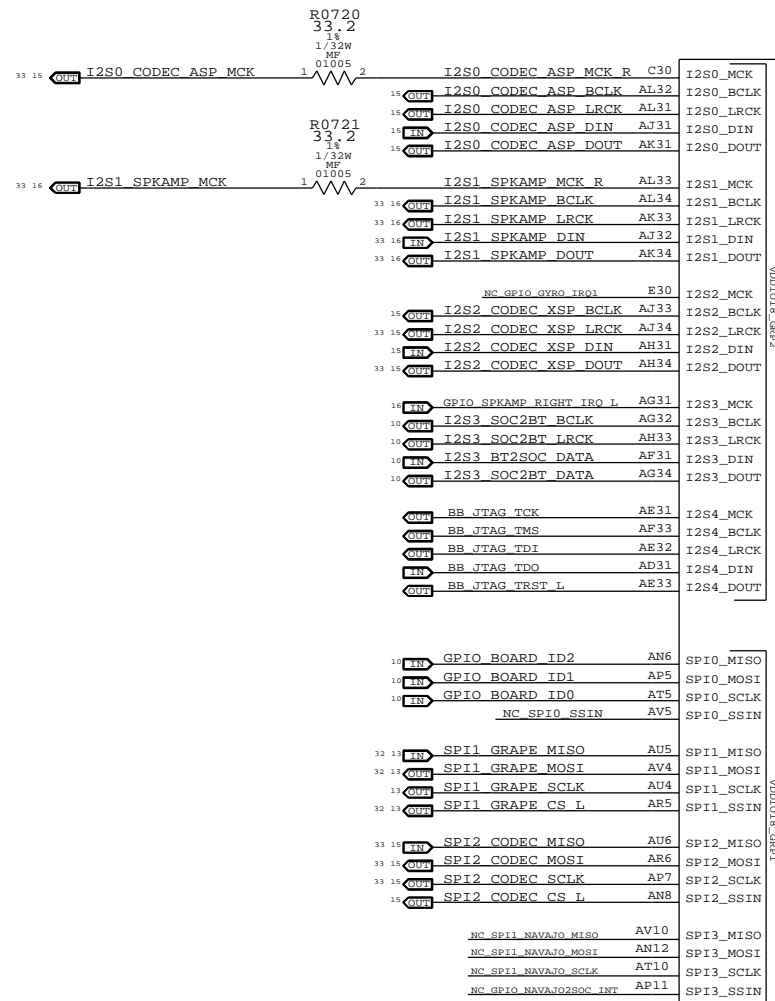
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34	121	POWER: ALIASES	J85_MLB_04	02/2013

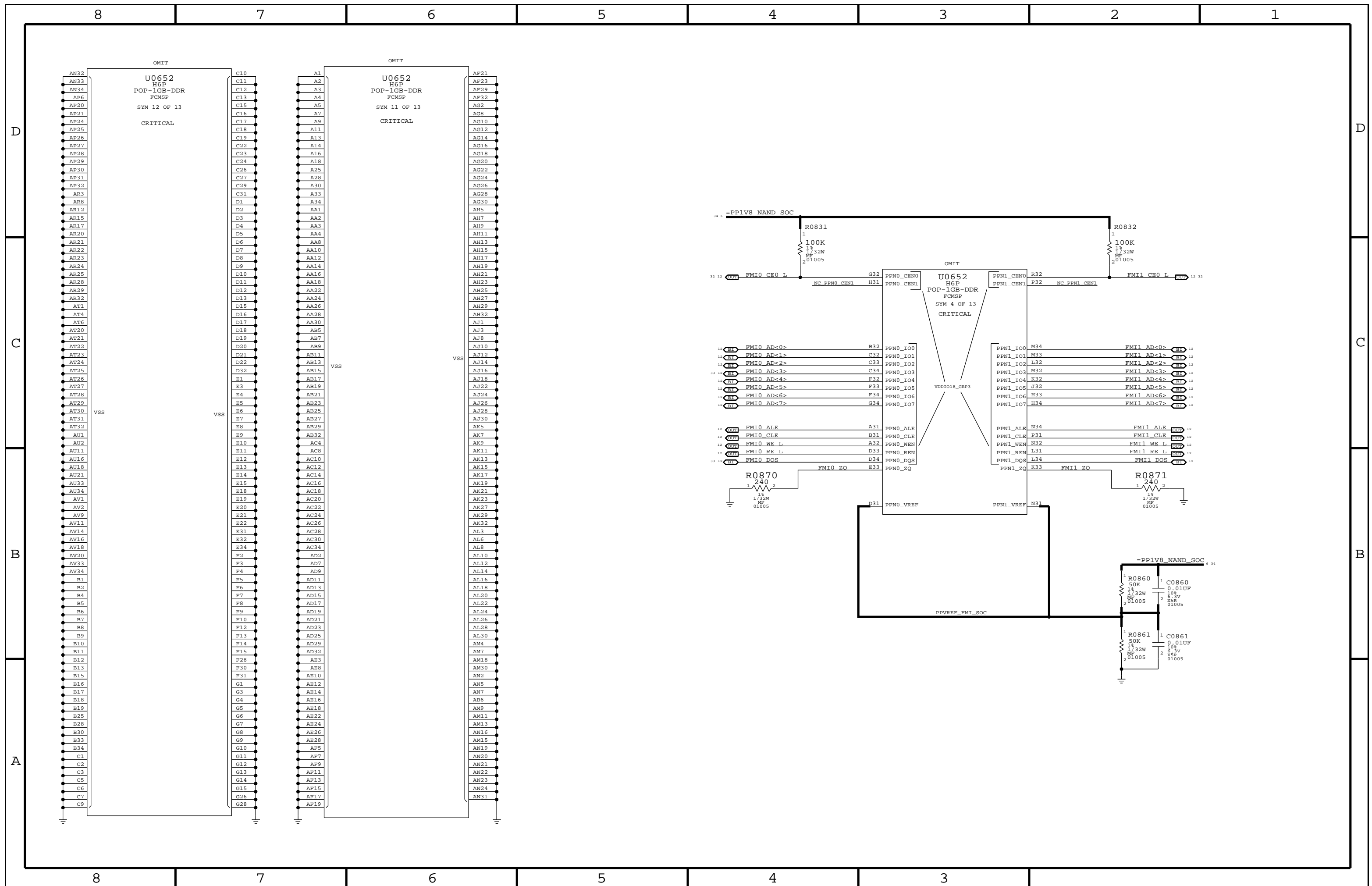


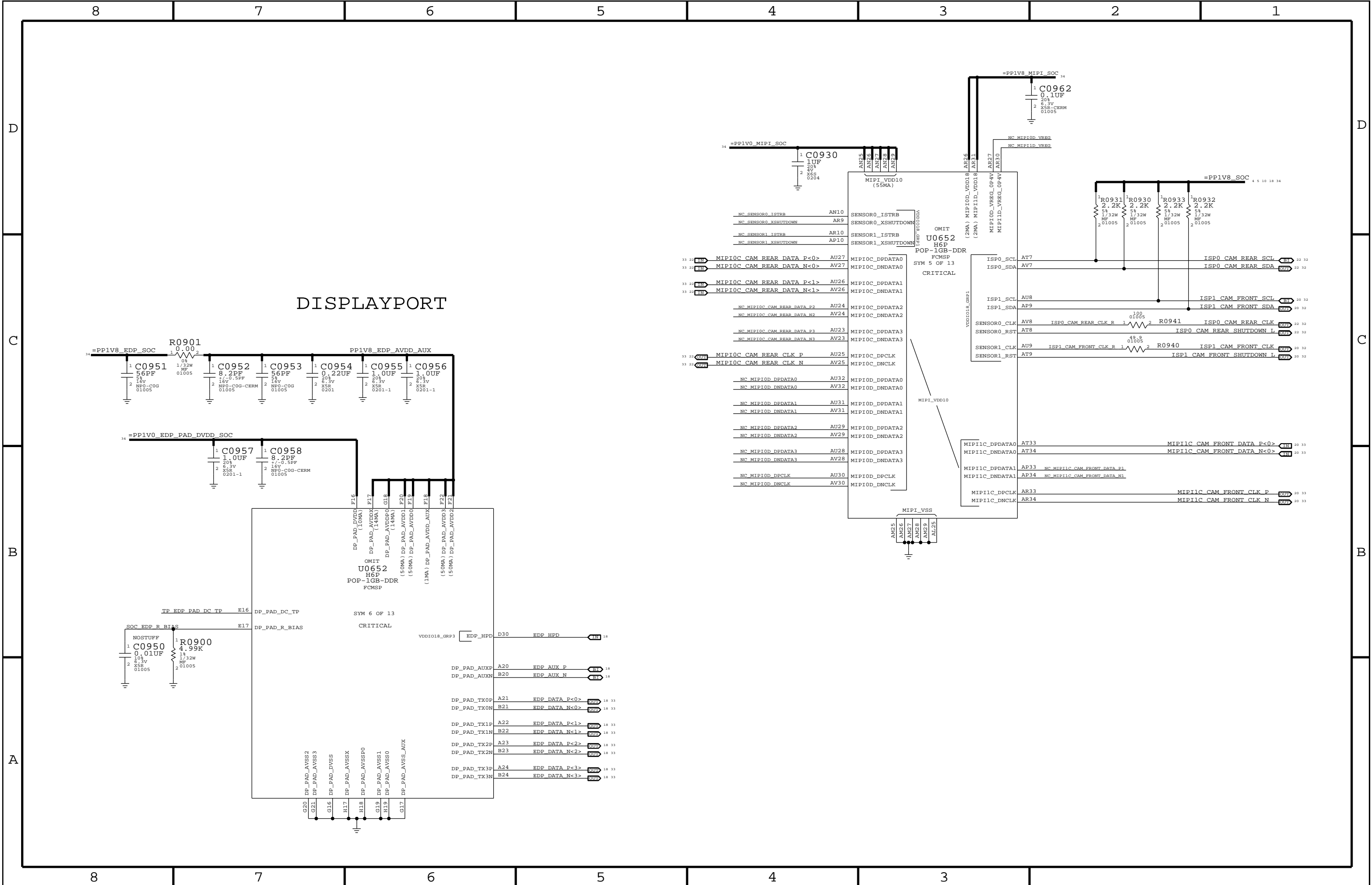
H6P: JTAG, USB, PLL, HSIC, XTAL

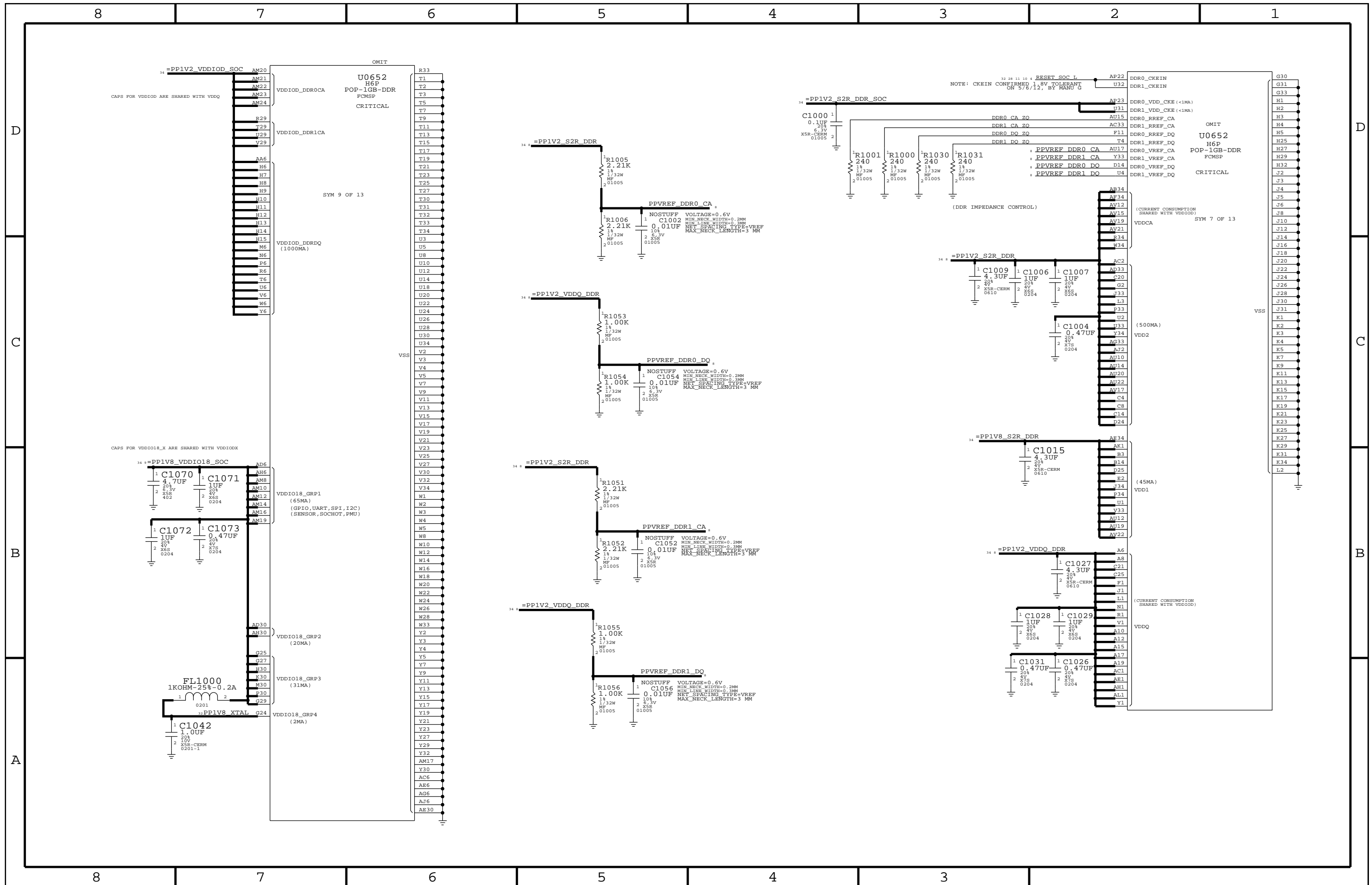


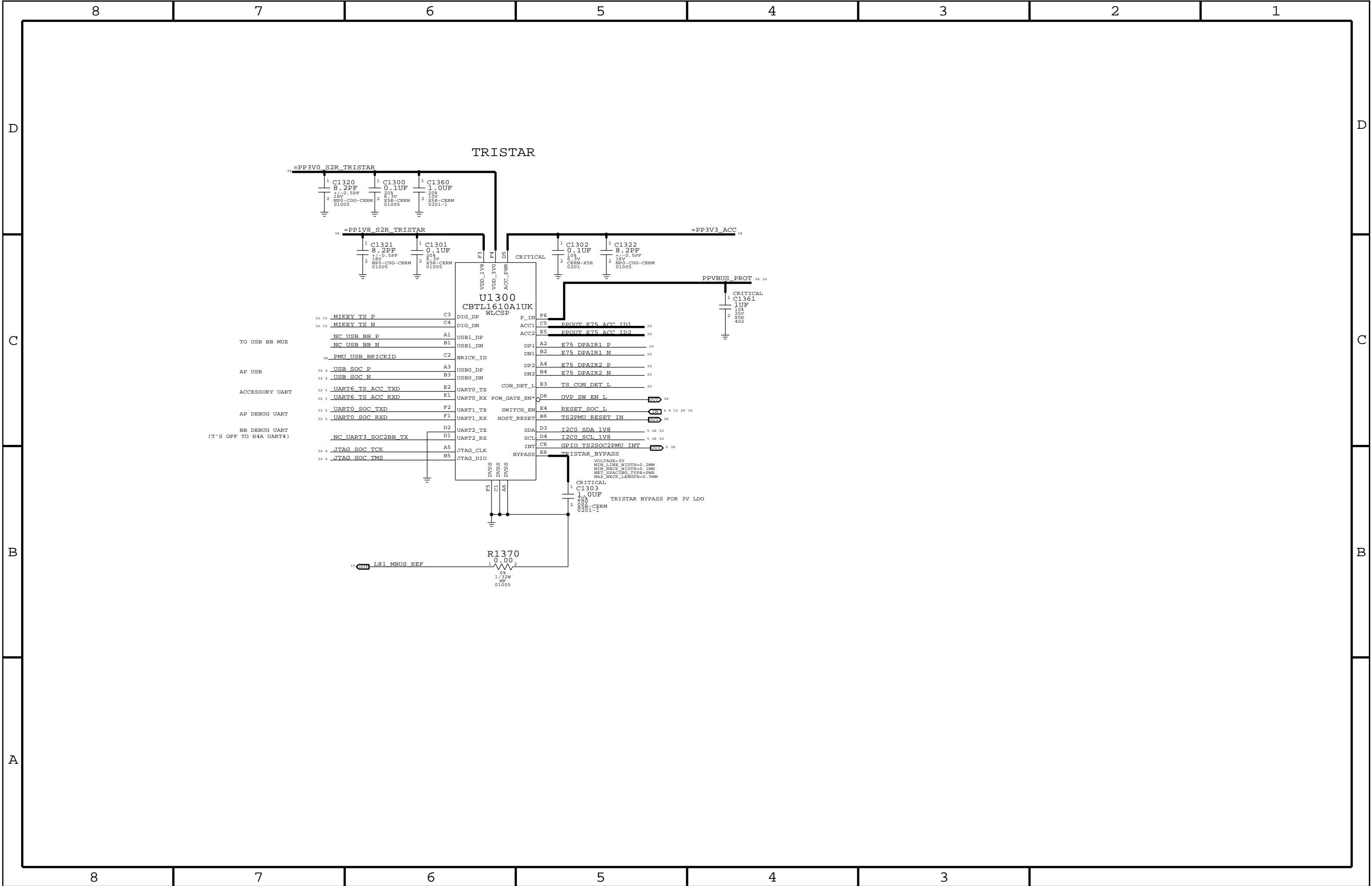
SOC I / OS

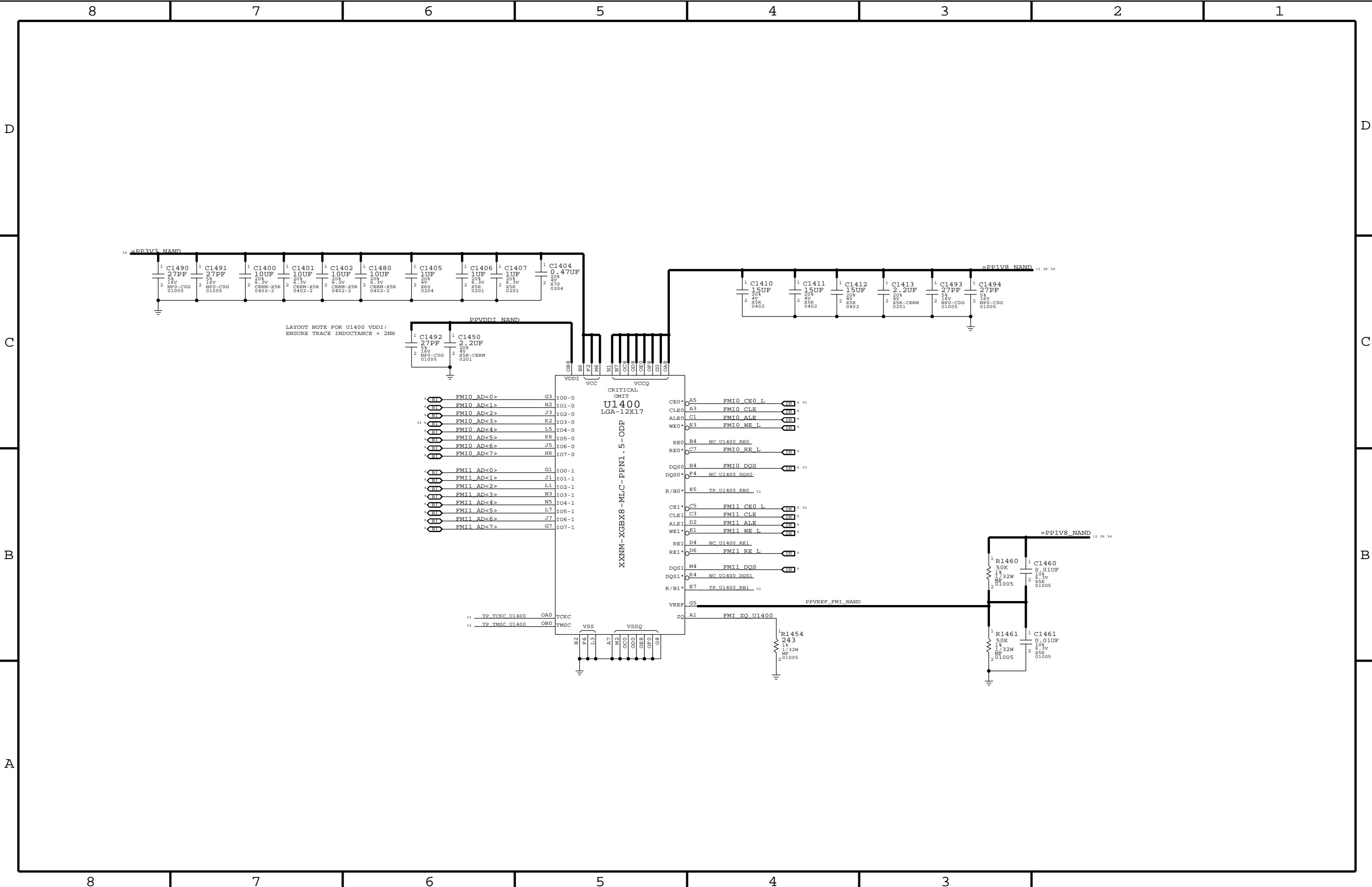




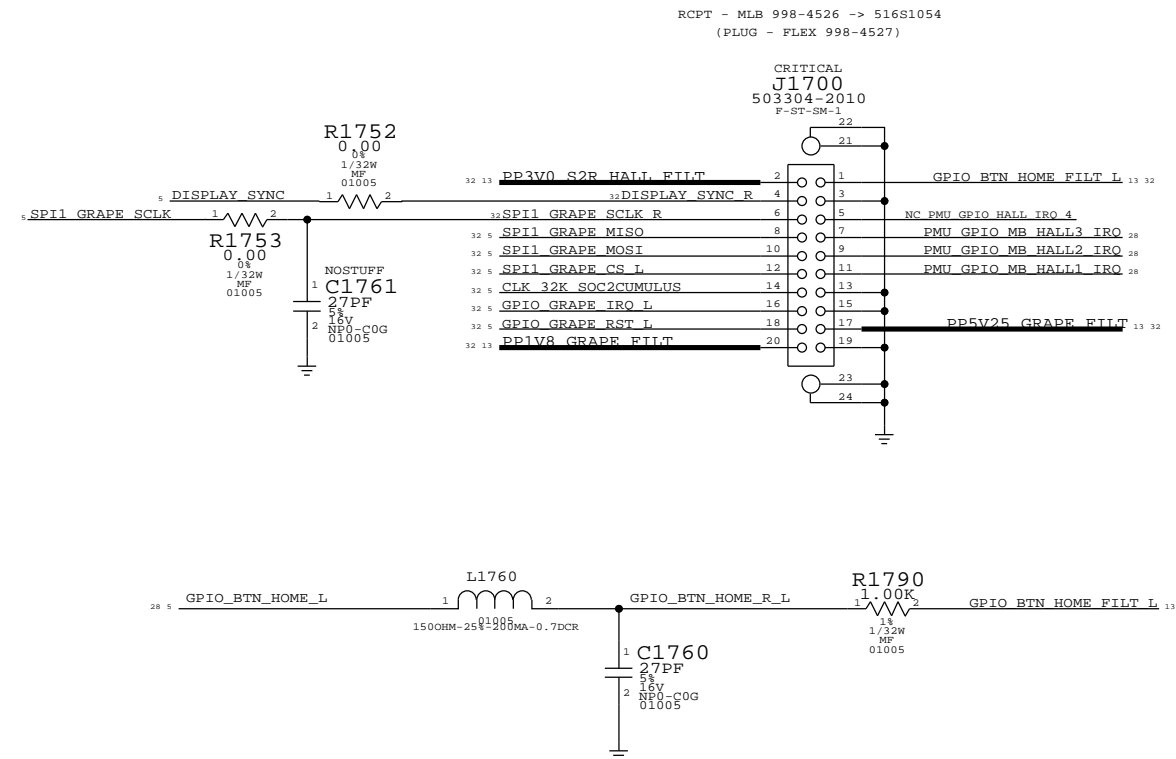
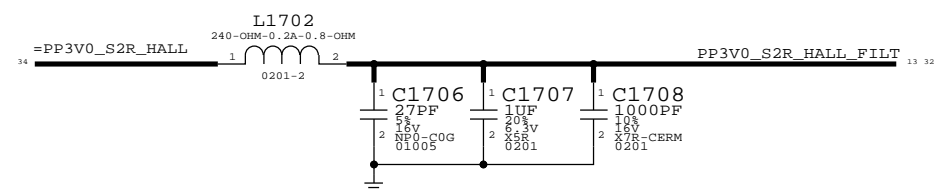
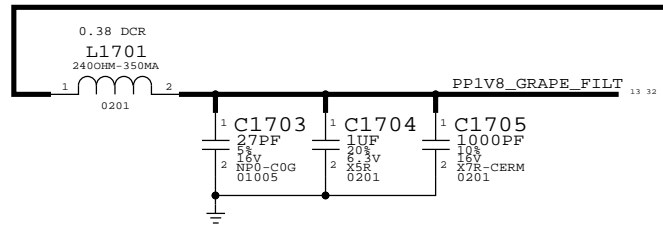
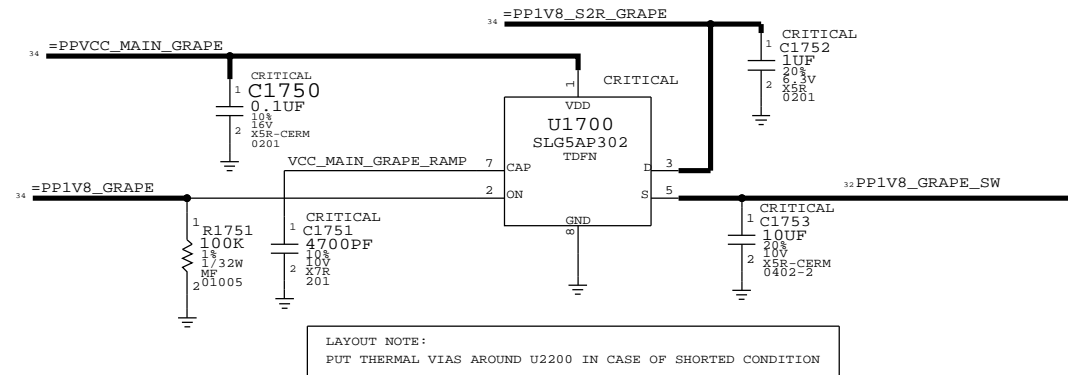
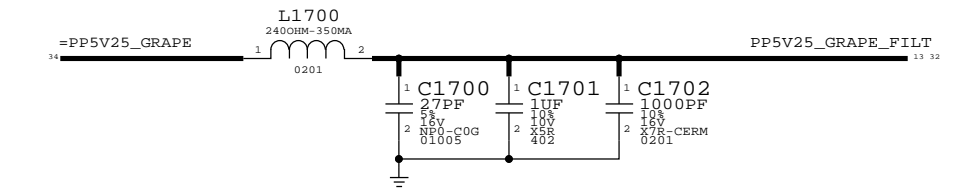


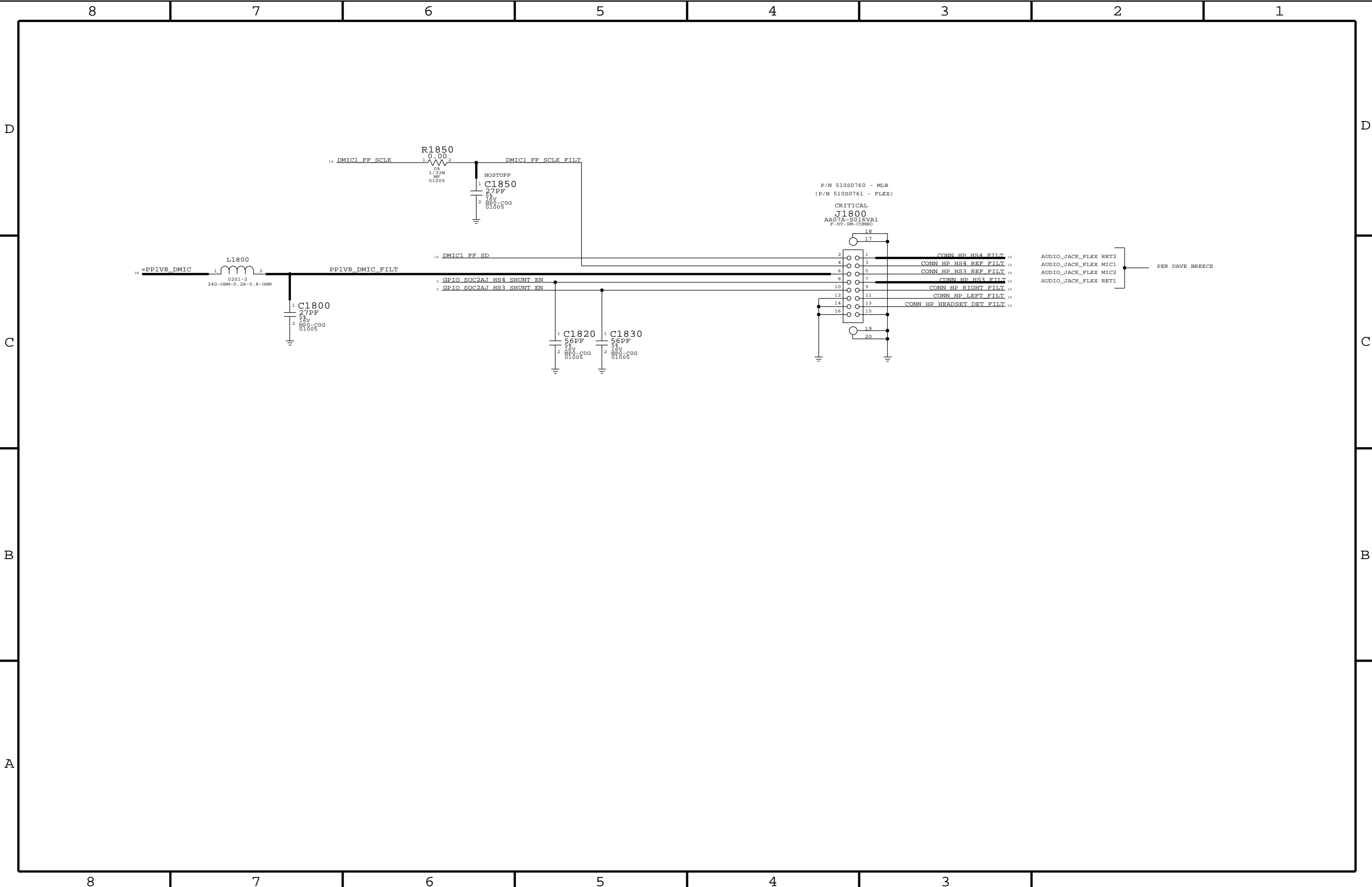


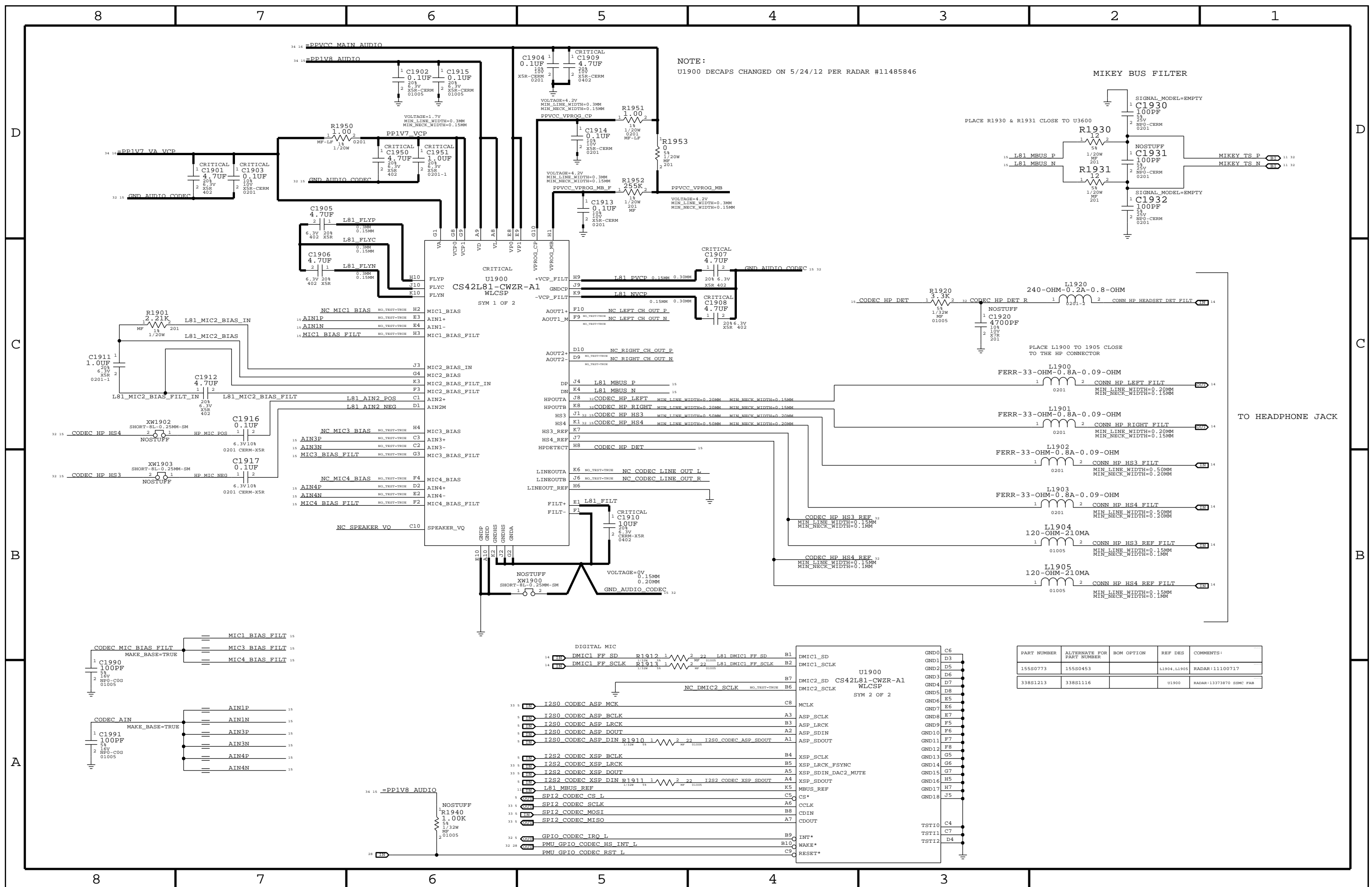




TOUCH SUBSYSTEM

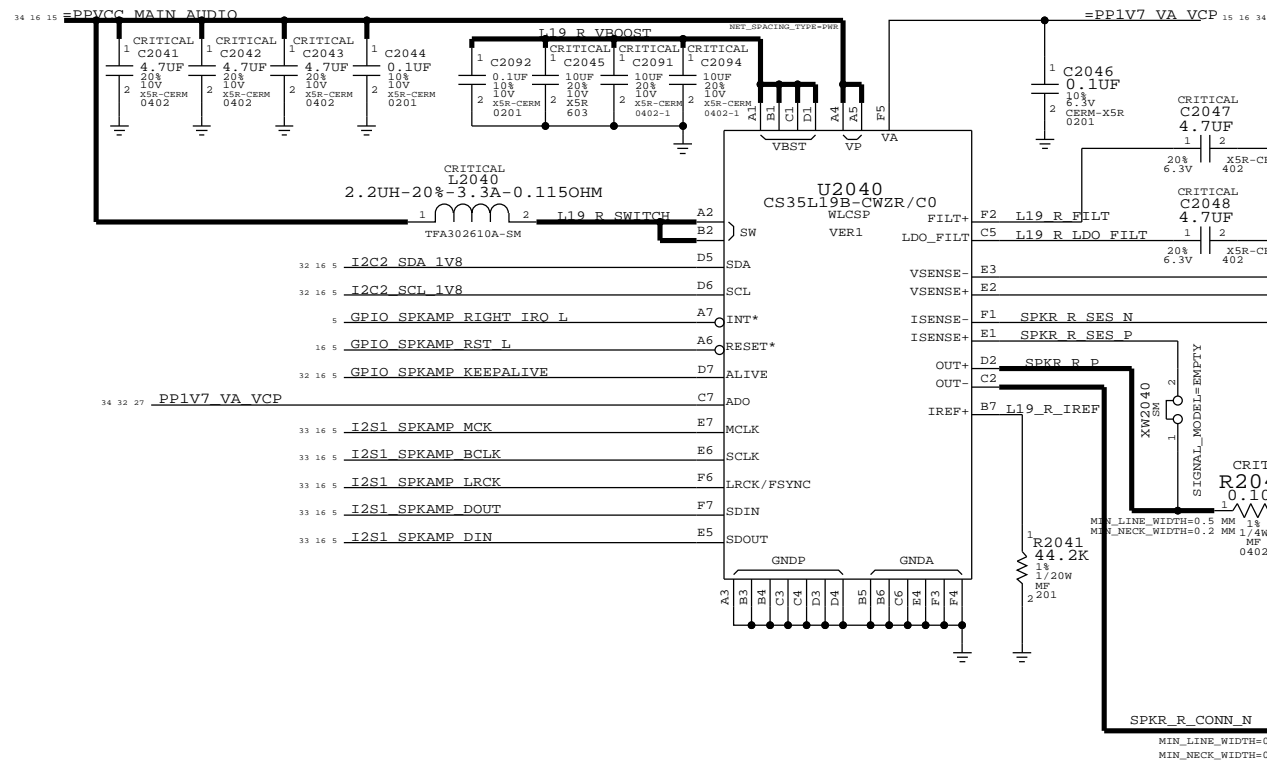






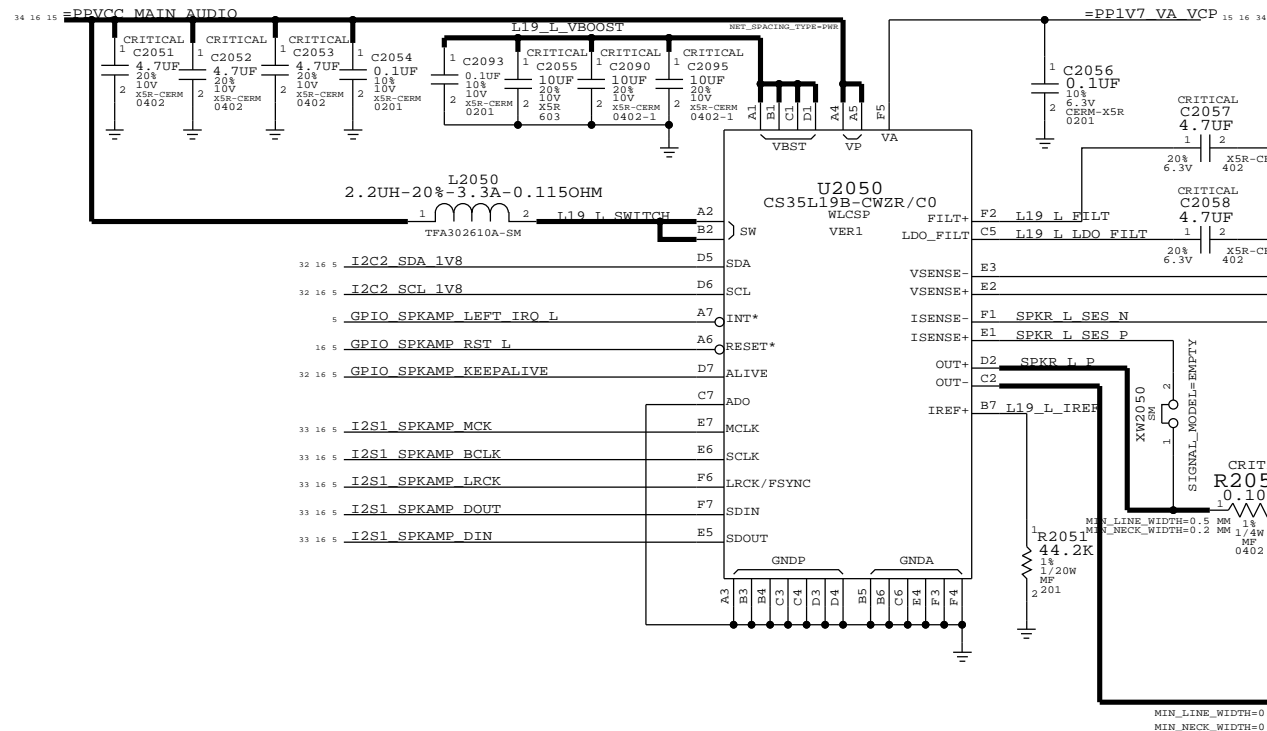
RIGHT SPEAKER AMP

I2C ADDRESS: 1000001X

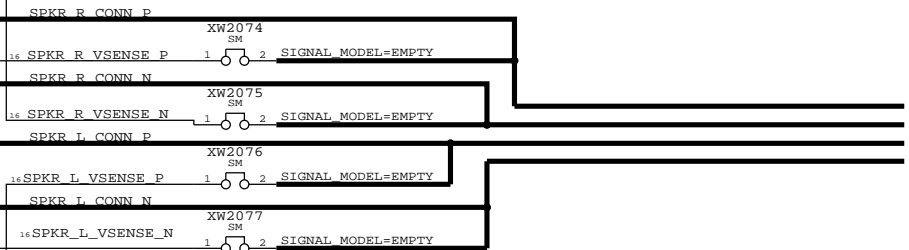


LEFT SPEAKER AMP

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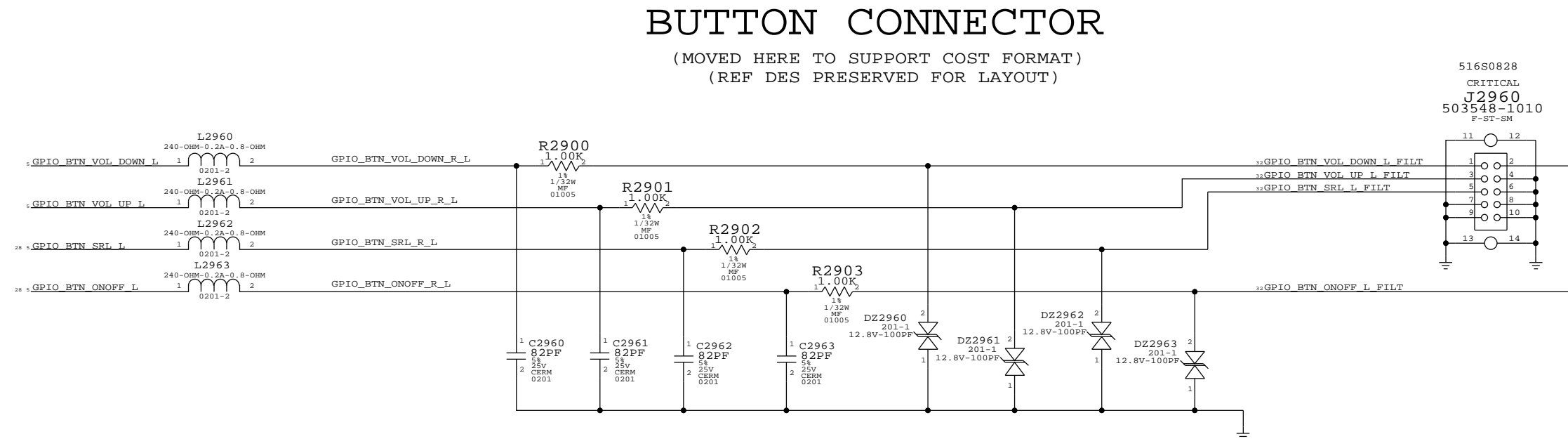


SPEAKER CONNECTOR



UPDATED: DEC 13

1. ALL THE EMI/DESENSE FILTER COMPONENTS HAVE BEEN REMOVED BASED ON PERFORMANCE ON J65
2. THE CURRENT VERSION OF L19 IS B0 AND WILL CHANGE TO C0 BY MARCH 2013. C0 FIXES PROCESS ISSUES.



LAYOUT NOTE:
PUT THERMAL VIAS AROUND U2200 IN CASE OF SHORTED CONDITION

CRITICAL
J2201
AA07A-S032-VA1
F-ST-SM-1

[illegible]

The schematic diagram shows the power supply section of the L2240. It includes a 240-ohm, 25W resistor (L2240) connected to a 27pF capacitor (C2260) and a 7.5k resistor (R2250). The output is connected to a 20.0k resistor (R2242) and a 32W resistor (R2242). The input is labeled EDP HPD EMI and the output is labeled EDP HPD.

NOTE:
PER GREG DE MERCEY, EDP_HPD PIN IS 2.5V TOLERANT
HOWEVER TO BE CONSERVATIVE, DIVIDER CKT IS NOT REMOVED

D

C

B

A

D

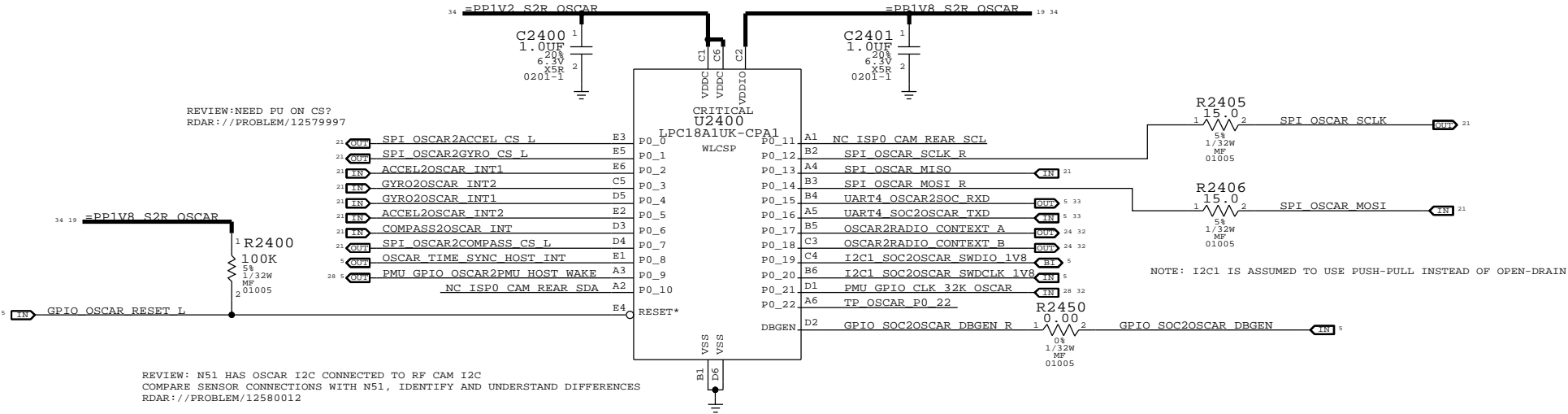
C

B

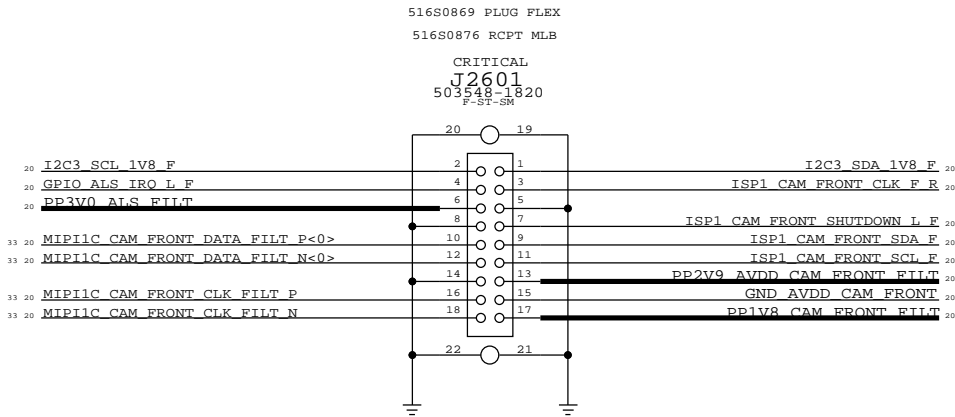
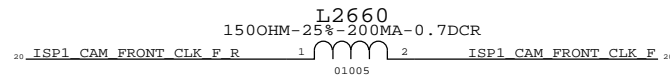
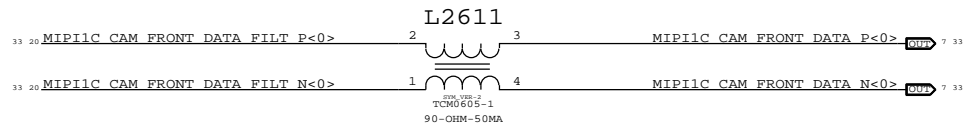
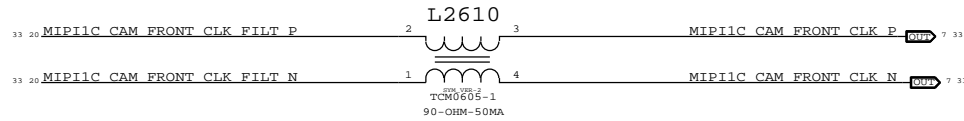
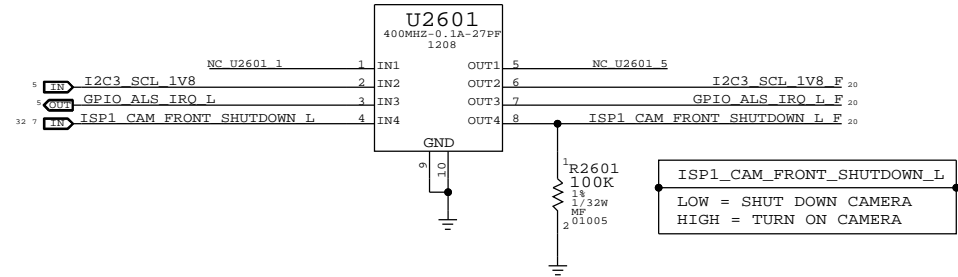
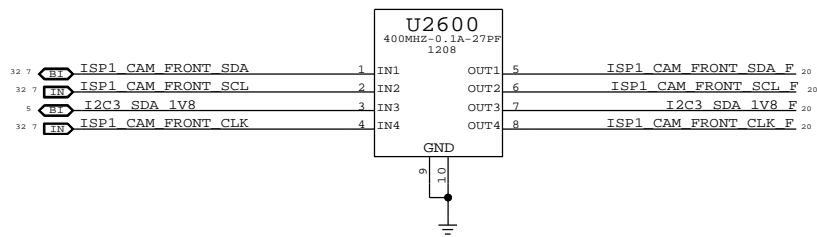
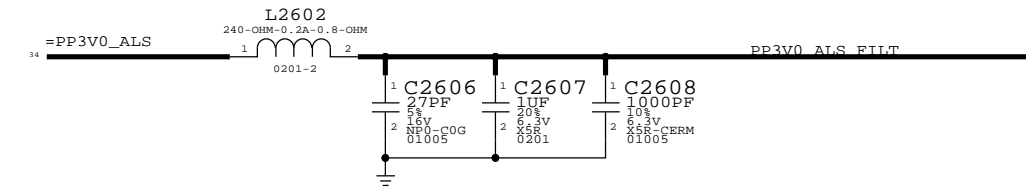
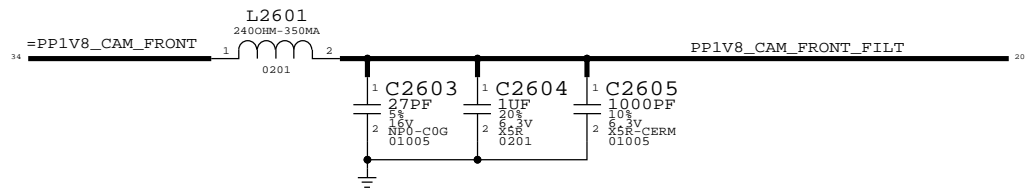
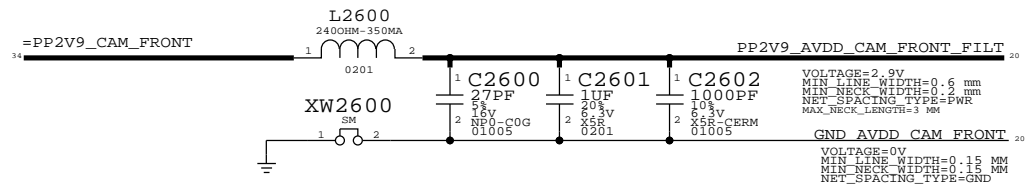
OSCAR

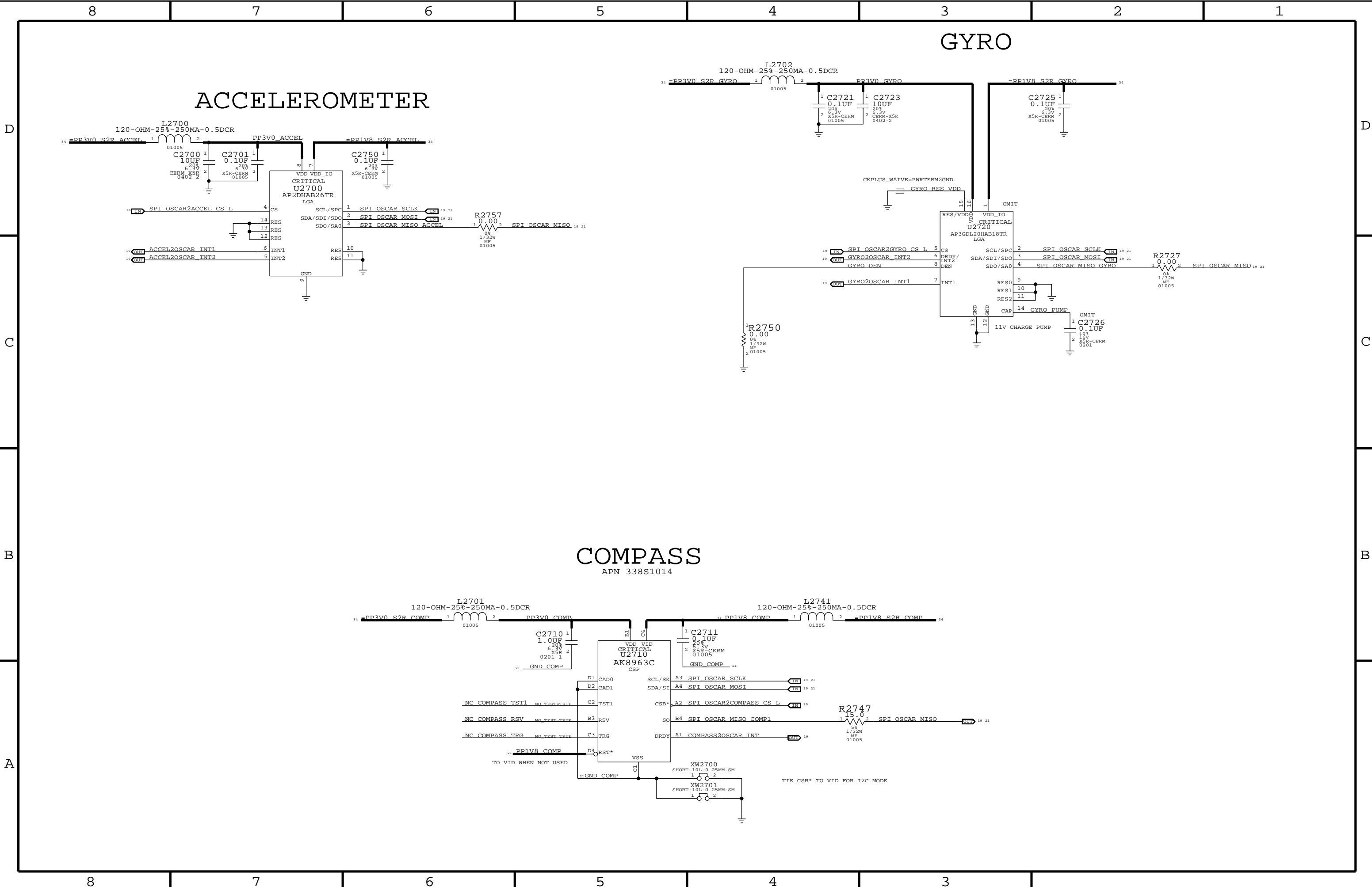
APN 337S4416

OSCAR VDDIO = 1.8V HIBERNATE (NEED TO WAKE HOST)
OSCAR CORE = 1.2V HIBERNATE (NEED TO RUN IN S2R)

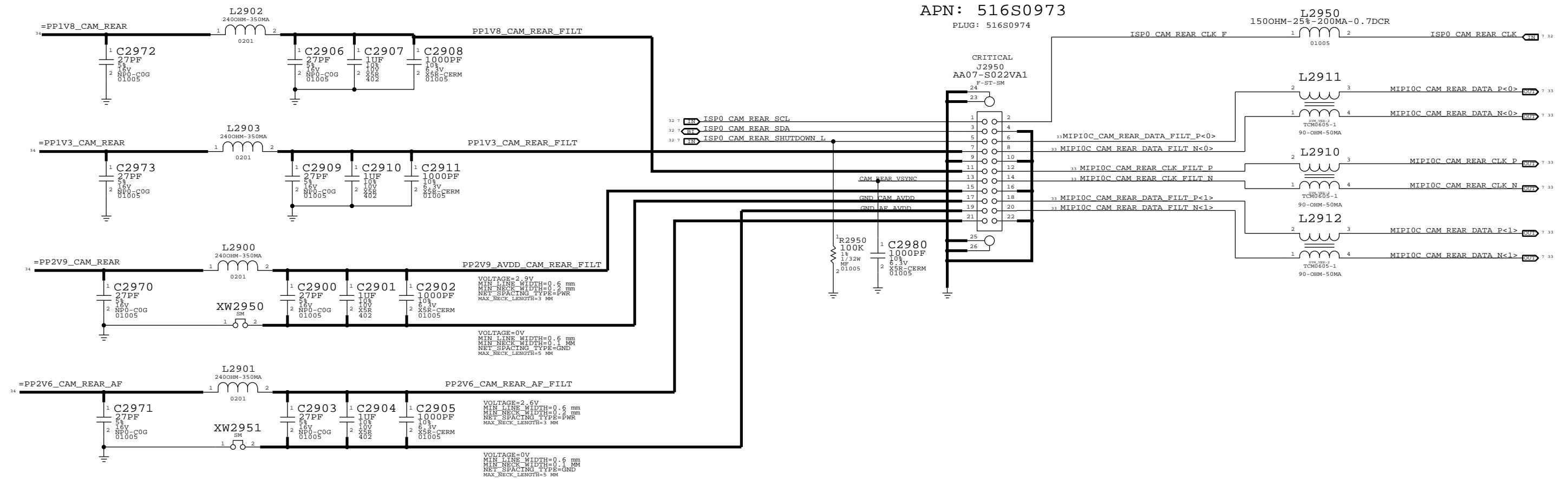


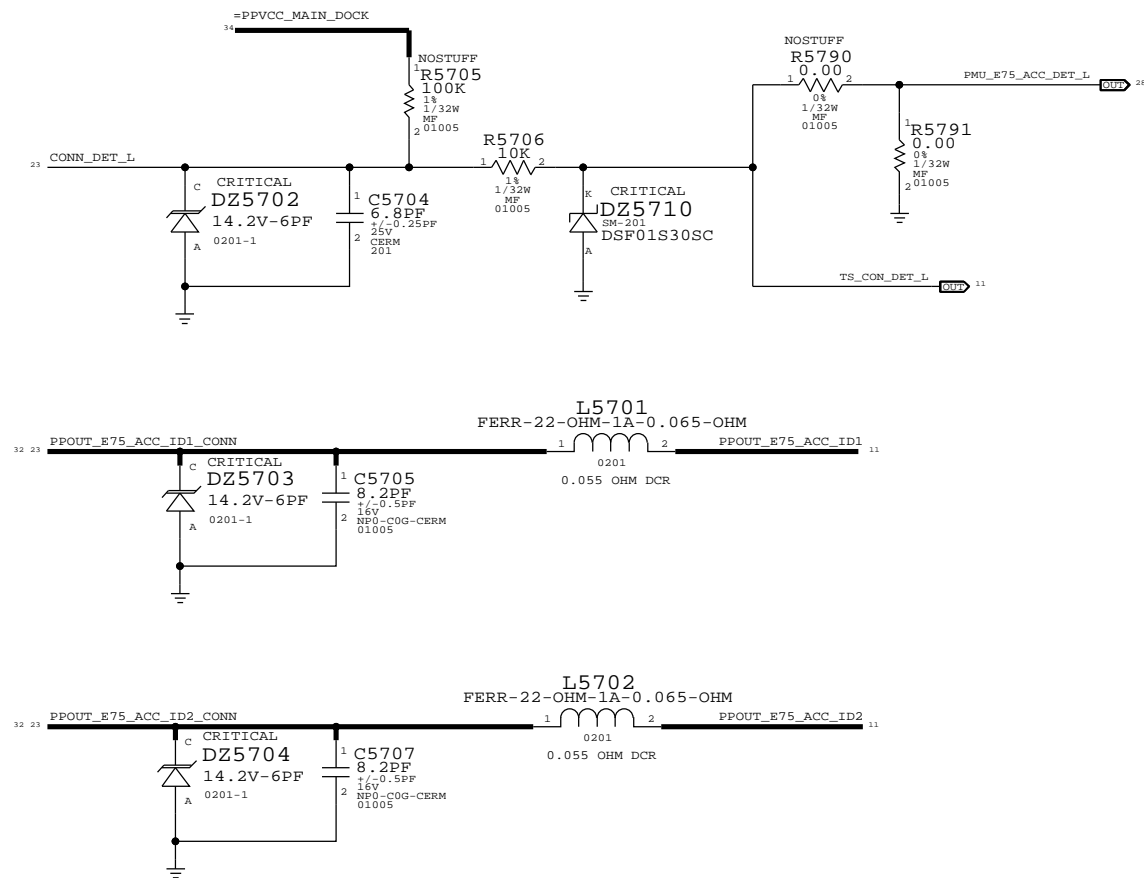
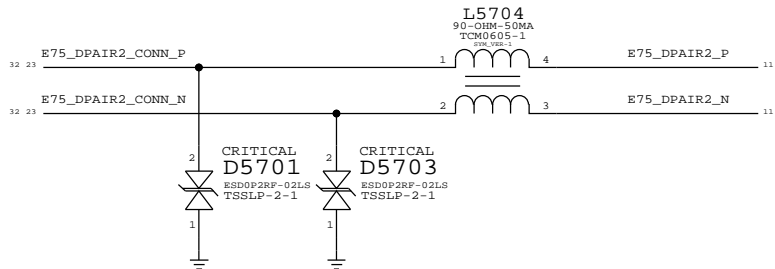
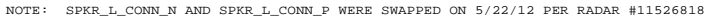
VGA FRONT CAMERA CONNECTOR

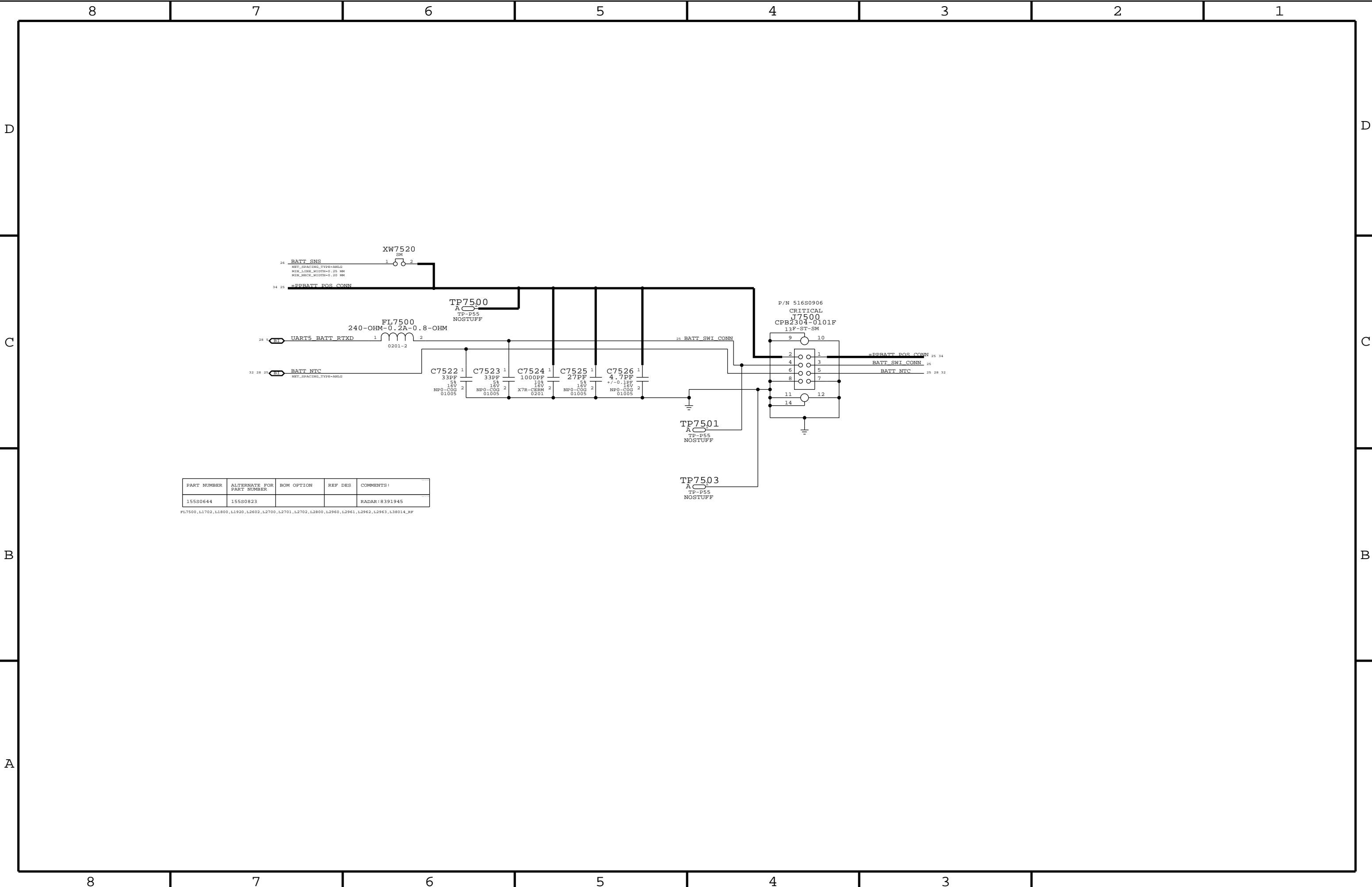


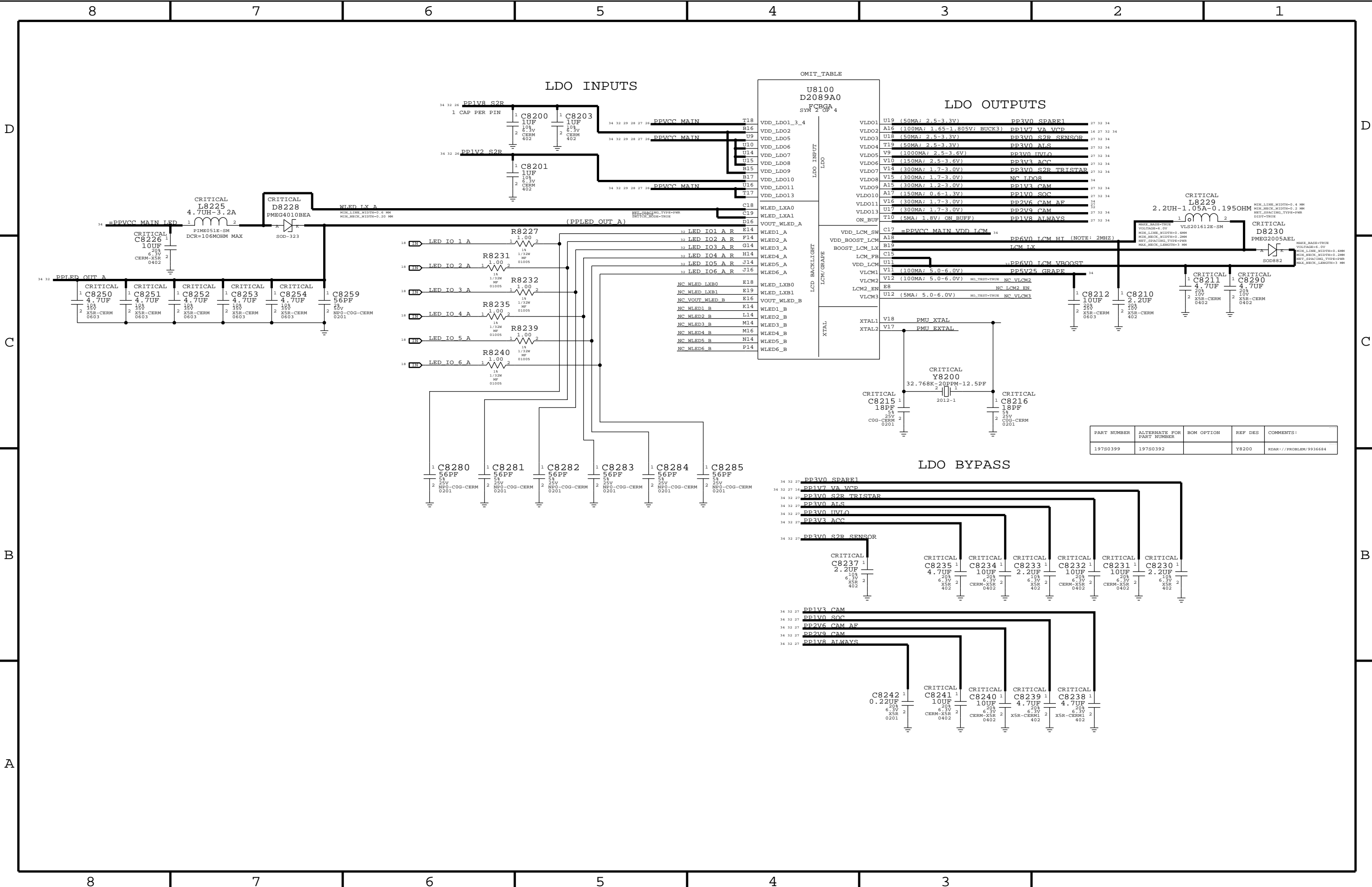


REAR CAMERA CONNECTOR









PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
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D

C

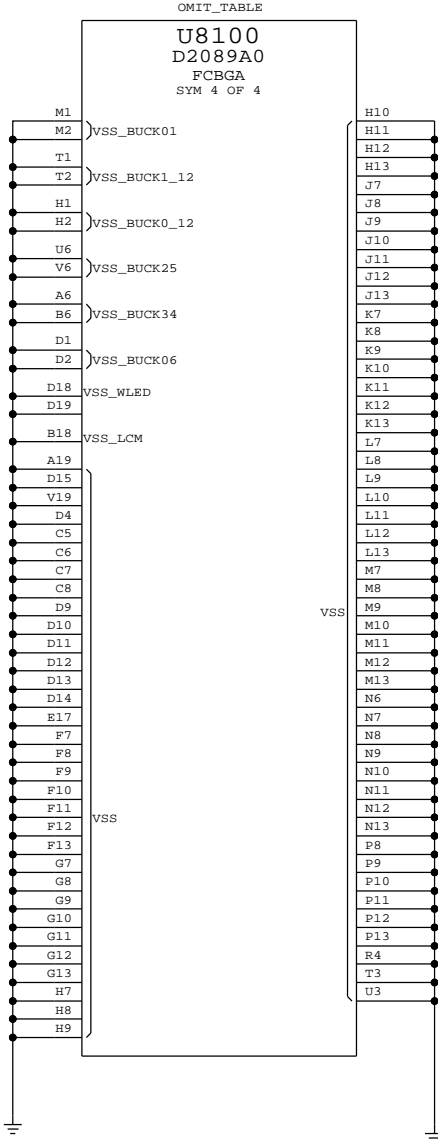
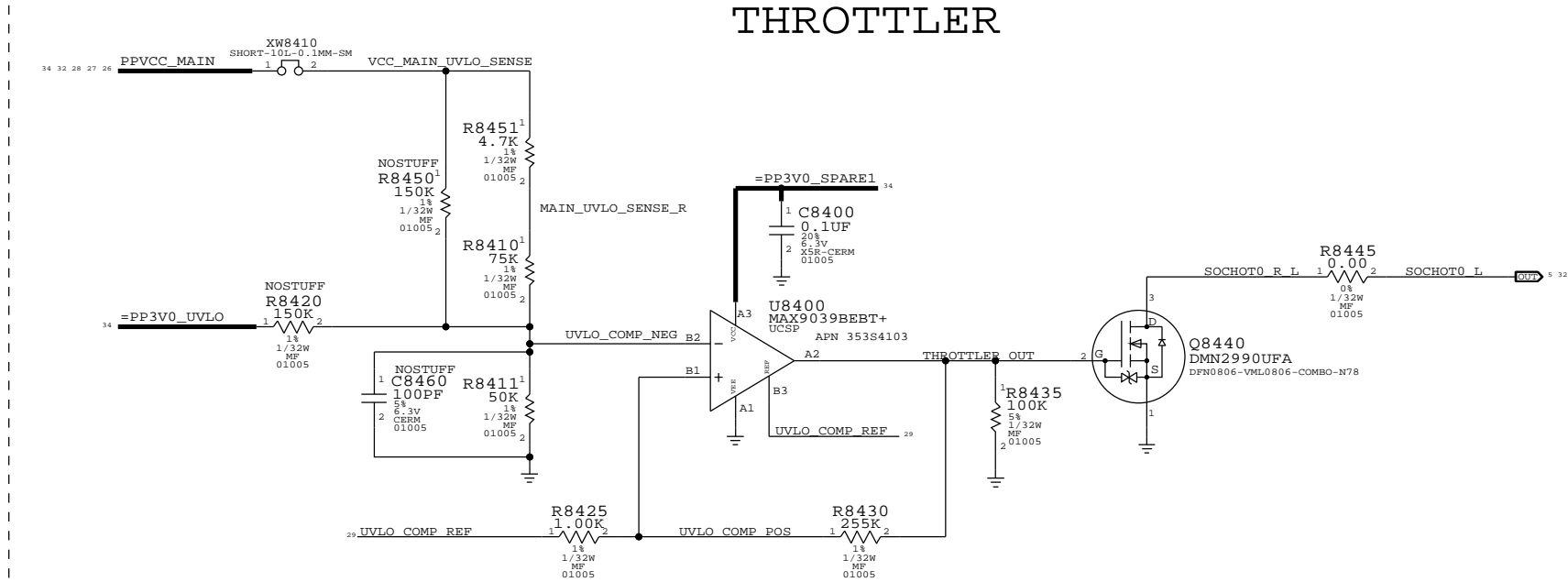
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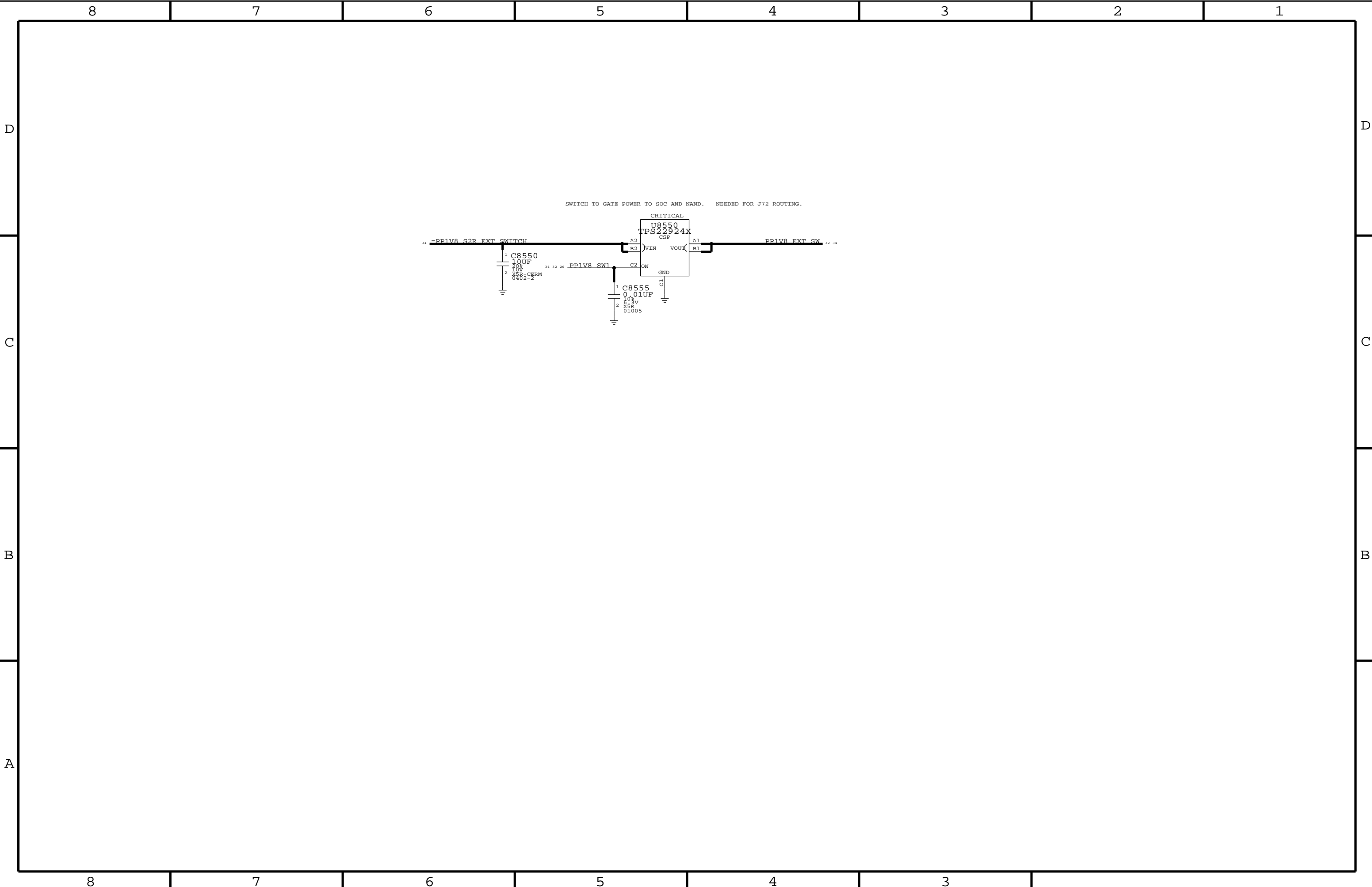
D

C

B



ADD A VIA PER PIN FOR ALL VSS_* AND VSSA_* PINS



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D

D

DEBUG

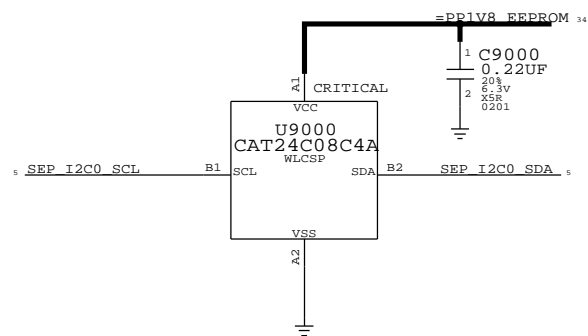


C

C

SEP EEPROM

UNPROGRAMMED P/N: 335S0894



B

B

A

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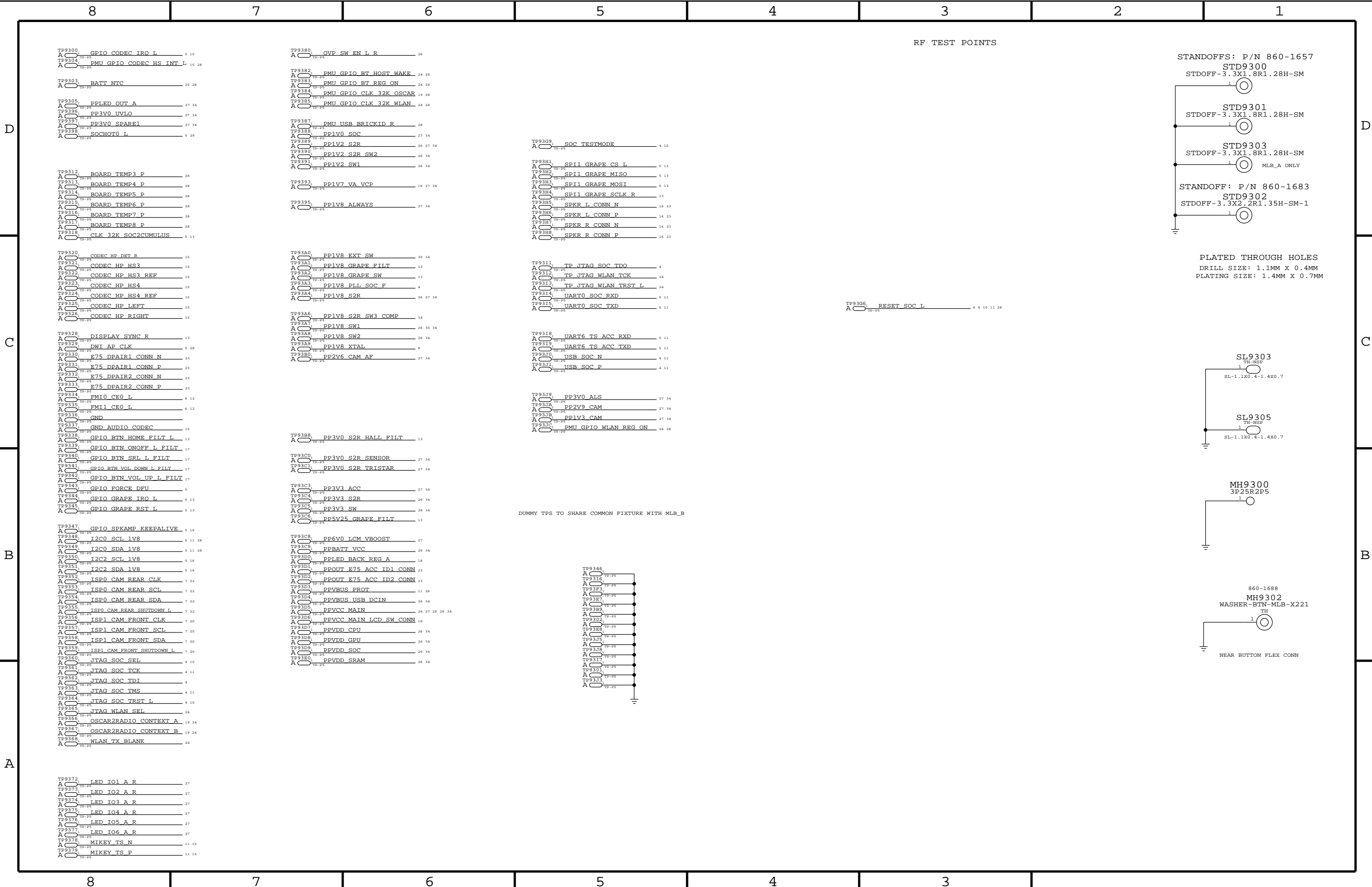
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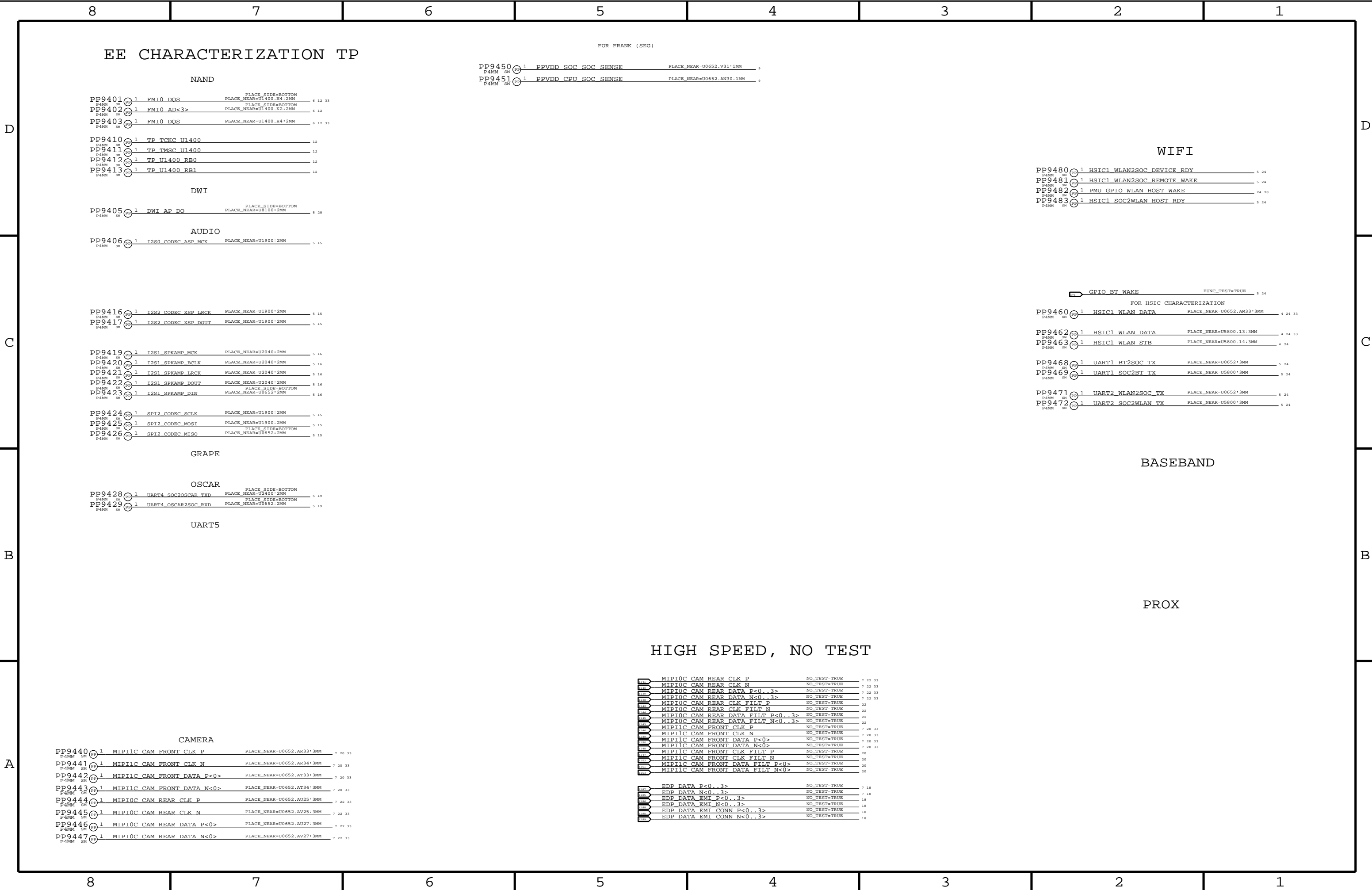
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8	7	6	5	4	3	2	1
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