

TABLE OF CONTENTS

Page Description

<CON_TC_SNO16>


REV	VARIANT	NVPN	ASSEMBLY
B1	BASE	600-10873-BASE-000	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU0000	600-10873-0000-100	P873, SKU 0, LP, GT218-300-Bx, 589/1402/687, 1024MB/64H, 128MB DDR3, DVI-DL+HDMI+VGA, DT
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Table of Contents

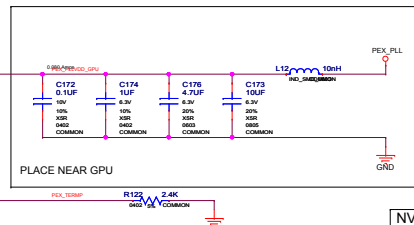
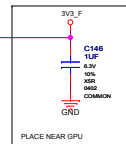
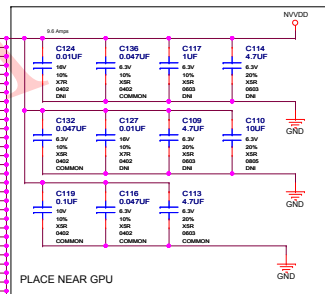
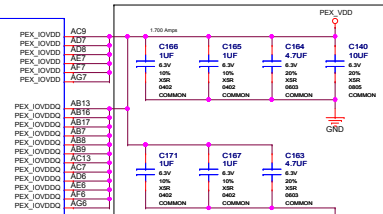
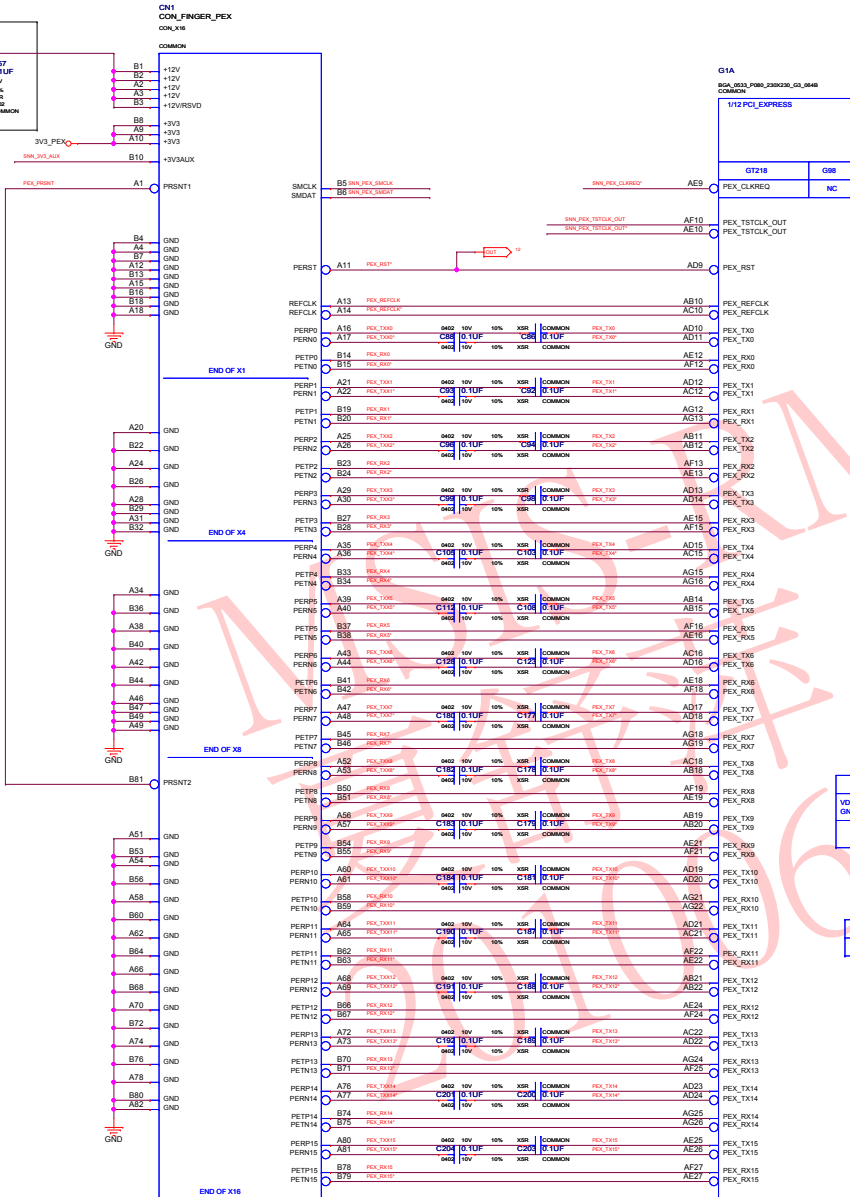
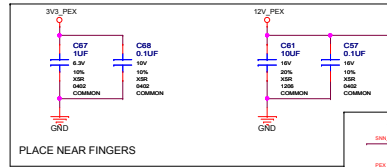
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NV_PN	600-10873-BASE-000		
PCB REV	P873-X01	PAGE	
BOM REV	A	DATE	26-FEB-2010

PCI Express Interface



Net Name	MIN_WIDTH	MAX_WIDTH
IN → PEK_PIGAT	120μ	
IN → PEK_TERRP	120μ	

Net Name	VOLTAGE	MAX_CURRENT
IN → PEK_PLLVDD_GPU	1.05V	0.125A 120μ

	Net Name	DIFF_PAIR	CRITICAL	IMPEDANCE
1	FXA_126	FXA_126	2	800Ω
2	FXA_126P	FXA_126	2	800Ω
3	FXA_131	FXA_131	2	800Ω
4	FXA_131P	FXA_131	2	800Ω
5	FXA_136	FXA_136	2	800Ω
6	FXA_136P	FXA_136	2	800Ω
7	FXA_150	FXA_150	2	800Ω
8	FXA_150P	FXA_150	2	800Ω
9	FXA_154	FXA_154	2	800Ω
10	FXA_154P	FXA_154	2	800Ω
11	FXA_170	FXA_170	2	800Ω
12	FXA_170P	FXA_170	2	800Ω
13	FXA_176	FXA_176	2	800Ω
14	FXA_176P	FXA_176	2	800Ω
15	FXA_192	FXA_192	2	800Ω
16	FXA_192P	FXA_192	2	800Ω
17	FXA_198	FXA_198	2	800Ω
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81	FXA_402	FXA_402	2	800Ω
82	FXA_402P	FXA_402	2	8

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PCI Express Interface

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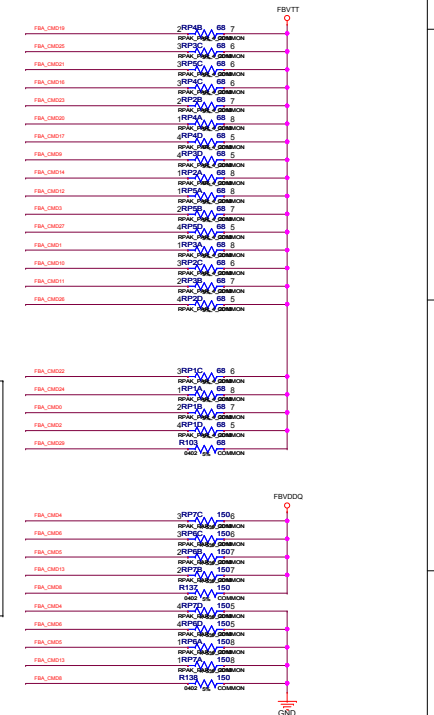
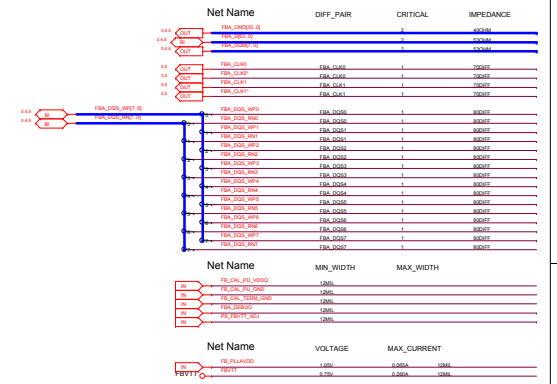
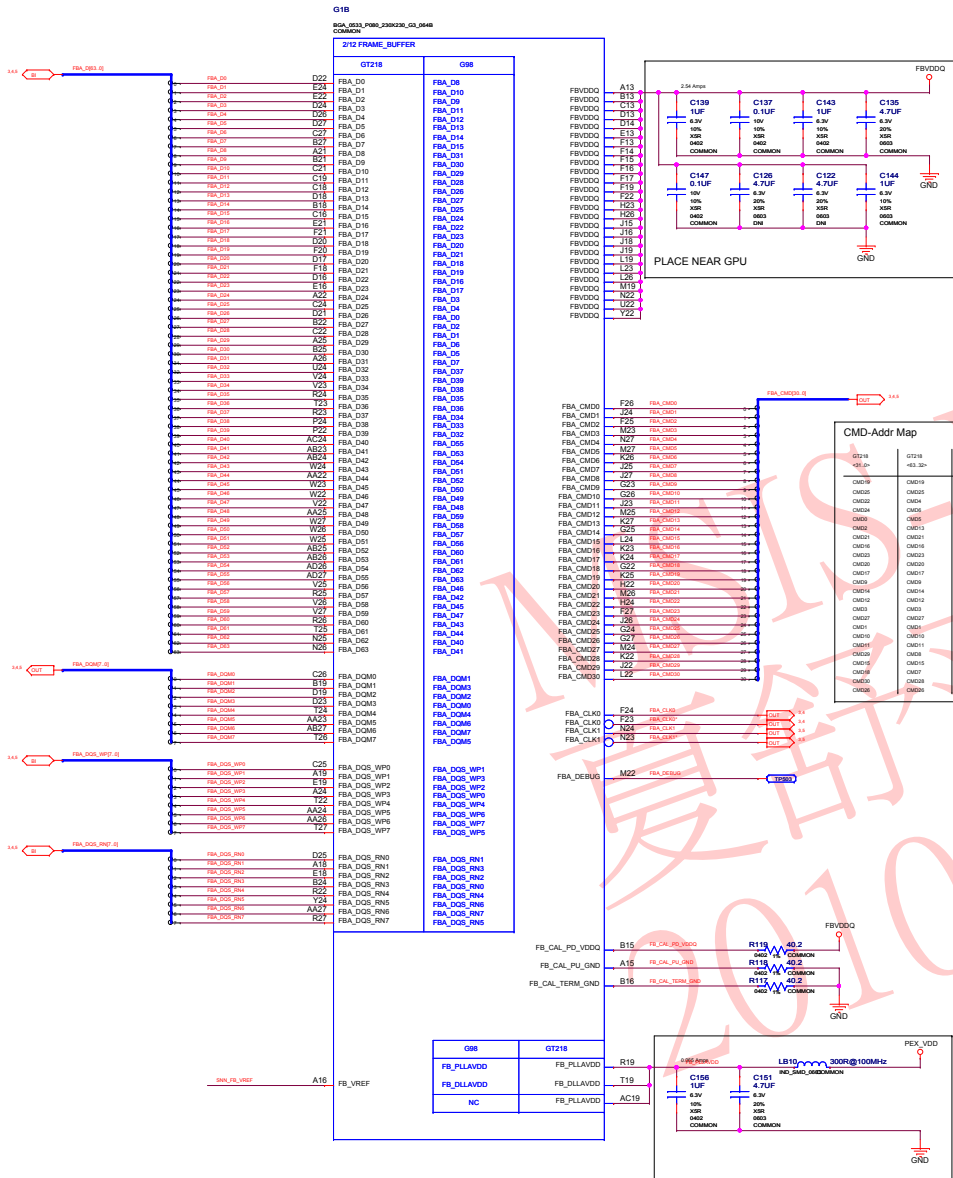
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PCB REV	P873-A01	PAGE	
BOM REV	A	DATE	26-FEB-2010



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Frame Buffer Interface



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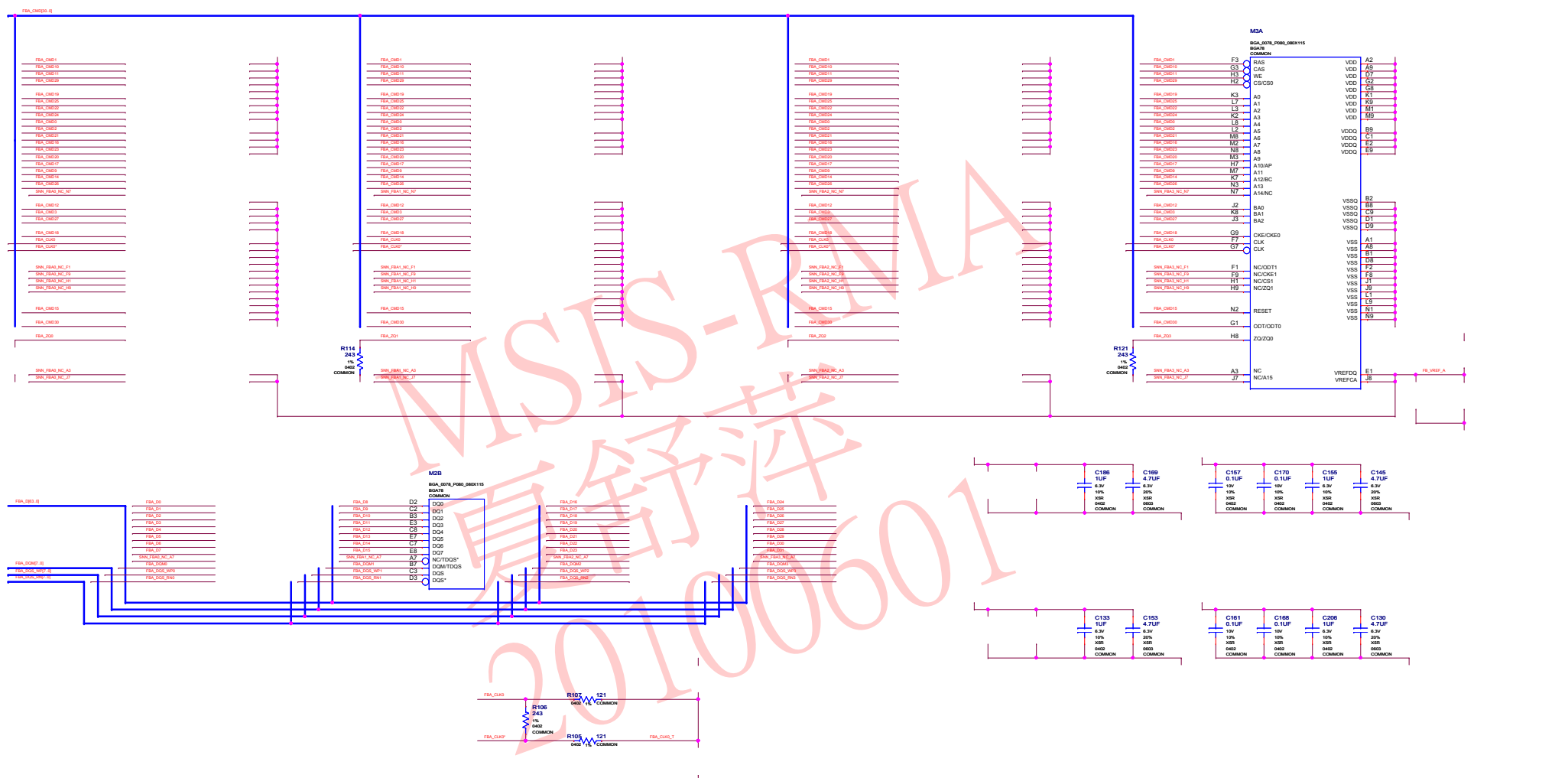
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NV_PN	600-10873-BASE-000
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PCB REV	P873-A01
BCM REV	A

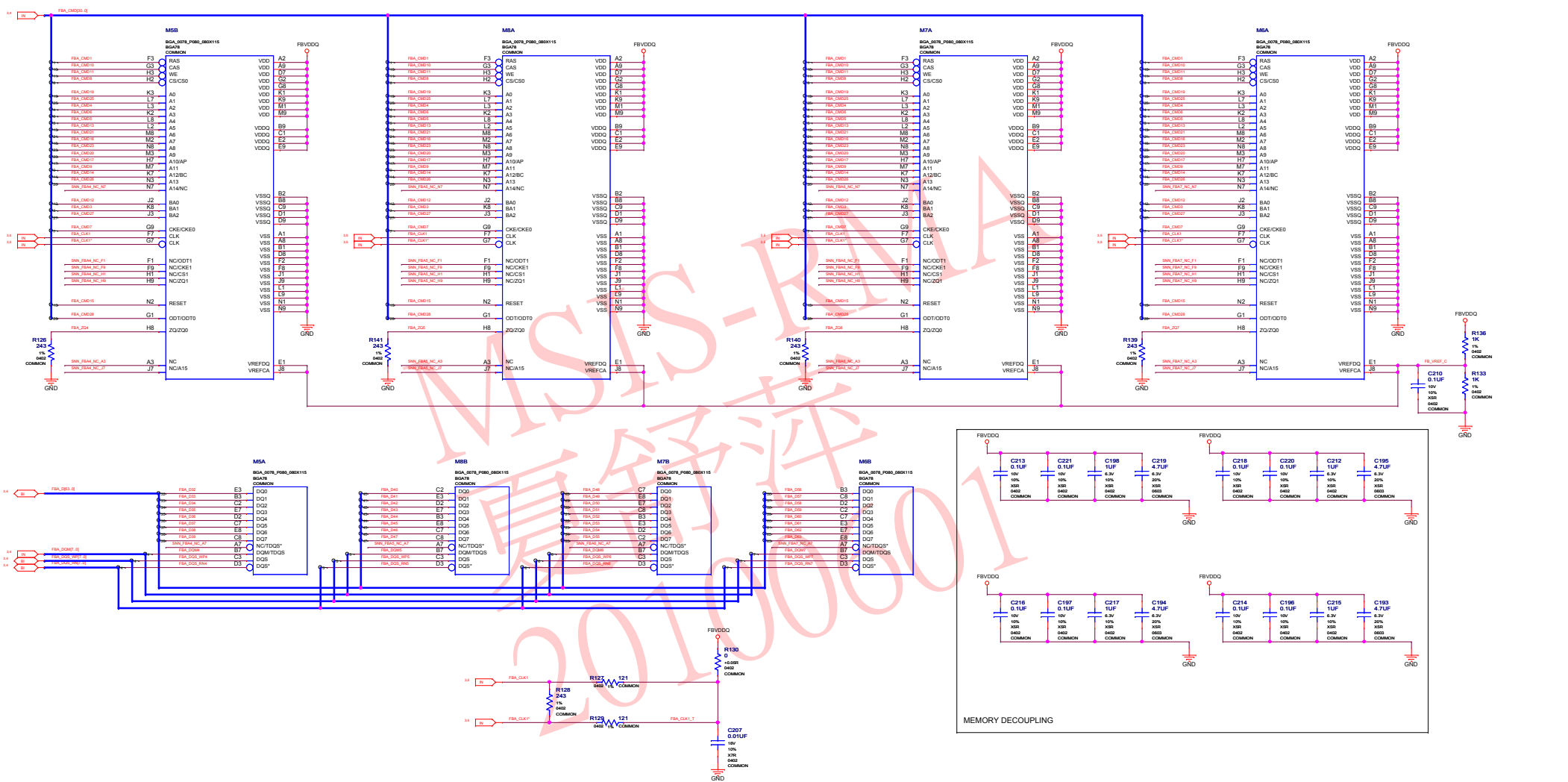
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PAGE DETAIL	Frame Buffer Interface

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Memory 1st Bank 32..63

Net Name	MIN_WIDTH	MAX_WIDTH
FL_VREF_C	70M	
FL_VREF_C	70M	
FL_VREF_C	70M	
FL_VREF_C	70M	
FL_VREF_C	70M	



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NV_PN

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PCB REV

P673-001

WORK REV

A

PAGE

1

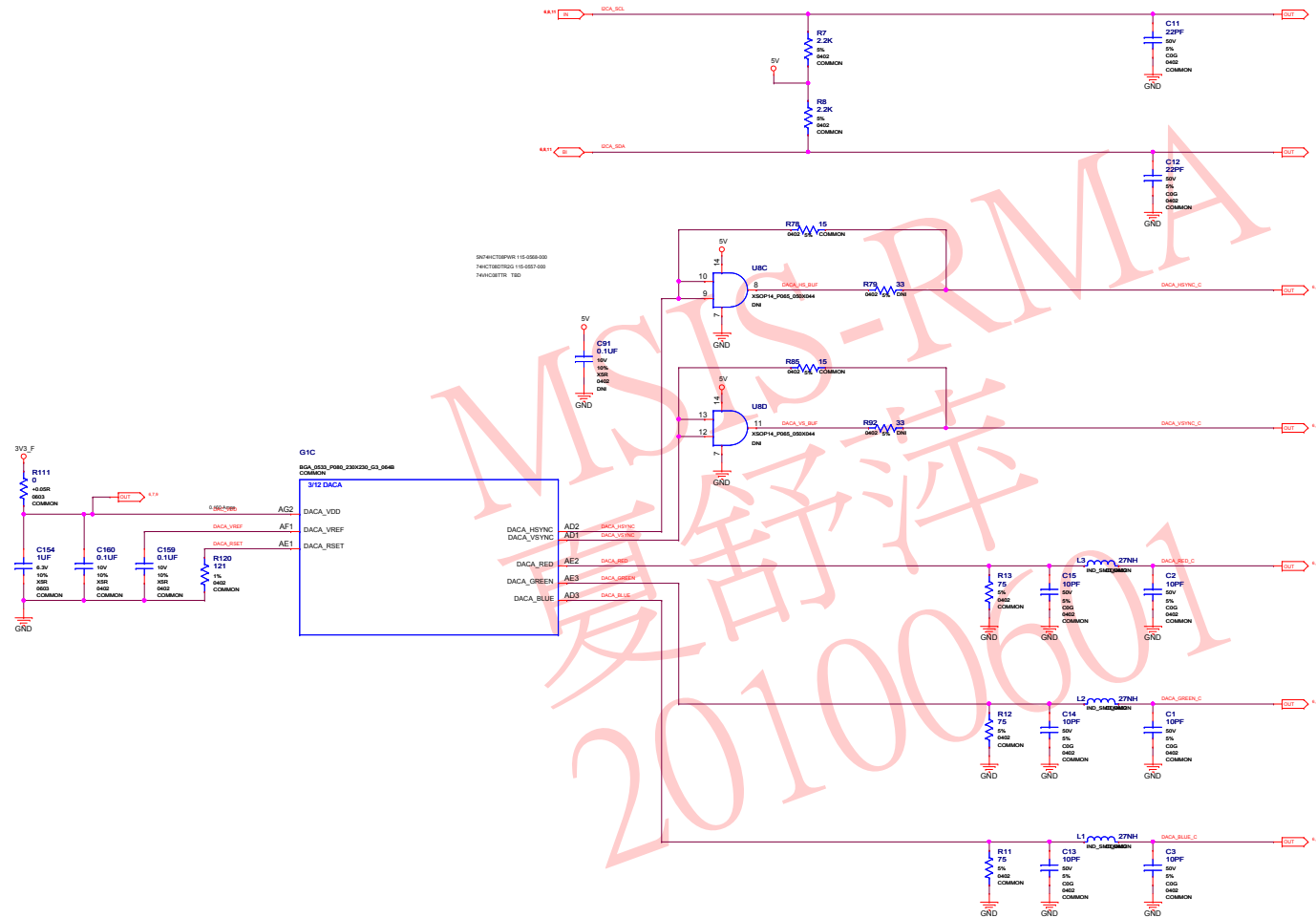
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ASSEMBLY: BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO_1UUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE: DETAIL Memory 1st Bank 32..63

DAC A VGA



Net Name		CRITICAL	IMPEDANCE
0.6	DATA_000	1	27.0000
	DATA_000ES	1	27.0000
	DATA_000F	1	27.0000
	DATA_000F-C	1	27.0000
	DATA_000ES-C	1	27.0000
0.6	DATA_000V	2	00.0000
	DATA_000V-ES	2	00.0000
	DATA_000F-REF	2	00.0000
	DATA_000F-REF	2	00.0000
	DATA_000V-ES-C	2	00.0000
0.6	DATA_000V-ES-C	2	00.0000
	DATA_000V-ES-C	2	00.0000
	DATA_000V-ES-C	2	00.0000
	DATA_000V-ES-C	2	00.0000
	DATA_000V-ES-C	2	00.0000

Net Name		MIN_WIDTH	MAX_WIDTH
0.1, 1	DATA_000	UNBL	
	DATA_000F	UNBL	
	DATA_000F	UNBL	
	DATA_000F	UNBL	

Net Name		VOLTAGE	MAX_CURRENT
0.7, 8	DATA_000	3.3V	3.000A 10000

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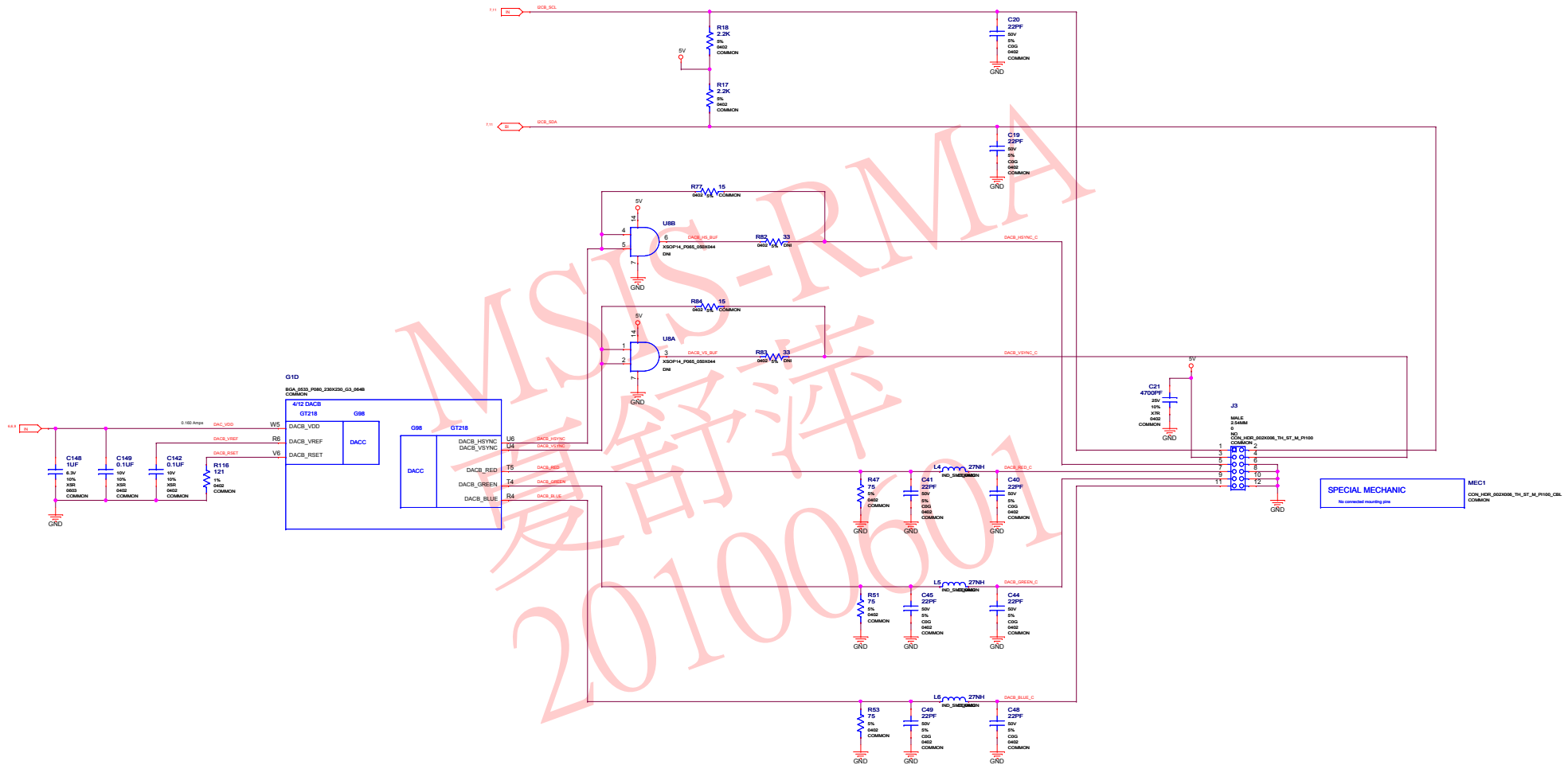
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PCB REV	P873-A01	PAGE	
BOM REV	A	DATE	26-FEB-2010

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DAC B VGA Header



Net Name		CRITICAL	IMPEDANCE
(H)	DACR_RED	1	17.50ohm
(H)	DACR_GREEN_A	1	17.50ohm
(H)	DACR_BLUE	1	17.50ohm
(H)	DACR_GREEN_C	1	17.50ohm
(H)	DACR_GREEN_D	1	17.50ohm
(H)	DACR_BLUE_C	1	17.50ohm
(H)	DACR_VSYNC	2	50.0ohm
(H)	DACR_VSYNC	2	50.0ohm
(H)	DACR_VSYNC_D	2	50.0ohm
(H)	DACR_VHS_BUF	2	50.0ohm
(H)	DACR_VHS_BUF	2	50.0ohm

Net Name		MIN_WIDTH	MAX_WIDTH
7.11	DQB_SQ_L		
7.11	DQB_SQ_A		
	DQB_VREF	12MS	
	DQB_RESET	12MS	

SPECIAL MECHANIC
No connected mounting pins

MEC1

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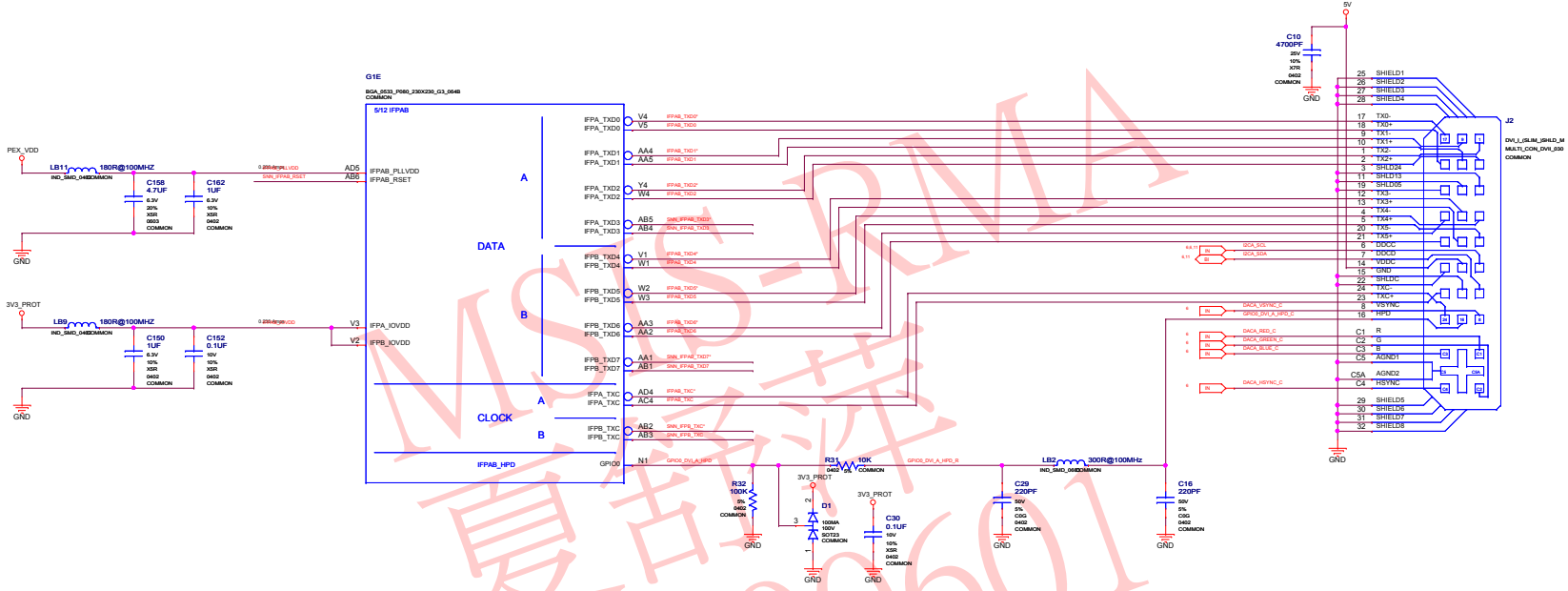
PCB REV	P873-AD1	PAGE	
BOM REV	A	DATE	26-FEB-2010

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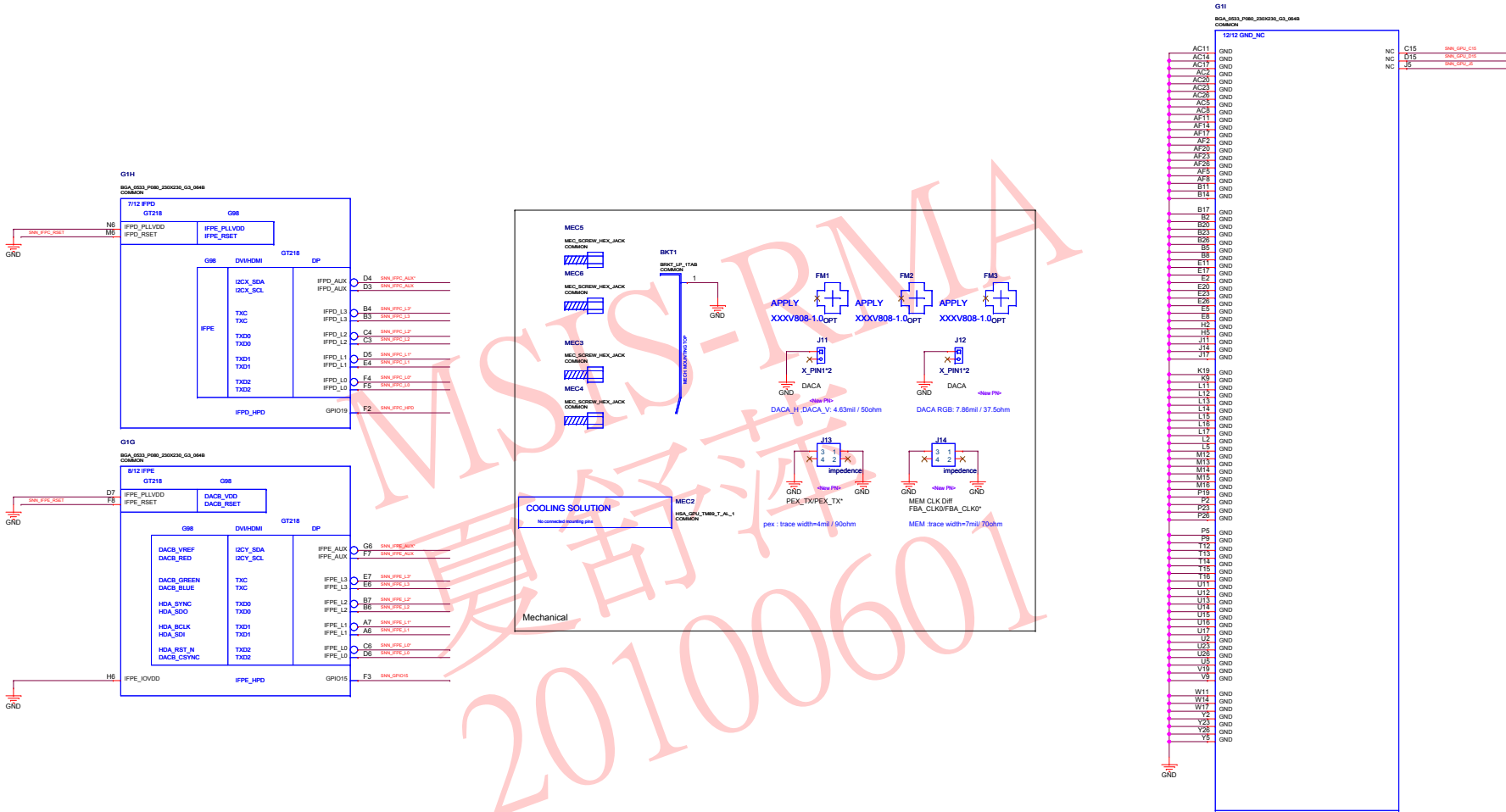
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DAC B VGA Header

TMDS Interface

[illegible]

Mechanical

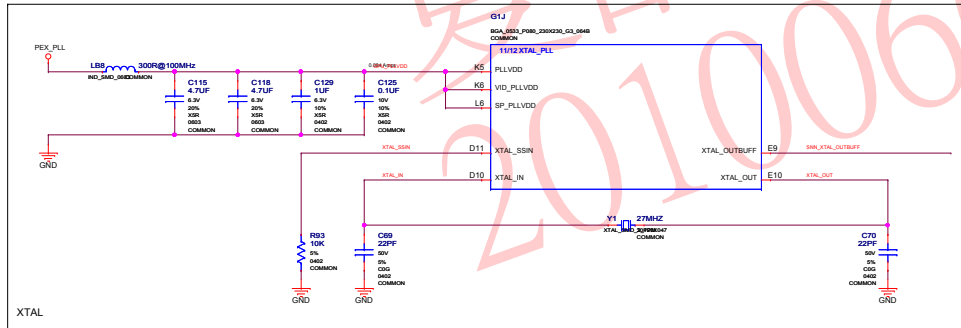
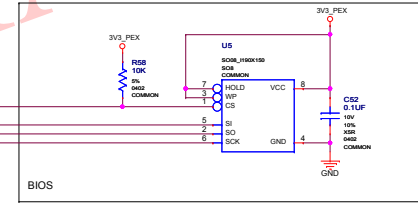
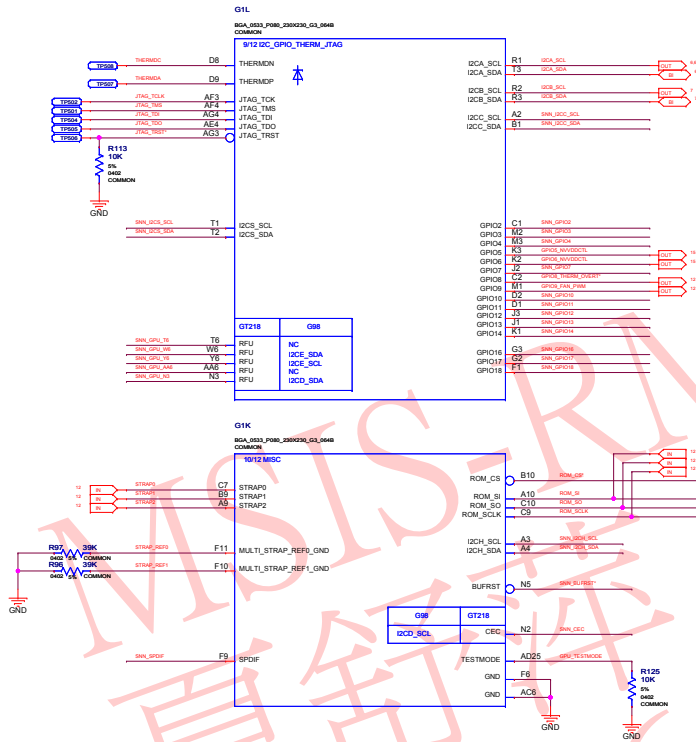


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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO_BUFFER ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Mechanical

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NV_PN	600-10873-BASE-000
PCB REV	P073-001
BOB REV	A
PAGE	
DATE	26 FEB 2010

XTAL, ROM, Misc

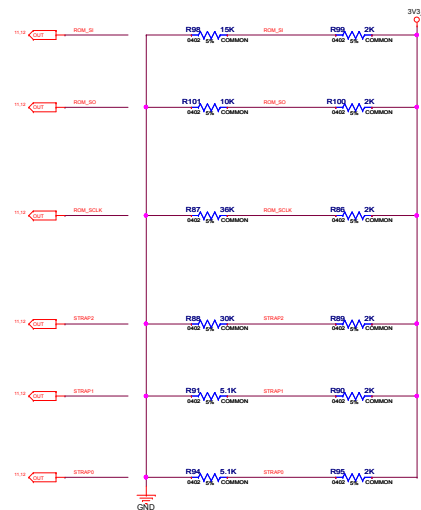
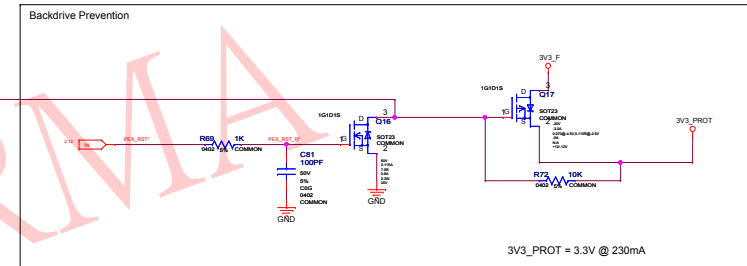
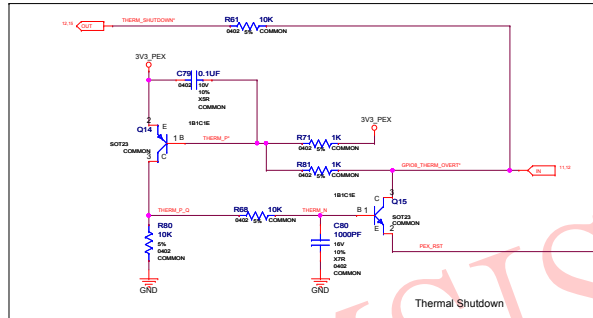


Net Name		CRITICAL		IMPEDANCE	
N1	XTAL_OUT	XTAL	1	800FT	
N2	XTAL_IN	XTAL	1	800FT	

Net Name		VOLTAGE		MAX_CURRENT	
N1	OPPL_PU_LOAD	1.00V	0.000A	0.000A	0.000A

Net Name		MIN_WIDTH		MAX_WIDTH	
N1	THERMOC		0.0001		
N2	THERMCA		0.0001		
N3	JFAC_TOLX				
N4	JFAC_TOLR				
N5	JFAC_TOLY				
N6	JFAC_TOLZ				
N7	JFAC_TOL1				
N8	JFAC_TOL2				
N9	STRAP_BEZ0				
N10	STRAP_BEZ1				
N11	XTAL_INN				
N12	XTAL_OUTN				
N13	XTAL_OUTP				
N14	XTAL_INP				
N15	XTAL_OUT				
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Thermal Protection, Protected 3V3, Straps, Fan PWM

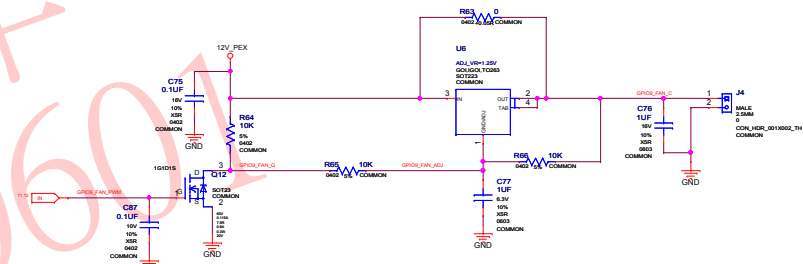


GT218 Straps

Bit Signal	Value
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				Multilevel Straps		
03	RAMEQ[EQ]	0000	Enable	5K to GND	0000	
		0001	Disable	10K to GND	0001	
03	RAMEQ[EQ]	0000	None	10K to GND	0000	
		0001	Setting	20K to GND	0001	
03	RAMEQ[EQ]	0000	None	20K to GND	0000	
		0001	Mean	30K to GND	0001	
		0010	27 (Default)	30K to GND	0000	
03	REG_A17	0	Write (Default)	30K to GND	0001	
		1	Read	30K to GND	0000	
03	FREQ	0	33MHz (Default)	30K to GND	0001	
		1	66MHz	30K to GND	0000	
03	SMB_A17_ADDR	0	None	5K to VCC	1000	
		1	0x0C	10K to VCC	1001	
03	VGA_DEVICE	0	On-chip codec 302	20K to VCC	1011	
		1	On-chip codec 300	30K to VCC	1010	
03	PCU_DEVICE_EXT	0	01210-300-A1	30K to VCC	1011	
		1	01210-300-A1	40K to VCC	1111	
03	SUB_VENDOR	0	No BIOS			
		1	BIOS			
03	SLOT_A18_CFG	0	Disable			
		1	Enable			
03	FEK_PXL_A18_TERRAIN	0	Disable			
		1	Enable			
03	PCU_DEVICE[0]	0000	01210-300-A1			
		0001	01210-300-A1			
03	PCU_DEVICE[1]					
03	PCU_DEVICE[2]					
03	PCU_DEVICE[3]					
03	PCU_DEVICE[4]					
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Multilevel Straps	
5K to GND	0000
10K to GND	0001
15K to GND	0010
20K to GND	0011
25K to GND	0100
30K to GND	0101
35K to GND	0110
40K to GND	0111
5K to VCC	1000
10K to VCC	1001
15K to VCC	1010
20K to VCC	1011
25K to VCC	1100
30K to VCC	1101
35K to VCC	1110
40K to VCC	1111



Net Name

	MIN_WIDTH	MAX_WIDTH
PXL_RST	2.31	
PXL_RST_3F	11.5	
PXL_RST_3F	12.0	
GPCLK_THERM_DOMAIN?	11.5	
THERM_3F_3F	11.5	
THERM_3F	12.0	
THERM_3F_THERM_DOMAIN?	11.5	
PXL_3F	11.5	
PXL_3F_3F	11.5	
PXL_3F_3F	11.5	
PXL_3F_3F	11.5	
PXL_3F_3F	11.5	
PXL_3F_3F	11.5	
GPCLK_THERM_DOMAIN?	11.5	
GPCLK_THERM_DOMAIN?	11.5	
GPCLK_THERM_DOMAIN?	11.5	
GPCLK_THERM_DOMAIN?	11.5	
GPCLK_THERM_DOMAIN?	11.5	
GPCLK_THERM_DOMAIN?	11.5	

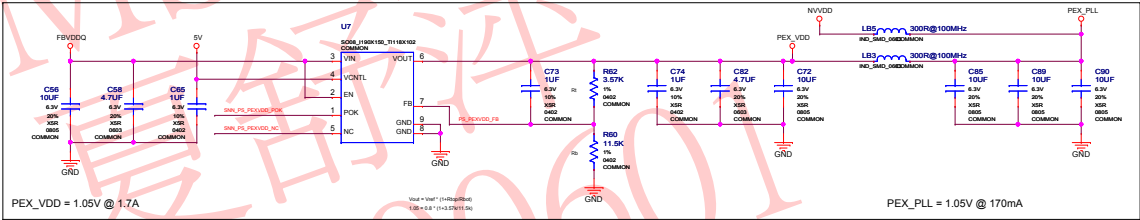
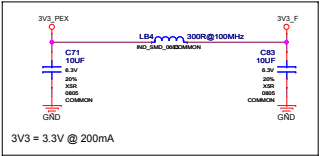
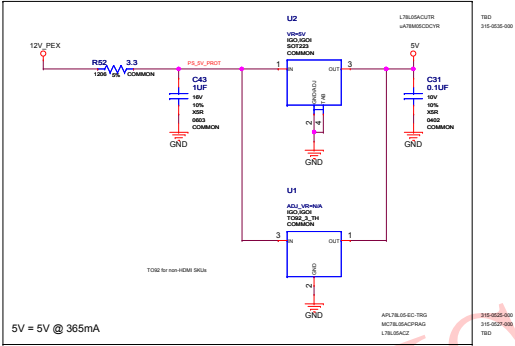
Net Name

	VOLTAGE	MAX_CURRENT
3V3_PROT	0.3V	0.25A
		0.05A

Power Supply I: PEX_VDD, 5V, 3V3

Net Name			MIN_WIDTH	MAX_WIDTH
13	PEX_PEXPLL_A01	128BL		
13	PEX_P2_P00T	128BL		
13	PEX_P2VDDC_00	128BL		
13	PEX_P2VDDC_F0	128BL		

Net Name			VOLTAGE	MAX_CURRENT
5V	5V	5V	5.00V	0.365A
PEX_PLL	PEX_PLL	PEX_PLL	1.05V	0.170A
PEX_VDD	PEX_VDD	PEX_VDD	1.05V	1.70A
3V3_F	3V3_F	3V3_F	3.3V	0.200A



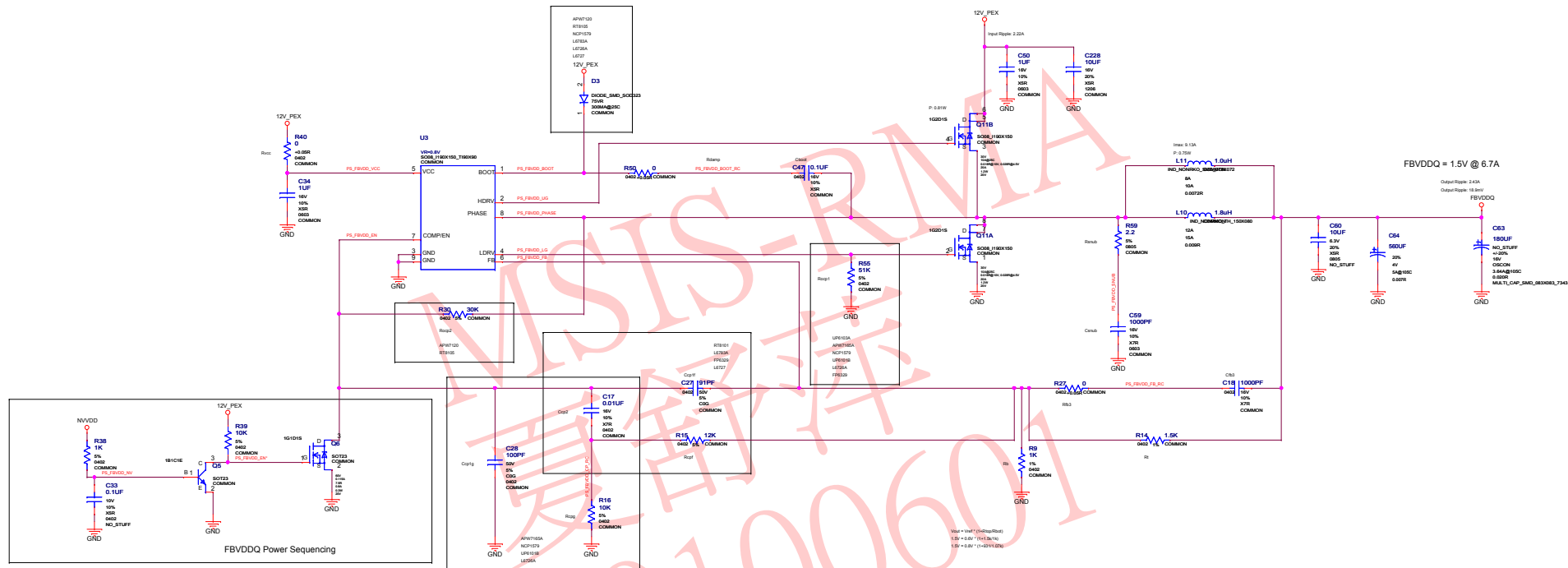
NVIDIA CORPORATION			
2701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95050, USA			
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PCB REV	P073-001	PAGE	
BOB REV	A	DATE	26-FEB-2010

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Power Supply II: FBVDD/Q

[illegible]

Net Name	VOLTAGE	MAX_CURRENT
FBVDDQ	1.5V	3A



PG	Freq	Vol
LP6105A	300kHz	0.8
APW120	300kHz	0.8
APW165A	300kHz	0.8
RT8105	300kHz	0.8
NCP1579	275kHz	0.8
RT8101	300kHz	0.8
LP6101B	300kHz	0.8
L6763A	300kHz	0.8
L6726A	270kHz	0.8
FP6329	300kHz	0.6
L6727	300kHz	0.8

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SANTA CLARA, CA 95050, USA

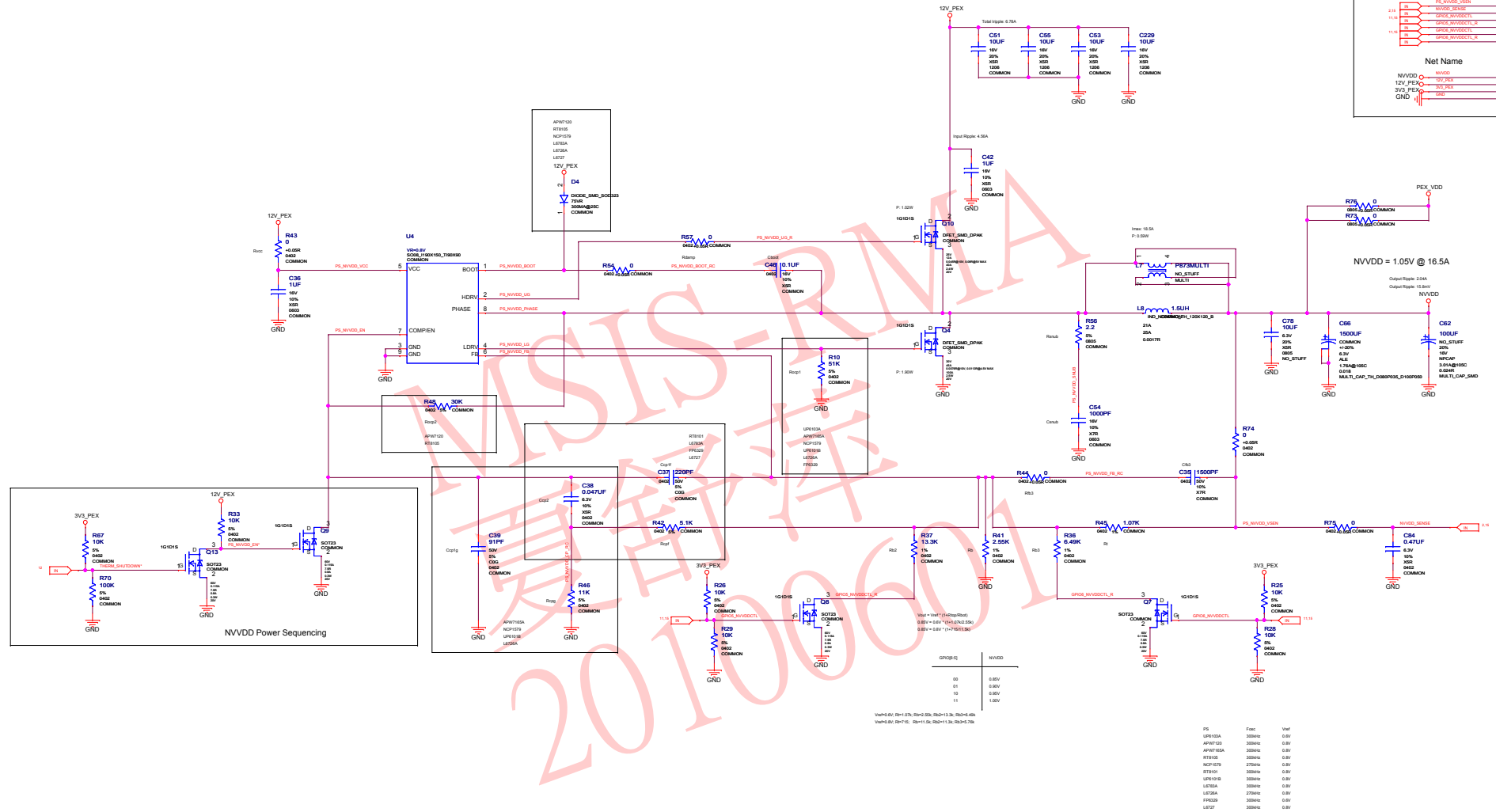



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PCB REV	P873-A01	PAGE	
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