

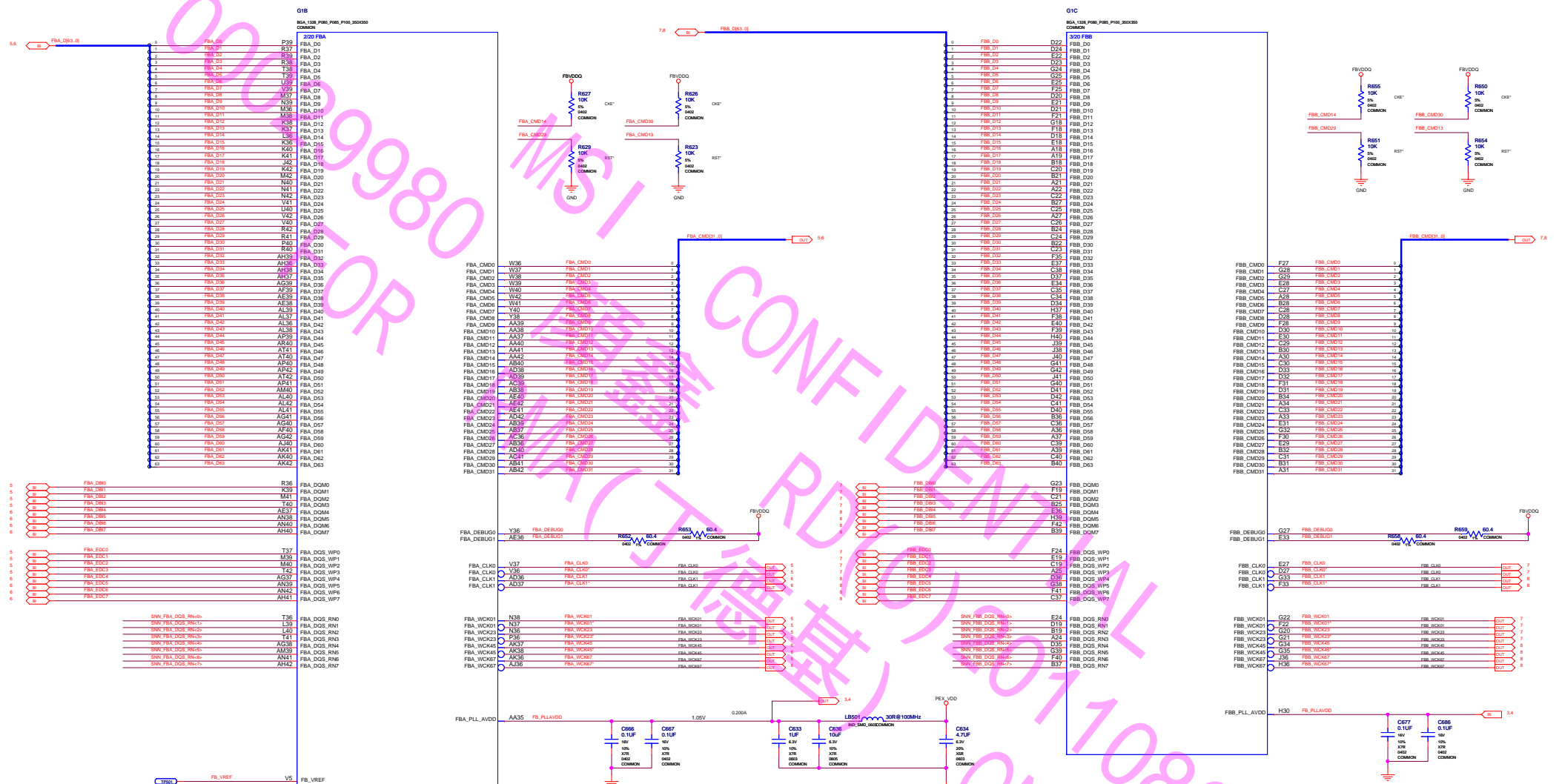


A	B	C	D
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MEMORY: GPU FB Partition A and B



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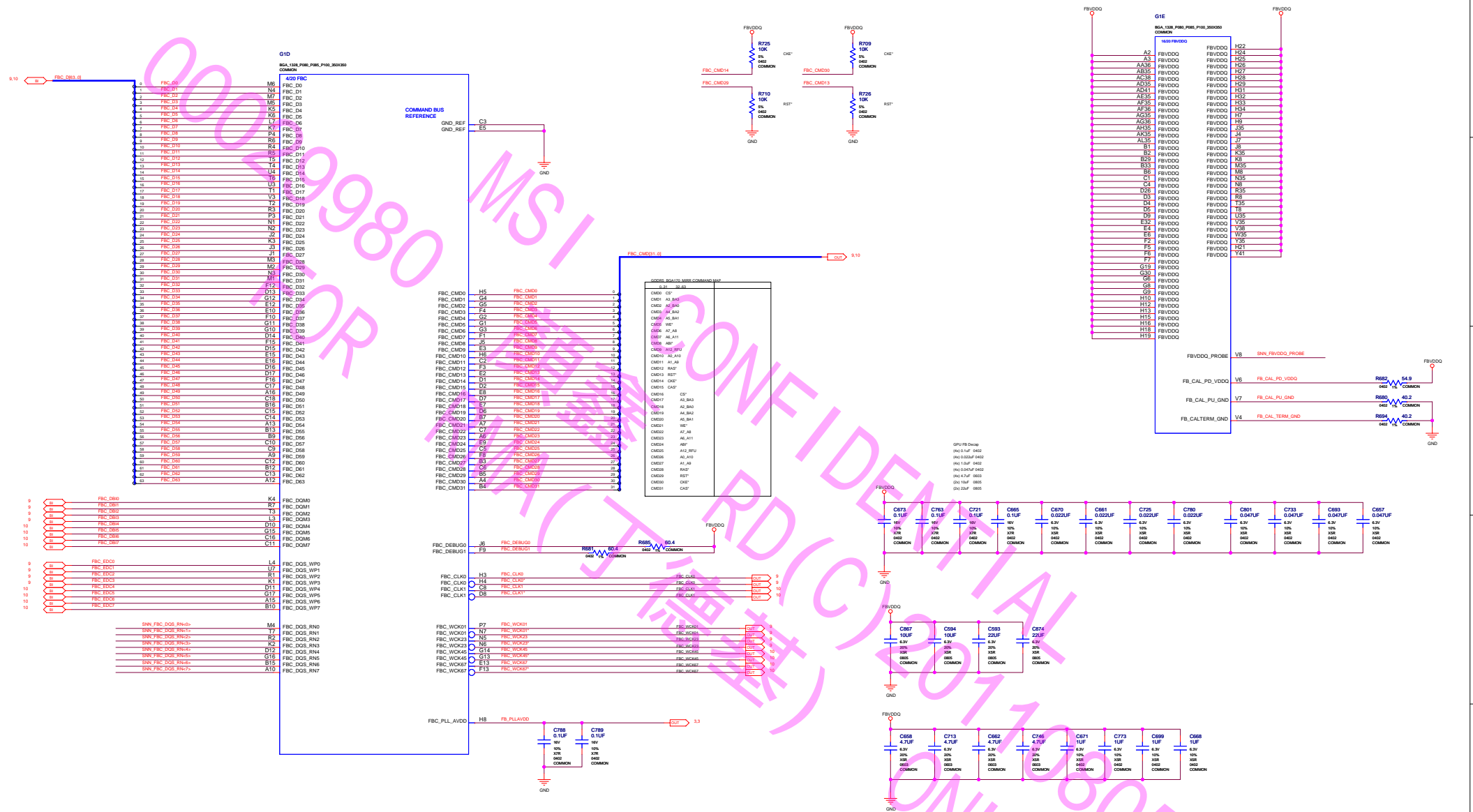
NV_PN	600-11050-BASE-QS1
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BOM REV	A	DATE	15-DEC-2010

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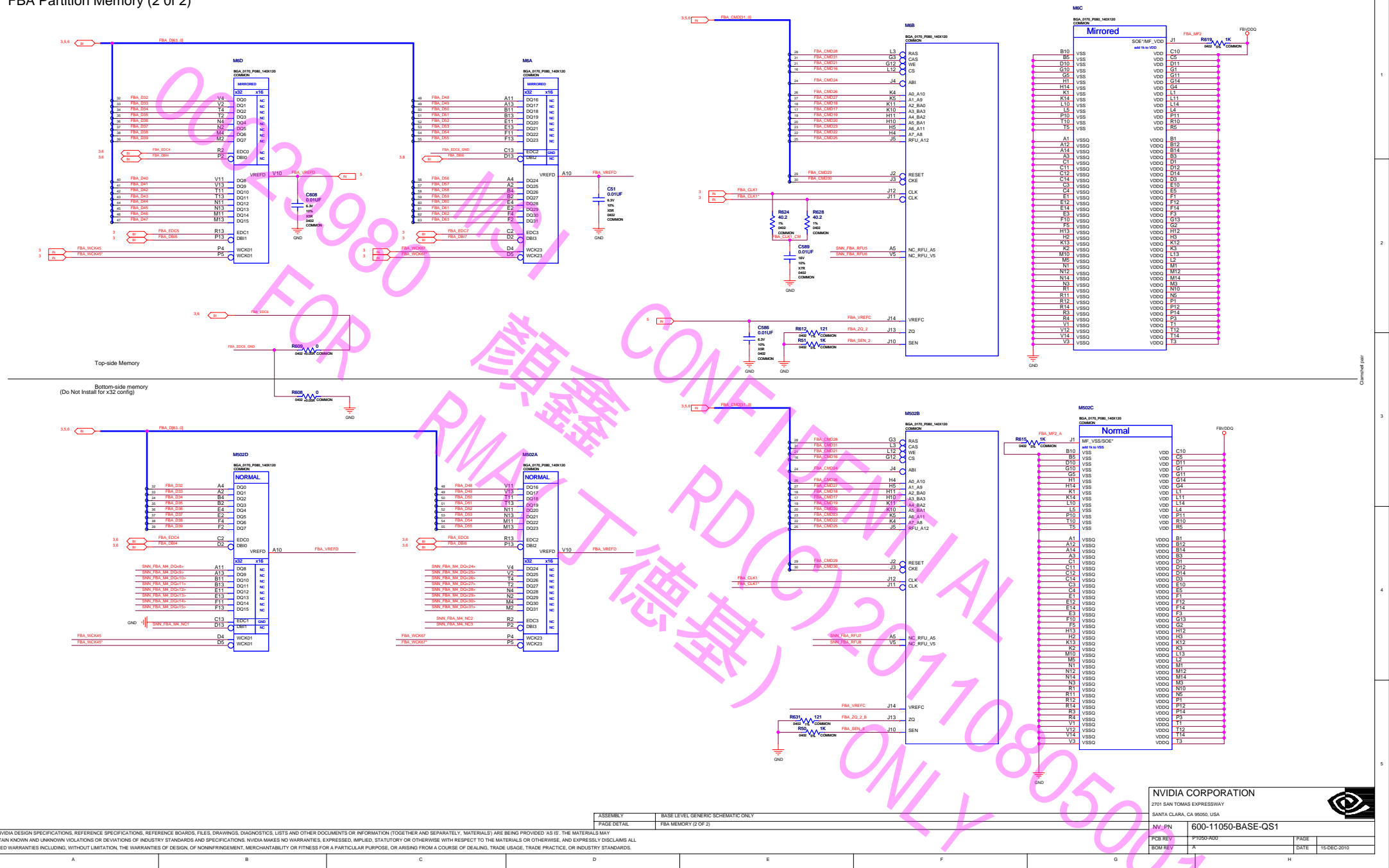
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	GPU FB PARTITION A AND B

# MEMORY: GPU FB Partition C AND GPU FB Decap





## FBA Partition Memory (2 Of 2)



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PCB REV	F1050-A00
BOM REV	A

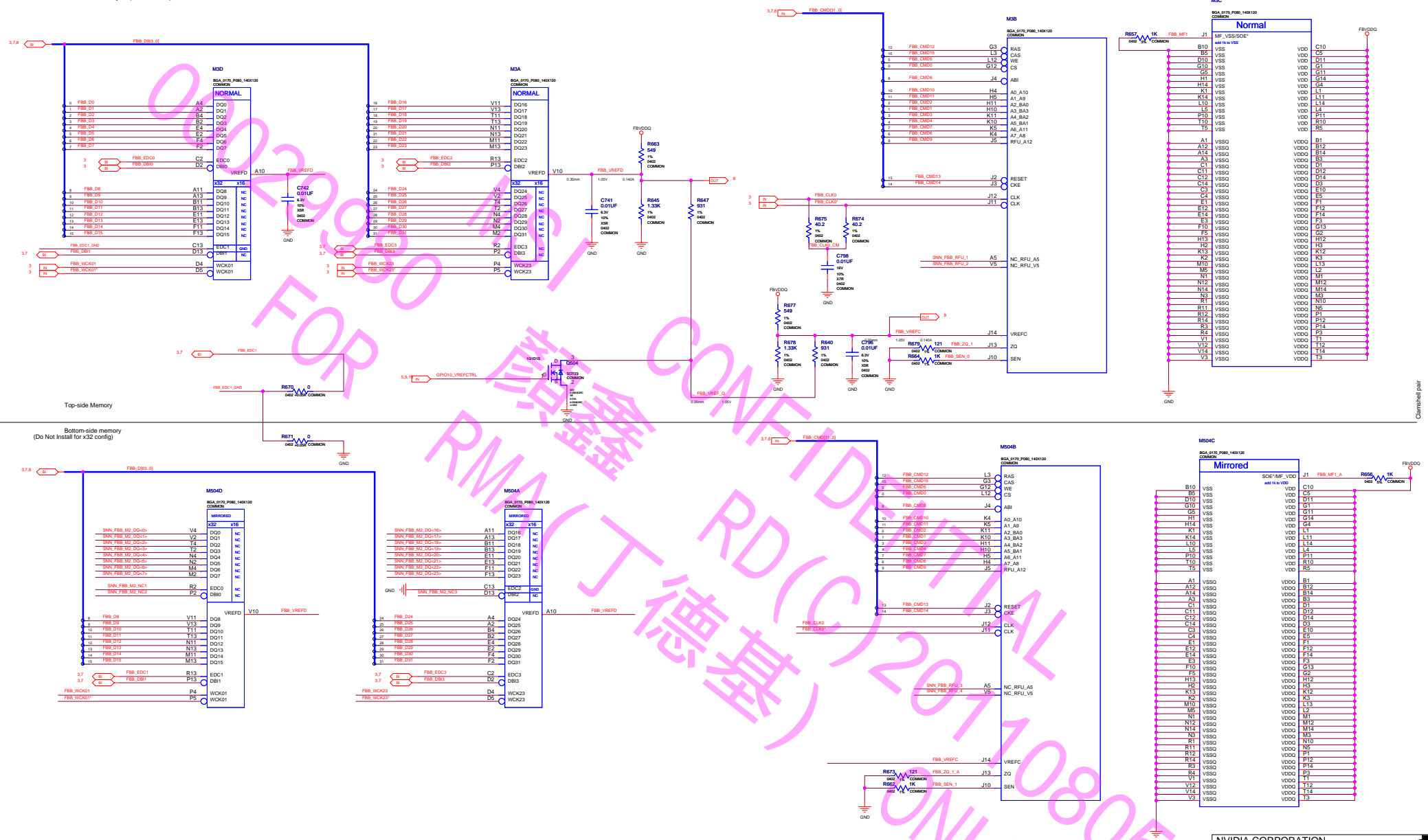
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	FBA MEMORY (2 OF 2)

## FBB Partition Memory (1 of 2)

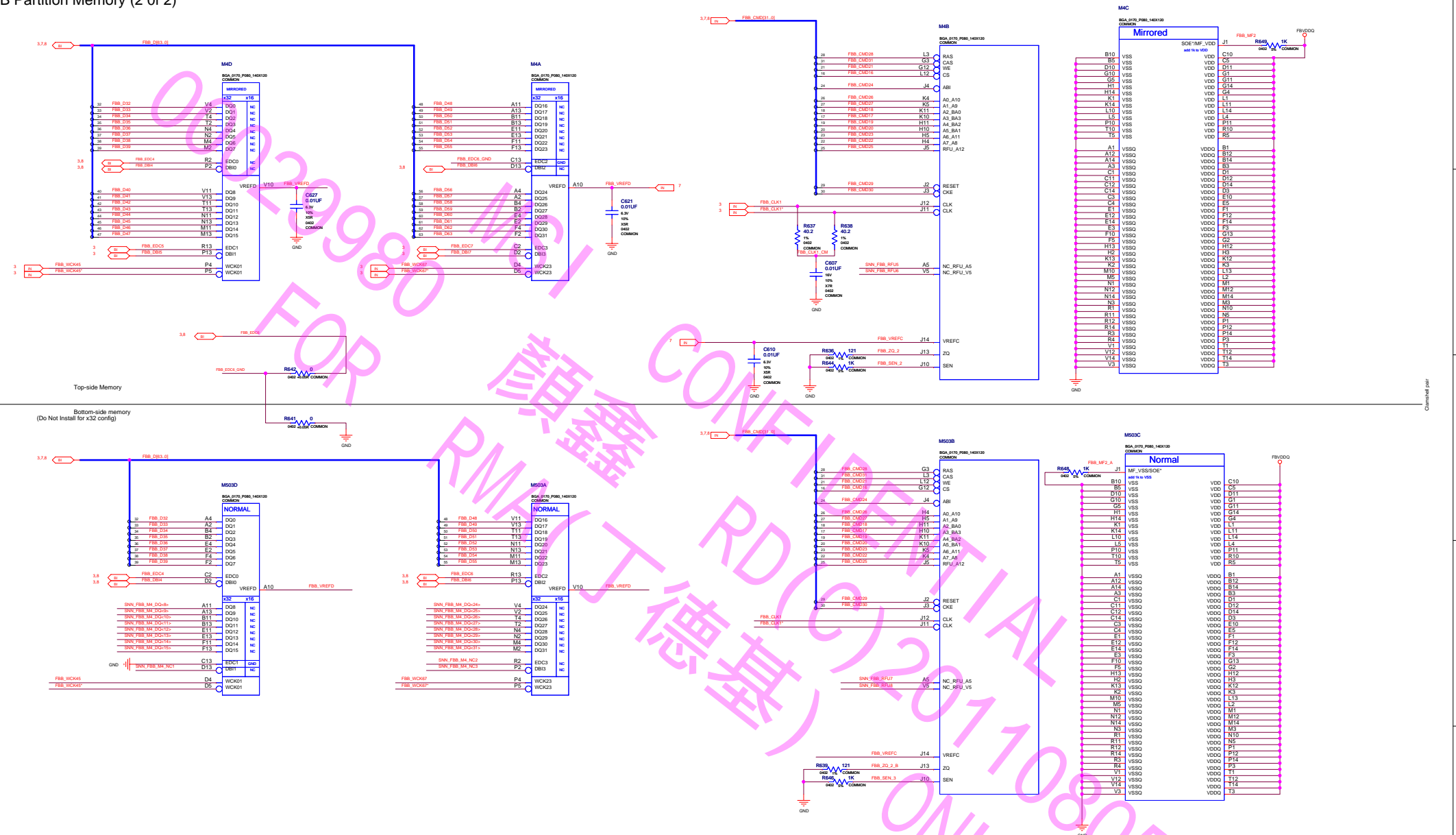


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<b>NV_PN</b>		<b>600-11050-BASE-QS1</b>	
<b>PCB REV</b>	<b>P1050-ADD</b>	<b>PAGE</b>	
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ASSEMBLY		BASE LEVEL GENERIC SCHEMATIC ONLY		SANTA CLARA, CA 95050, USA	
PAGE DETAIL		FBI MEMORY (1 OF 2)		NV_PN 600-11050-BASE-QS1 PCB REV P1050-ADD BOM REV A	
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## FBB Partition Memory (2 Of 2)



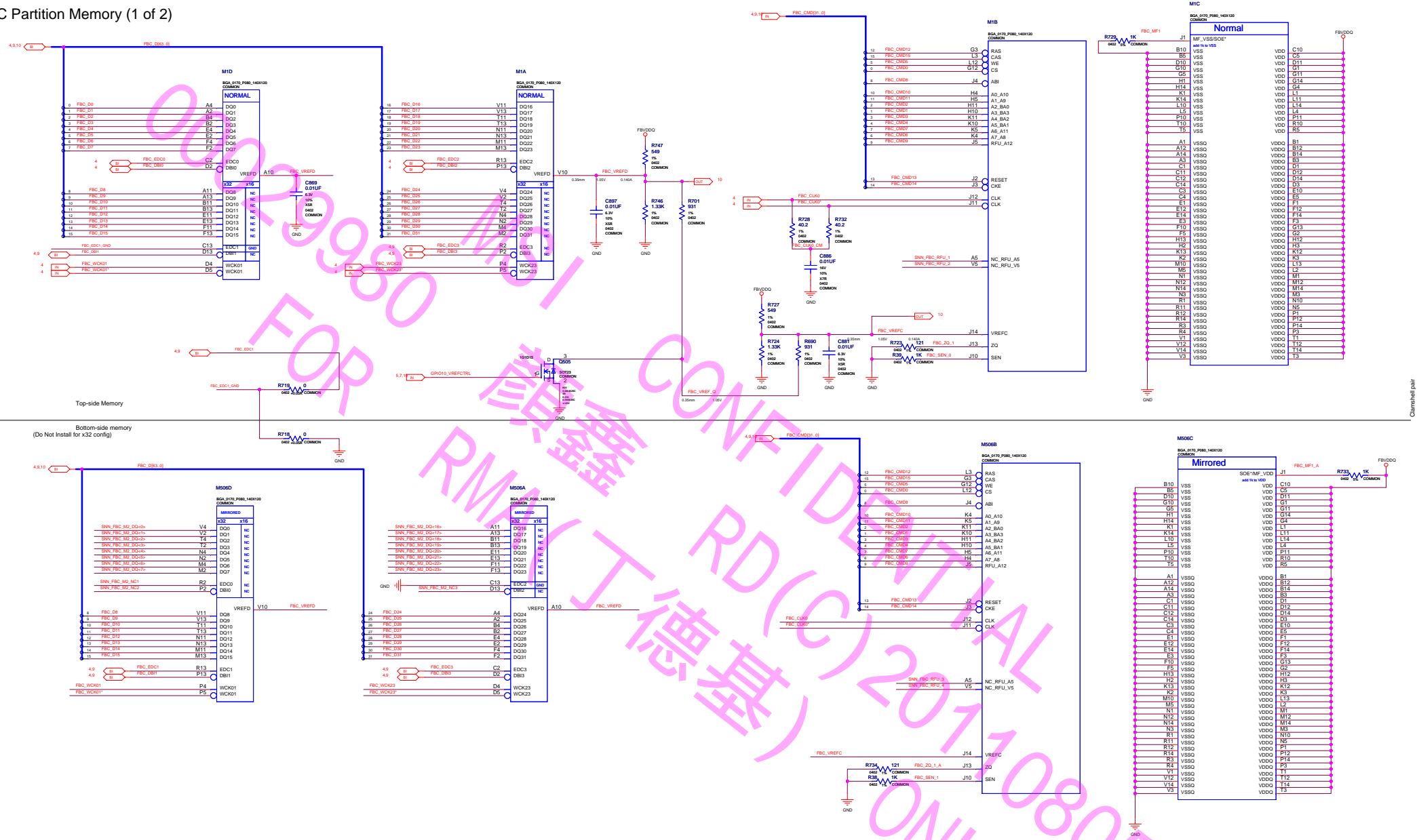
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	FBB MEMORY (2 OF 2)

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NV_PN	600-11050-BASE-QS1		
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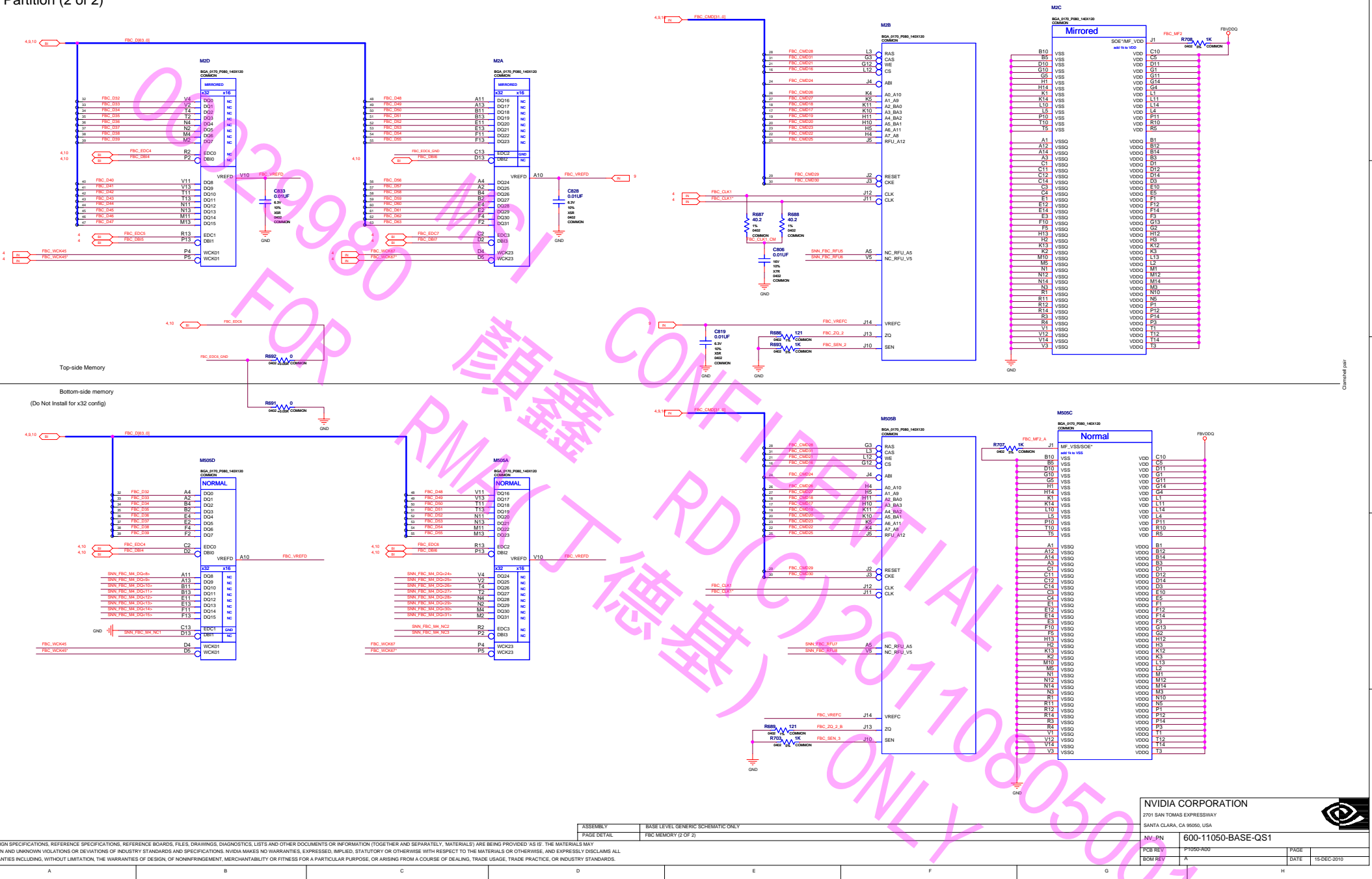



## FBC Partition Memory (1 of 2)



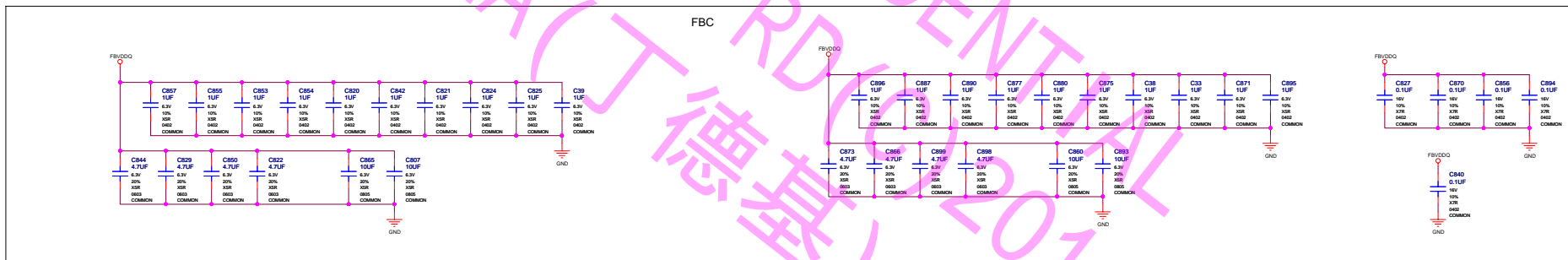
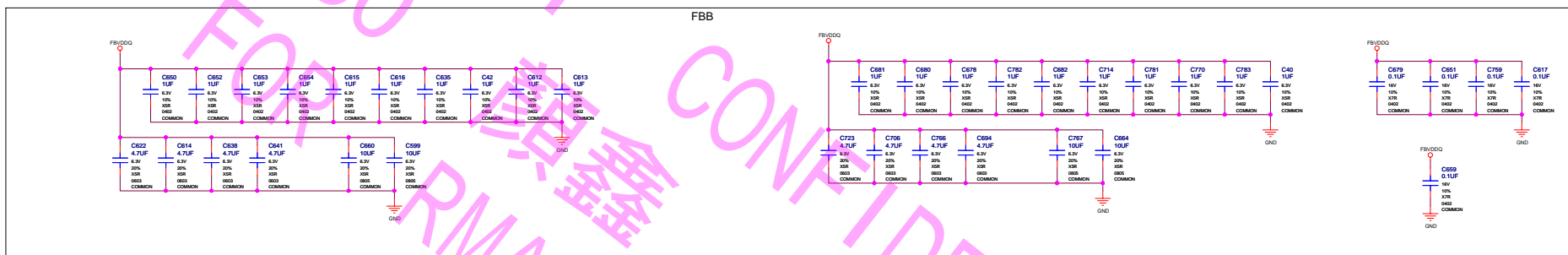
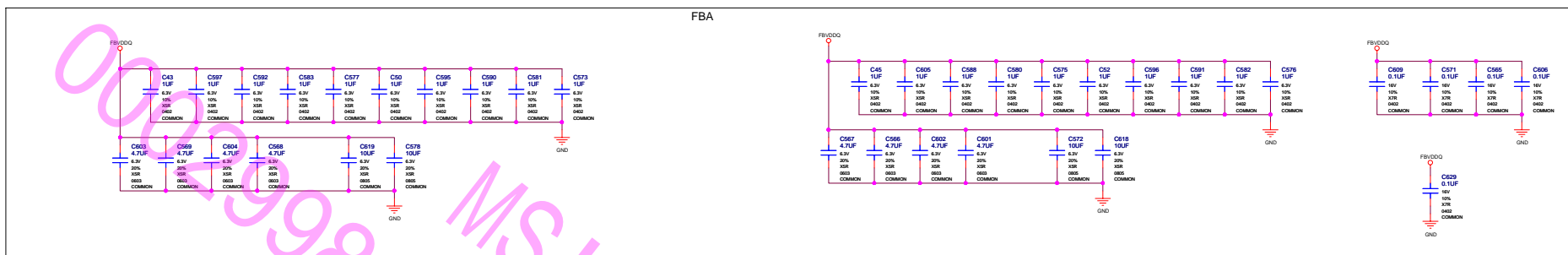
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<b>NV_PN</b>		<b>600-11050-BASE-QS1</b>	
<b>PCB REV</b>	<b>P1050-ADD</b>	<b>PAGE</b>	
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## FBC Partition (2 of 2)



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## FB MEMORY DECAP



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PAGE DETAIL	FB MEMORY DECAP

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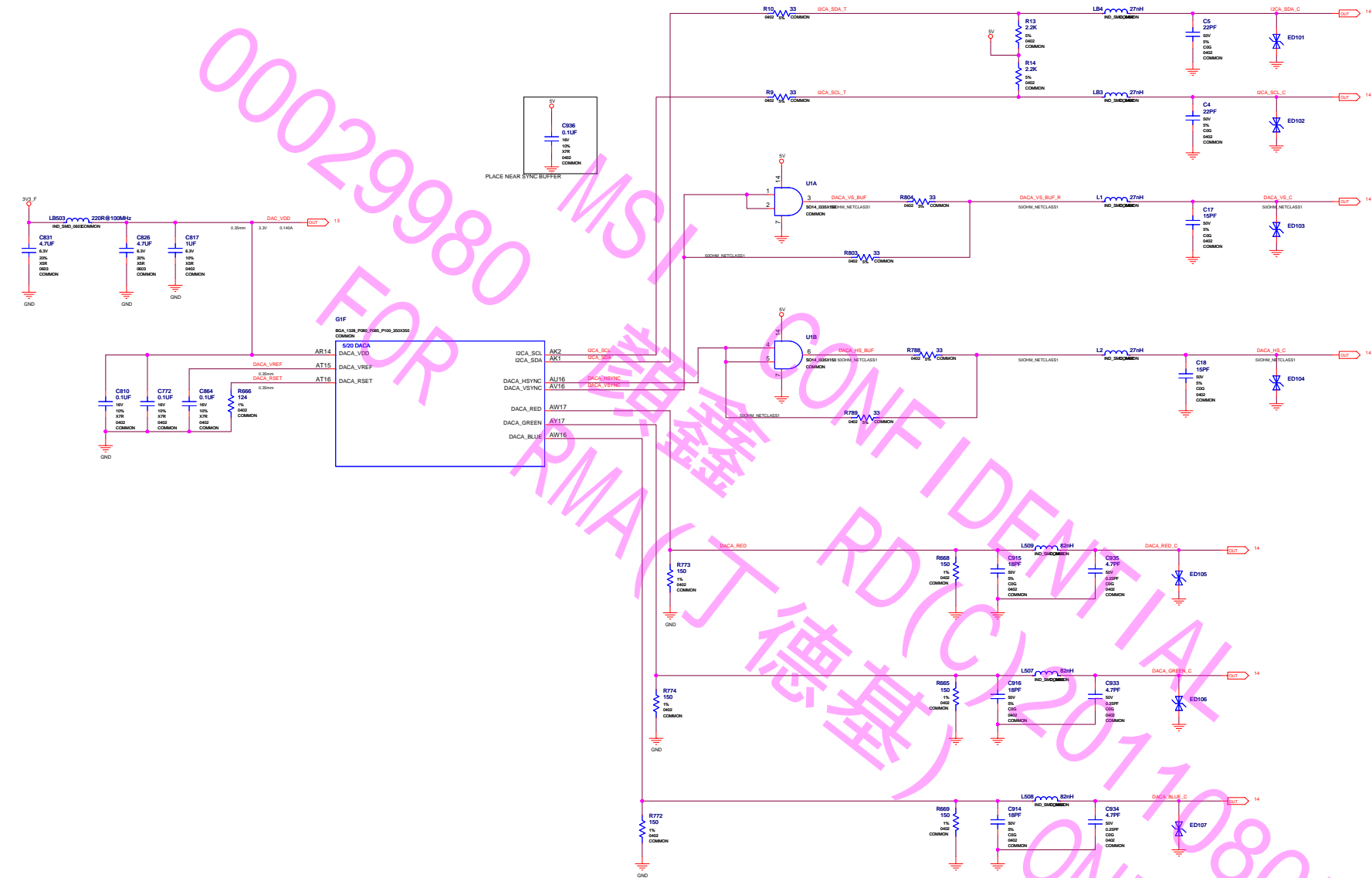
NV PN	600-11050-BASE-QS1
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PCB REV	P1050-A00	PAGE	
BOM REV	A	DATE	15-DEC-2010

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# DACA for South DVI Interface

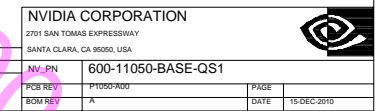
# DACA RGB-FILTER



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BCM REV	A
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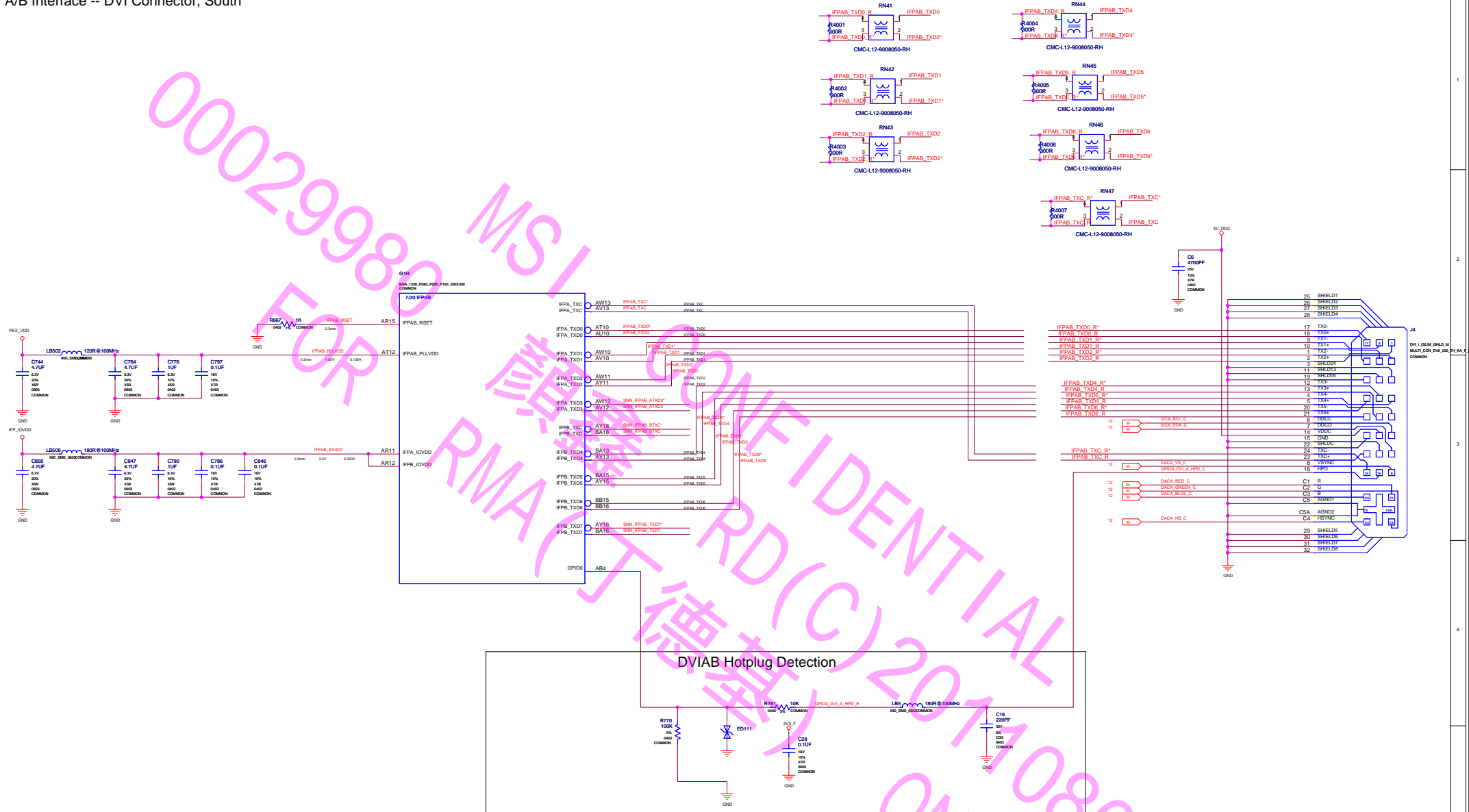
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## DACB RGB-FILTER



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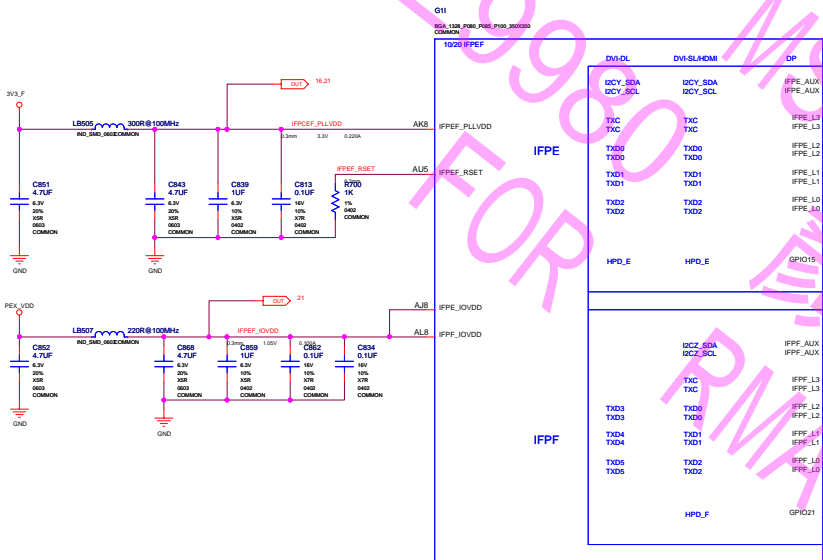
# IFP A/B Interface -- DVI Connector, South



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IFP E/F Interface -- DVI Connector, Mid



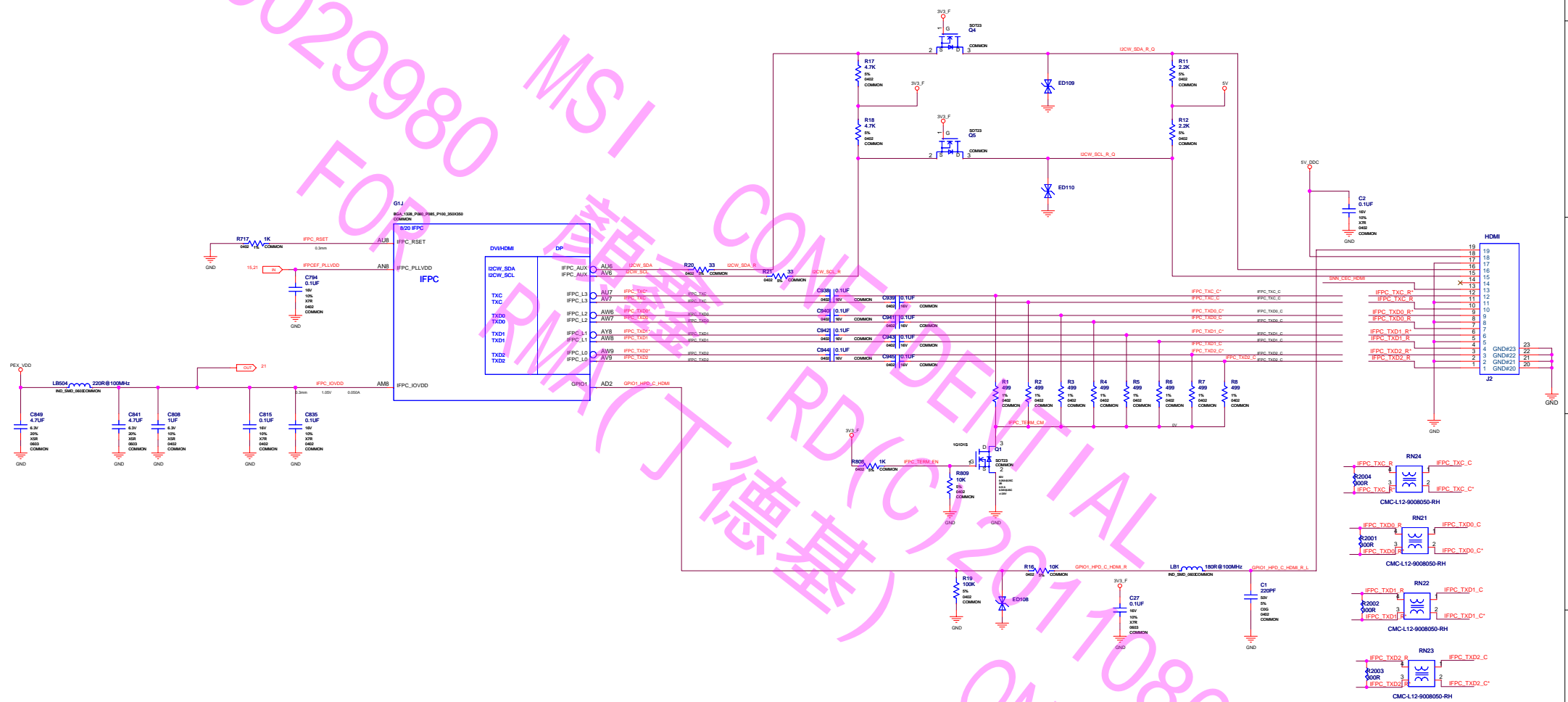
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ASSEMBLY	BASE LEVEL, GENERIC SCHEMATIC ONLY
PAGE DETAIL	IFPE/F Interface -- DVI Connector, Mid

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BCBR REV	A	DATE	15-DEC-2010



# IFP C Interface -- miniHDMI, North



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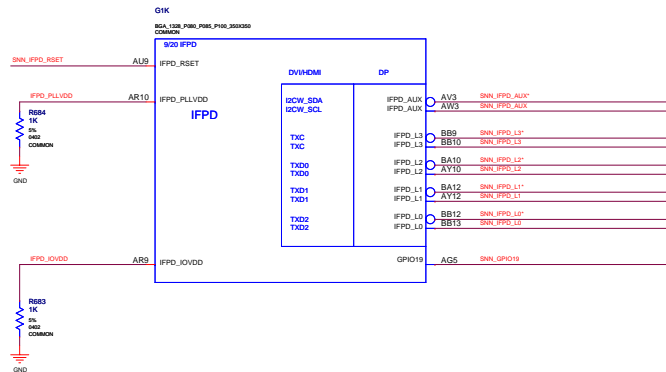
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	IFP C Interface -- HDMI

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ICOM REV	A
PAGE	15-DEC-2010

## IFP D INTERFACE (UNUSED), DP from IFPE, Mid

Three cases to be considered:

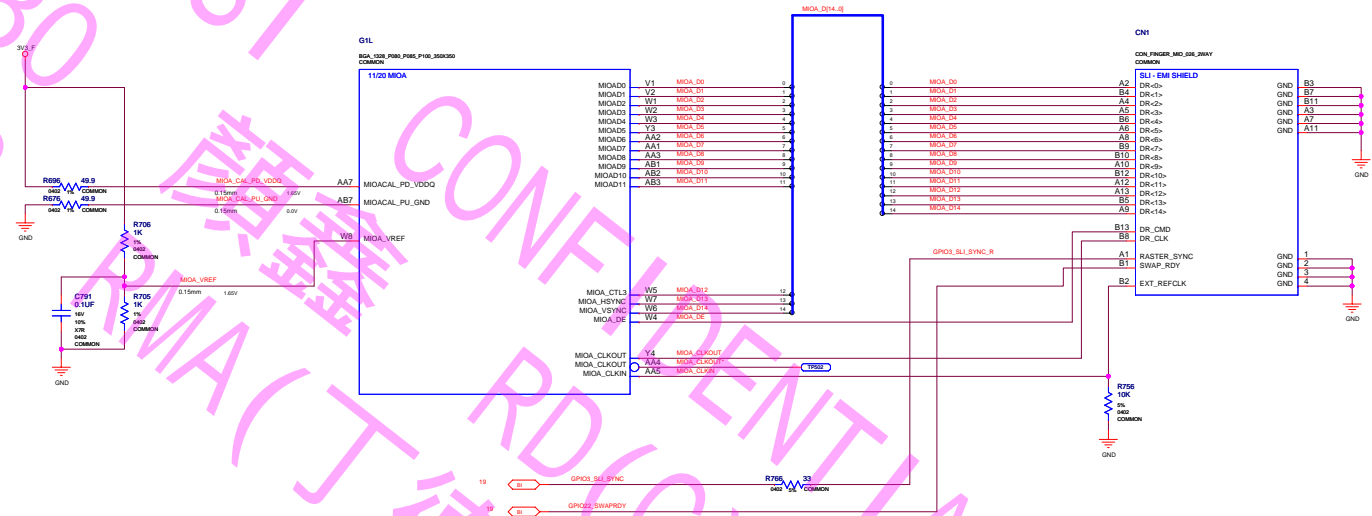
1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through
3. DP AUX to DVI connector: AUX level shifted to DDC



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	DP FROM IFPE, and IFPD (UNUSED)

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MIO A Interface, SLI

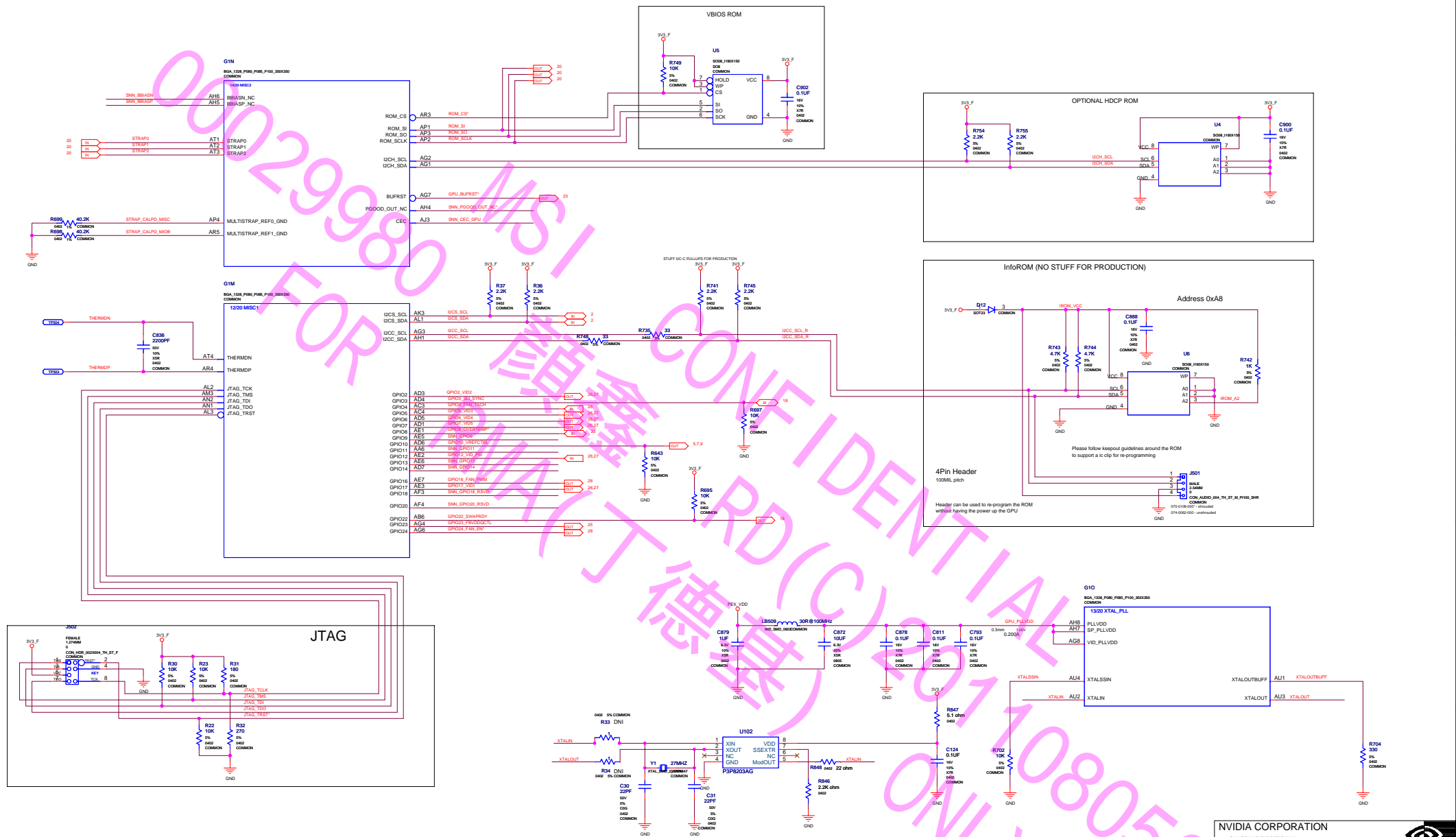



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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	MIO A Interface, SLI

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BCOM REV	A	DATE	15-DEC-2010

MISC: GPIO, I2C, ROM, HDCP, and XTAL

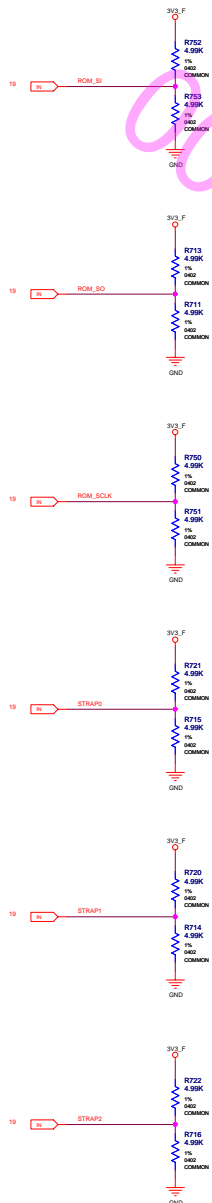


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NV_PN	600-11050-BASE-QS1		
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	MISC: GPIO, I2C, ROM, HDCP, and XTAL

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Strap Configuration



Strap	Strap Pin	Strap Value	Strap Function
ROM_SO	VGA_DEVICE	LSB	1: VGA DEVICE
ROM_SI	RAM_DEVICE	MSB	0: 64MBIT (0)
ROM_SCLK	PEX_PLL_EN_TERM10	LSB	0: 64MBIT (0)
STRAP0	USER_BITS[0]	MSB	0: 64MBIT (0)
STRAP1	SGIO_FLASH_CFG_LUT_ADDR[0]	MSB	0: 64MBIT (0)
STRAP2	PCI_DEVICE	MSB	0: 64MBIT (0)

Strap Pin	Strap Value	Strap Function
0	0	0: 64MBIT (0)
1	1	1: 64MBIT (1)
2	2	2: 64MBIT (2)
3	3	3: 64MBIT (3)
4	4	4: 64MBIT (4)
5	5	5: 64MBIT (5)
6	6	6: 64MBIT (6)
7	7	7: 64MBIT (7)
8	8	8: 64MBIT (8)
9	9	9: 64MBIT (9)
10	10	10: 64MBIT (10)
11	11	11: 64MBIT (11)
12	12	12: 64MBIT (12)
13	13	13: 64MBIT (13)
14	14	14: 64MBIT (14)
15	15	15: 64MBIT (15)

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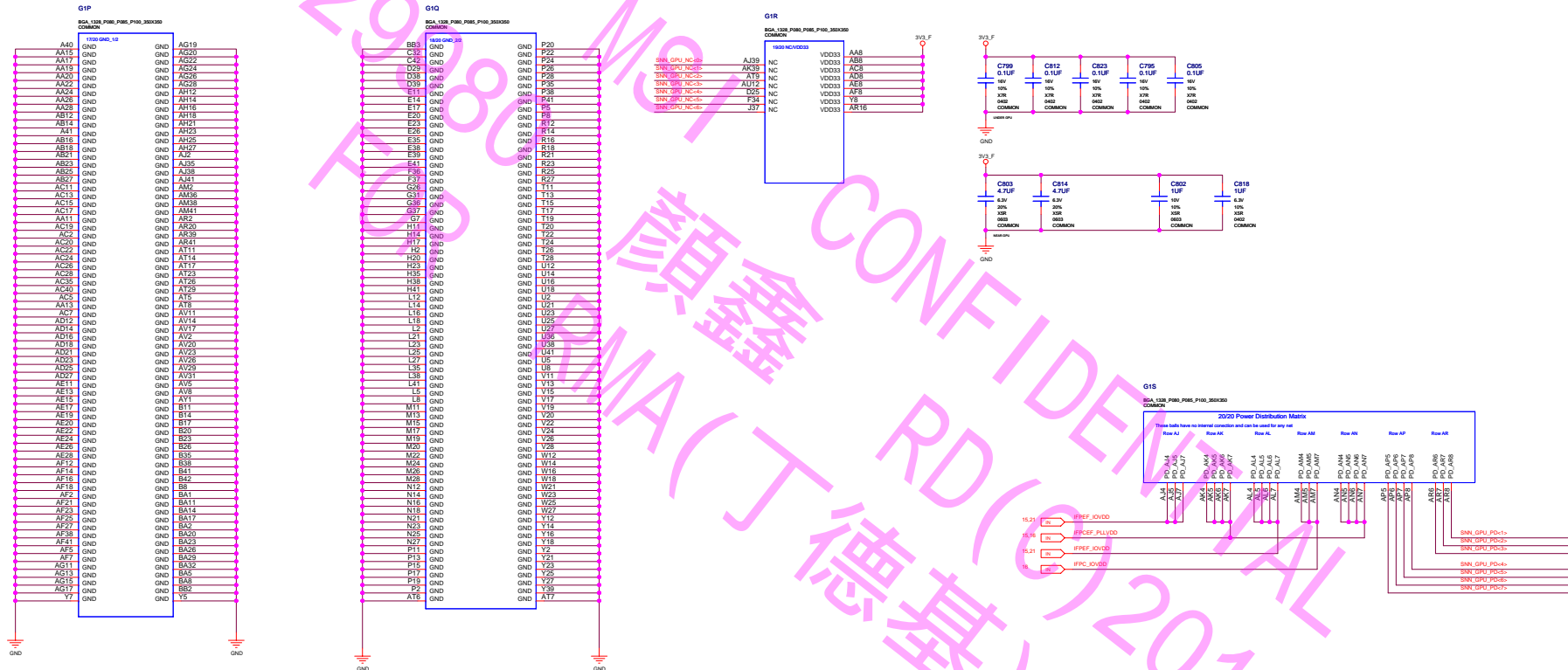
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## PWR and GND Signals



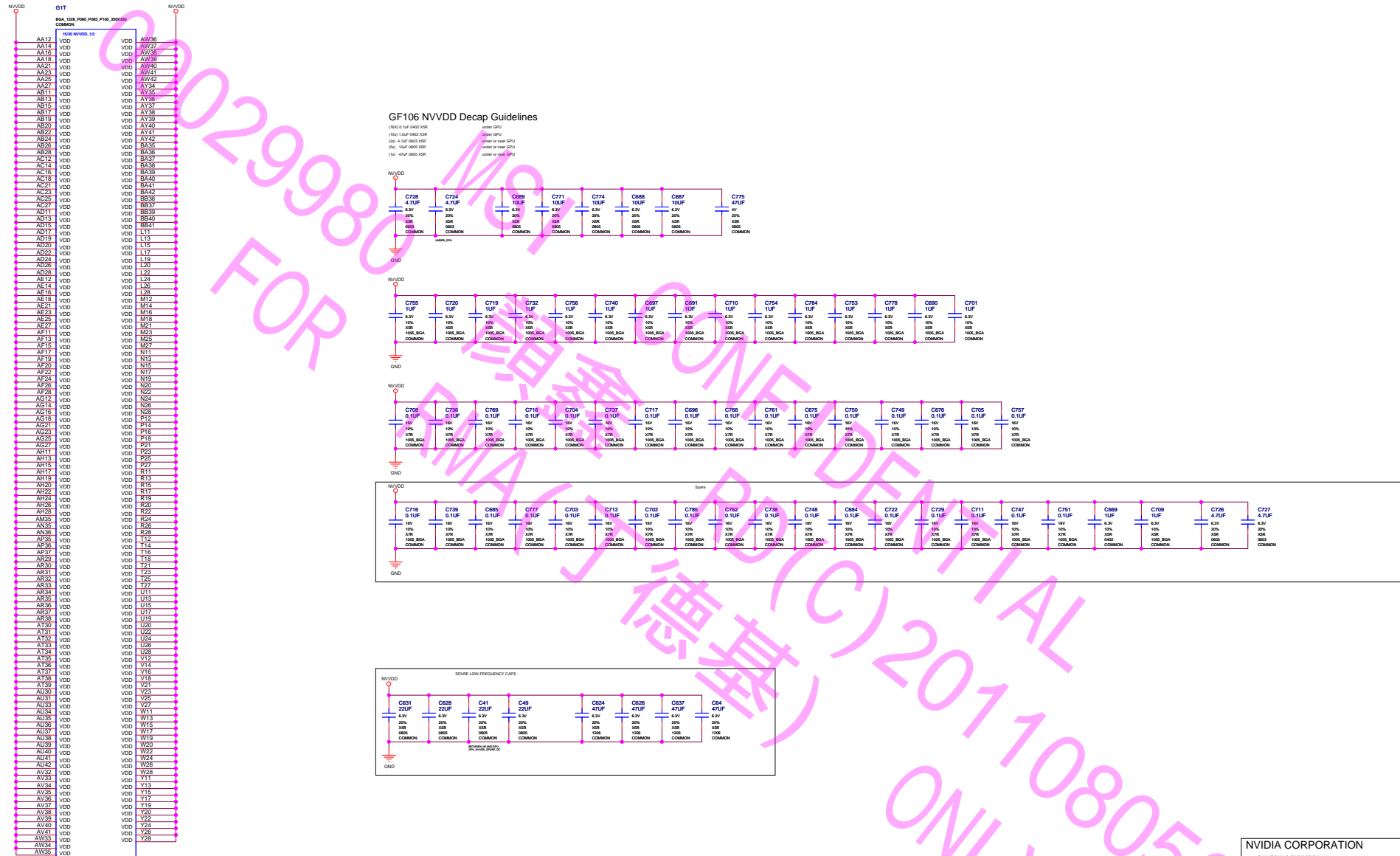
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	PWR and GND Signals

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## NVVDD Decoupling



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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	NVVDD Decoupling

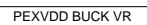
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NV PN	600-11050-BASE-QS1		
PCB REV	P1050-ADD	PAGE	
BOM REV	A	DATE	15-DEC-2010







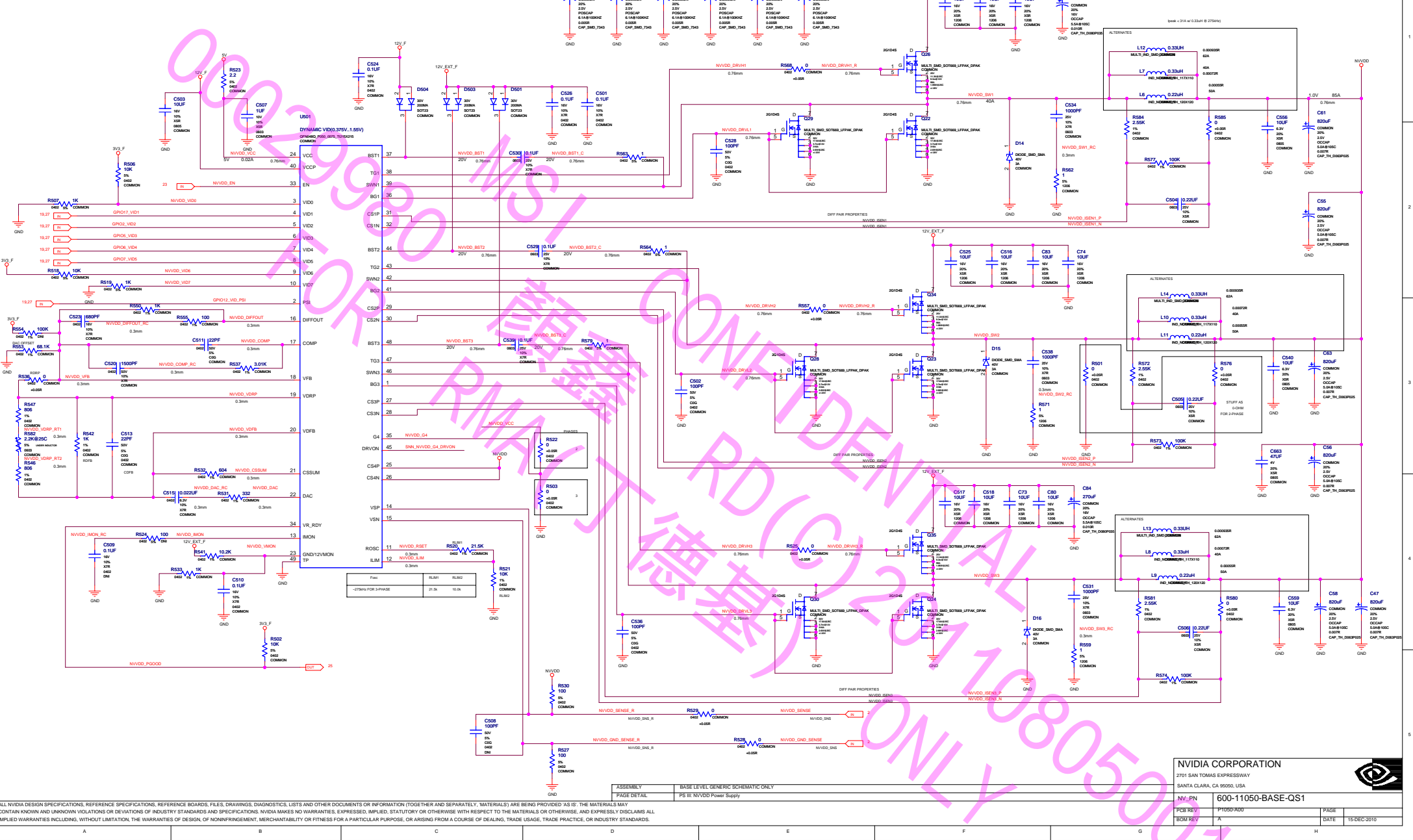



$$V_{out} = V_{ref} * (1 + R_{top} / (R_{bot} || R_{bot1}))$$

NV_PN	600-11050-BASE-QS1		
PCB REV	P1050-A00	PAGE	
BOM REV	A	DATE	15-DEC-2010

# PS III: NVVDD Power Supply

2-PHASE OR 3-PHASE DESIGN  
(UNSTUFF PHASE 2 FOR 2-PHASE CONFIG)





NVAPI VID3 NVAPI VID4 NVAPI VID5 NVAPI VID6 NVAPI VID7									
VID3	VID4	VID5	VID6	VID7	VID8	VID9	VID10	VID11	VID12
0	0	0	0	0	1.2125				
0	0	0	0	1	1.2000				
0	0	0	1	0	1.1875				
0	0	0	1	1	1.1750				
0	0	1	0	0	1.1625				
0	0	1	0	1	1.1500				
0	0	1	1	0	1.1375				
0	0	1	1	1	1.1250				
0	1	0	0	0	1.1125				
0	1	0	0	1	1.1000				
0	1	0	1	0	1.0875				
0	1	0	1	1	1.0750				
0	1	1	0	0	1.0625				
0	1	1	0	1	1.0500				
0	1	1	1	0	1.0375				
0	1	1	1	1	1.0250				
1	0	0	0	0	1.0125				
1	0	0	0	1	1.0000				
1	0	0	1	0	0.9875				
1	0	0	1	1	0.9750				
1	0	1	0	0	0.9625				
1	0	1	0	1	0.9500				
1	0	1	1	0	0.9375				
1	0	1	1	1	0.9250				
1	1	0	0	0	0.9125				
1	1	0	0	1	0.9000				
1	1	0	1	0	0.8875				
1	1	0	1	1	0.8750				
1	1	1	0	0	0.8625				
1	1	1	0	1	0.8500				
1	1	1	1	0	0.8375				
1	1	1	1	1	0.8250				

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