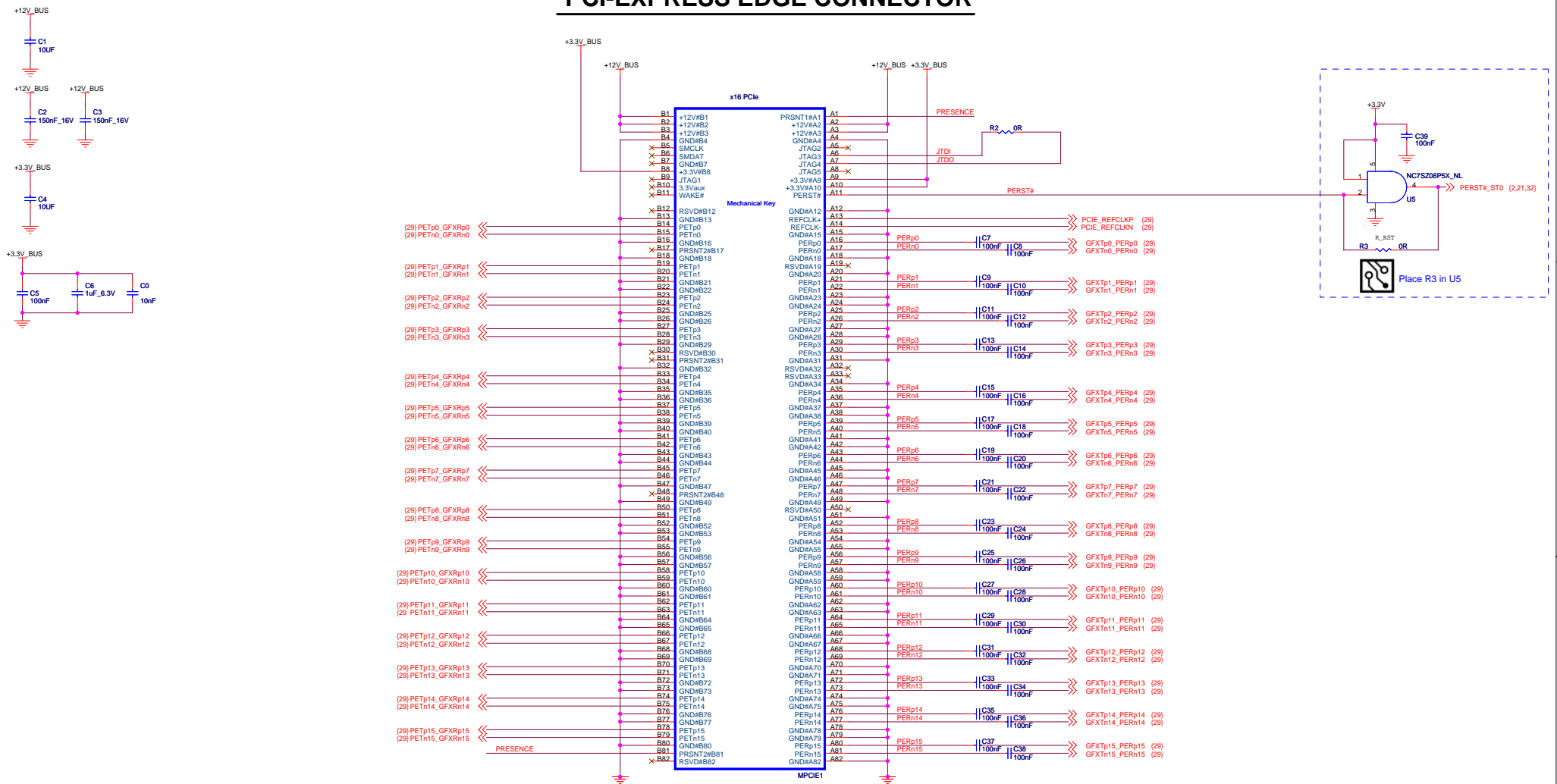




PCI-EXPRESS EDGE CONNECTOR



SYMBOL LEGEND	
DNI	DO NOT INSTALL
#	ACTIVE LOW
	DIGITAL GROUND
	ANALOG GROUND
BUO	BRING UP ONLY

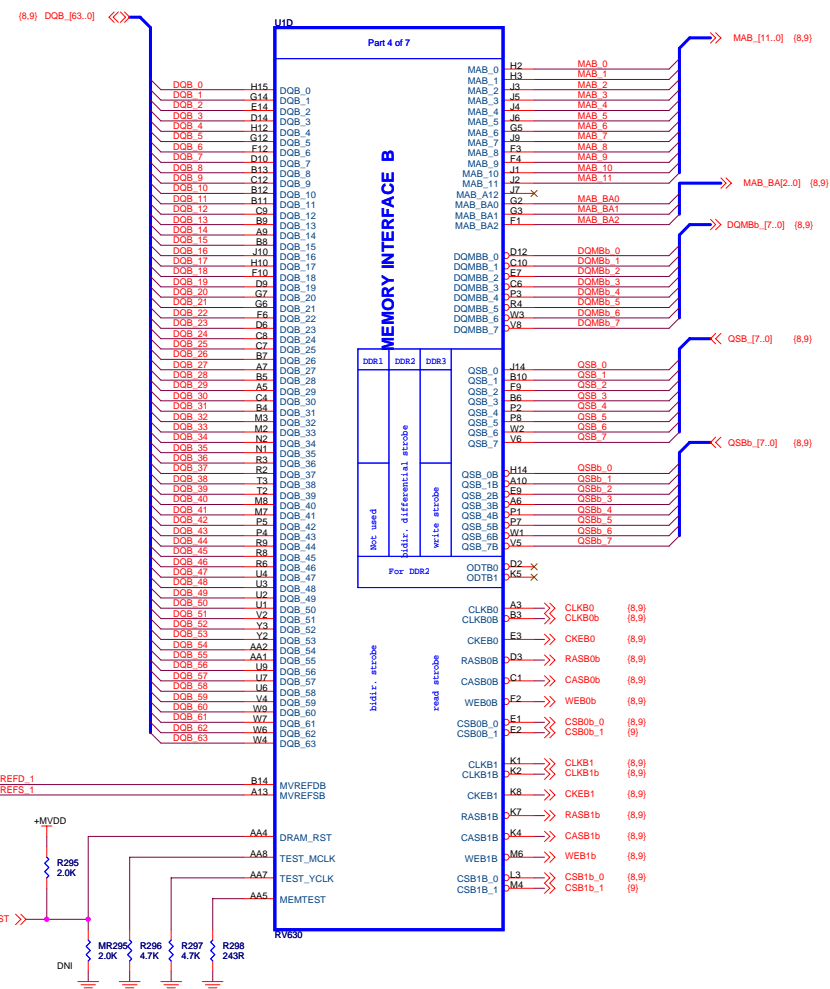
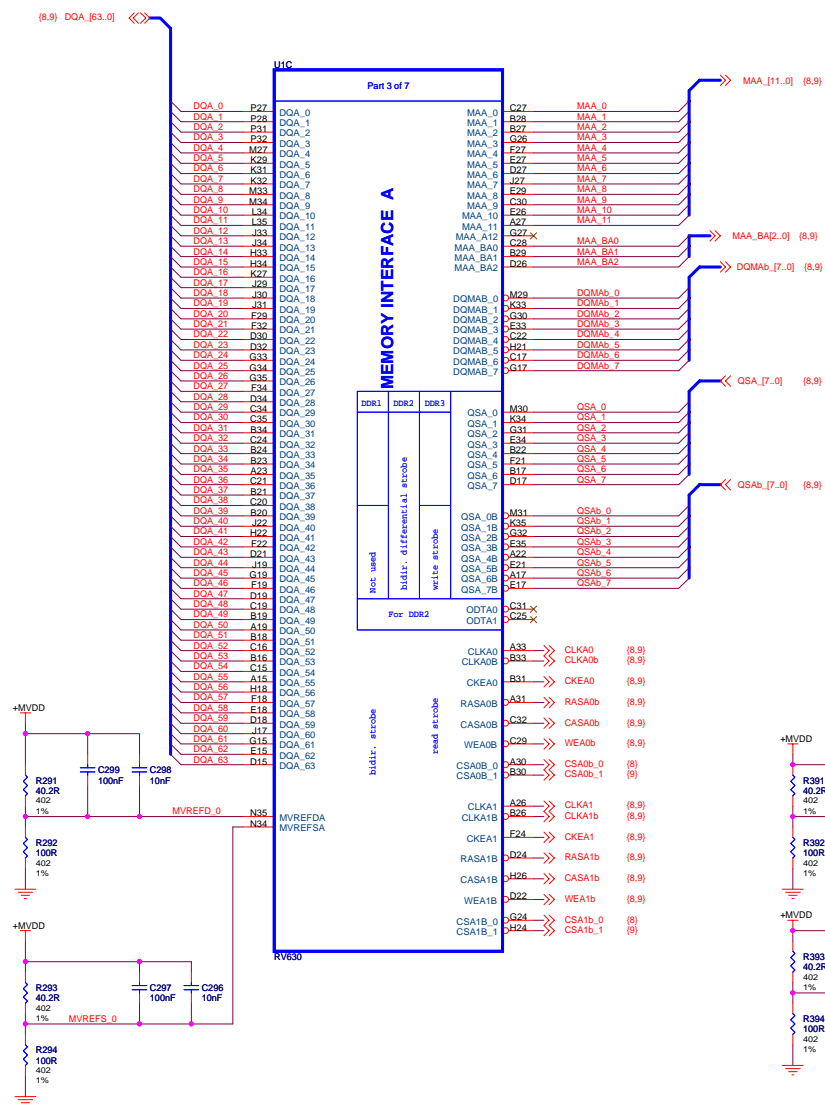


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Title				PEX8547+RV630 GDDR3 VGA CARD			
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[illegible]

Title		RV630 GDDR3 - ASIC Power	
Size C	Document Number 105-B148xx-00A		
Date:	Tuesday, May 15, 2007	Sheet	4 of 36



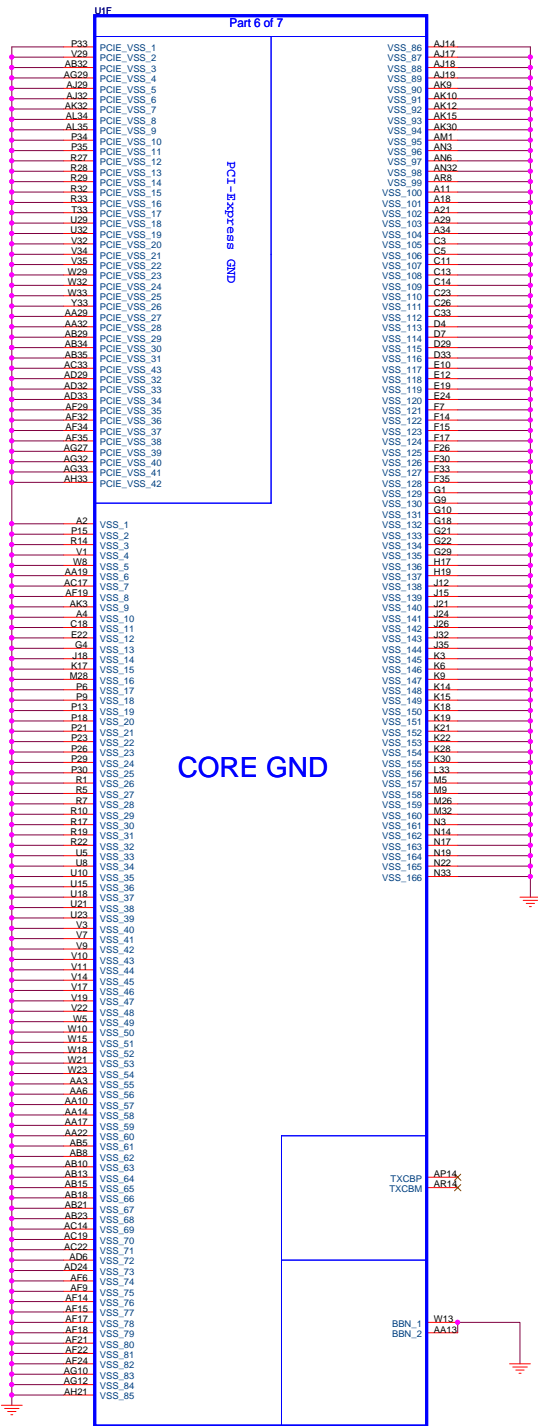
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Title RV630 GDDR3 - ASIC Memory Interface (Channel A & B)

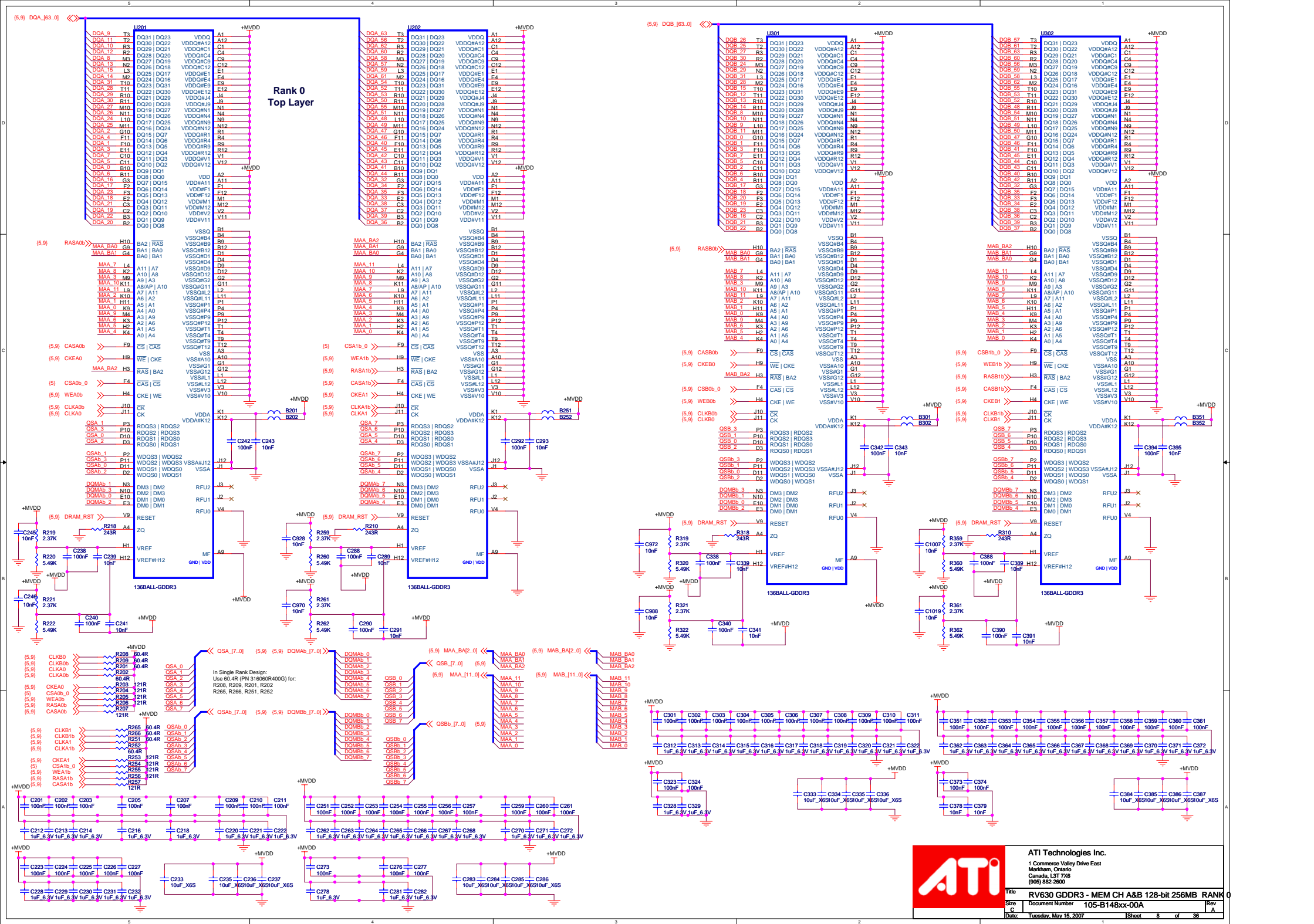
Size	Document Number
6	105-B148xx-00A

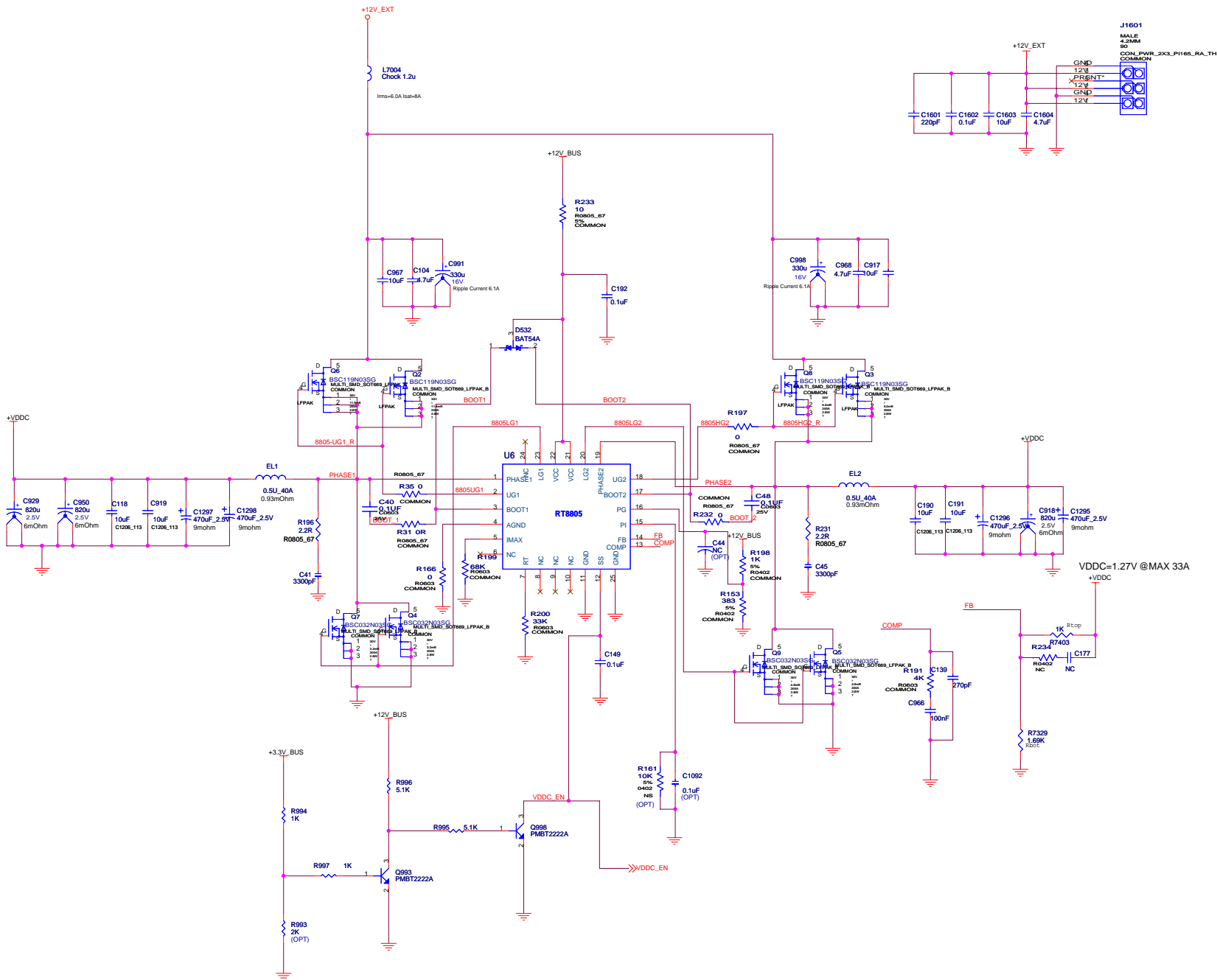
C	105-B148XX-00A		
Date:	Tuesday, May 15, 2007	Sheet	5 of 36

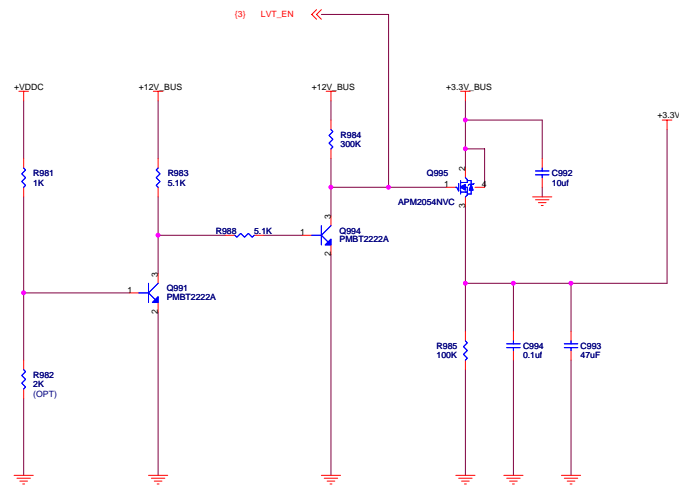


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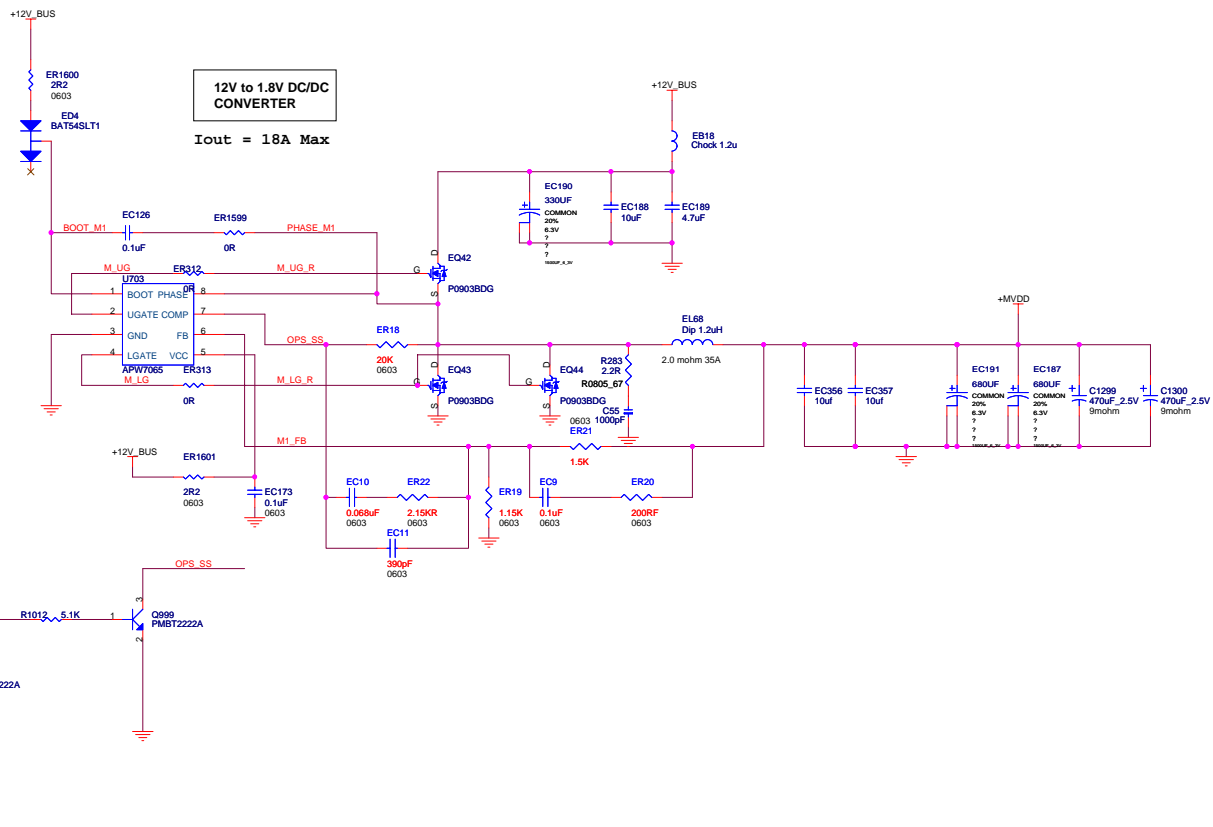
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Size	Document Number	105-B148xx-00A	Rev
C			A
Date:	Tuesday, May 15, 2007	Sheet	6 of 36

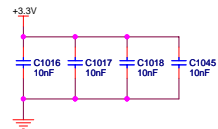
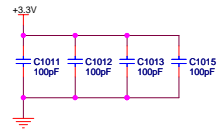
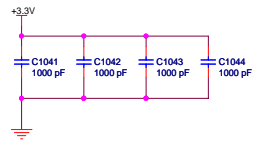
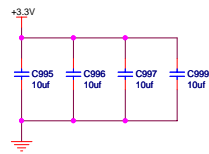






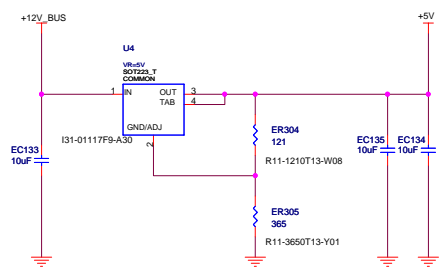
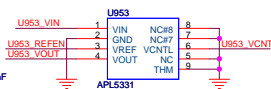
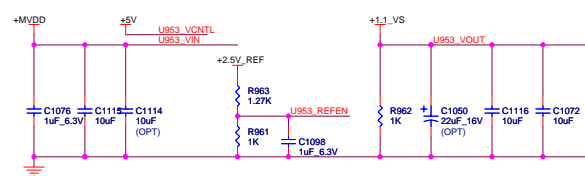
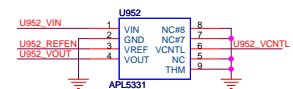
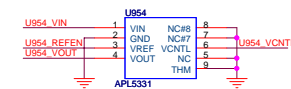
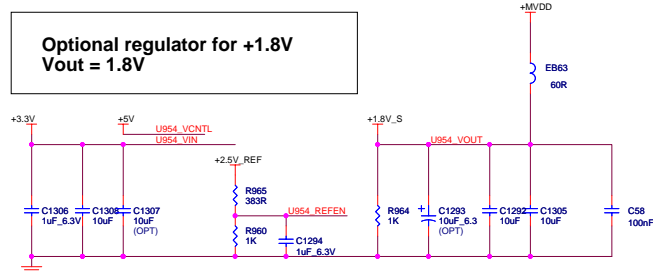
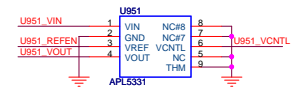
CORE REGULATOR VDDC





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Title		RV630 GDDR3 - Power Management	
Size	Document Number	105-B148xx-00A	Rev
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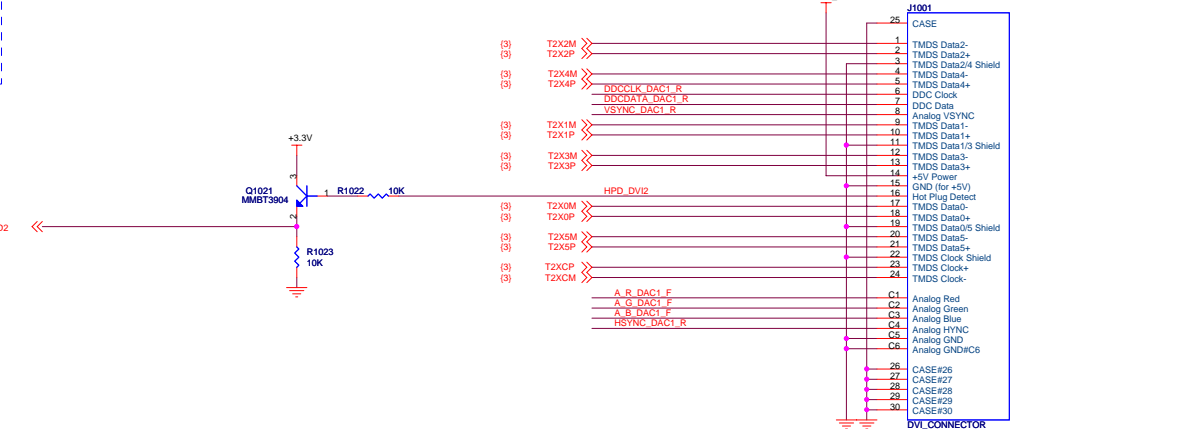
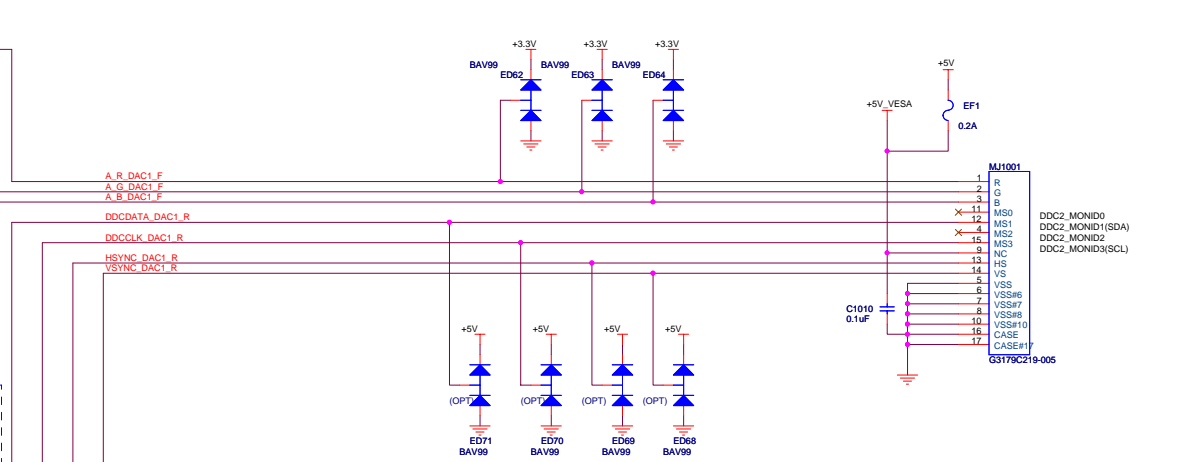
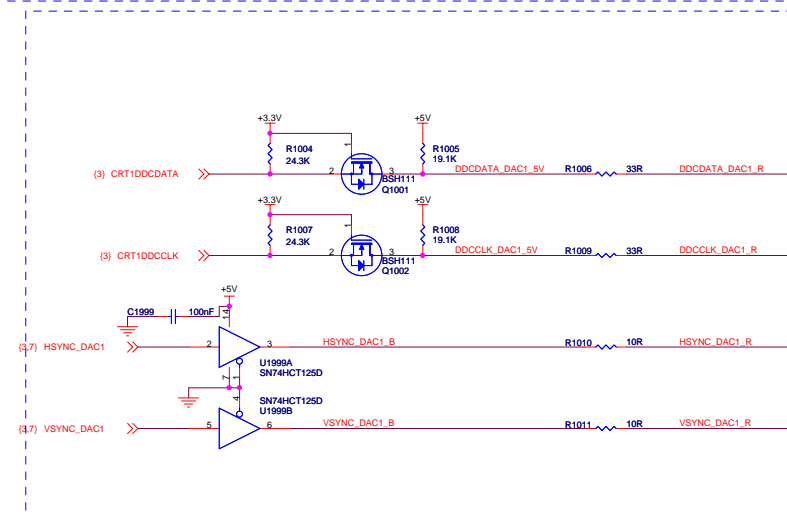
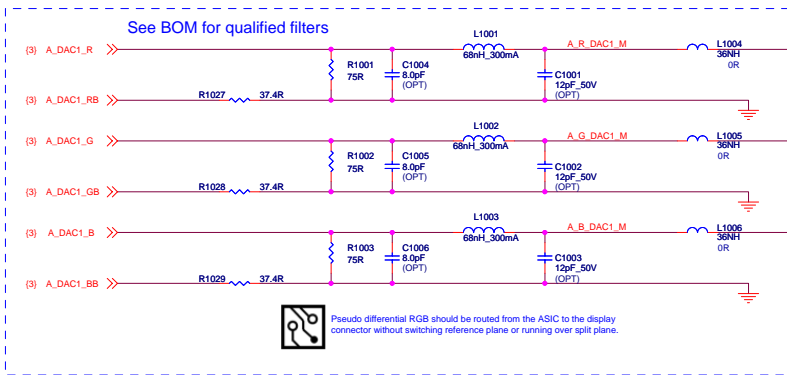


Shared Power Rails



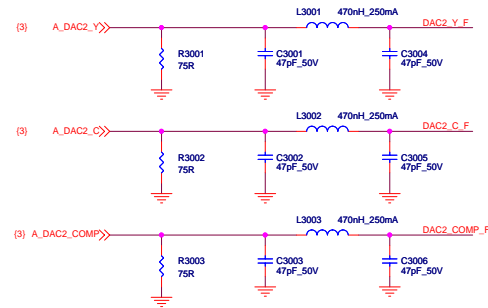
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Title RV630 GDDR3 - Linear Regulators			
Size C	Document Number 105-B148xx-00A		Rev A
Date: Tuesday, May 15, 2007	Sheet	14 of 36	

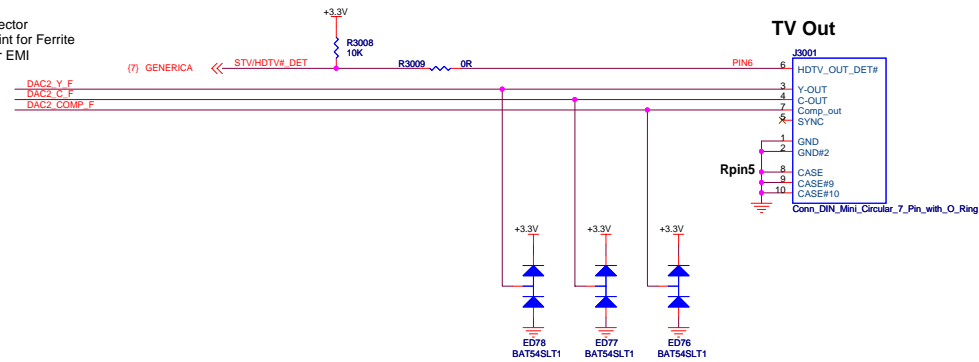


DB15 pin	Standard VGA	DDC1 Host	DDC2B or DDC2B Host	DDC2AB Host	DDC1/2 Display
11	Monitor ID bit 0	Monitor ID bit 0	Monitor ID bit 0	Monitor ID bit 0	Optional
12	Monitor ID bit 1	Monitor ID bit 1	Monitor ID bit 1	Monitor ID bit 1	Optional
4	Monitor ID bit 2	Monitor ID bit 2	Monitor ID bit 2	Monitor ID bit 2	Optional
15	Monitor ID bit 3	Monitor ID bit 3	Monitor ID bit 3	Monitor ID bit 3	Optional
9	N/C	+5V	+5V	+5V	Optional
Hardware Support	No	Yes	Yes	No	Yes

Based on VESA Display Data Channel (DDC) Standard Ver. 3 Dec. 15, 1997



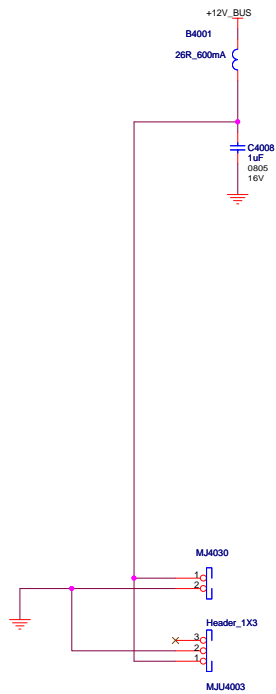
Place near connector
OR leaves footprint for Ferrite
Beads if req'd for EMI



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Title	RV630 GDDR3 - TVO	Rev	A
Size	Document Number	105-B148xx-00A	
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Title			RV630 GDDR3- Thermal Management		
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DVI/DVI SCREWS with top tab

MEC4
MEC_HEX_JACK_SCREW
COMMON
MEC3
MEC_HEX_JACK_SCREW
COMMON
MEC2
MEC_HEX_JACK_SCREW
COMMON
MEC1
MEC_HEX_JACK_SCREW
COMMON

BKT1
BRACKET
8020042500G

DNI
SK1
RV410SOCKET

MT2
MT_Hole_0.136_in_6VIA

PCB1
PCB
109-GN982-00A

<Variant Name>



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Title RV630 GDDR3- Mechanical

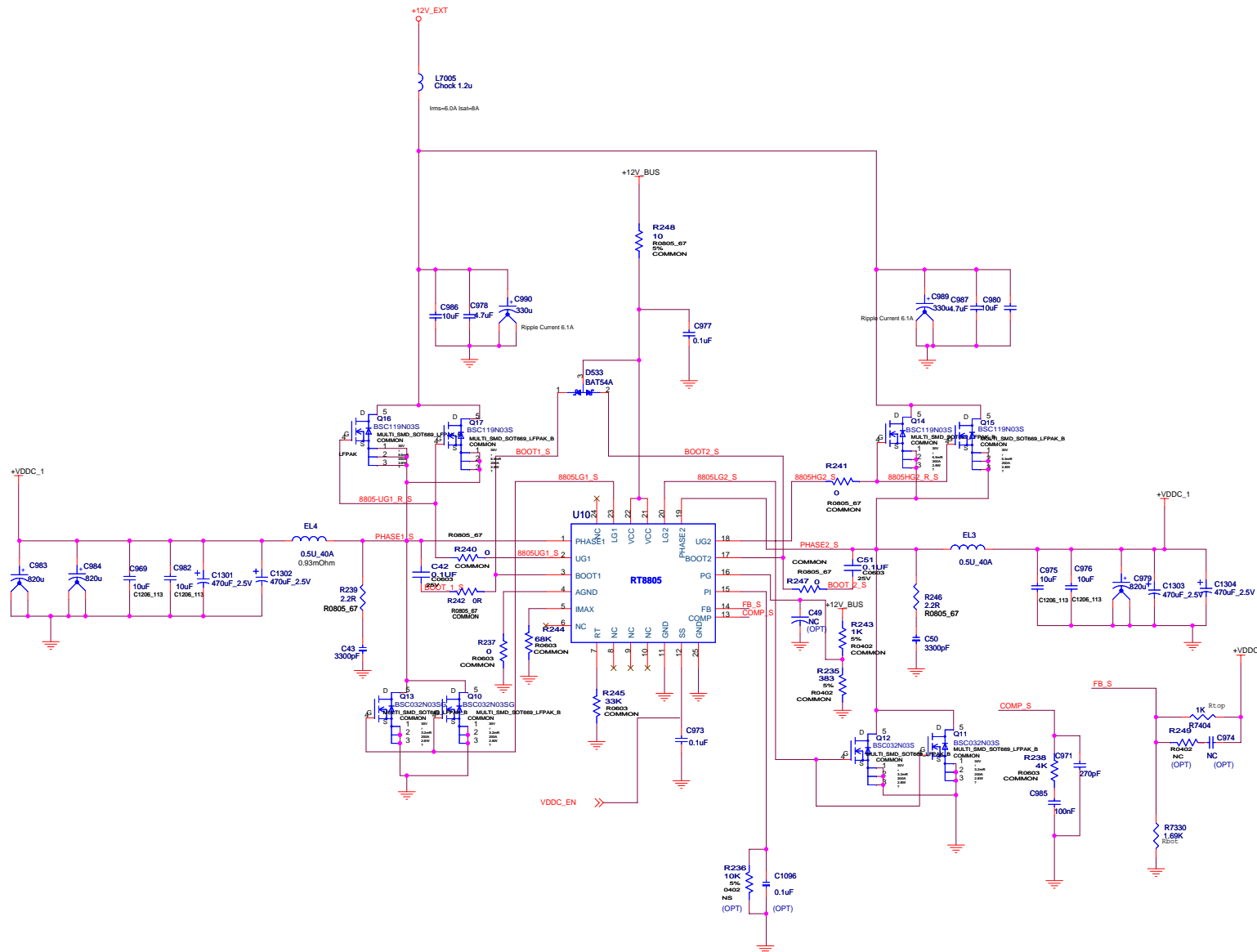
Size C Document Number 105-B148xx-00A



Date: Wednesday, May 16, 2007

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Rev A

PCI-EXPRESS EDGE CONNECTOR



SYMBOL LEGEND	
DNI	DO NOT INSTALL
#	ACTIVE LOW
	DIGITAL GROUND
	ANALOG GROUND
BUO	BRING UP ONLY



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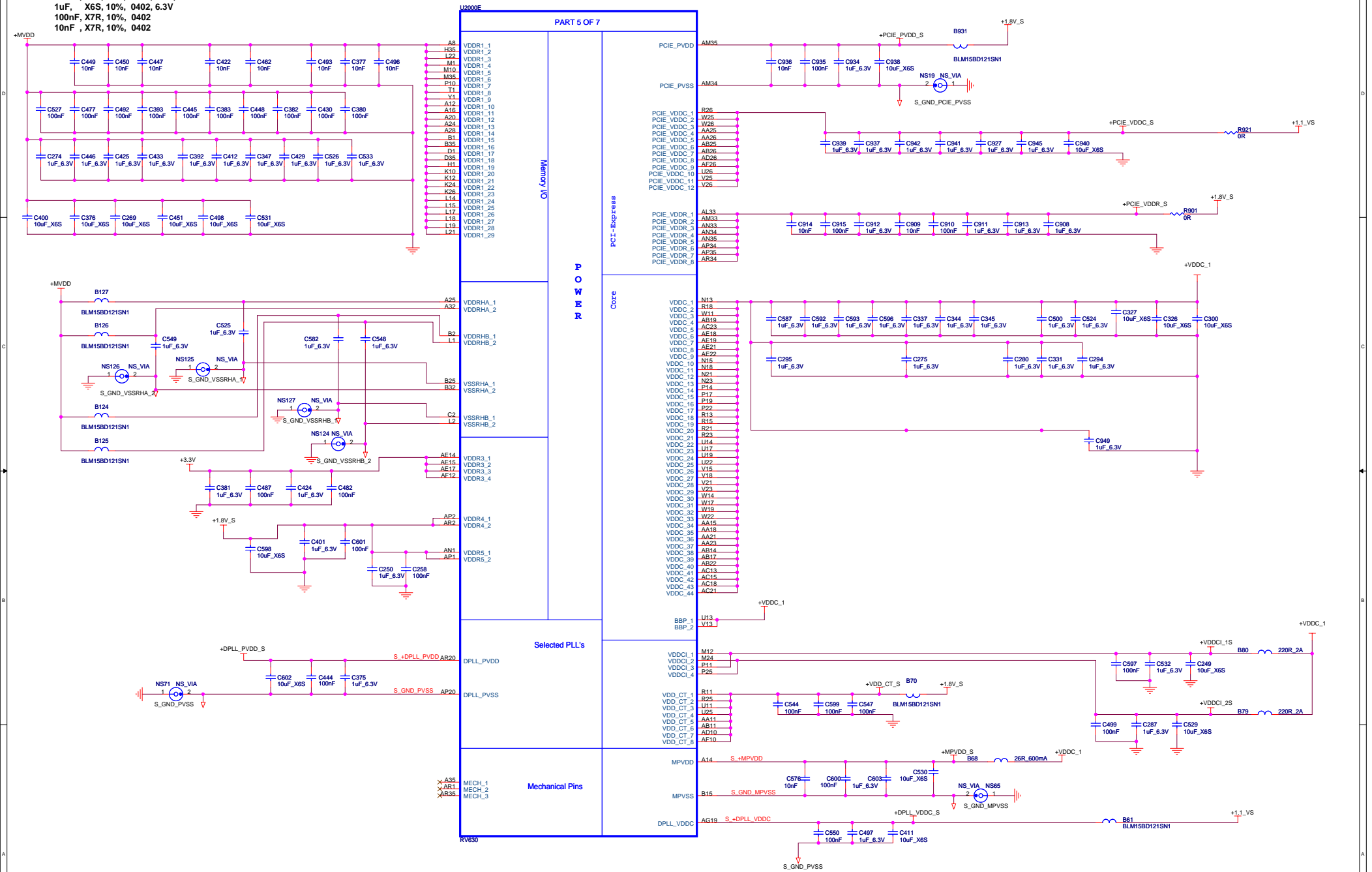
Title PEX8547+RV630 GDDR3 VGA CARD

Size	Document Number	DUAL GPU
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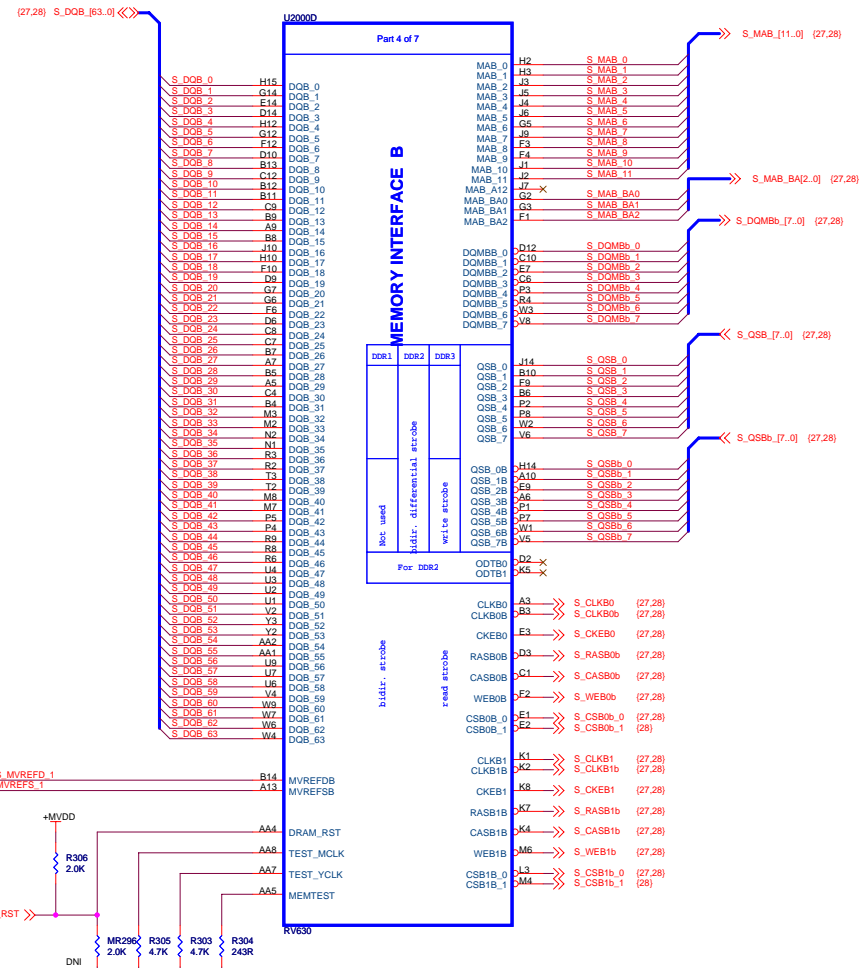
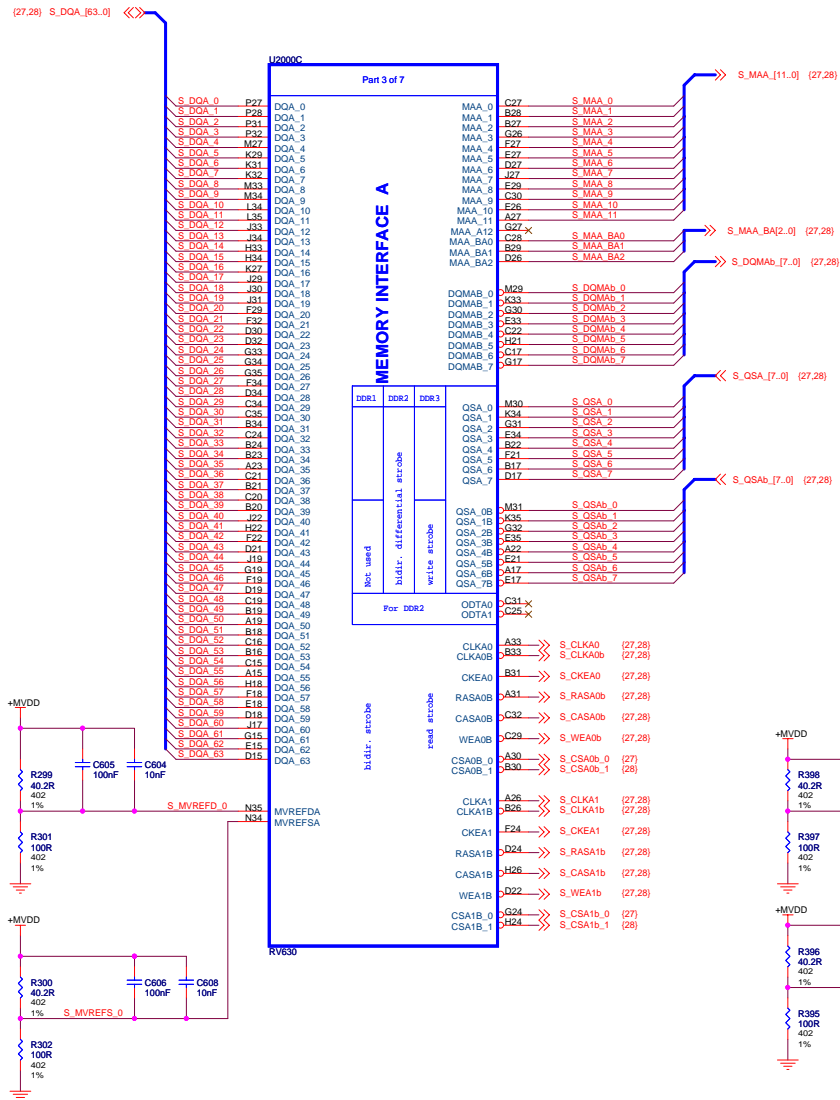
C	
Date:	Tuesday, May 15, 2007

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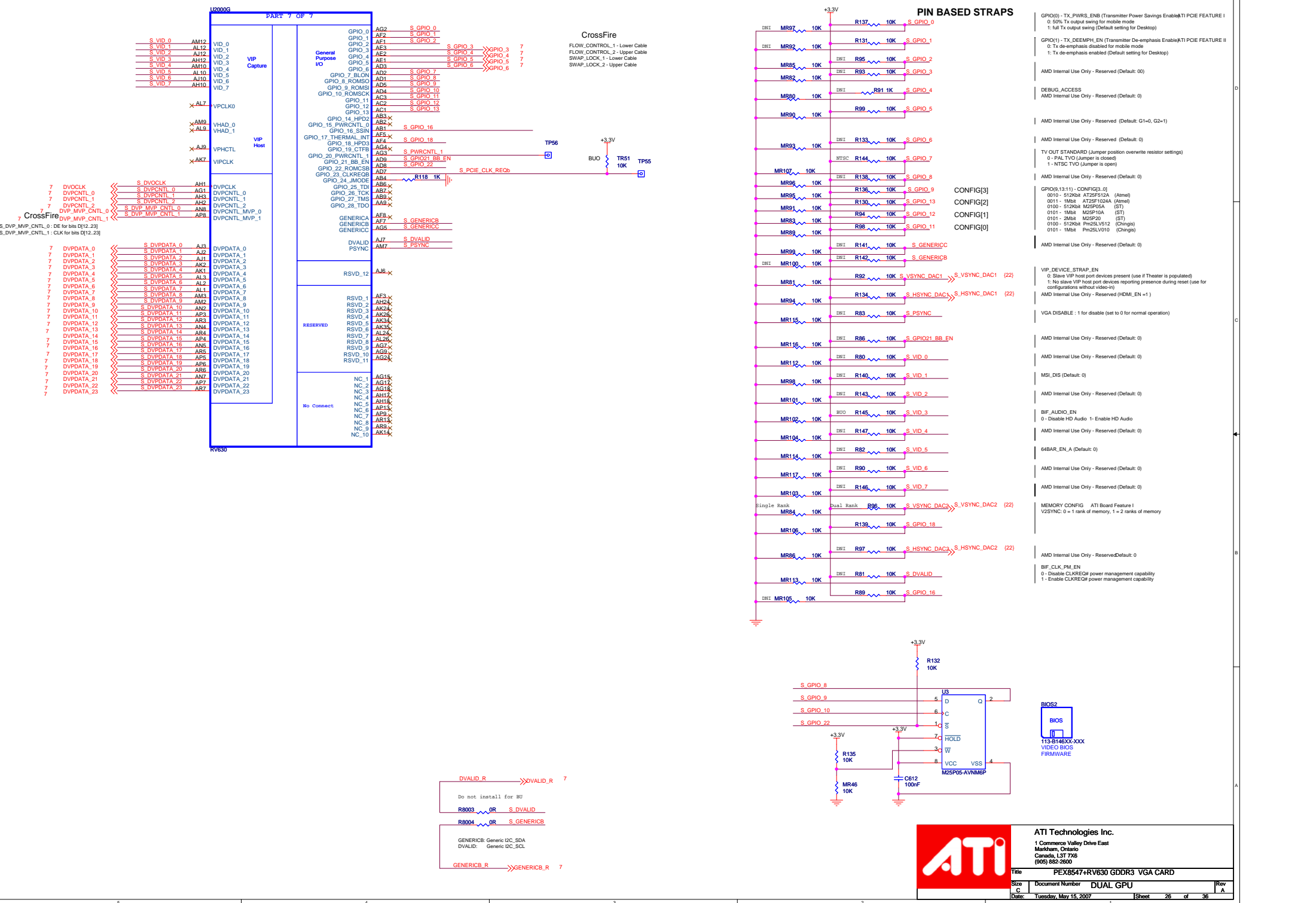
Recommended caps:
(see BOM for qualified values/vendors)
10uF , X6S, 10%, 0805, 6.3V, 1.4MM MAX THICK
1uF , X6S, 10%, 0402, 6.3V
100nF, X7R, 10%, 0402
10nF , X7R, 10%, 0402



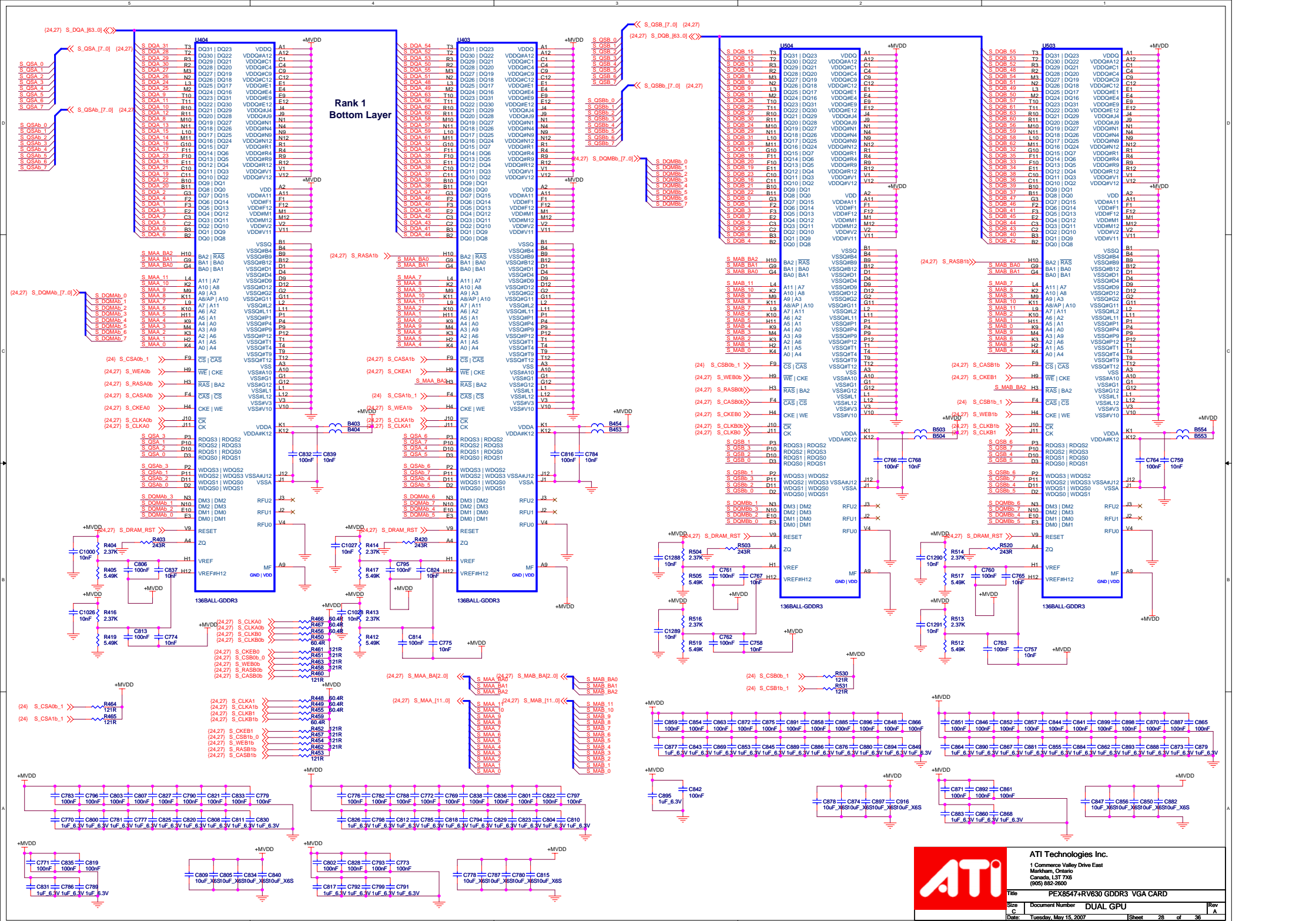
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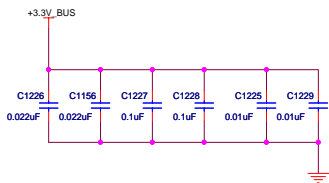


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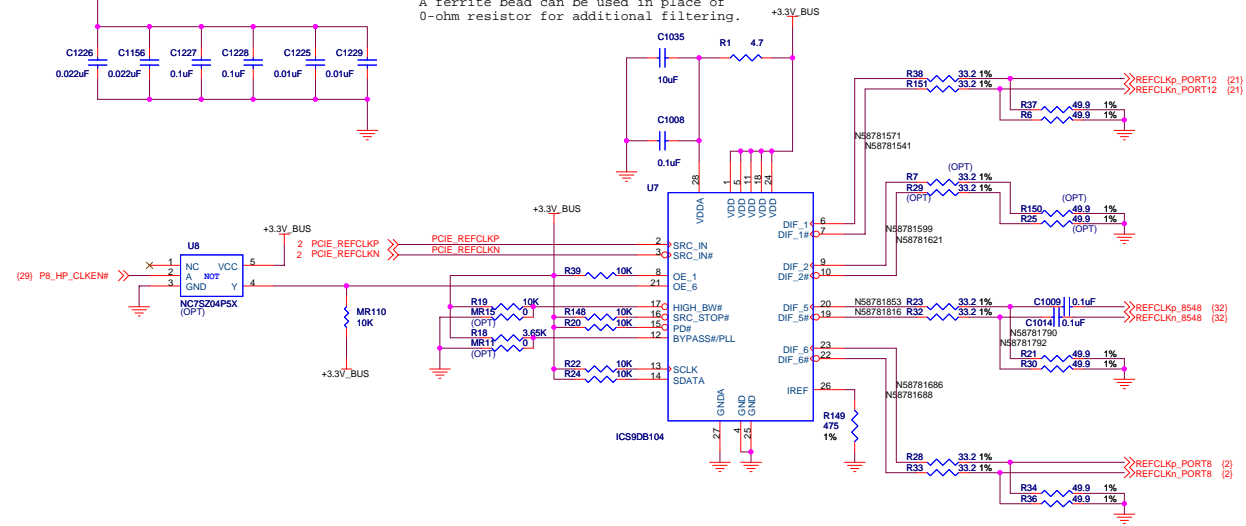








A ferrite bead can be used in place of 0-ohm resistor for additional filtering.



U2001B

PLX PEX8547AA-PBGA736

STATION 1

STATION 1 LANES

STATION 1
PORT STATUS

PEX_PETp16	B6	GFXTp15_PERp15	<<GFXTp15_PERp15	1
PEX_PETp16	A5	GFXTp15_PERn15	<<GFXTp15_PERn15	1
PEX_PERp16	D6	PETp15_GFXRp15	<<PETp15_GFXRp15	1
PEX_PERp16	E5	PETn15_GFXRn15	<<PETn15_GFXRn15	1
PEX_PETp17	B7	GFXTp14_PERp14	<<GFXTp14_PERp14	1
PEX_PETp17	A7	GFXTp14_PERn14	<<GFXTp14_PERn14	1
PEX_PERp17	D7	PETp14_GFXRp14	<<PETp14_GFXRp14	1
PEX_PERp17	E7	PETn14_GFXRn14	<<PETn14_GFXRn14	1
PEX_PETp18	B9	GFXTp13_PERp13	<<GFXTp13_PERp13	1
PEX_PETp18	A9	GFXTp13_PERn13	<<GFXTp13_PERn13	1
PEX_PERp18	D9	PETp13_GFXRp13	<<PETp13_GFXRp13	1
PEX_PERp18	E9	PETn13_GFXRn13	<<PETn13_GFXRn13	1
PEX_PETp19	B11	GFXTp12_PERp12	<<GFXTp12_PERp12	1
PEX_PETp19	A11	GFXTp12_PERn12	<<GFXTp12_PERn12	1
PEX_PERp19	D11	PETp12_GFXRp12	<<PETp12_GFXRp12	1
PEX_PERp19	E11	PETn12_GFXRn12	<<PETn12_GFXRn12	1
PEX_PETp20	B13	GFXTp11_PERp11	<<GFXTp11_PERp11	1
PEX_PETp20	A13	GFXTp11_PERn11	<<GFXTp11_PERn11	1
PEX_PERp20	D13	PETp11_GFXRp11	<<PETp11_GFXRp11	1
PEX_PERp20	E13	PETn11_GFXRn11	<<PETn11_GFXRn11	1
PEX_PETp21	B15	GFXTp10_PERp10	<<GFXTp10_PERp10	1
PEX_PETp21	A15	GFXTp10_PERn10	<<GFXTp10_PERn10	1
PEX_PERp21	D15	PETp10_GFXRp10	<<PETp10_GFXRp10	1
PEX_PERp21	E15	PETn10_GFXRn10	<<PETn10_GFXRn10	1
PEX_PETp22	B17	GFXTp9_PERp9	<<GFXTp9_PERp9	1
PEX_PETp22	A17	GFXTp9_PERn9	<<GFXTp9_PERn9	1
PEX_PERp22	D17	PETp9_GFXRp9	<<PETp9_GFXRp9	1
PEX_PERp22	E17	PETn9_GFXRn9	<<PETn9_GFXRn9	1
PEX_PETp23	B19	GFXTp8_PERp8	<<GFXTp8_PERp8	1
PEX_PETp23	A19	GFXTp8_PERn8	<<GFXTp8_PERn8	1
PEX_PERp23	D19	PETp8_GFXRp8	<<PETp8_GFXRp8	1
PEX_PERp23	E19	PETn8_GFXRn8	<<PETn8_GFXRn8	1
PEX_PETp24	B21	GFXTp7_PERp7	<<GFXTp7_PERp7	1
PEX_PETp24	A21	GFXTp7_PERn7	<<GFXTp7_PERn7	1
PEX_PERp24	D21	PETp7_GFXRp7	<<PETp7_GFXRp7	1
PEX_PERp24	E21	PETn7_GFXRn7	<<PETn7_GFXRn7	1
PEX_PETp25	B23	GFXTp6_PERp6	<<GFXTp6_PERp6	1
PEX_PETp25	A23	GFXTp6_PERn6	<<GFXTp6_PERn6	1
PEX_PERp25	D23	PETp6_GFXRp6	<<PETp6_GFXRp6	1
PEX_PERp25	E23	PETn6_GFXRn6	<<PETn6_GFXRn6	1
PEX_PETp26	B25	GFXTp5_PERp5	<<GFXTp5_PERp5	1
PEX_PETp26	A25	GFXTp5_PERn5	<<GFXTp5_PERn5	1
PEX_PERp26	D25	PETp5_GFXRp5	<<PETp5_GFXRp5	1
PEX_PERp26	E25	PETn5_GFXRn5	<<PETn5_GFXRn5	1
PEX_PETp27	B27	GFXTp4_PERp4	<<GFXTp4_PERp4	1
PEX_PETp27	A27	GFXTp4_PERn4	<<GFXTp4_PERn4	1
PEX_PERp27	D27	PETp4_GFXRp4	<<PETp4_GFXRp4	1
PEX_PERp27	E27	PETn4_GFXRn4	<<PETn4_GFXRn4	1
PEX_PETp28	B29	GFXTp3_PERp3	<<GFXTp3_PERp3	1
PEX_PETp28	A29	GFXTp3_PERn3	<<GFXTp3_PERn3	1
PEX_PERp28	D29	PETp3_GFXRp3	<<PETp3_GFXRp3	1
PEX_PERp28	E29	PETn3_GFXRn3	<<PETn3_GFXRn3	1
PEX_PETp29	B31	GFXTp2_PERp2	<<GFXTp2_PERp2	1
PEX_PETp29	A31	GFXTp2_PERn2	<<GFXTp2_PERn2	1
PEX_PERp29	D31	PETp2_GFXRp2	<<PETp2_GFXRp2	1
PEX_PERp29	E31	PETn2_GFXRn2	<<PETn2_GFXRn2	1
PEX_PETp30	B33	GFXTp1_PERp1	<<GFXTp1_PERp1	1
PEX_PETp30	A33	GFXTp1_PERn1	<<GFXTp1_PERn1	1
PEX_PERp30	D33	PETp1_GFXRp1	<<PETp1_GFXRp1	1
PEX_PERp30	E33	PETn1_GFXRn1	<<PETn1_GFXRn1	1
PEX_PETp31	B35	GFXTp0_PERp0	<<GFXTp0_PERp0	1
PEX_PETp31	A35	GFXTp0_PERn0	<<GFXTp0_PERn0	1
PEX_PERp31	D35	PETp0_GFXRp0	<<PETp0_GFXRp0	1
PEX_PERp31	E35	PETn0_GFXRn0	<<PETn0_GFXRn0	1

PEX8547-AA25BI



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Title PEX8547+RV630 GDDR3 VGA CARD

Size C Document Number DUAL GPU

Date: Tuesday, May 15, 2007

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VDD33

R282
390L36C
X L35C
X L33C
NCPEX_PORT_GOOD[12]
PORT
STATUS

U2001C

PLX PEX8547AA-PBGA736

STATION 2

STATION 2 LANES

PEX8547-AA25BI

PEX_PET[32]	T35	P8_Tp0		C1077	0.1uF	PETp0_GFXRp0_M	>>>	PETp0_GFXRp0_M	2	
PEX_PET[32]	T36	P8_Tn0				PETn0_GFXRn0_M	>>>	PETn0_GFXRn0_M	2	
PEX_PER[32]	T32	P8_PERp0	C1127	0.1uF	C1073	0.1uF	GFXTp0_PERp0_M	>>>	GFXTp0_PERp0_M	2
PEX_PER[32]	T32	P8_ERn0	C1128	0.1uF		GFXTn0_PERn0_M	>>>	GFXTn0_PERn0_M	2	
PEX_PET[33]	V35	P8_Tp1		C1074	0.1uF	PETp1_GFXRp1_M	>>>	PETp1_GFXRp1_M	2	
PEX_PET[33]	V36	P8_Tn1				PETn1_GFXRn1_M	>>>	PETn1_GFXRn1_M	2	
PEX_PER[33]	V32	P8_PERp1	C1135	0.1uF	C1075	0.1uF	GFXTp1_PERp1_M	>>>	GFXTp1_PERp1_M	2
PEX_PER[33]	V32	P8_PERn1	C1157	0.1uF		GFXTn1_PERn1_M	>>>	GFXTn1_PERn1_M	2	
PEX_PET[34]	Y35	P8_Tp2		C1037	0.1uF	PETp2_GFXRp2_M	>>>	PETp2_GFXRp2_M	2	
PEX_PET[34]	Y36	P8_Tn2				PETn2_GFXRn2_M	>>>	PETn2_GFXRn2_M	2	
PEX_PER[34]	Y32	P8_PERp2	C1158	0.1uF	C1038	0.1uF	GFXTp2_PERp2_M	>>>	GFXTp2_PERp2_M	2
PEX_PER[34]	Y32	P8_PERn2	C1159	0.1uF		GFXTn2_PERn2_M	>>>	GFXTn2_PERn2_M	2	
PEX_PET[35]	AB35	P8_Tp3		C1039	0.1uF	PETp3_GFXRp3_M	>>>	PETp3_GFXRp3_M	2	
PEX_PET[35]	AB36	P8_Tn3				PETn3_GFXRn3_M	>>>	PETn3_GFXRn3_M	2	
PEX_PER[35]	AB32	P8_PERp3	C1160	0.1uF	C1046	0.1uF	GFXTp3_PERp3_M	>>>	GFXTp3_PERp3_M	2
PEX_PER[35]	AB32	P8_PERn3	C1161	0.1uF		GFXTn3_PERn3_M	>>>	GFXTn3_PERn3_M	2	
PEX_PET[36]	AD35	P8_Tp4		C1064	0.1uF	PETp4_GFXRp4_M	>>>	PETp4_GFXRp4_M	2	
PEX_PET[36]	AD36	P8_Tn4				PETn4_GFXRn4_M	>>>	PETn4_GFXRn4_M	2	
PEX_PER[36]	AD32	P8_PERp4	C1162	0.1uF	C1065	0.1uF	GFXTp4_PERp4_M	>>>	GFXTp4_PERp4_M	2
PEX_PER[36]	AD32	P8_PERn4	C1163	0.1uF		GFXTn4_PERn4_M	>>>	GFXTn4_PERn4_M	2	
PEX_PET[37]	AE35	P8_Tp5		C1068	0.1uF	PETp5_GFXRp5_M	>>>	PETp5_GFXRp5_M	2	
PEX_PET[37]	AE36	P8_Tn5				PETn5_GFXRn5_M	>>>	PETn5_GFXRn5_M	2	
PEX_PER[37]	AE32	P8_PERp5	C1164	0.1uF	C1067	0.1uF	GFXTp5_PERp5_M	>>>	GFXTp5_PERp5_M	2
PEX_PER[37]	AE32	P8_PERn5	C1165	0.1uF		GFXTn5_PERn5_M	>>>	GFXTn5_PERn5_M	2	
PEX_PET[38]	AH35	P8_Tp6		C1068	0.1uF	PETp6_GFXRp6_M	>>>	PETp6_GFXRp6_M	2	
PEX_PET[38]	AH36	P8_Tn6				PETn6_GFXRn6_M	>>>	PETn6_GFXRn6_M	2	
PEX_PER[38]	AH32	P8_PERp6	C1166	0.1uF	C1069	0.1uF	GFXTp6_PERp6_M	>>>	GFXTp6_PERp6_M	2
PEX_PER[38]	AH32	P8_PERn6	C1167	0.1uF		GFXTn6_PERn6_M	>>>	GFXTn6_PERn6_M	2	
PEX_PET[39]	AK35	P8_Tp7		C1079	0.1uF	PETp7_GFXRp7_M	>>>	PETp7_GFXRp7_M	2	
PEX_PET[39]	AK36	P8_Tn7				PETn7_GFXRn7_M	>>>	PETn7_GFXRn7_M	2	
PEX_PER[39]	AK32	P8_PERp7	C1230	0.1uF	C1071	0.1uF	GFXTp7_PERp7_M	>>>	GFXTp7_PERp7_M	2
PEX_PER[39]	AK32	P8_PERn7	C1231	0.1uF		GFXTn7_PERn7_M	>>>	GFXTn7_PERn7_M	2	
PEX_PET[40]	AR34	P8_Tp8		C1062	0.1uF	PETp8_GFXRp8_M	>>>	PETp8_GFXRp8_M	2	
PEX_PET[40]	AT34	P8_Tn8				PETn8_GFXRn8_M	>>>	PETn8_GFXRn8_M	2	
PEX_PER[40]	AM34	P8_PERp8	C1232	0.1uF	C1060	0.1uF	GFXTp8_PERp8_M	>>>	GFXTp8_PERp8_M	2
PEX_PER[40]	AM34	P8_PERn8	C1233	0.1uF		GFXTn8_PERn8_M	>>>	GFXTn8_PERn8_M	2	
PEX_PET[41]	AR32	P8_Tp9		C1061	0.1uF	PETp9_GFXRp9_M	>>>	PETp9_GFXRp9_M	2	
PEX_PET[41]	AT32	P8_Tn9				PETn9_GFXRn9_M	>>>	PETn9_GFXRn9_M	2	
PEX_PER[41]	AM32	P8_PERp9	C1234	0.1uF	C1063	0.1uF	GFXTp9_PERp9_M	>>>	GFXTp9_PERp9_M	2
PEX_PER[41]	AM32	P8_PERn9	C1235	0.1uF		GFXTn9_PERn9_M	>>>	GFXTn9_PERn9_M	2	
PEX_PET[42]	AR30	P8_Tp10		C1047	0.1uF	PETp10_GFXRp10_M	>>>	PETp10_GFXRp10_M	2	
PEX_PET[42]	AT30	P8_Tn10				PETn10_GFXRn10_M	>>>	PETn10_GFXRn10_M	2	
PEX_PER[42]	AM30	P8_PERp10	C1236	0.1uF	C1048	0.1uF	GFXTp10_PERp10_M	>>>	GFXTp10_PERp10_M	2
PEX_PER[42]	AM30	P8_PERn10	C1237	0.1uF		GFXTn10_PERn10_M	>>>	GFXTn10_PERn10_M	2	
PEX_PET[43]	AR28	P8_Tp11		C1049	0.1uF	PETp11_GFXRp11_M	>>>	PETp11_GFXRp11_M	2	
PEX_PET[43]	AT28	P8_Tn11				PETn11_GFXRn11_M	>>>	PETn11_GFXRn11_M	2	
PEX_PER[43]	AM28	P8_PERp11	C1238	0.1uF	C1051	0.1uF	GFXTp11_PERp11_M	>>>	GFXTp11_PERp11_M	2
PEX_PER[43]	AM28	P8_PERn11	C1239	0.1uF		GFXTn11_PERn11_M	>>>	GFXTn11_PERn11_M	2	
PEX_PET[44]	AR26	P8_Tp12		C1052	0.1uF	PETp12_GFXRp12_M	>>>	PETp12_GFXRp12_M	2	
PEX_PET[44]	AT26	P8_Tn12				PETn12_GFXRn12_M	>>>	PETn12_GFXRn12_M	2	
PEX_PER[44]	AM26	P8_PERp12	C1240	0.1uF	C1053	0.1uF	GFXTp12_PERp12_M	>>>	GFXTp12_PERp12_M	2
PEX_PER[44]	AM26	P8_PERn12	C1241	0.1uF		GFXTn12_PERn12_M	>>>	GFXTn12_PERn12_M	2	
PEX_PET[45]	AR24	P8_Tp13		C1054	0.1uF	PETp13_GFXRp13_M	>>>	PETp13_GFXRp13_M	2	
PEX_PET[45]	AT24	P8_Tn13				PETn13_GFXRn13_M	>>>	PETn13_GFXRn13_M	2	
PEX_PER[45]	AM24	P8_PERp13	C1242	0.1uF	C1055	0.1uF	GFXTp13_PERp13_M	>>>	GFXTp13_PERp13_M	2
PEX_PER[45]	AM24	P8_PERn13	C1243	0.1uF		GFXTn13_PERn13_M	>>>	GFXTn13_PERn13_M	2	
PEX_PET[46]	AR22	P8_Tp14		C1056	0.1uF	PETp14_GFXRp14_M	>>>	PETp14_GFXRp14_M	2	
PEX_PET[46]	AT22	P8_Tn14				PETn14_GFXRn14_M	>>>	PETn14_GFXRn14_M	2	
PEX_PER[46]	AM22	P8_PERp14	C1244	0.1uF	C1057	0.1uF	GFXTp14_PERp14_M	>>>	GFXTp14_PERp14_M	2
PEX_PER[46]	AM22	P8_PERn14	C1245	0.1uF		GFXTn14_PERn14_M	>>>	GFXTn14_PERn14_M	2	
PEX_PET[47]	AR20	P8_Tp15		C1059	0.1uF	PETp15_GFXRp15_M	>>>	PETp15_GFXRp15_M	2	
PEX_PET[47]	AT20	P8_Tn15				PETn15_GFXRn15_M	>>>	PETn15_GFXRn15_M	2	
PEX_PER[47]	AM20	P8_PERp15	C1246	0.1uF	C1059	0.1uF	GFXTp15_PERp15_M	>>>	GFXTp15_PERp15_M	2
PEX_PER[47]	AM20	P8_PERn15	C1247	0.1uF		GFXTn15_PERn15_M	>>>	GFXTn15_PERn15_M	2	



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STATION 0

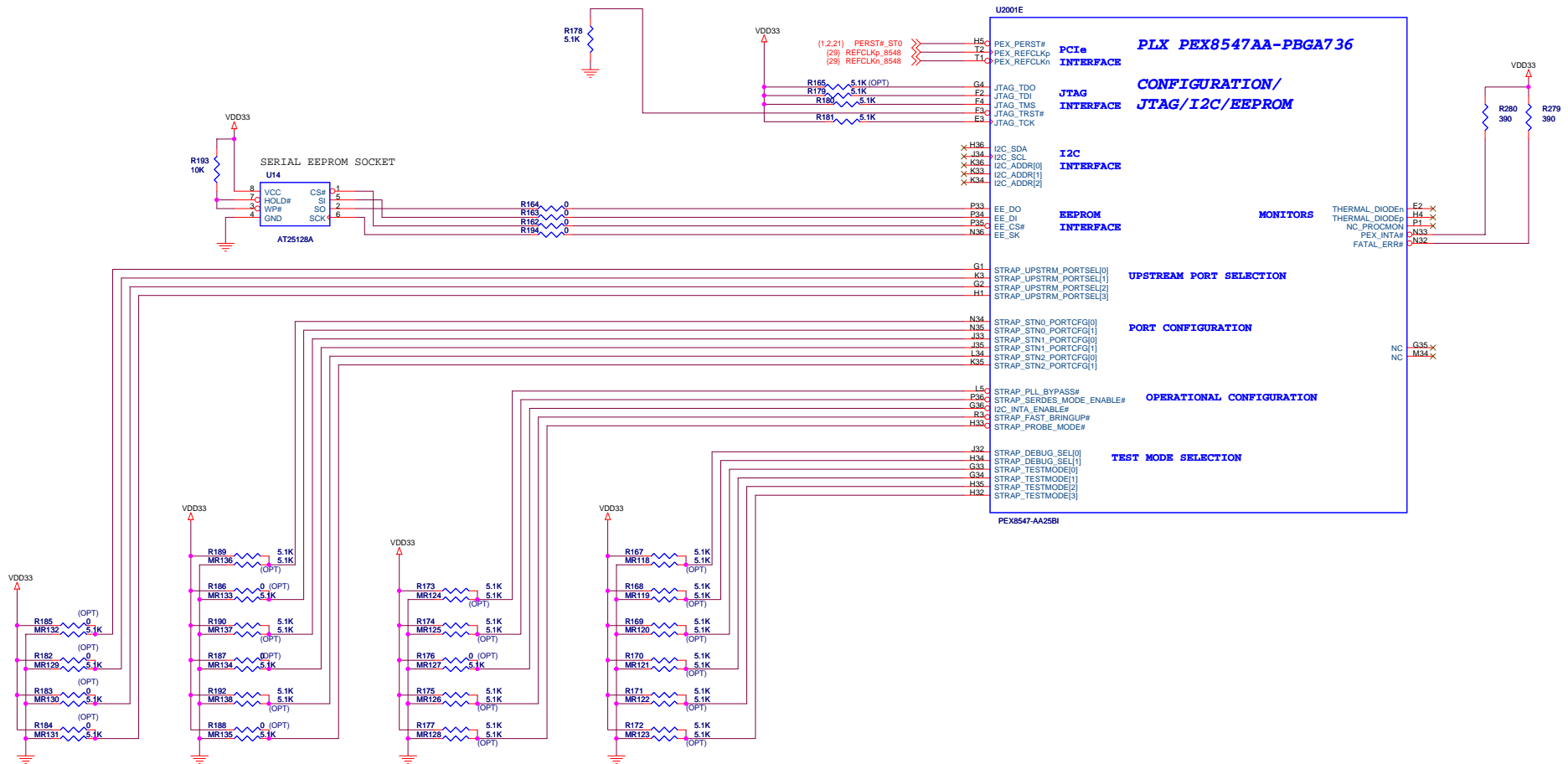
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STATION 0 PORT STATUS

PEX8547-AA25BI

PEX_PET[0]	V2	P12 Tp15	C1079	0.1uF	S_PETp15_GFXRp15	PETp15_GFXRp15	21
PEX_PET[0]	V4	P12 Tn15	C1276	0.1uF	S_PETn15_GFXRn15	PETrn15_GFXRn15	21
PEX_PER[0]	V4	P12 PERp15	C1279	0.1uF	S_GFXTp15_PERp15	PETrn15_GFXRn15	21
PEX_PER[0]	V5	P12 PERn15	C1279	0.1uF	S_GFXTn15_PERn15	PETrn15_GFXRn15	21
PEX_PET[1]	V2	P12 Tp14	C1111	0.1uF	S_PETp14_GFXRp14	PETp14_GFXRp14	21
PEX_PET[1]	V4	P12 Tn14	C1276	0.1uF	S_PETn14_GFXRn14	PETrn14_GFXRn14	21
PEX_PET[1]	V5	P12 PERp14	C1277	0.1uF	S_GFXTp14_PERp14	PETrn14_GFXRn14	21
PEX_PET[1]	V5	P12 PERn14	C1277	0.1uF	S_GFXTn14_PERn14	PETrn14_GFXRn14	21
PEX_PET[2]	AB2	P12 Tp13	C1110	0.1uF	S_PETp13_GFXRp13	PETp13_GFXRp13	21
PEX_PET[2]	AB1	P12 Tn13	C1274	0.1uF	S_PETn13_GFXRn13	PETrn13_GFXRn13	21
PEX_PET[2]	AB4	P12 PERp13	C1274	0.1uF	S_GFXTp13_PERp13	PETrn13_GFXRn13	21
PEX_PET[2]	AB5	P12 PERn13	C1275	0.1uF	S_GFXTn13_PERn13	PETrn13_GFXRn13	21
PEX_PET[3]	AD2	P12 Tp12	C1107	0.1uF	S_PETp12_GFXRp12	PETp12_GFXRp12	21
PEX_PET[3]	AD1	P12 Tn12	C1272	0.1uF	S_PETn12_GFXRn12	PETrn12_GFXRn12	21
PEX_PET[3]	AD4	P12 PERp12	C1272	0.1uF	S_GFXTp12_PERp12	PETrn12_GFXRn12	21
PEX_PET[3]	AD5	P12 PERn12	C1273	0.1uF	S_GFXTn12_PERn12	PETrn12_GFXRn12	21
PEX_PET[4]	AE2	P12 Tp11	C1105	0.1uF	S_PETp11_GFXRp11	PETp11_GFXRp11	21
PEX_PET[4]	AE1	P12 Tn11	C1270	0.1uF	S_PETn11_GFXRn11	PETrn11_GFXRn11	21
PEX_PET[4]	AE4	P12 PERp11	C1270	0.1uF	S_GFXTp11_PERp11	PETrn11_GFXRn11	21
PEX_PET[4]	AE5	P12 PERn11	C1271	0.1uF	S_GFXTn11_PERn11	PETrn11_GFXRn11	21
PEX_PET[5]	AH2	P12 Tp10	C1103	0.1uF	S_PETp10_GFXRp10	PETp10_GFXRp10	21
PEX_PET[5]	AH1	P12 Tn10	C1268	0.1uF	S_PETn10_GFXRn10	PETrn10_GFXRn10	21
PEX_PET[5]	AH4	P12 PERp10	C1268	0.1uF	S_GFXTp10_PERp10	PETrn10_GFXRn10	21
PEX_PET[5]	AH5	P12 PERn10	C1269	0.1uF	S_GFXTn10_PERn10	PETrn10_GFXRn10	21
PEX_PET[6]	AK2	P12 Tp9	C1080	0.1uF	S_PETp9_GFXRp9	PETp9_GFXRp9	21
PEX_PET[6]	AK1	P12 Tn9	C1266	0.1uF	S_PETn9_GFXRn9	PETrn9_GFXRn9	21
PEX_PET[6]	AK4	P12 PERp9	C1267	0.1uF	S_GFXTp9_PERp9	PETrn9_GFXRn9	21
PEX_PET[6]	AK5	P12 PERn9	C1267	0.1uF	S_GFXTn9_PERn9	PETrn9_GFXRn9	21
PEX_PET[7]	AR2	P12 Tp8	C1082	0.1uF	S_PETp8_GFXRp8	PETp8_GFXRp8	21
PEX_PET[7]	AT2	P12 Tn8	C1264	0.1uF	S_PETn8_GFXRn8	PETrn8_GFXRn8	21
PEX_PET[7]	AN2	P12 PERp8	C1265	0.1uF	S_GFXTp8_PERp8	PETrn8_GFXRn8	21
PEX_PET[7]	AM2	P12 PERn8	C1265	0.1uF	S_GFXTn8_PERn8	PETrn8_GFXRn8	21
PEX_PET[8]	AR4	P12 Tp7	C1090	0.1uF	S_PETp7_GFXRp7	PETp7_GFXRp7	21
PEX_PET[8]	AT4	P12 Tn7	C1262	0.1uF	S_PETn7_GFXRn7	PETrn7_GFXRn7	21
PEX_PET[8]	AN4	P12 PERp7	C1262	0.1uF	S_GFXTp7_PERp7	PETrn7_GFXRn7	21
PEX_PET[8]	AM4	P12 PERn7	C1263	0.1uF	S_GFXTn7_PERn7	PETrn7_GFXRn7	21
PEX_PET[9]	AR6	P12 Tp6	C1088	0.1uF	S_PETp6_GFXRp6	PETp6_GFXRp6	21
PEX_PET[9]	AT6	P12 Tn6	C1260	0.1uF	S_PETn6_GFXRn6	PETrn6_GFXRn6	21
PEX_PET[9]	AN6	P12 PERp6	C1261	0.1uF	S_GFXTp6_PERp6	PETrn6_GFXRn6	21
PEX_PET[9]	AM6	P12 PERn6	C1261	0.1uF	S_GFXTn6_PERn6	PETrn6_GFXRn6	21
PEX_PET[10]	AR8	P12 Tp5	C1086	0.1uF	S_PETp5_GFXRp5	PETp5_GFXRp5	21
PEX_PET[10]	AT8	P12 Tn5	C1258	0.1uF	S_PETn5_GFXRn5	PETrn5_GFXRn5	21
PEX_PET[10]	AN8	P12 PERp5	C1259	0.1uF	S_GFXTp5_PERp5	PETrn5_GFXRn5	21
PEX_PET[10]	AM8	P12 PERn5	C1259	0.1uF	S_GFXTn5_PERn5	PETrn5_GFXRn5	21
PEX_PET[11]	AR10	P12 Tp4	C1084	0.1uF	S_PETp4_GFXRp4	PETp4_GFXRp4	21
PEX_PET[11]	AT10	P12 Tn4	C1256	0.1uF	S_PETn4_GFXRn4	PETrn4_GFXRn4	21
PEX_PET[11]	AN10	P12 PERp4	C1257	0.1uF	S_GFXTp4_PERp4	PETrn4_GFXRn4	21
PEX_PET[11]	AM10	P12 PERn4	C1257	0.1uF	S_GFXTn4_PERn4	PETrn4_GFXRn4	21
PEX_PET[12]	AR12	P12 Tp3	C1101	0.1uF	S_PETp3_GFXRp3	PETp3_GFXRp3	21
PEX_PET[12]	AT12	P12 Tn3	C1254	0.1uF	S_PETn3_GFXRn3	PETrn3_GFXRn3	21
PEX_PET[12]	AN12	P12 PERp3	C1254	0.1uF	S_GFXTp3_PERp3	PETrn3_GFXRn3	21
PEX_PET[12]	AM12	P12 PERn3	C1255	0.1uF	S_GFXTn3_PERn3	PETrn3_GFXRn3	21
PEX_PET[13]	AR14	P12 Tp2	C1099	0.1uF	S_PETp2_GFXRp2	PETp2_GFXRp2	21
PEX_PET[13]	AT14	P12 Tn2	C1252	0.1uF	S_PETn2_GFXRn2	PETrn2_GFXRn2	21
PEX_PET[13]	AN14	P12 PERp2	C1253	0.1uF	S_GFXTp2_PERp2	PETrn2_GFXRn2	21
PEX_PET[13]	AM14	P12 PERn2	C1253	0.1uF	S_GFXTn2_PERn2	PETrn2_GFXRn2	21
PEX_PET[14]	AR16	P12 Tp1	C1094	0.1uF	S_PETp1_GFXRp1	PETp1_GFXRp1	21
PEX_PET[14]	AT16	P12 Tn1	C1250	0.1uF	S_PETn1_GFXRn1	PETrn1_GFXRn1	21
PEX_PET[14]	AN16	P12 PERp1	C1251	0.1uF	S_GFXTp1_PERp1	PETrn1_GFXRn1	21
PEX_PET[14]	AM16	P12 PERn1	C1251	0.1uF	S_GFXTn1_PERn1	PETrn1_GFXRn1	21
PEX_PET[15]	AR18	P12 Tp0	C1097	0.1uF	S_PETp0_GFXRp0	PETp0_GFXRp0	21
PEX_PET[15]	AT18	P12 Tn0	C1248	0.1uF	S_PETn0_GFXRn0	PETrn0_GFXRn0	21
PEX_PET[15]	AN18	P12 PERp0	C1248	0.1uF	S_GFXTp0_PERp0	PETrn0_GFXRn0	21
PEX_PET[15]	AM18	P12 PERn0	C1248	0.1uF	S_GFXTn0_PERn0	PETrn0_GFXRn0	21





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CAP to be placed near ball

R195 0

A ferrite bead can be used in place of 0-ohm resistor for additional filtering.



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POWER

PEX8547-AA25BI



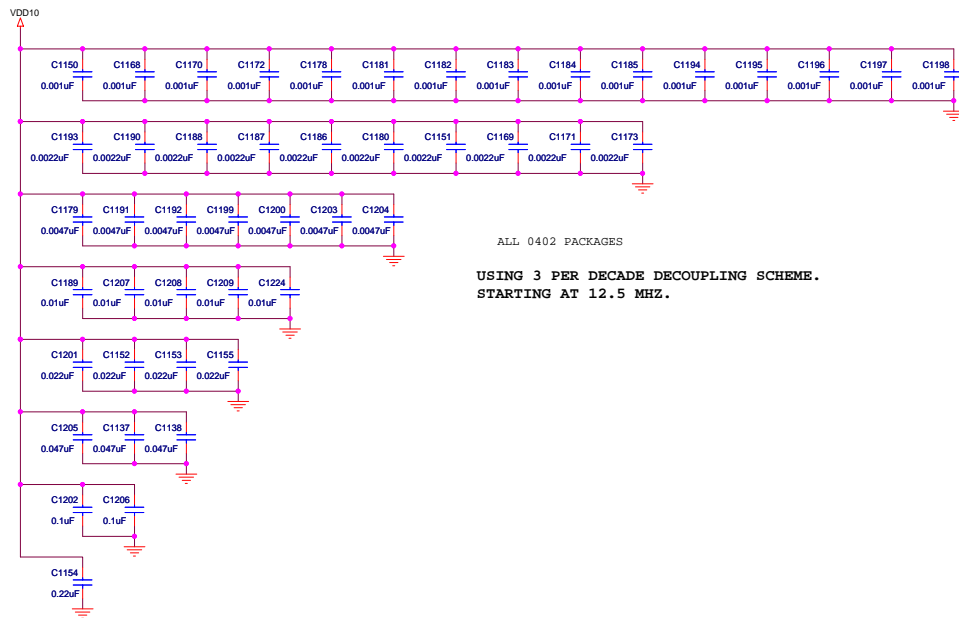
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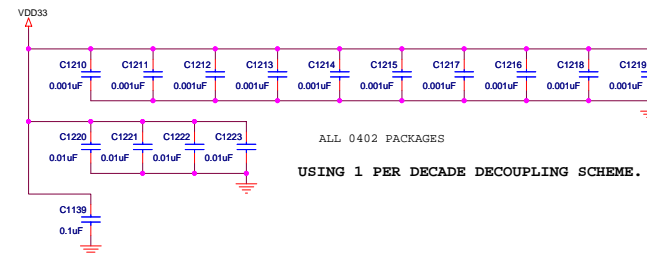
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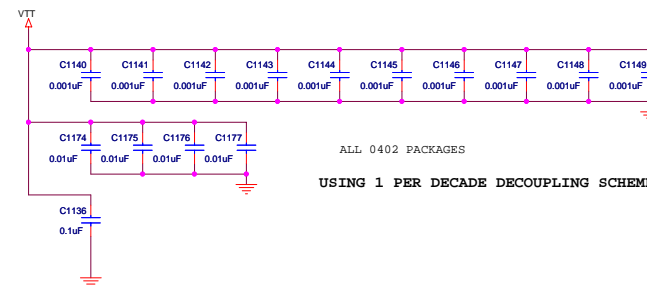
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