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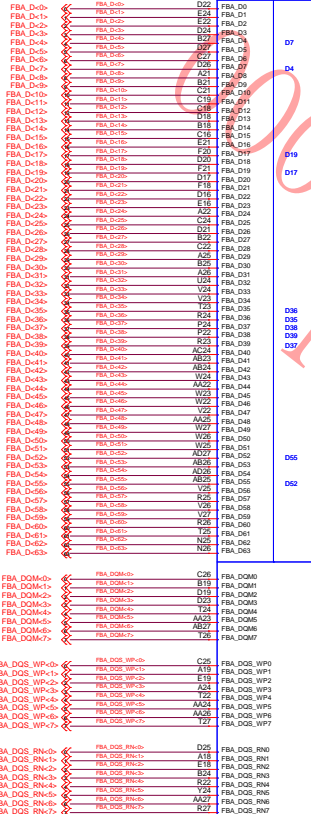
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
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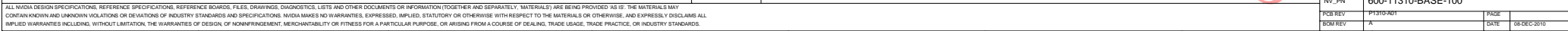
NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA			
NV_PN	600-11310-BASE-100		
PCB REV	P1310-AD1	PAGE	
BOM REV	A	DATE	108-DEC-2010

Frame Buffer Interface

2112 FRAME BUFFER



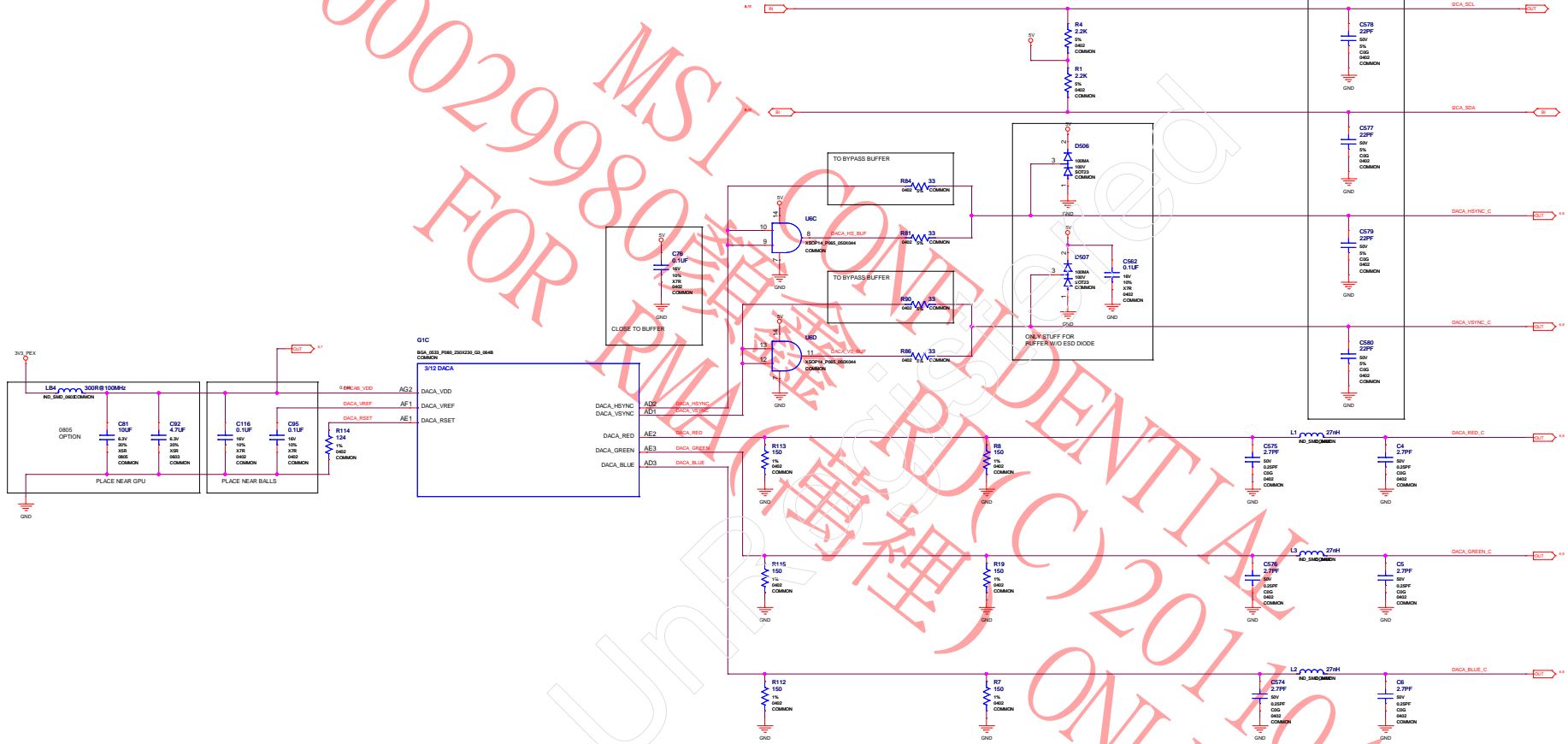
Net Name		MIN_WIDTH
P1	FB_VREF_A	120M
P1	FB_VREF_B	120M



DAC A VGA

Net Name		CRITICAL	IMPEDANCE
R1	DACA_RED	1	500OHM
R2	DACA_GREEN	1	500OHM
R3	DACA_BLUE	1	500OHM
R4	DACA_RED_C	1	500OHM
R5	DACA_GREEN_C	1	500OHM
R6	DACA_BLUE_C	1	500OHM
R7	DACA_HSYNE	2	500OHM
R8	DACA_VSYNE	2	500OHM
R9	DACA_HS_BUF	2	500OHM
R10	DACA_VS_BUF	2	500OHM
R11	DACA_HSYNE_C	2	500OHM
R12	DACA_VSYNE_C	2	500OHM

Net Name	MIN_WIDTH	MAX_CURRENT
R1	DACA_VDD	1.00A 0.16A



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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	DAC A VGA

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BOM REV	A
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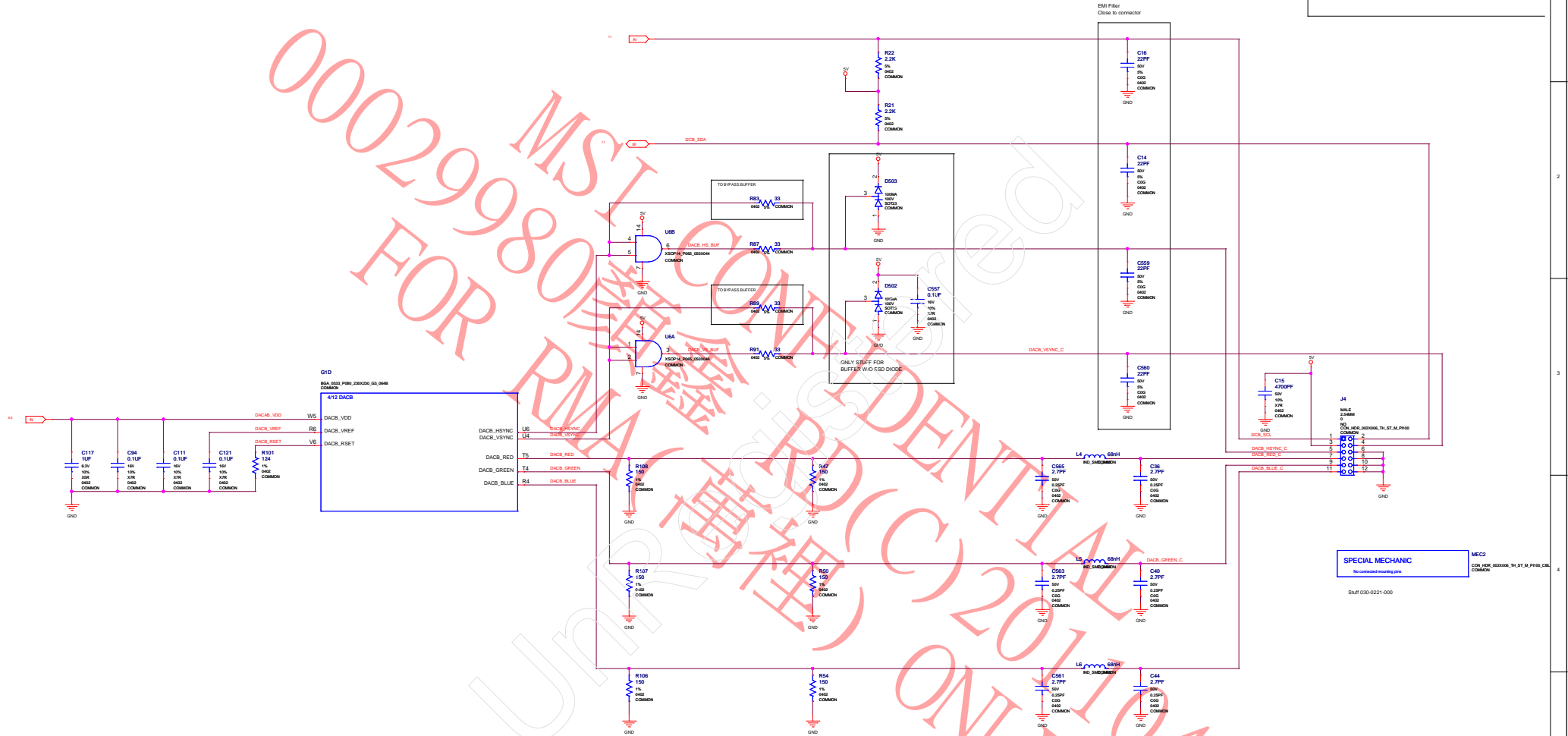
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DAC B VGA Header

Net Name		CRITICAL	IMPEDANCE
N1	DACB_RED	1	S100M
N1	DACB_GREEN	1	S100M
N1	DACB_BLUE	1	S100M
N1	DACB_RED_C	1	S100M
N1	DACB_GREEN_C	1	S100M
N1	DACB_BLUE_C	1	S100M
N1			
N1	DACB_HSYNC	2	S100M
N1	DACB_VSYNC	2	S100M
N1	DACB_HSYNC_C	2	S100M
N1	DACB_VSYNC_C	2	S100M
N1	DACB_HS_BUF	2	S100M
N1	DACB_VS_BUF	2	S100M



SPECIAL MECHANIC

Stall 030-0221-000

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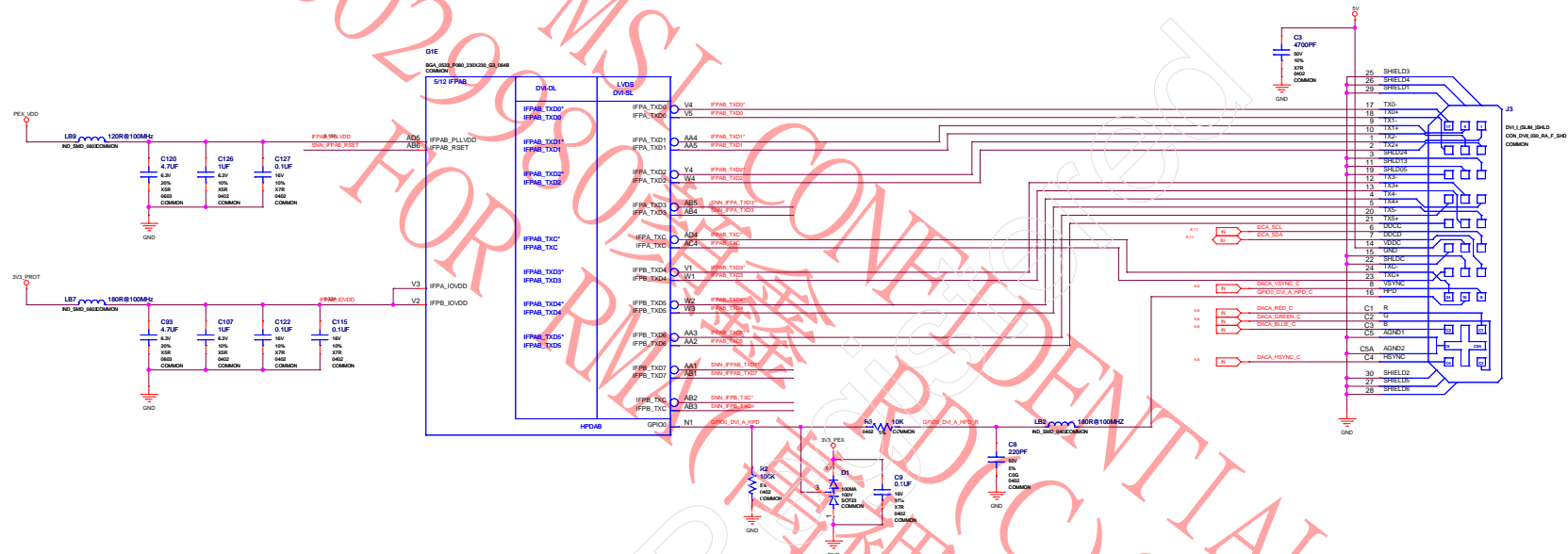
DATE

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08-DEC-2010

IFPAB TMDS Interface

Net Name		DIFF_PAIR	CRITICAL	IMPEDANCE
P1	IFPAB_T200*	IFPAB_T200_1	+	100Ω
P2	IFPAB_T200*	IFPAB_T200_2	+	100Ω
P3	IFPAB_T201*	IFPAB_T201_1	+	100Ω
P4	IFPAB_T201*	IFPAB_T201_2	+	100Ω
P5	IFPAB_T202*	IFPAB_T202_1	+	100Ω
P6	IFPAB_T202*	IFPAB_T202_2	+	100Ω
P7	IFPAB_T203*	IFPAB_T203_1	+	100Ω
P8	IFPAB_T203*	IFPAB_T203_2	+	100Ω
P9	IFPAB_T204*	IFPAB_T204_1	+	100Ω
P10	IFPAB_T204*	IFPAB_T204_2	+	100Ω
P11	IFPAB_T205*	IFPAB_T205_1	+	100Ω
P12	IFPAB_T205*	IFPAB_T205_2	+	100Ω
P13	IFPAB_T206*	IFPAB_T206_1	+	100Ω
P14	IFPAB_T206*	IFPAB_T206_2	+	100Ω
Net Name		VOLTAGE	MAX CURRENT	
P1	IFPAB_PLVDD	1.05V	0.23A	500A
P2	IFPAB_VDDO	3.3V	0.26A	500A



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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	IFPAB TMS Interface

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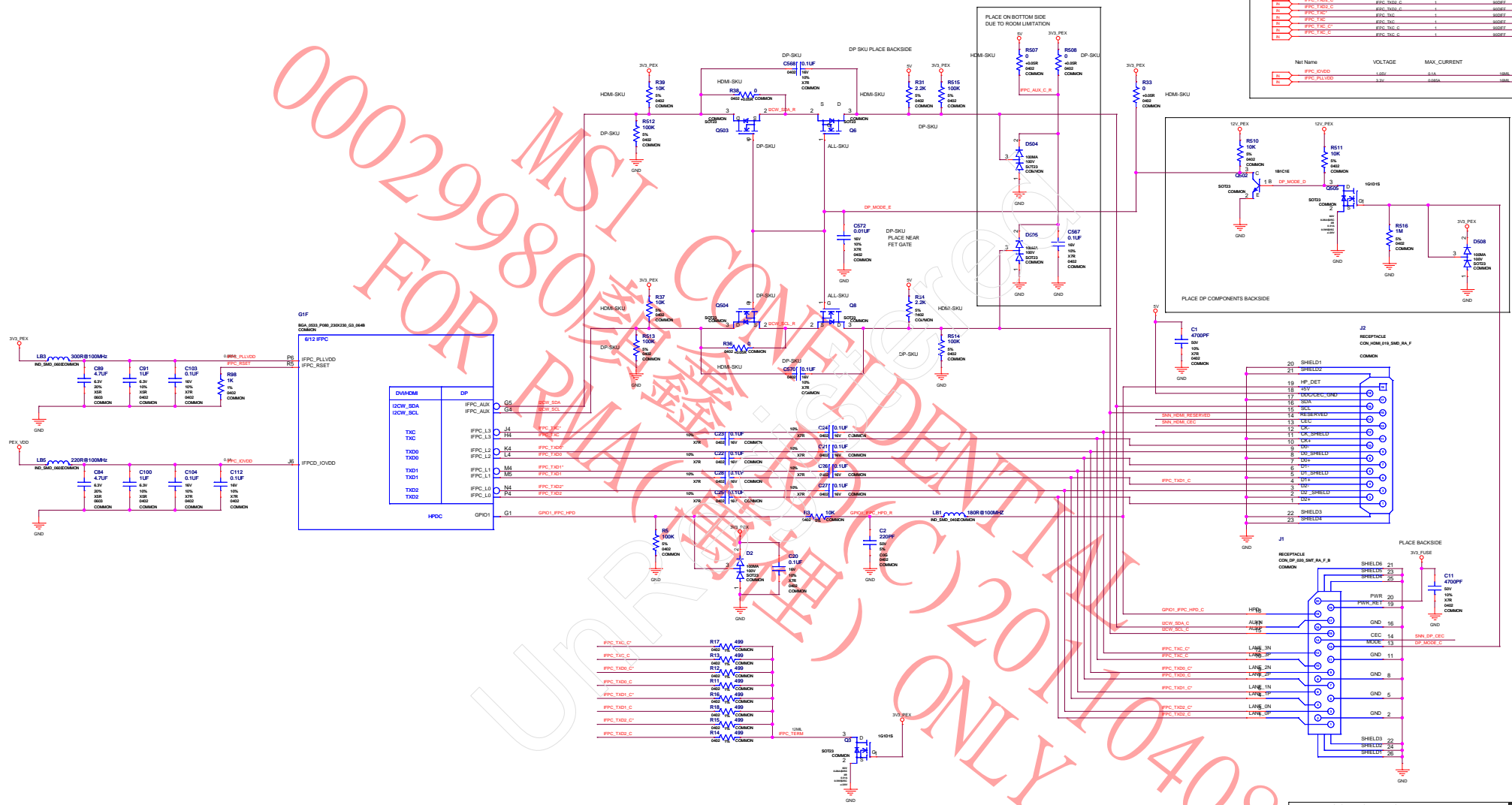
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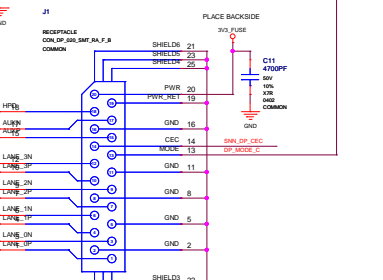
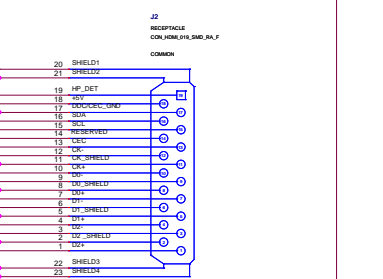
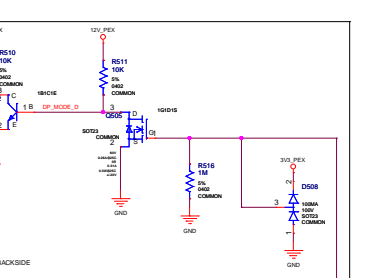
F&B REV	1/1/2012 REV
BOM REV	A

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IFPC HDMI/DP Connector



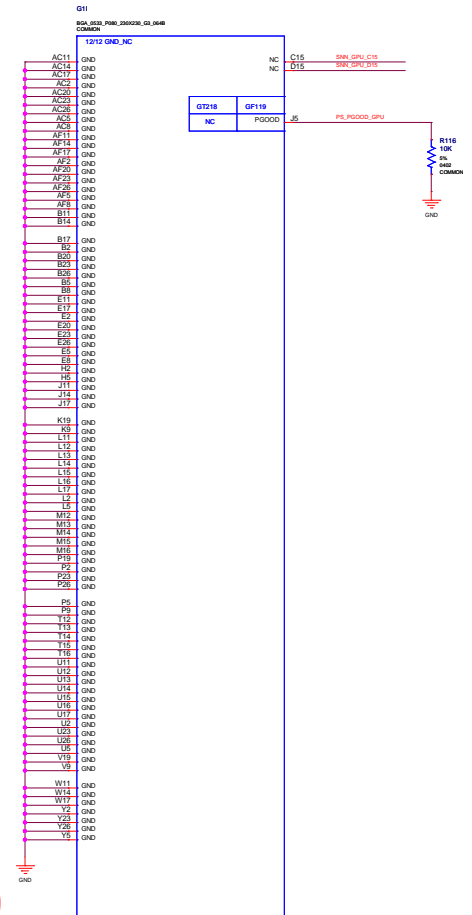
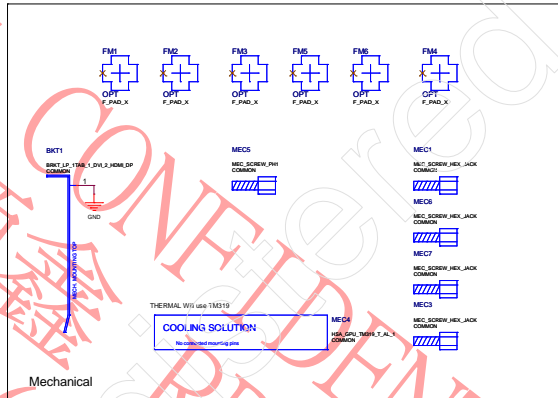
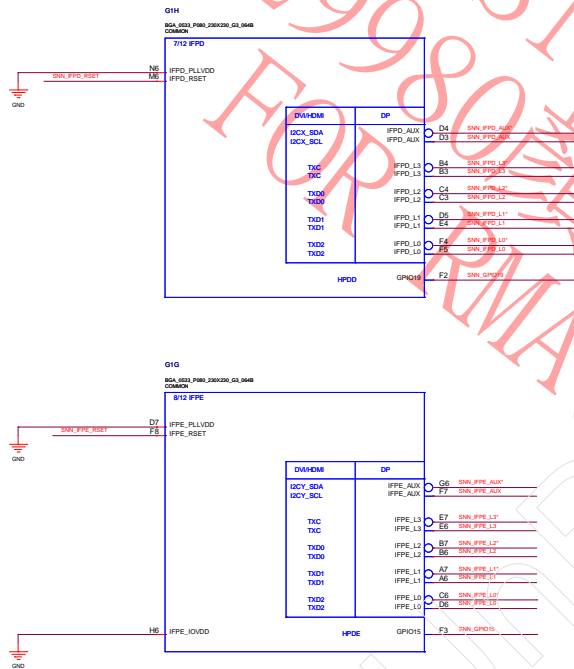
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IFPC_T01P	IFPC_T01P	1	100Ω
IFPC_T02P	IFPC_T02P	1	100Ω
IFPC_T03P	IFPC_T03P	1	100Ω
IFPC_T04P	IFPC_T04P	1	100Ω
IFPC_T05P	IFPC_T05P	1	100Ω
IFPC_T06P	IFPC_T06P	1	100Ω
IFPC_T07P	IFPC_T07P	1	100Ω
IFPC_T08P	IFPC_T08P	1	100Ω
IFPC_T09P	IFPC_T09P	1	100Ω
IFPC_T10P	IFPC_T10P	1	100Ω
IFPC_T11P	IFPC_T11P	1	100Ω
IFPC_T12P	IFPC_T12P	1	100Ω
IFPC_T13P	IFPC_T13P	1	100Ω
IFPC_T14P	IFPC_T14P	1	100Ω
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IFPC_T99P	IFPC_T99P	1	100Ω
IFPC_T100P	IFPC_T100P	1	100Ω



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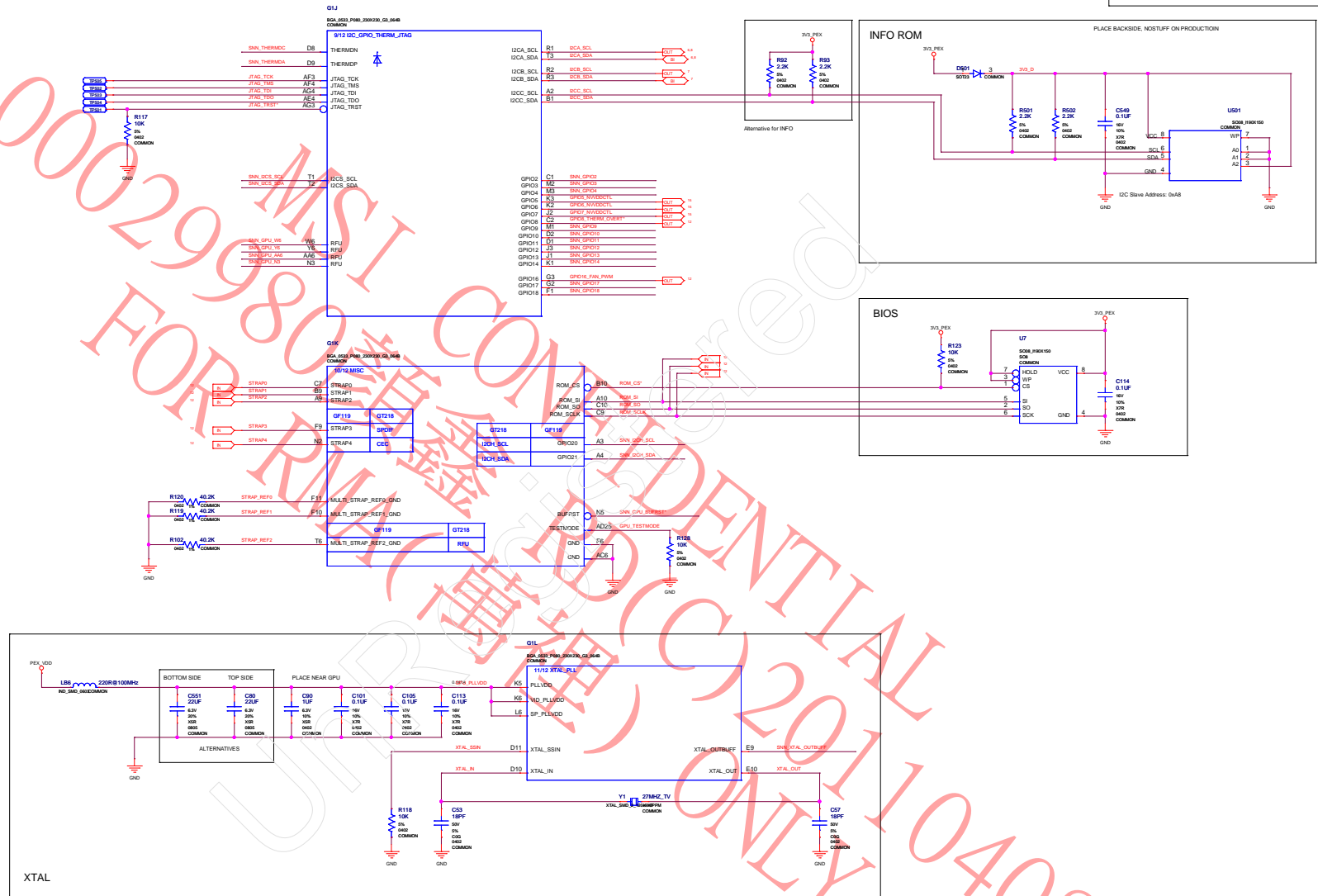
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IFPD, IFPE Interface(Not used), Mechanical parts



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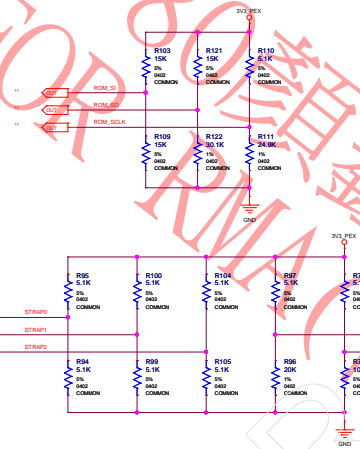
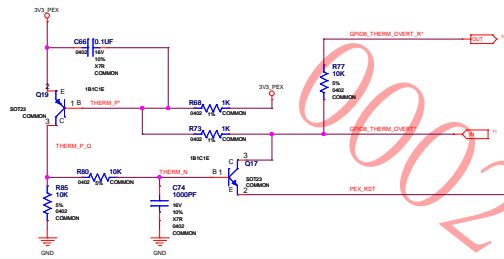
XTAL, ROM, INFO ROM,JTAG



Net Name	DIFF_PAIR	CRITICAL	IMPEDANCE
N XTAL_OUT	XTAL	1	50OHM
N XTAL_IN	XTAL	1	50OHM

Net Name	VOLTAGE	MAX_CURRENT
N GPU_PLLVDD	1.05V	0.140A
N SVS_D		120mA

Thermal Protection, Protected 3V3, Straps, Fan

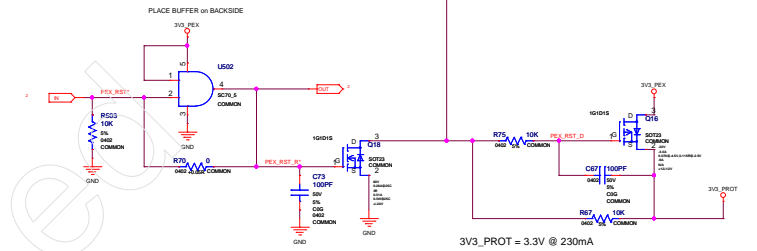


Strap	P U/PD	Rval
0	PD	5k
1	PD	10k
2	PD	15k
3	PD	20k
4	PD	25k
5	PD	30k
6	PD	35k
7	PD	45k
8	PU	5k
9	PU	10k
A	PU	15k
B	PU	20k
C	PU	25k
D	PU	30k
E	PU	35k
F	PU	45k

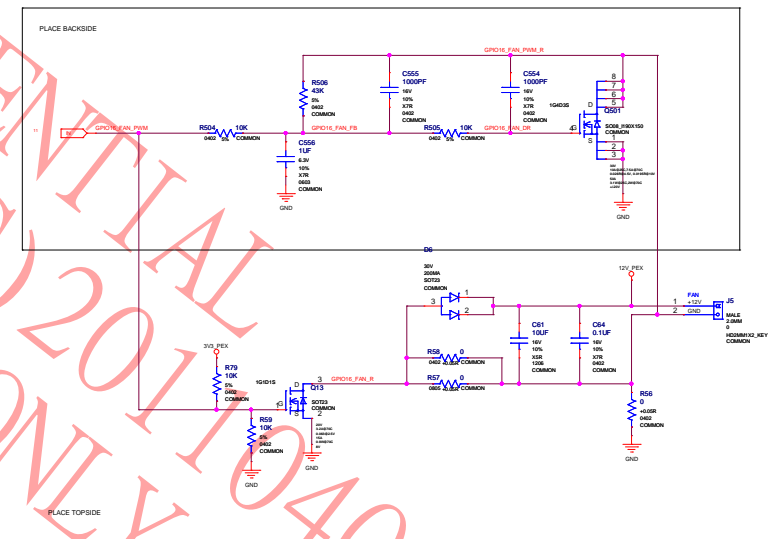
STRAP PIN MODE TABLE



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GF119	STRAP0	USER[3:0]	RAMCFG0	3GIO_PADCFG_LUT_ADR0
	STRAP1	3GIO_PADCFG_ADR[3:0]	RAMCFG1	3GIO_PADCFG_LUT_ADR1
GF119	STRAP2	PCI_DEVID[3:0]	RAMCFG2	3GIO_PADCFG_LUT_ADR2
	STRAP3	SOR[3:0]_EXPOSED	TBD	TBD
	STRAP4	RESERVED.RESERVED.PCIE_MAX_SPEED.DP_PLL_VDD33V	TBD	TBD
GF119	ROM_SCLK	PCI_DEVID[4], SUB_VENDOR, SLOT_CLK, PEX_PLL_EN_TERM	PCI_DEVID3	3GIO_PADCFG_LUT_ADR3
	ROM_SCLK	PCI_DEVID[4].SUB_VENDOR.PCI_DEVID[5].PEX_PLL_EN_TERM	TBD	TBD
	ROM_SI	RAMCFG[3:0]	PCI_DEVID_EXT	SUB_VENDOR
GF119	ROM_SO	XCLK_417.FB0_BAR_SIZE.SMB_ALT_ADDR.VGA_DEVICE	XCLK_417	XCLK_277
	ROM_SO	FB[1].FB[0].SMB_ALT_ADDR.VGA_DEVICE	TBD	TBD

Backdrive Prevention



FAN CIRCUIT



 	
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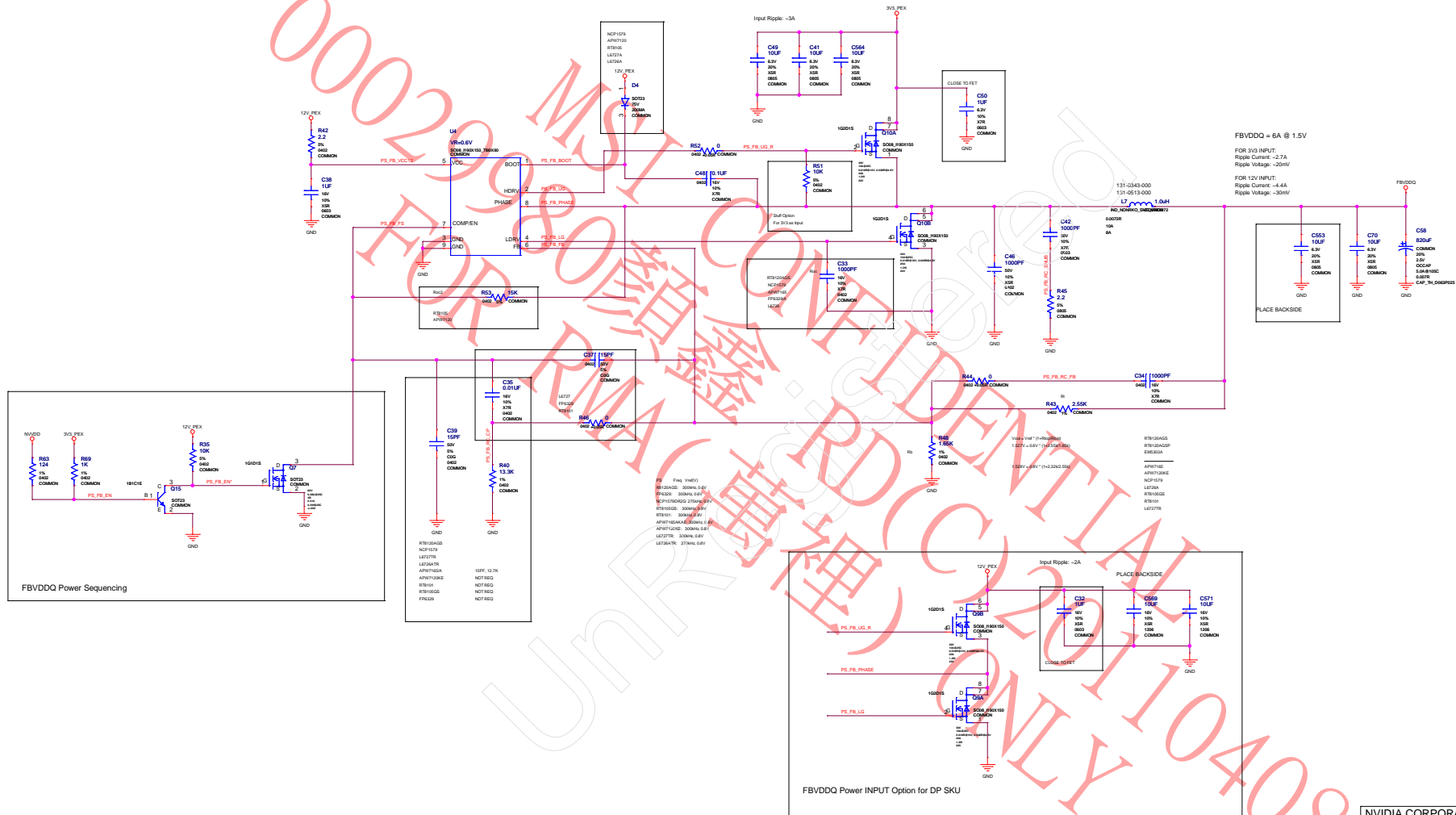
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Power Supply II: FBVDDQ

	Net Name	MIN_LINE_WIDTH	VOLTAGE	NV_NET_MAX_CURRENT
PS:0000	PS_0000	1.00	1.00	50
R	PS_FB_VCC12	1.00		
	PS_FB_V5	1.00		
B	PS_FB_FB	1.00		
B	PS_FB_R2_FB	1.00		
	PS_FB_R3_OP	1.00		
	PS_FB_BOOT	1.00		
B	PS_FB_VCC	1.00		
B	PS_FB_VSSOE	1.00		
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B	PS_FB_R2_SINB	1.00		



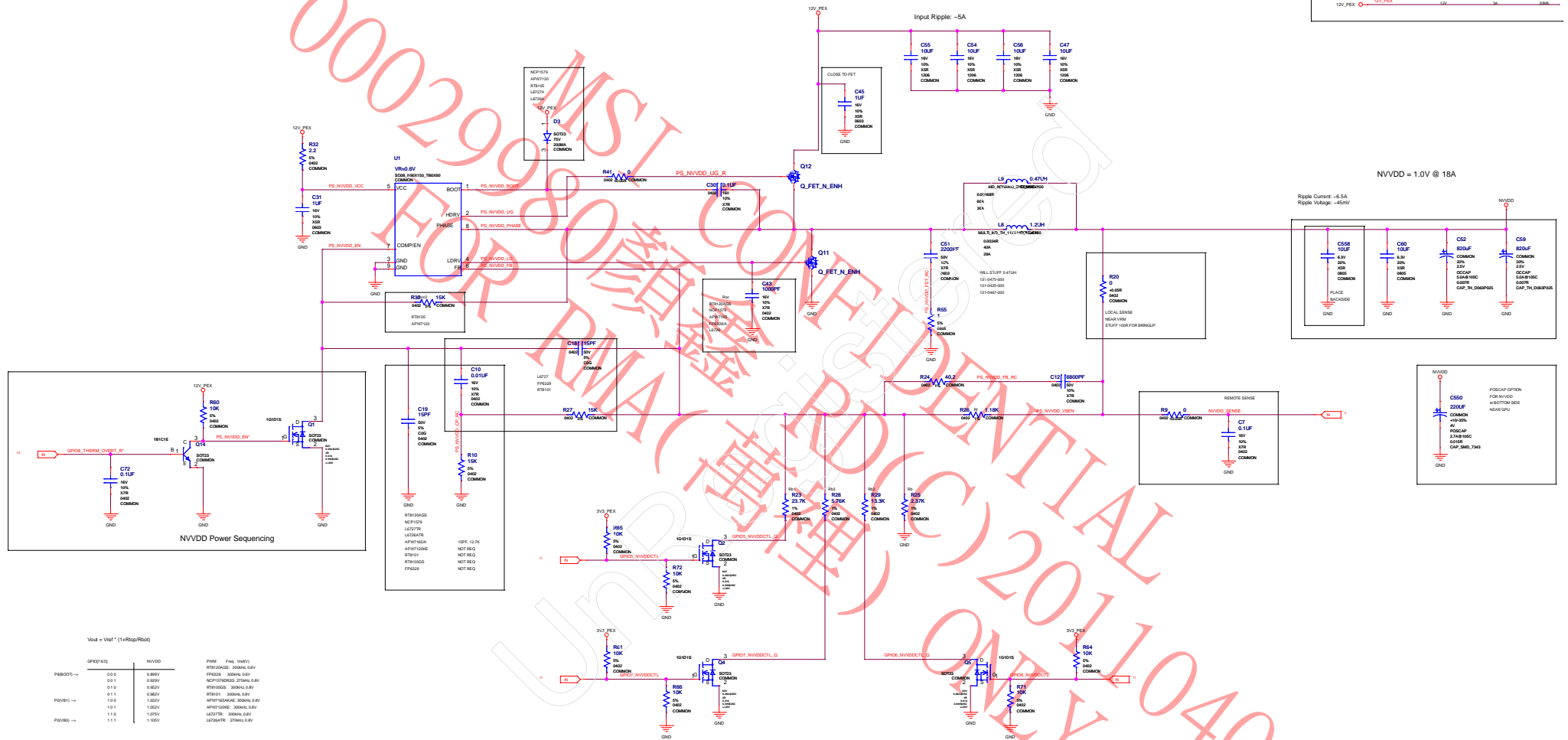
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PAGE DETAIL	Power Supply II: FBVDDQ

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Power Supply III: NVVDD

Net Name		MIN_WIDTH	
IN	PS_N100D_FH	100k	
IN	PS_N000D_V00	100k	
IN	PS_N000D_B00F	100k	
IN	PS_N000D_V0F	100k	
IN	PS_N000D_V0F_X	100k	
IN	PS_N000D_V0F_X0	100k	
IN	PS_N000D_PHASE	100k	
IN	PS_N000D_F0F_0F	100k	
IN	PS_N000D_FH	100k	
IN	PS_N000D_F0F_0F0	100k	
IN	PS_N000D_F0F_0F0	100k	
Net Name		VOLTAGE	MAX_CURRENT
N000D_0	N000D	1.2V	200mA
CPU_PEX	CPU_PEX	1.2V	3A



	GPIO[6:5]	NVDDO
PA(SOOT) →	0 0 0	0.899V
	0 0 1	0.929V
	0 1 0	0.952V
	0 1 1	0.982V
P0(VB1) →	1 0 0	1.022V
	1 0 1	1.052V
P0(VB0) →	1 1 0	1.075V
	1 1 1	1.105V

PWM	Freq	Vref(V)
RTB120AGS	300kHz	0.6V
FPB22z	300kHz	0.5V
NCP152GR2G2	275kHz	0.8V
RTB105GS	300kHz	0.8V
RTB101	300kHz	0.8V
APW7165XAE	300kHz	0.8V
APW7120KE	300kHz	0.8V
LE227TR	300kHz	0.8V
LE262ATR	270kHz	0.8V


COMMON
GPIO7_NVIOCTL

R66
10K
5%
0402
COMMON

1

5%
0402
COMMON

10

NVIDIA CORPORATION			
2701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95050, USA			
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