PG160-B02 8GB GDDR6, 256b, X16 Tall DVI-D + USBC/DP + DP + HDMI/DP + DP TABLE OF CONTENTS Page Page Description Description Page Description Table of Contents 26 PS: 1V8_PLL, 1V8_AON 51 MECH Block Diagram 2 27 PS: 5V 52 PTC 3 PCI Express 28 PS: PEXVDD PCIE RC TERM 29 PS: FBVDDQ PS: FBVDDQ PH2 MEMORY: GPU FB_AB 30 PS: NVVDD Controller MEMORY: FBA[31:0] 31 PS: NVVDD Controller OVR3i MEMORY: FBA[63:32] 32 MEMORY: FBB[31:0] PS: NVVDD Phase 1,2 PS: NVVDD Phase 3,4 MEMORY: FBB[63:32] 34 10 MEMORY: GPU FB_CD 35 PS: NVVDD Phase 5,6 MEMORY: FBC[31:0] 36 PS: Input Power Balancing Switcher 11 12 MEMORY: FBC[63:32] 37 PS: Input, Filtering, and Monitoring 13 MEMORY: FBD[31:0] 38 PS: STEERING, UPB & HOT-UNPLUG MEMORY: FBD[63:32] PS: Type-C BuckBoost 14 39 GPU PWR & GND 40 PD PPC 15 16 GPU Decoupling 41 PS: 12V & 3V3_A SWITCHER GPU Decoupling2 SEQ: 5V, 1V8, NV3V3 ENABLE 18 IO: IFPAB DVI-D-DL 43 SEQ: NV, PEX, FB ENABLE IO: IFPE DP SEQ: VOLTAGE MONITOR 19 20 IO: IFPF USBC 45 SEQ: DISCHARGE IO: IFPC HDMI/DP SEQ: MISC 21 46 IO: IFPD DP LOGO LED 47 22 23 IO: NVHS Interface and Frame Lock 48 LED DRIVER & BOOST MISC1: Thermal, JTAG, GPIO, STEREO 24 49 DUAL FAN 25 MISC2: ROM, XTAL, STRAPS 50 PS: Pre-filter RD Center Colorful Co.,Ltd 深期非七彩虹州拔 发展 有限 公 可 <a href="mailto:ASSEMBLY_DESCRIPTION Table of Contents

PAGE DETAIL

ALL NOW RESIDENT SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE DOUBLES CRUSS CONTROL SERVICE CONTROL SER





































































































