

P681-A01 GT215/216 DESKTOP GB1-128 DDR3
PCI-EXPRESSx16 DL-DVI VGA HDMI

Ver. 0A

Ver. 10

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Page 21: NVVDD POWER SUPPLY

05/07

Page 01 1.Remove C690,C70 47uF

Page 10 1.Remove J1 D-Sub

Page 11 1.Remove R16,Q2

Page 12 1.Remove R1,Q1

Page 16 1.Change Crystal Footprint to 2-PIN SMD

2.Remove C71 47uF

05/08

Page 17 1.Remove Thermal Sensor Circuit

Page 18 1.Remove U2,C54,R31,R30,D16 INFOROM circuit

Page 19 1.Remove 3V3 to 1.8V circuit

2.Change 5V REGULATOR Circuit

Page 20 1.Change L8 Footprint to 1.6uH

2.Change Footprint to multi cap for EL 1500uF

Page 21 1.Change NVVDD to UP6161

2.Change L12 footprint to 1.2uH

05/10

Page 05 1. Remove Decoupling for EMI cap

Page 20 1. C84 change Footprint to multi cap

05/11

Page 21 1.Add C39 270uF

2.Add R922,R924 for APW7068 OCSET

Page 16 1.Add XTALIN R566 Resister

Add XTALOUT R573 Resister

Page 02 1.Change C51 to 0805 1UF and ADD C54 0805 1UF

Page 03 1.C642,C666,C633 change to .01UF

Page 05 1.Remove C516,C525,C549,C556,C541,C512

Remove C513,C546,C540,C528,C547,C507

Remove C544,C545,C520,C523,C532,C508

Remove C535,C526,C543,C553,C537,C88

Page 08 1.Remove C566,C567,C576,C578,C82,C564

Remove C619,C592,C586,C585,C622,C574

Remove C705,C706,C680,C729,C691,C683

Remove C742,C738,C731,C725,C743,C728

Page 20 1.Remove C86,C562,C563,C87

05/12

Page 17 1.Remove 4-PIN FAN Circuit

Page 12 1.Change Q509 to Q513,Q514 SOT23 footprint

Page 16 1.Change FAN Screw hole

Page 20 1.Add C102 820uF

05/14

Page 21 1.Remove D94 scottky diode

05/19

Page 11 1.Add EMI bridge

Page 12 2.Add EMI bridge

05/21

Page 03 1.Add RP24 termination risister

Page 06 2.Add RP23 termination risister

05/26

SWAP CMD

Page 03/06 RP5.1 · RP5.2 FBC_CMD10 · RP5.3 · RP5.4 FBC_CMD22

RP4.1 · RP4.2 FBC_CMD18 · RP4.3 · RP4.4 FBC_CMD7

RP24.1, RP24.2 FBA_CMD30, RP24.3, RP24.4 FBA_CMD7

RP20.1, RP20.2 FBA_CMD14, RP20.3, RP20.4 FBA_CMD18

RP14.1, RP14.2 FBA_CMD1, RP14.3, RP14.4 FBA_CMD20

RP12.3, RP12.4 FBA_CMD29

05/27

Page 05 1. Add Decoupling for EMI cap C80,C524,C517,C102

07/31

Page 02 1.Remove JTAG Circuit

Page 09 1.Remove EMI filter,protection diode,C780,C775,C779

Page 10 1.Remove EMI filter,protection diode,C771,C772,C773

Page 12 1.IFP_PLLVDD change netname to 3V3

Page 15 1.Remove R545,C664,R554 of MIOA

Page 17 1.Remove JTAG Circuit

2.Remove FAN PWM Circuit

Page 18 1.Remove HDCP EEROM

Page 19 1.Remove U4 UP7703 Circuit

2.Change 5V Circuit SOT223-->SOT252

Page 20 1.Remove C86,C105,C90

2.Add R917 LMOS Gate risister

3.Add L10

4.Add L9,LB23,LB24

5.Add EL 680UF FootPrint

Page 21 1.Add L13

2.Remove APW7068 OCSET

3.Add GPIO 5.6 Circuit

4.Remove C927,C931

08/04

Page 21 1.Remove C933,C30,C31,C38

2.Change C39 FootPrint for 470uF

3.NVVDD Power Sequencing and PWM power change to 12V from 12V_F

Page 20 1.Add C86,C87,R26

2.Remove C557

3.Power Sequencing and PWM power change to 12V from 12V_F

REV	VARIANT	NVPN	ASSEMBLY
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1	SKU001	600-10681-0001-100	GT216-300 600/1500MHz 1024MB 64Mx16 BGAT100 900MHz DDR3 DVI+VGA+HDMI
2	SKU002	600-10681-0002-100	GT216-300 600/1500MHz 1024MB 64Mx16 BGAT100 1000MHz DDR3 DVI+VGA+HDMI
3	SKU011	600-10681-0011-100	GT216-300 600/1500MHz 1024MB 64Mx16 BGAT100 900MHz DDR3 DVI+VGA+HDMI
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO. STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	P681-AST OVERVIEW

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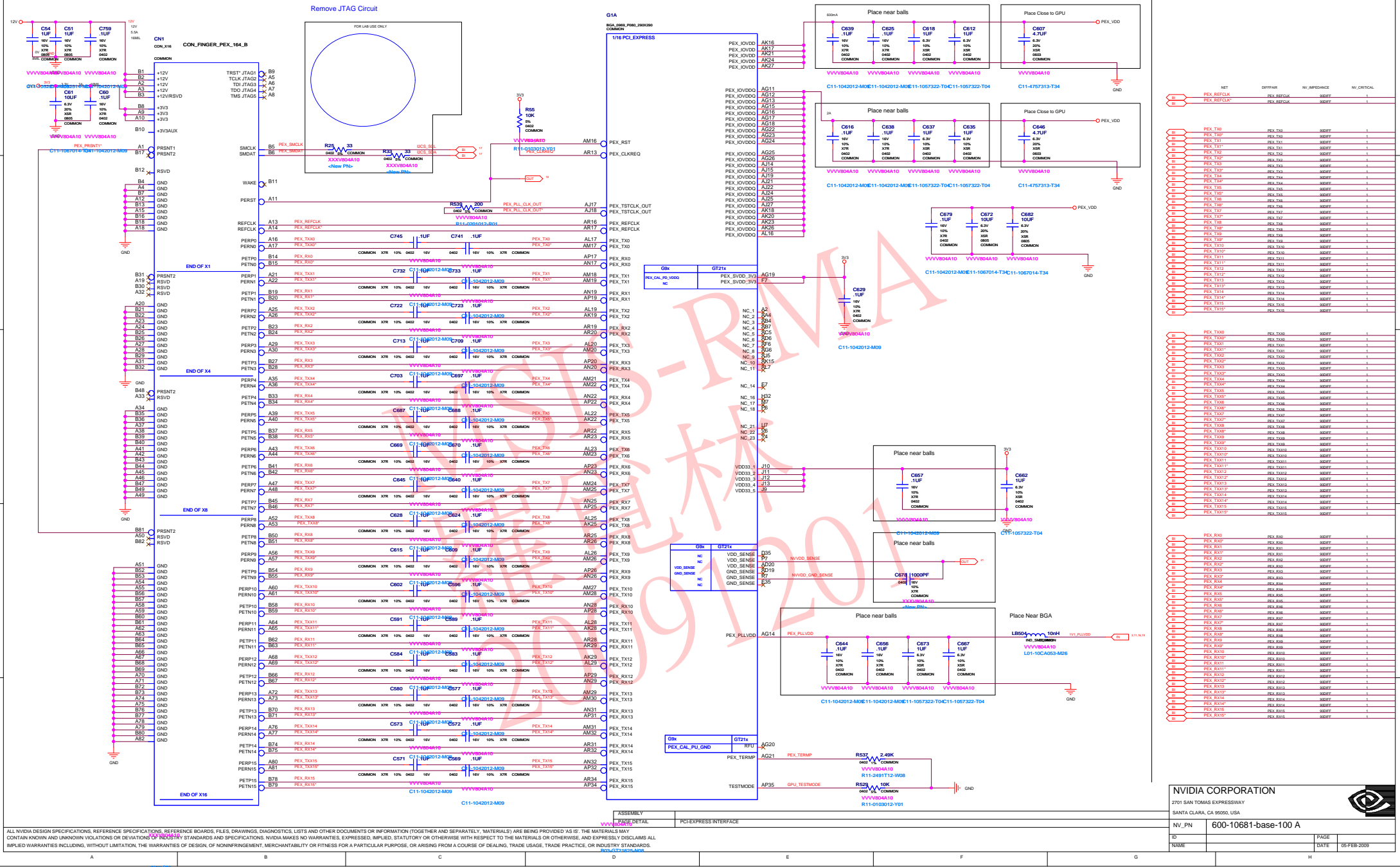
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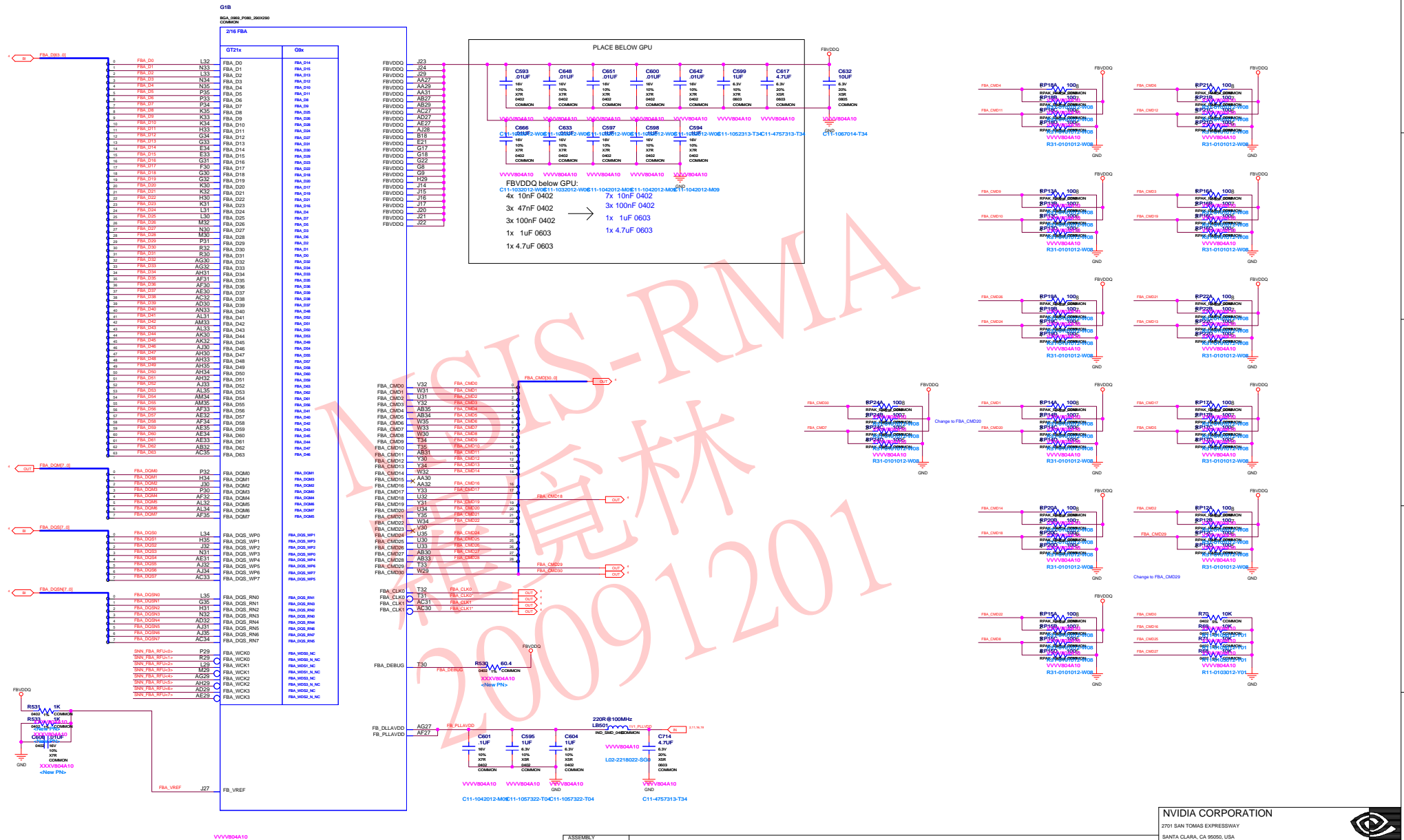
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PCI-EXPRESS INTERFACE



PARTITION A FRAME BUFFER INTERFACE



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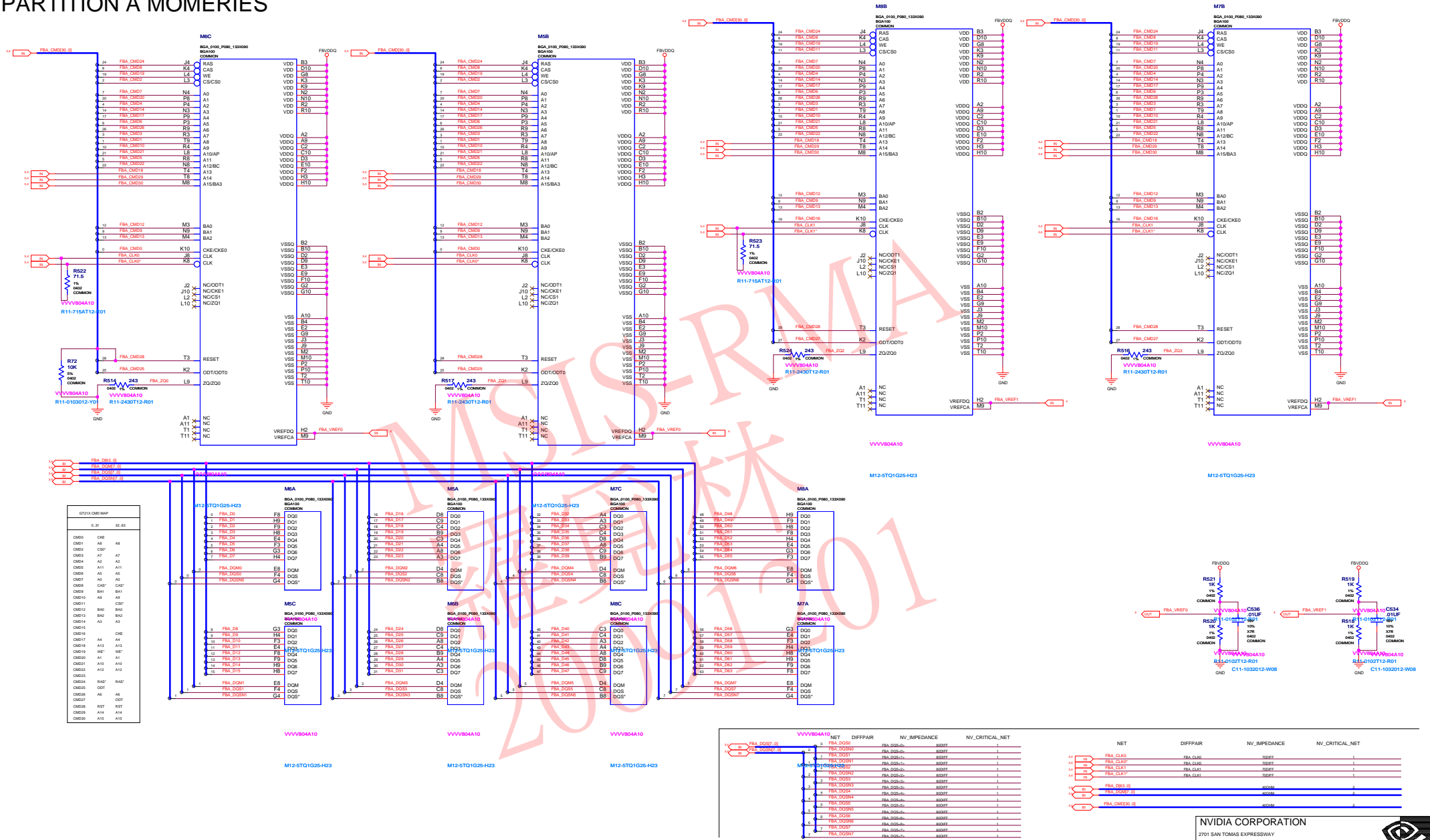
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PARTITION A MOMERIES

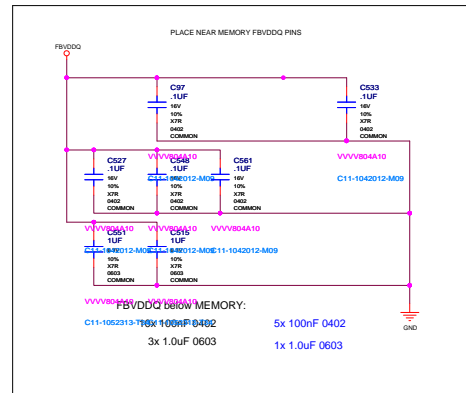


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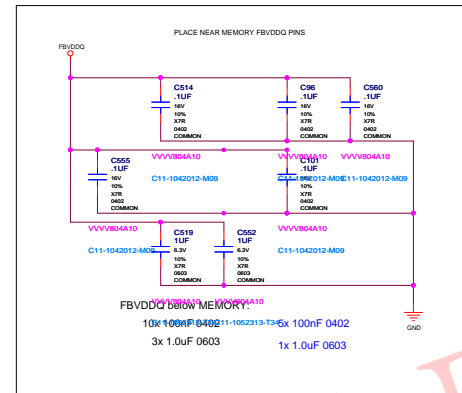
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PAGE RETAIL PARTITION A MEMORIES

FBA DECOUPLING CAPS & NVVDD DECOUPLING CAPS

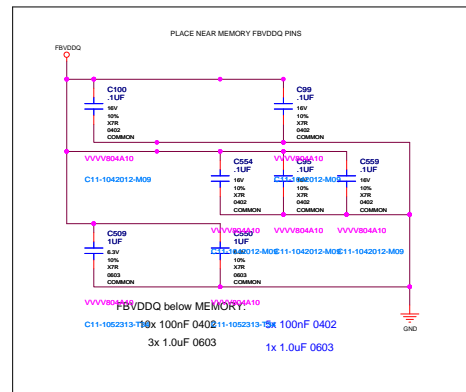
Decoupling for FBA 0..15



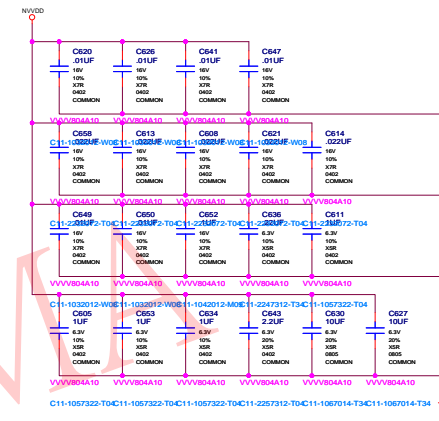
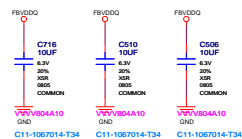
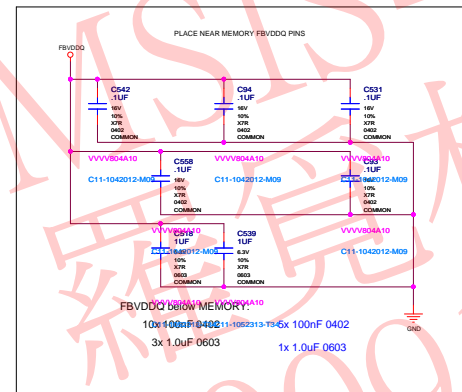
Decoupling for FBA 16..31



Decoupling for FBA 32..47



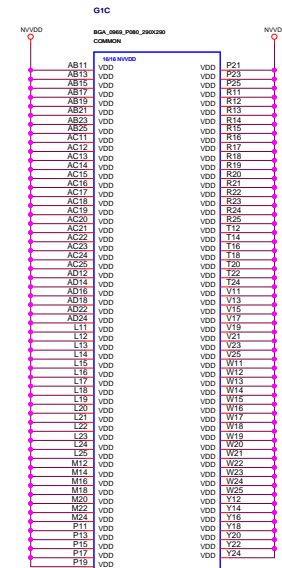
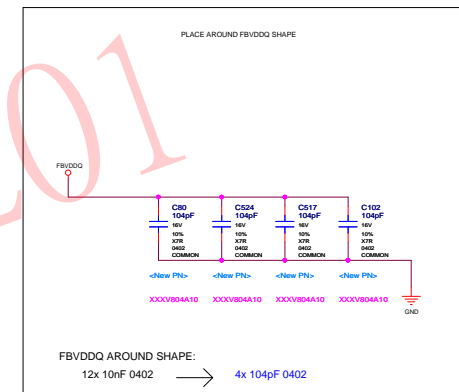
Decoupling for FBA 48..63



NVVDD under GPU.

4x 0.01uF 0402 X7R → 6x 0.01uF 0402 X7R
5x 0.022uF 0402 X7R
2x 0.047uF 0402 X7R → 0x 0.047uF 0402 X7R
1x 0.1uF 0402 X7R
1x 0.22uF 0402 X7R
4x 1.0uF 0402 X5R
1x 2.2uF 0402 X5R
2x 10uF 0805 X5R

Decoupling for EMI



VVVV804A10

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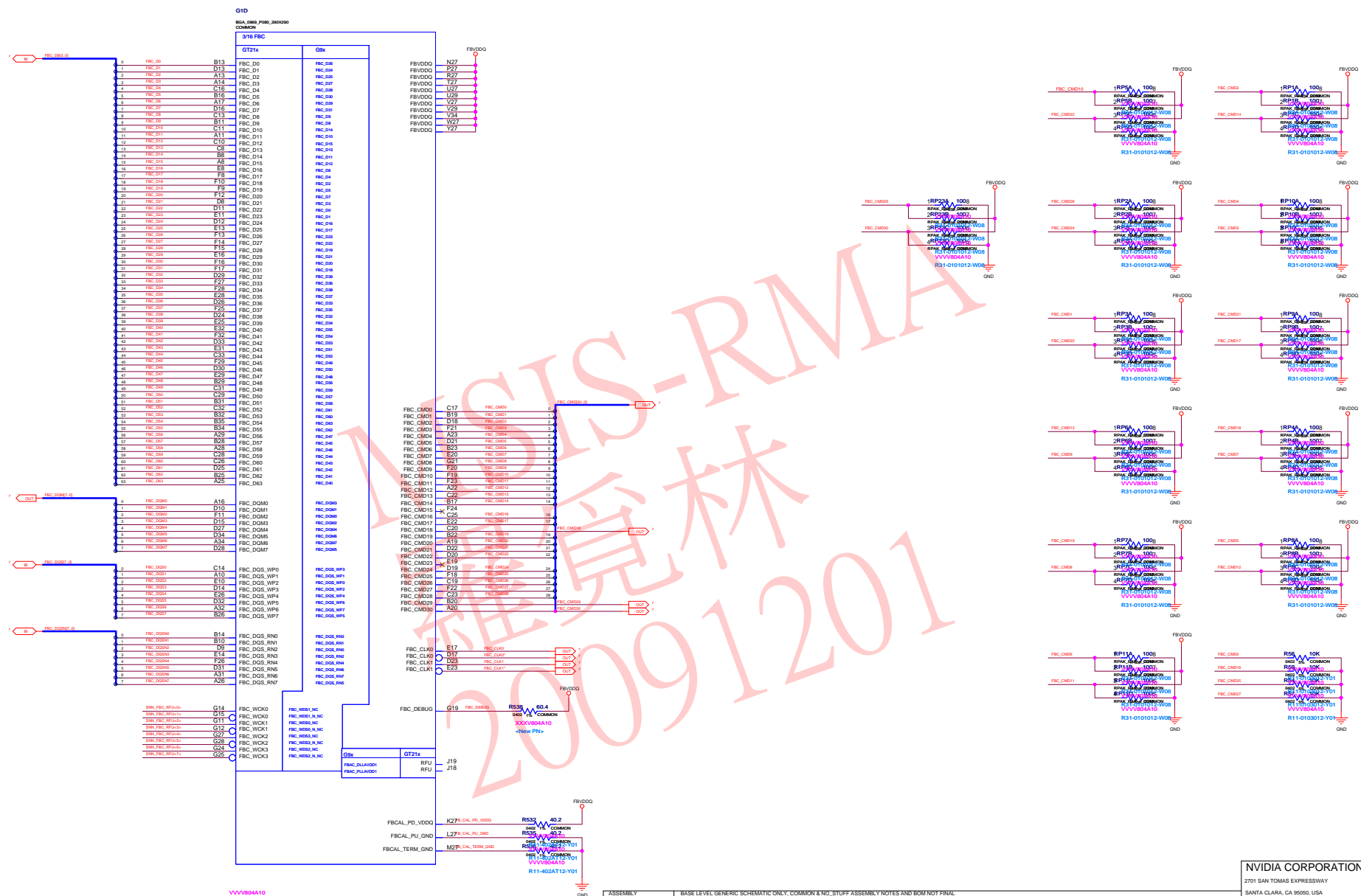
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	FBA DECOUPLING CAPS & NVVDD DECOUPLING CAPS

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PARTITION C FRAME BUFFER INTERFACE



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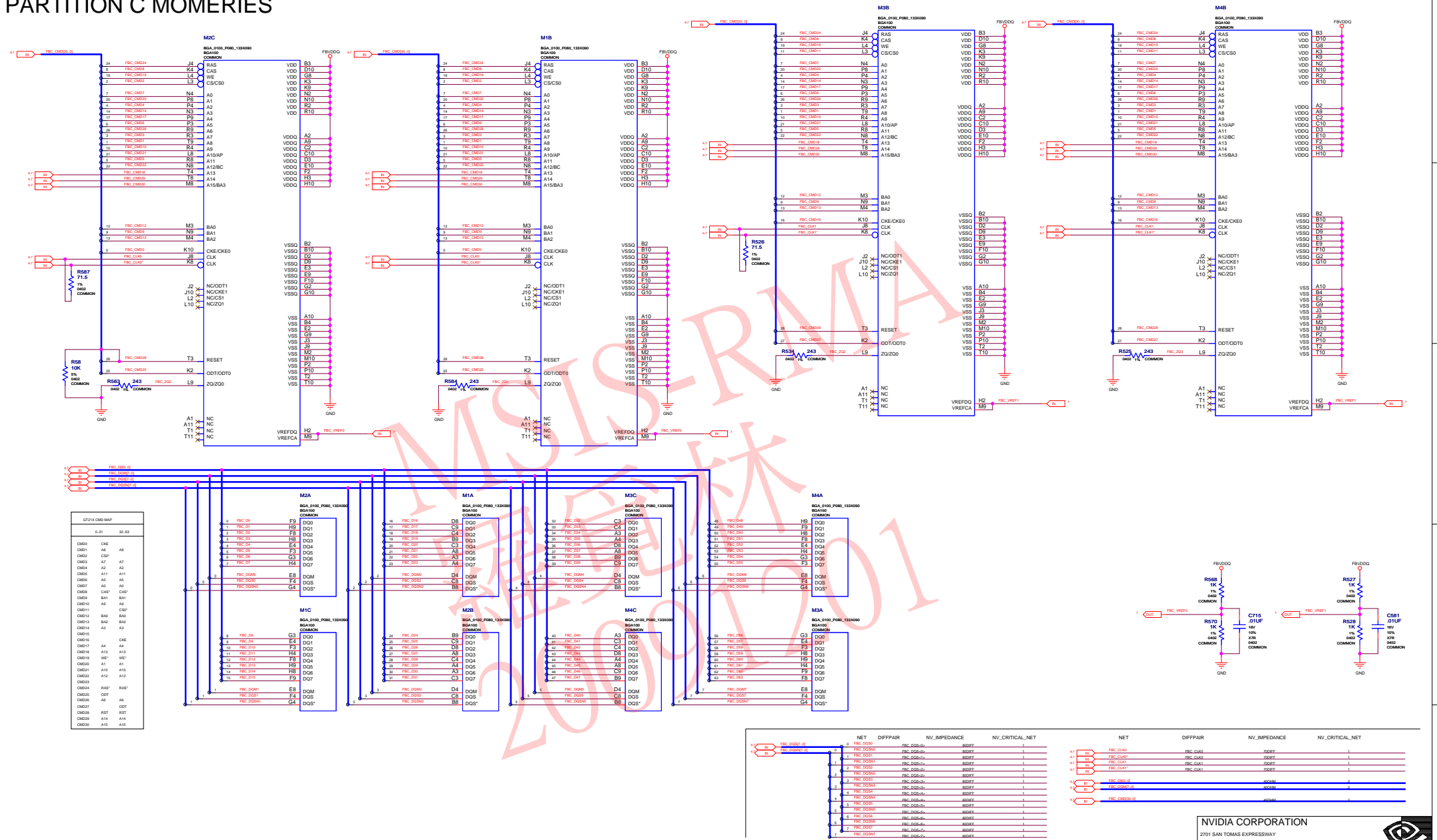
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PAGE DETAIL	PARTITION C FRAME BUFFER INTERFACE

PARTITION C MOMERIES



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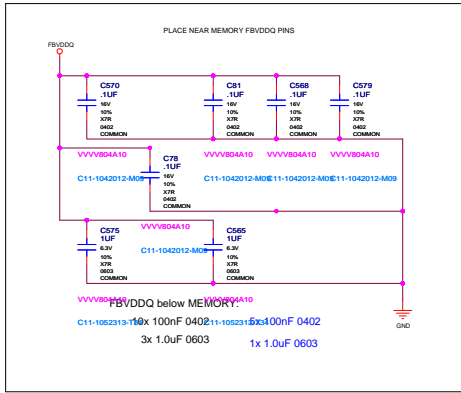
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PAGE DETAIL	PARTITION C MEMORIES

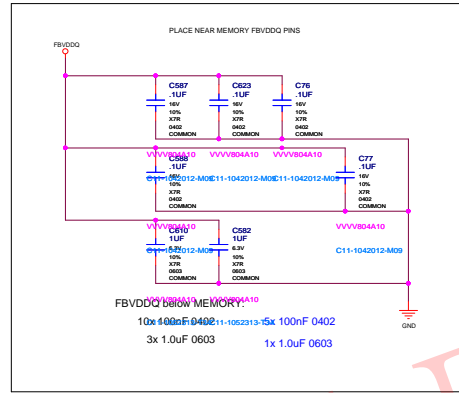
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FBC DECOUPLING CAPS & NVVDD DECOUPLING CAPS

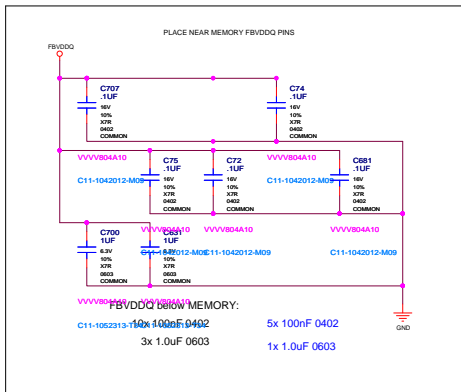
Decoupling for FBC 0..15



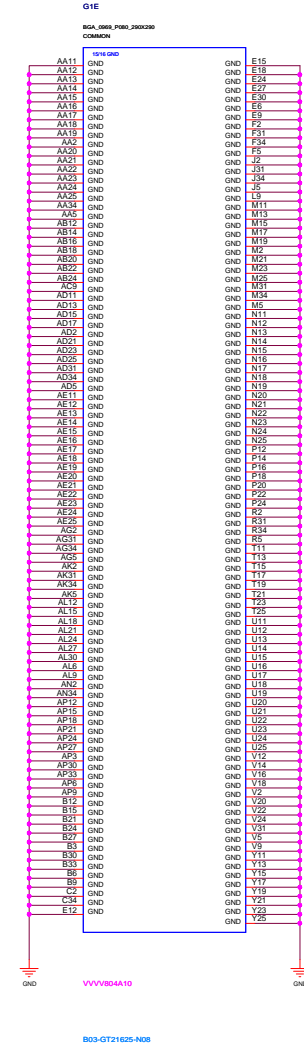
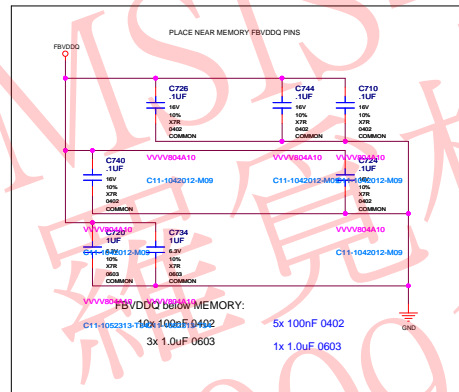
Decoupling for FBC 16..31



Decoupling for FBC 32..47



Decoupling for FBC 48..63



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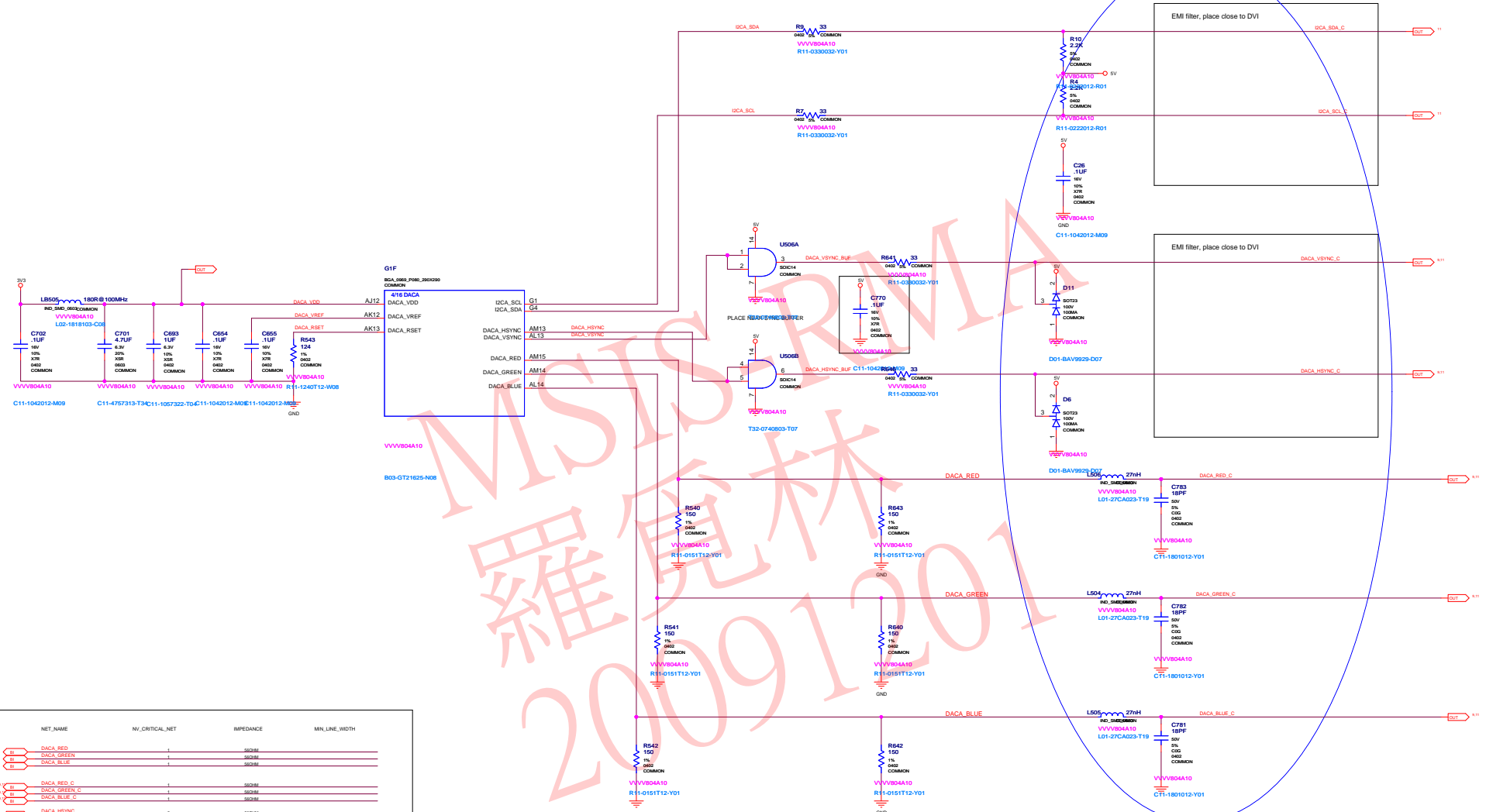
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DACA (SOUTH DVI-I)

Remove EMI filter, protection diode, C780, C775, C779



	NET_NAME	NV_CRITICAL_NET	IMPEDANCE	MIN_LINE_WIDTH
W	DACA_RED	1	50ohm	
W	DACA_GREEN	1	50ohm	
W	DACA_BLUE	1	50ohm	
W	DACA_RED_C	1	50ohm	
W	DACA_GREEN_C	1	50ohm	
W	DACA_BLUE_C	1	50ohm	
W	DACA_HVING	2	50ohm	
W	DACA_HVING_B	2	50ohm	
W	DACA_HVING_C	2	50ohm	
W	DACA_HVING_BUF	2	50ohm	
W	DACA_HVING_BUF	2	50ohm	
W	DACA_HVING_C	2	50ohm	
W	DACA_HVING_C	2	50ohm	

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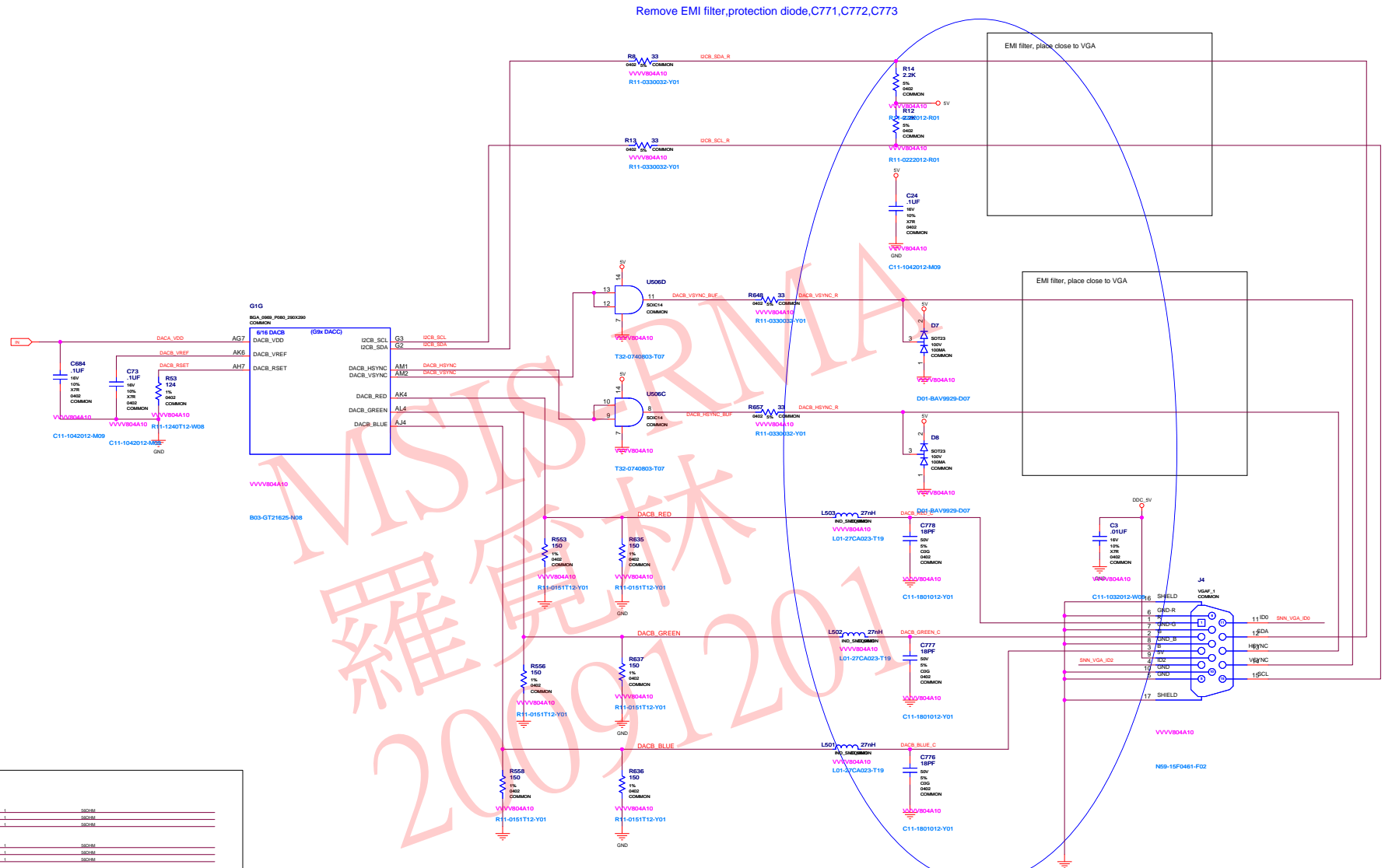
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACA (SOUTH DVI-I)

DACB (MID VGA)



B	DACB_RED	1	5020H
B	DACB_GREEN	1	5020H
B	DACB_BLUE	1	5020H
B	DACB_RED_C	1	5020H
B	DACB_GREEN_C	1	5020H
B	DACB_BLUE_C	1	5020H
B	DACB_VESTING	2	5020H
B	DACB_VESTING_A	2	5020H
B	DACB_VESTING_B	2	5020H
B	DACB_VESTING_BUF	2	5020H
B	DACB_VESTING_BUF	2	5020H
B	DACB_VESTING_C	2	5020H
B	DACB_VESTING_D	2	5020H

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACB (MID VGA)

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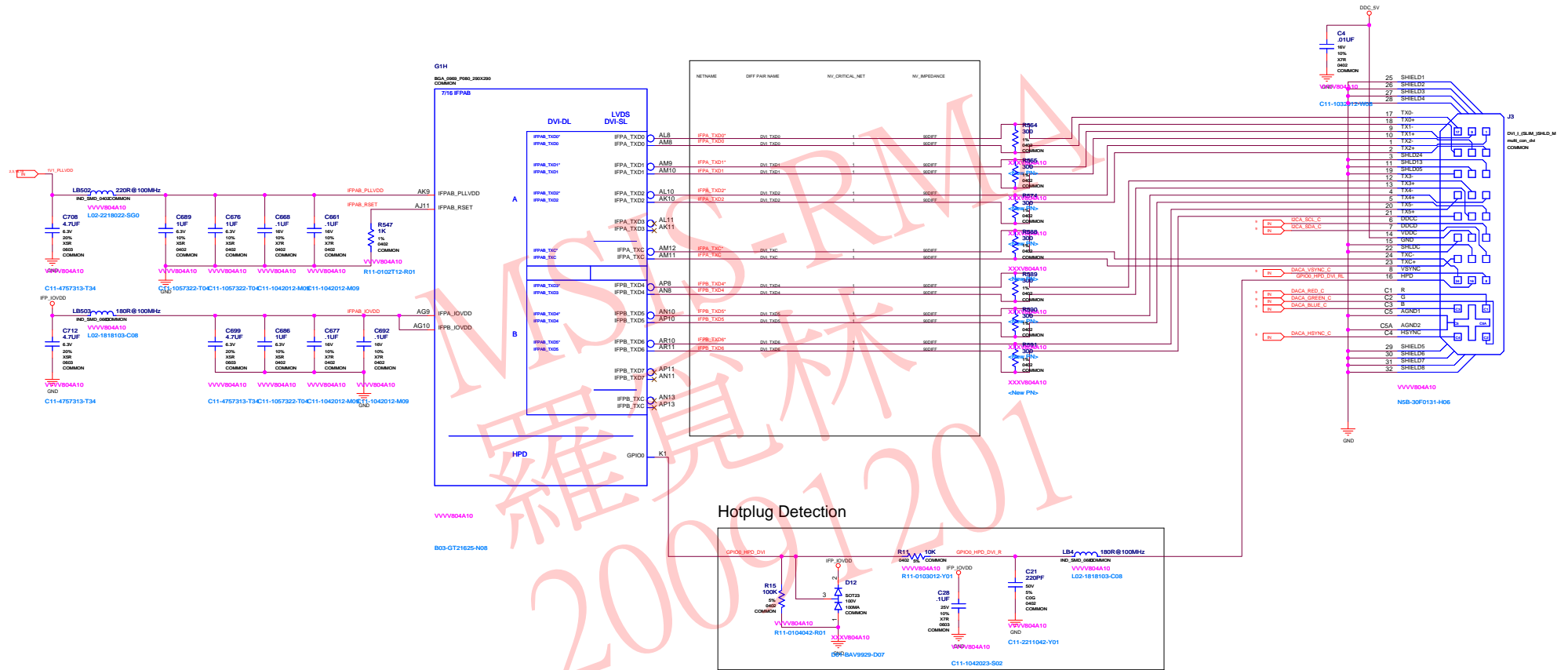


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IFP AB (SOUTH DVI-I)



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	IFP AB (SOUTH DVI-I)

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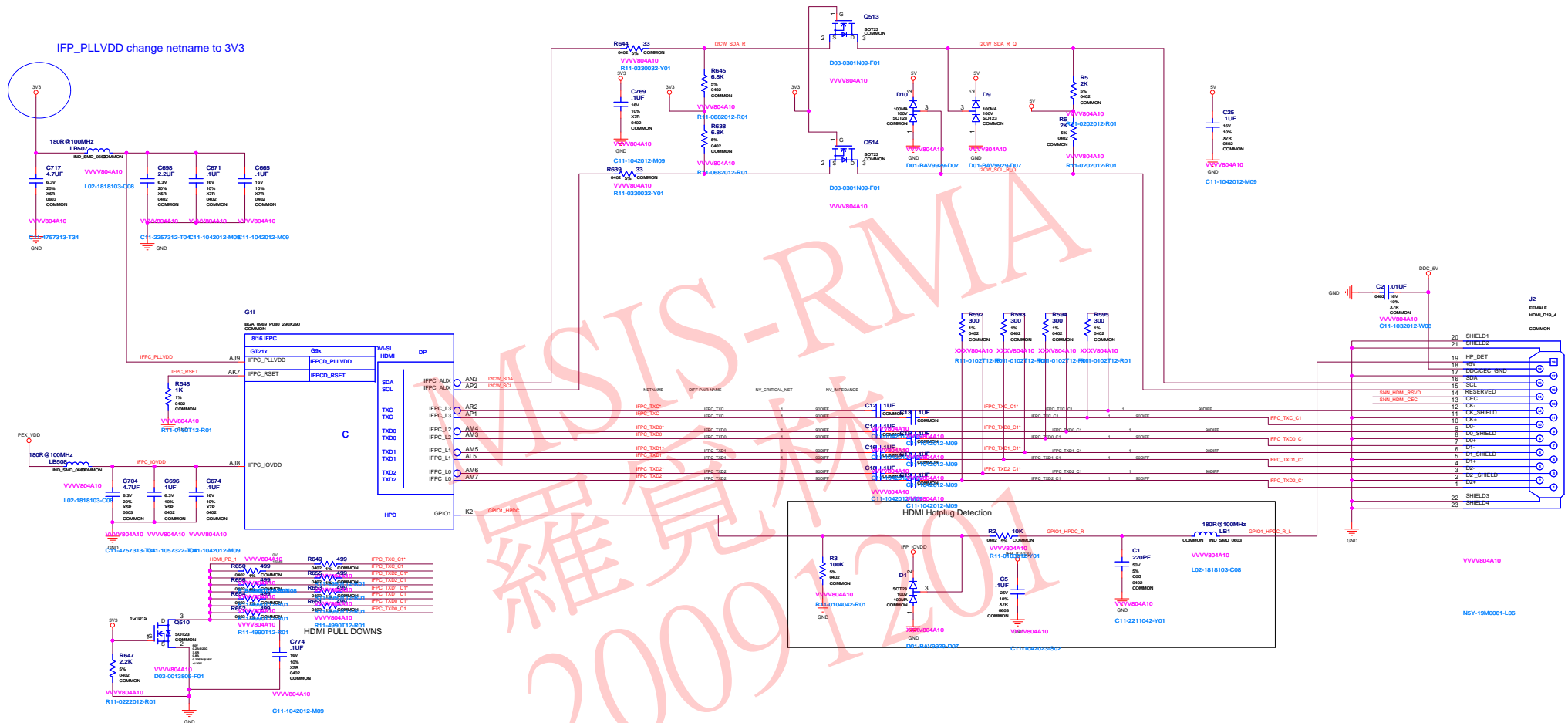


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IFP C (NORTH HDMI)



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	IFP C (NORTH HDMI)

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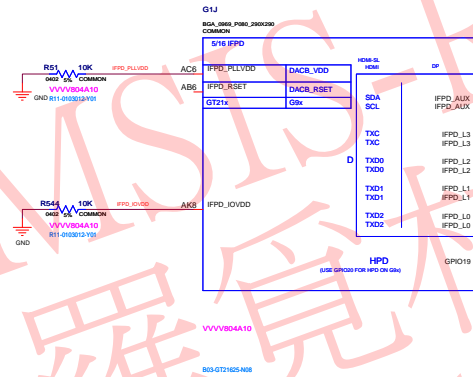
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IFP D (UNUSED)



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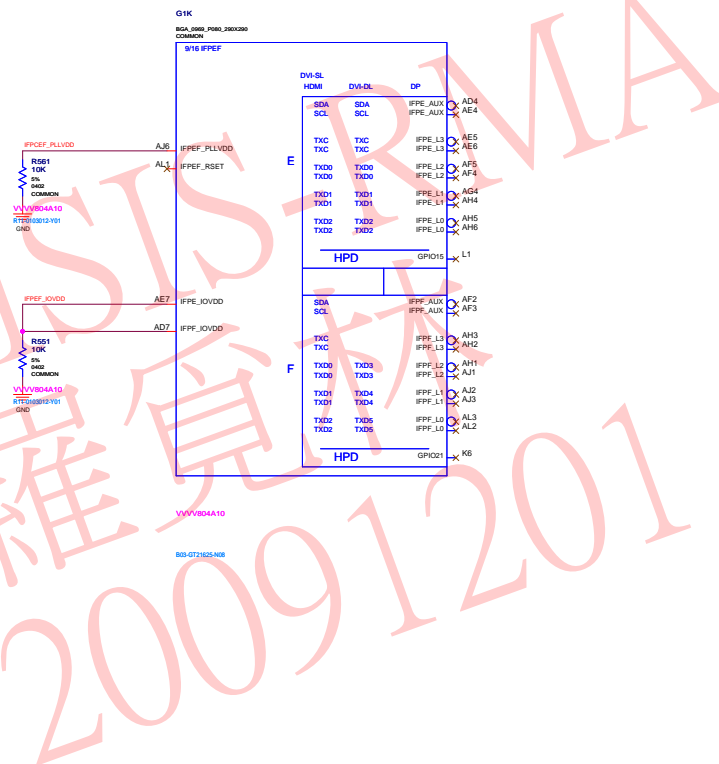


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NAME		DATE	05-FEB-2009

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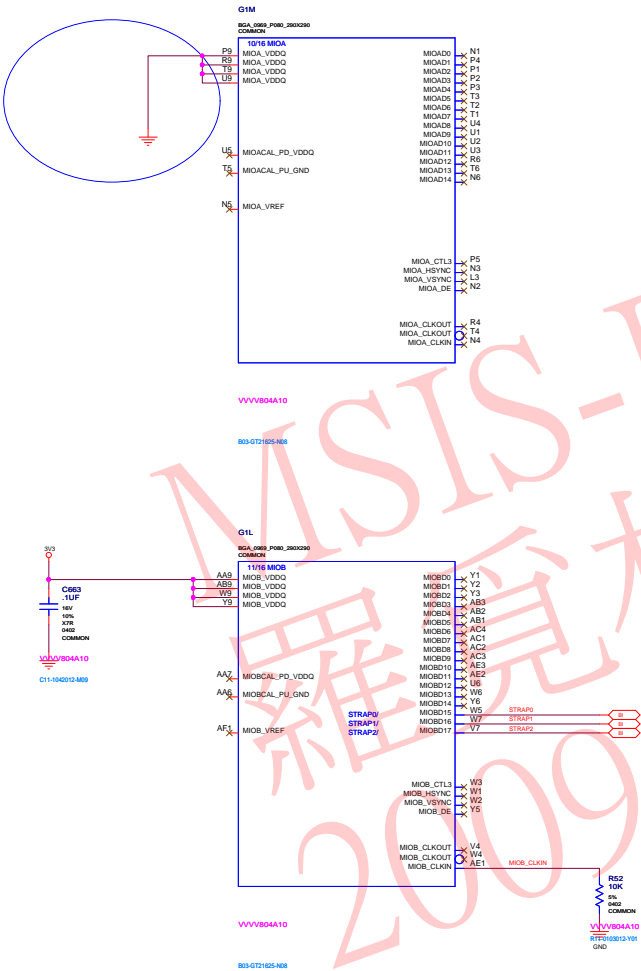
IFP EF (UNUSED)



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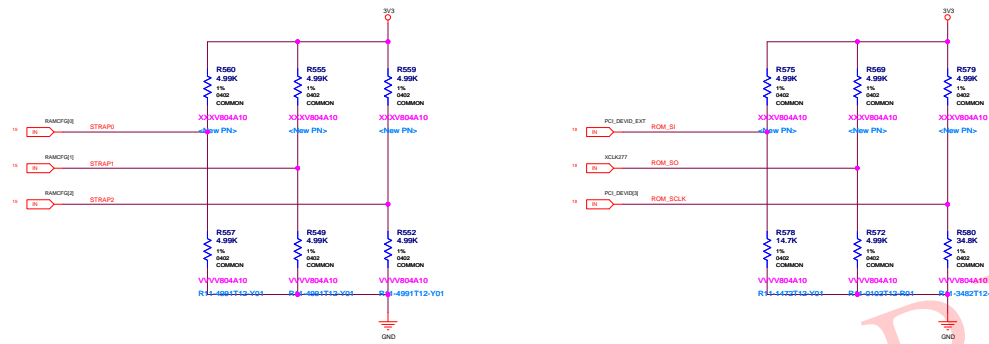
MIOA & MIOB

Remove R545,C664,R554 of MIOA

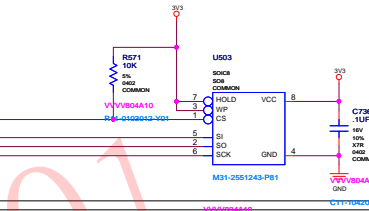
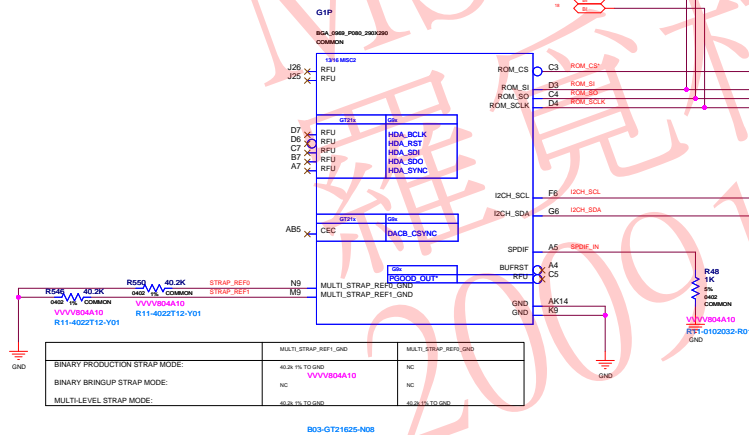


A	B	C	D	E	F	G	H
---	---	---	---	---	---	---	---

STRAPPING OPTIONS



Strap0	USERID[0]	EDID: 1111	45k PU
Strap1	3G0_PNOCFG[0]	DESKTOP 0000	5k PD
Strap2	PD_DEV[0]	GT716-200 (XAT) 0001	15k PD
ROM_S1	RWKCFG[0]	0001 128M 64Mx16 DORS Omicron	1001 64M 64Mx16 DORS Omicron
		0010 128M 64Mx16 DORS Icyon	1010 64M 64Mx16 DORS Icyon
ROM_S1	RWKCFG[0]	0011 128M 64Mx16 DORS Samsung	1011 64M 64Mx16 DORS Samsung
		0101 128M 128Mx16 DORS Omicron	1101 64M 128Mx16 DORS Omicron
ROM_S1X		0110 128M 128Mx16 DORS Icyon	1110 64M 128Mx16 DORS Icyon
		0111 128M 128Mx16 DORS Samsung	1111 64M 128Mx16 DORS Samsung
ROM_S1X	3 POL_DEV2_EXT 2 S1A_VENDOR (1 = BIOS was present) 1 S1C2_CK_CFG (1 = command ROM KMGPS) 0 POL_PU_EN_TERR (0 = disable p4 term)	0110	35k PD
ROM_SQ	3 KX_41T (0 = POL 270MHz) 2 P1_3_0M6_S02 (0 = POL 30MHz) 1 S0M6_ALT_ADDR (LAPOR, 1 = multiGPU) 0 VISA_DEVICE (0=0, device, 1=LAPOR VISA device)	0001	5k PD
<div> <div>0 PD 3k</div> <div>1 PD 10k</div> <div>2 PD 15k</div> <div>3 PD 20k</div> <div>4 PD 25k</div> <div>5 PD 30k</div> <div>6 PD 35k</div> <div>7 PD 40k</div> </div> <div> <div>8 PD 3k</div> <div>9 PD 10k</div> <div>10 PD 15k</div> <div>11 PD 20k</div> <div>12 PD 25k</div> <div>13 PD 30k</div> <div>14 PD 35k</div> <div>15 PD 40k</div> </div>			



For Lab Use Only
I2C ADDRESS: 0XA8

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	BIOS ROM, HDCP ROM, STRAPPING OPTIONS

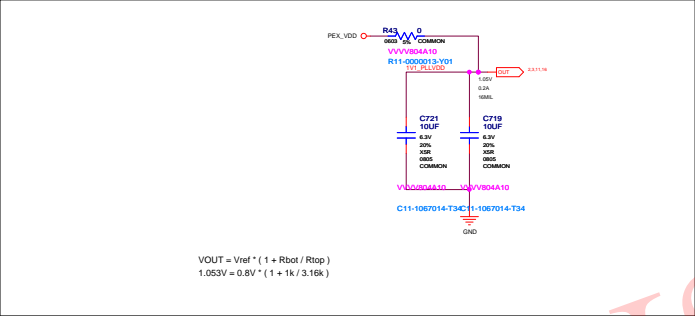
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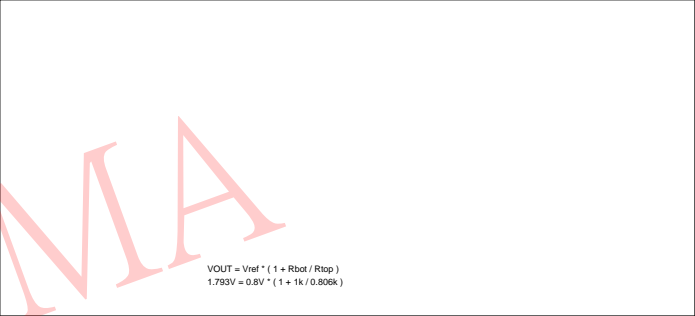
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LINEAR POWER SUPPLIES

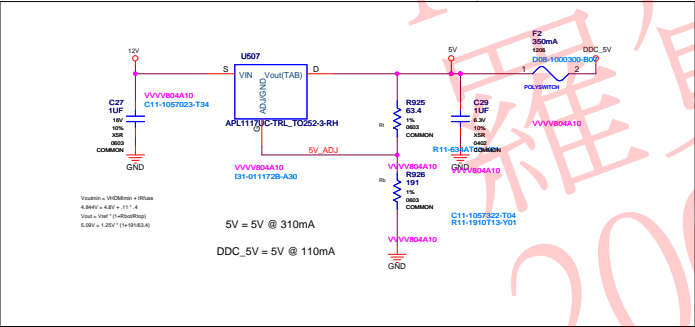
PEX_PLLVDD SUPPLY (OPTIAN)



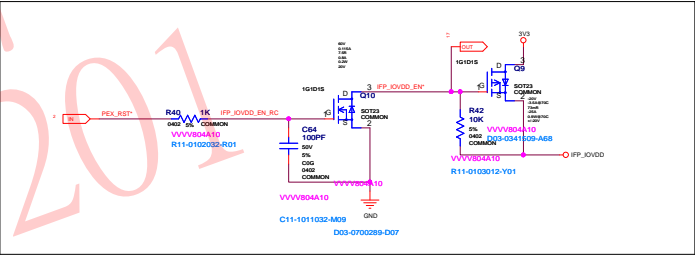
IFP_PLLVDD SUPPLY



5V & DDC_5V REGULATOR



IFP_I0VDD BACKDRIVE PREVENTION



FBVDDQ/PEXVDD POWER SUPPLY

PEXVDD = 1.05V @ 3A

$$V_{out} = V_{ref} * (1 + R_{top} / R_{bot})$$

$$1.05V = 0.8V * (1 + 1.00k / 3.16k)$$

$$FBVDDQ = V_{ref} * (1 + R_{top} / R_{bot})$$

$$1.50V = 0.8V * (1 + 1.58k / 1.82k) @ GPIO13 = 0$$

$$1.80V = 0.8V * (1 + 1.58k / 1.26k) @ GPIO13 = 1 (DEFAULT)$$

- 1.Remove C86,C105,C90
- 2.Add R917 LMOS Gate resister
- 3.Add L10
- 4.Add L9,R23,R24,R26,C86,C87,R26
- 5.Add EL 680UF FootPrint

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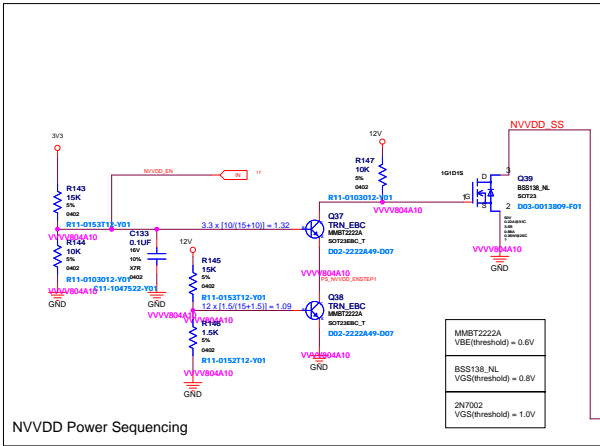
ID	PAGE
NAME	DATE 05/FEB/2009

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	FBVDDQ/PEXVDD POWER SUPPLY

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Power Supply III: NVVDD

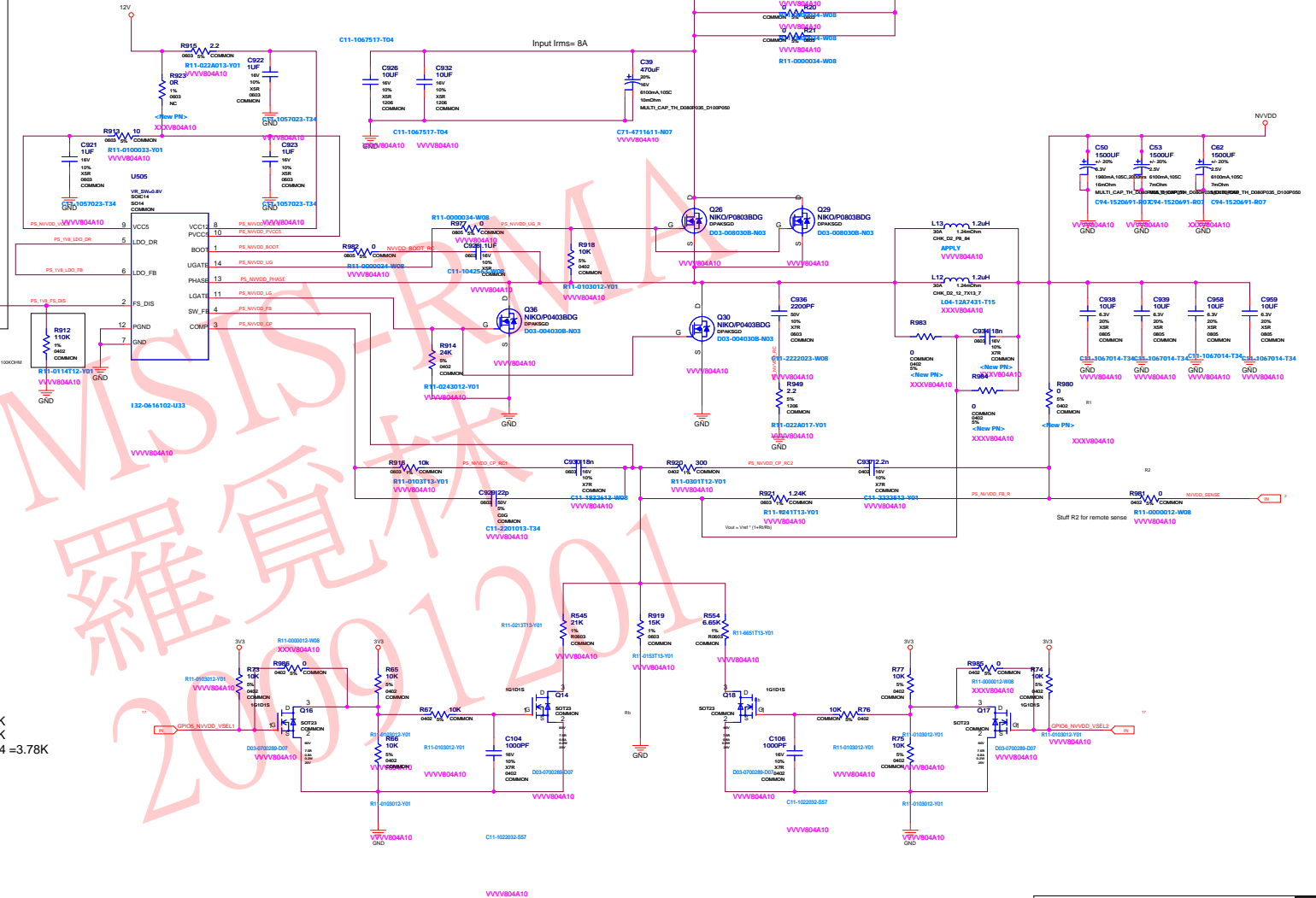
- 1.Add L13
- 2.Remove APW7068 OCSET
- 3.Add GPIO 5,6 Circuit
- 4.Remove C927,C931,C933,C30,C38,C31
- 5.change C39 Footprint for 470uF and 270uF



$$V_{out} = V_{ref} \cdot (1 + R_t/R_b)$$

$$V_{out} = 0.8 \cdot (1 + 1.24k/R_{bn})$$

$R_{b1} = R_{919} // R_{545} = 8.75K$
 $R_{b2} = R_{919} // R_{554} = 4.61K$
 $R_{b3} = R_{919} // R_{545} // R_{554} = 3.78K$



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ASSEMBLY PAGE DETAIL
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NVVDD POWER SUPPLY

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[illegible]

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XTAL, MECHANICALS, THERMALS

