

ASUSTeK COMPUTER INC.	GTX1050 Standard Operation Procedure	Doc. No:	
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		Revision:2.80	
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0Rev.	Modified Chapters	Modification Description	Issued Dept.	Issued by	Revised Date
1.00		First release	GRTD	Maico_Zhang	Oct.10.2016
1.01		Add new P/N(YV0A51-A03)	GRTD	Maico_Zhang	Oct.10.2016
1.02		Add new PCBA(YV0A52-A13)	GRTD	Maico_Zhang	Oct.10.2016
1.03		Add new PCBA (YV0A70-A01)	GRTD	Maico_Zhang	Oct.10.2016
1.04		Add new P/N(YV0A31-A01)	GRTD	Maico_Zhang	Nov.4.2016
1.05		Add new P/N(YV0A30-A01)	GRTD	Maico_Zhang	Nov.4.2016
1.06		Add new P/N(YV0A81-A01)	GRTD	Maico_Zhang	Nov.4.2016
1.07		Add new P/N(YV0A82-A01)	GRTD	Maico_Zhang	Nov.4.2016
1.08		Add new P/N(YV0A82-A11)	GRTD	Maico_Zhang	Nov.4.2016
1.09		Add new P/N(YV0AA0-A01)	GRTD	Maico_Zhang	Nov.4.2016
1.10		Add new P/N(YV0A50-A01)	GRTD	Maico_Zhang	Nov.4.2016
1.11		Add new PCBA(YV0A70-A11)	GRTD	Maico_Zhang	Nov.4.2016
1.12		Add new PCBA(YV0A70-B01)	GRTD	Maico_Zhang	Nov.4.2016
1.13		Add new P/N(YV0A54-A01)	GRTD	Maico_Zhang	Nov.15.2016
1.14		Add new P/N(YV0A80-A03)	GRTD	Maico_Zhang	Nov.15.2016
1.15		Add new P/N(YV0A84-A01)	GRTD	Maico_Zhang	Nov.15.2016
1.16		Add new PCBA(YV0A84-A11)	GRTD	Maico_Zhang	Nov.15.2016
1.17		Add new P/N(YV0AD0-A01)	GRTD	Maico_Zhang	Nov.15.2016
1.18		Add new P/N(YV0AD1-A01)	GRTD	Maico_Zhang	Nov.15.2016
1.19		Add new PCBA(YV0AA0-B01)	GRTD	Maico_Zhang	Nov.24.2016
1.20		Add new PCBA(YV0A80-A13)	GRTD	Maico_Zhang	Nov.24.2016
1.21		Add new PCBA(YV0A54-A11)	GRTD	Maico_Zhang	Nov.24.2016
1.22		Add new PCBA(YV0A52-A26)	GRTD	Maico_Zhang	Nov.24.2016
1.23		Add new PCBA(YV0A52-A35)	GRTD	Maico_Zhang	Nov.24.2016
1.24		Add new P/N(YV0A82-A24)	GRTD	Maico_Zhang	Nov.24.2016
1.25		Add new P/N(YV0A82-A34)	GRTD	Maico_Zhang	Nov.24.2016
1.26		Add new PCBA(YV0A70-A21)	GRTD	Maico_Zhang	Nov.24.2016
1.27		Add new PCBA(YV0A81-A15)	GRTD	Maico_Zhang	Nov.24.2016
1.28		Add new P/N(YV0A30-A14)	GRTD	Maico_Zhang	Feb.5.2016
1.29		Add new P/N(YV0A31-A14)	GRTD	Maico_Zhang	Feb.5.2016
1.30		Add new PCBA(YV0AD0-A11)	GRTD	Maico_Zhang	Feb.5.2016
1.31		Add new PCBA(YV0AD1-A11)	GRTD	Maico_Zhang	Feb.5.2016
1.32		Add new P/N(YV0A50-A15)	GRTD	Maico_Zhang	Feb.5.2016

Authorize by		Review by		Originator By	Maico
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1.33		Add new P/N(YV0A51-A16)	GRTD	Maico_Zhang	Feb.5.2016
1.34		Add new PCBA(YV0AA0-B13)	GRTD	Maico_Zhang	Feb.5.2016
1.35		Add new PCBA(YV0A70-B12)	GRTD	Maico_Zhang	Feb.15.2016
1.36		Add new PCBA(YV0AA0-A13)	GRTD	Maico_Zhang	Dec.29.2016
1.37		Add new P/N(YV0A80-A24)	GRTD	Maico_Zhang	Jan.26.2017
1.38		Add new P/N(YV0A82-A45)	GRTD	Maico_Zhang	Jan.26.2017
1.39		Add new P/N(YV0A84-A22)	GRTD	Maico_Zhang	Jan.26.2017
1.40		Add new P/N(YV0A81-A26)	GRTD	Maico_Zhang	Feb.17.2017
1.41		Add new P/N(YV0A82-A57)	GRTD	Maico_Zhang	Feb.17.2017
1.42		Add new PCBA(YV0A50-A26)	GRTD	Maico_Zhang	Mar.21.2017
1.43		Add new P/N(YV0A80-A34)	GRTD	Maico_Zhang	Mar.21.2017
1.44		Add new P/N(YV0A81-A35)	GRTD	Maico_Zhang	Mar.21.2017
1.45		Add new PCBA(YV0A51-A27)	GRTD	Maico_Zhang	Mar.31.2017
1.46		Add new PCBA(YV0A54-A23)	GRTD	Maico_Zhang	Jun.15.2017
1.47		Add new P/N(YV0A32-A01)	GRTD	Maico_Zhang	Jul.3.2017
1.48		Add new PCBA(YV0A32-A01)	GRTD	Maico_Zhang	Jul.3.2017
1.49		Add new PCBA(YV0A52-A48)	GRTD	Maico_Zhang	Jul.3.2017
1.50		Add new P/N(YV0A56-A01)	GRTD	Maico_Zhang	Jul.3.2017
1.51		Add new PCBA(YV0A55-A01)	GRTD	Maico_Zhang	Jul.14.2017
1.52		Add new PCBA(YV0AX1-A01)	GRTD	Maico_Zhang	Jul.14.2017
1.53		Add new PCBA(YV0AX0-A04)	GRTD	Maico_Zhang	Jul.14.2017
1.54		Add new PN(YV0A30-A27)	GRTD	Maico_Zhang	Jul.25.2017
1.55		Add new PCBA(YV0A31-A27)	GRTD	Maico_Zhang	Jul.25.2017
1.56		Add new PCBA(YV0A34-A01)	GRTD	Maico_Zhang	Jul.25.2017
1.57		Add new PCBA(YV0A34-A11)	GRTD	Maico_Zhang	Jul.25.2017
1.58		Add new PCBA(YV0A57-A01)	GRTD	Maico_Zhang	Aug.1.2017
1.59		Add new PN(YV0A71-A01)	GRTD	Maico_Zhang	Aug.1.2017
1.60		Add new PN(YV0AA1-A01)	GRTD	Maico_Zhang	Aug.1.2017
1.61		Add new PCBA(YV0AA2-A01)	GRTD	Maico_Zhang	Aug.15.2017
1.62		Add new PCBA(YV0A72-A01)	GRTD	Maico_Zhang	Aug.15.2017
1.63		Add new PN(YV0A70-B27)	GRTD	Maico_Zhang	Aug.15.2017
1.64		Add new PCBA(YV0A30-A47)	GRTD	Maico_Zhang	Aug.15.2017
1.65		Add new PCBA(YV0A31-A47)	GRTD	Maico_Zhang	Aug.15.2017
1.66		Add new PCBA(YV0A34-A31)	GRTD	Maico_Zhang	Aug.15.2017
1.67		Add new PCBA(YV0AX3-A02)	GRTD	Maico_Zhang	Aug.15.2017
1.68		Add new PCBA(YV0AX2-A02)	GRTD	Maico_Zhang	Aug.15.2017
1.69		Add new PCBA(YV0A31-A57)	GRTD	Maico_Zhang	Aug.15.2017
1.70		Add new PCBA(YV0A30-A57)	GRTD	Maico_Zhang	Aug.15.2017
1.71		Add new PCBA(YV0AX0-A02)	GRTD	Maico_Zhang	Aug.29.2017
1.72		Add new PN(YV0BB0-A02)	GRTD	Maico_Zhang	Aug.29.2017
1.73		Add new PN(YV0AX1-A14)	GRTD	Maico_Zhang	Aug.29.2017
1.74		Add new PCBA(YV0A30-A37)	GRTD	Maico_Zhang	Sep.15.2017

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1.75		Add new PCBA(YV0A31-A37)	GRTD	Maico_Zhang	Sep.15.2017
1.76		Add new PCBA(YV0A34-A21)	GRTD	Maico_Zhang	Sep.15.2017
1.77		Add new PCBA(YV0BA1-A02)	GRTD	Maico_Zhang	Oct.5.2017
1.78		Add new PCBA(YV0BA2-A01)	GRTD	Maico_Zhang	Oct.5.2017
1.79		Add new PN(YV0AD0-A24)	GRTD	Maico_Zhang	Oct.5.2017
1.80		Add new PCBA(YV0AD1-A24)	GRTD	Maico_Zhang	Oct.23.2017
1.81		Add new PN(YV0BB1-A01)	GRTD	Maico_Zhang	Oct.23.2017
1.82		Add new PCBA(YV0BB2-A01)	GRTD	Maico_Zhang	Oct.23.2017
1.83		Add new PN(YV0AX0-A27)	GRTD	Maico_Zhang	Oct.23.2017
1.84		Add new PCBA(YV0BA0-A01)	GRTD	Maico_Zhang	Oct.23.2017
1.85		Add new PCBA(YV0BA0-A11)	GRTD	Maico_Zhang	Oct.23.2017
1.86		Add new PCBA(YV0BB0-A12)	GRTD	Maico_Zhang	Oct.23.2017
1.87		Add new PCBA(YV0A72-A12)	GRTD	Maico_Zhang	Oct.23.2017
1.88		Add new PCBA(YV0BB0-A22)	GRTD	Maico_Zhang	Oct.23.2017
1.89		Add new PCBA(YV0A71-A12)	GRTD	Maico_Zhang	Nov.16.2017
1.90		Add new PCBA(YV0A71-A22)	GRTD	Maico_Zhang	Nov.16.2017
1.91		Add new PCBA(YV0A52-A5B)	GRTD	Maico_Zhang	Nov.16.2017
1.92		Add new PCBA(YV0BB0-A32)	GRTD	Maico_Zhang	Nov.24.2017
1.93		Add new PCBA(YV0BB3-A01)	GRTD	Maico_Zhang	Dec.13.2017
1.94		Add new PCBA(YV0BB4-A01)	GRTD	Maico_Zhang	Dec.13.2017
1.95		Add new PCBA(YV0A72-A25)	GRTD	Maico_Zhang	Dec.13.2017
1.96		Add new PCBA(YV0BA3-A12)	GRTD	Maico_Zhang	Dec.28.2017
1.97		Add new PCBA(YV0A30-A69)	GRTD	Maico_Zhang	Dec.28.2017
1.98		Add new PCBA(YV0A34-A43)	GRTD	Maico_Zhang	Dec.28.2017
1.99		Add new PCBA(YV0A31-A69)	GRTD	Maico_Zhang	Dec.28.2017
2.00		Add new PN(YV0AD0-A37)	GRTD	Maico_Zhang	Jan.4.2018
2.01		Add new PCBA(YV0AD1-A36)	GRTD	Maico_Zhang	Jan.4.2018
2.02		Add new PN(YV0AX0-A39)	GRTD	Maico_Zhang	Jan.4.2018
2.03		Add new PCBA(YV0BA4-A12)	GRTD	Maico_Zhang	Jan.4.2018
2.04		Add new PN(YV0BB4-A13)	GRTD	Maico_Zhang	Jan.12.2018
2.05		Add new PCBA(YV0BM0-A02)	GRTD	Maico_Zhang	Jan.12.2018
2.06		Add new PCBA(YV0BB1-A16)	GRTD	Maico_Zhang	Jan.12.2018
2.07		Add new PCBA(YV0BB2-A17)	GRTD	Maico_Zhang	Jan.12.2018
2.08		Add new PCBA(YV0AX2-A15)	GRTD	Maico_Zhang	Jan.12.2018
2.09		Add new PCBA(YV0AX3-A15)	GRTD	Maico_Zhang	Jan.31.2018
2.10		Add new PCBA(YV0BB3-A13)	GRTD	Maico_Zhang	Jan.31.2018
2.11		Add new PCBA(YV0BB5-A01)	GRTD	Maico_Zhang	Jan.31.2018
2.12		Add new PCBA(YV0BB5-A11)	GRTD	Maico_Zhang	Jan.31.2018
2.13		Add new PCBA(YV0BB4-A23)	GRTD	Maico_Zhang	Jan.31.2018
2.14		Add new PCBA(YV0AX0-A49)	GRTD	Maico_Zhang	Jan.31.2018
2.15		Add new PCBA(YV0BB0-A44)	GRTD	Maico_Zhang	Jan.31.2018
2.16		Add new PCBA(YV0BB3-A24)	GRTD	Maico_Zhang	Feb.28.2018

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2.17		Add new PCBA(YV0BM0-A12)	GRTD	Maico_Zhang	Feb.28.2018
2.18		Add new PCBA(YV0BB4-A34)	GRTD	Maico_Zhang	Mar.16.2018
2.19		Add new PCBA(YV0BB4-A44)	GRTD	Maico_Zhang	Mar.16.2018
2.20		Add new PCBA(YV0BB4-A54)	GRTD	Maico_Zhang	Mar.16.2018
2.21		Add new PCBA(YV0BB0-A55)	GRTD	Maico_Zhang	Mar.16.2018
2.22		Add new PCBA(YV0BB0-A65)	GRTD	Maico_Zhang	Mar.16.2018
2.23		Add new PCBA(YV0BB3-A34)	GRTD	Maico_Zhang	Mar.16.2018
2.24		Add new PCBA(YV0BB5-A32)	GRTD	Maico_Zhang	Mar.16.2018
2.25		Add new PCBA(YV0BB5-A42)	GRTD	Maico_Zhang	Mar.16.2018
2.26		Add new PCBA(YV0BB0-A76)	GRTD	Maico_Zhang	Apr.10.2018
2.27		Add new PCBA(YV0BB3-A54)	GRTD	Maico_Zhang	Apr.10.2018
2.28		Add new PCBA(YV0BB4-A64)	GRTD	Maico_Zhang	Apr.10.2018
2.29		Add new PCBA(YV0BB4-A84)	GRTD	Maico_Zhang	Apr.10.2018
2.30		Add new PCBA(YV0A30-B02)	GRTD	Maico_Zhang	Apr.10.2018
2.31		Add new PCBA(YV0A31-B01)	GRTD	Maico_Zhang	Apr.10.2018
2.32		Add new PCBA(YV0A34-B01)	GRTD	Maico_Zhang	Apr.10.2018
2.33		Add new PCBA(YV0BB2-B28)	GRTD	Maico_Zhang	May.10.2018
2.34		Add new PCBA(YV0BA0-A24)	GRTD	Maico_Zhang	May.10.2018
2.35		Add new PCBA(YV0BM1-A01)	GRTD	Maico_Zhang	May.10.2018
2.36		Add new PCBA(YV0BM2-A01)	GRTD	Maico_Zhang	May.10.2018
2.37		Add new PCBA(YV0BM3-A01)	GRTD	Maico_Zhang	May.10.2018
2.38		Add new PCBA(YV0BB5-A52)	GRTD	Maico_Zhang	May.10.2018
2.39		Add new PCBA(YV0BM0-A22)	GRTD	Maico_Zhang	May.10.2018
2.40		Add new PCBA(YV0BB4-A74)	GRTD	Maico_Zhang	May.10.2018
2.41		Add new PCBA(YV0BM0-A32)	GRTD	Maico_Zhang	May.10.2018
2.42		Add new PCBA(YV0BM0-A42)	GRTD	Maico_Zhang	May.10.2018
2.43		Add new PCBA(YV0BB5-A82)	GRTD	Maico_Zhang	May.10.2018
2.44		Add new PCBA(YV0BL0-A01)	GRTD	Maico_Zhang	Jun.13.2018
2.45		Add new PCBA(YV0BM1-A11)	GRTD	Maico_Zhang	Jun.13.2018
2.46		Add new PCBA(YV0BM2-A11)	GRTD	Maico_Zhang	Jun.13.2018
2.47		Add new PCBA(YV0BM3-A11)	GRTD	Maico_Zhang	Jun.13.2018
2.48		Add new PN(YV0BZ0-A01)	GRTD	Maico_Zhang	Jun.13.2018
2.49		Add new PCBA(YV0BZ1-A01)	GRTD	Maico_Zhang	Jun.13.2018
2.50		Add new PN(YV0BL1-A01)	GRTD	Maico_Zhang	Jun.13.2018
2.51		Add new PCBA(YV0A30-C02)	GRTD	Maico_Zhang	Jul.6.2018
2.52		Add new PCBA(YV0A31-C04)	GRTD	Maico_Zhang	Jul.30.2018
2.53		Add new PCBA(YV0A30-C14)	GRTD	Maico_Zhang	Jul.30.2018
2.54		Add new PCBA(YV0A31-C14)	GRTD	Maico_Zhang	Jul.30.2018
2.55		Add new PCBA(YV0BM1-A22)	GRTD	Maico_Zhang	Jul.30.2018
2.56		Add new PCBA(YV0BM2-A22)	GRTD	Maico_Zhang	Aug.10.2018
2.57		Add new PCBA(YV0BM3-A23)	GRTD	Maico_Zhang	Aug.10.2018
2.58		Add new PCBA(YV0BM0-A63)	GRTD	Maico_Zhang	Aug.10.2018

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2.59		Add new PCBA(YV0BZ1-A12)	GRTD	Maico_Zhang	Aug.22.2018
2.60		Add new PCBA(YV0BZ0-A12)	GRTD	Maico_Zhang	Aug.22.2018
2.61		Add new PCBA(YV0BL2-A01)	GRTD	Maico_Zhang	Sep.6.2018
2.62		Add new PCBA(YV0BL3-A01)	GRTD	Maico_Zhang	Sep.6.2018
2.63		Add new PCBA(YV0BL0-A12)	GRTD	Maico_Zhang	Sep.6.2018
2.64		Add new PCBA(YV0BL1-A13)	GRTD	Maico_Zhang	Sep.6.2018
2.65		Add new PCBA(YV0BM1-A33)	GRTD	Maico_Zhang	Oct.8.2018
2.66		Add new PCBA(YV0BM2-A33)	GRTD	Maico_Zhang	Oct.8.2018
2.67		Add new PCBA(YV0BM3-A33)	GRTD	Maico_Zhang	Oct.8.2018
2.68		Add new PCBA(YV0BA0-A35)	GRTD	Maico_Zhang	Oct.25.2018
2.69		Add new PCBA(YV0AD0-A01)	GRTD	Maico_Zhang	Oct.25.2018
2.70		Add new PCBA(YV0AD1-A01)	GRTD	Maico_Zhang	Oct.25.2018
2.71		Add new PCBA(YV0BM0-A74)	GRTD	Maico_Zhang	Nov.13.2018
2.72		Add new PCBA(YV0BM4-A01)	GRTD	Maico_Zhang	Dec.26.2018
2.73		Add new PCBA(YV0BL3-A11)	GRTD	Maico_Zhang	Dec.26.2018
2.74		Add new PCBA(YV0BM0-A84)	GRTD	Maico_Zhang	Feb.21.2019
2.75		Add new PCBA(YV0BM1-A43)	GRTD	Maico_Zhang	Feb.21.2019
2.76		Add new PCBA(YV0BM2-A43)	GRTD	Maico_Zhang	Feb.21.2019
2.77		Add new PCBA(YV0BM3-A43)	GRTD	Maico_Zhang	Mar.28.2019
2.78		Add new PCBA(YV0BM1-A53)	GRTD	Maico_Zhang	Jun.26.2019
2.79		Add new PCBA(YV0BM2-A53)	GRTD	Maico_Zhang	Jun.26.2019
2.80		Add new PCBA(YV0BM3-A53)	GRTD	Maico_Zhang	Jun.26.2019

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1.Objectives

Create test procedure.

2.Scope

Model:GTX1050

PN: YV0A52(A02) YV0A51(A03) YV0A52(A13) YV0A70(A01) YV0A31(A01)
YV0A30(A01) YV0A81(A01) YV0A82(A01) YV0A82(A11) YV0AA0(A01)
YV0A50(A05) YV0A70(A11) YV0A70(B01) YV0A54(A01) YV0A80(A03)
YV0AD0(A01) YV0AD1(A01) YV0A84(A01) YV0A84(A11) YV0AA0(B01)
YV0A80(A13) YV0A54(A11) YV0A52(A26) YV0A52(A34) YV0A82(A24)
YV0A82(A34) YV0A70(A21) YV0A81(A15) YV0A30(A14) YV0A31(A14)
YV0A50(A15) YV0A51(A16) YV0AD0(A11) YV0AD1(A01) YV0AA0(B13)
YV0A70(B12) YV0AA0(A13) YV0A80(A24) YV0A82(A42) YV0A84(A22)
YV0A81(A26) YV0A82(A57) YV0A50(A26) YV0A80(A34) YV0A81(A35)
YV0A51(A27) YV0A54(A23) YV0A32(A01) YV0A33(A01) YV0A52(A48)
YV0A56(A01) YV0A55(A01) YV0AX1(A01) YV0AX0(A04)YV0A30(A27)
YV0A31(A27) YV0A34(A01) YV0A34(A11) YV0A57(A01) YV0A71(A01)
YV0AA1(A01) YV0AA2(A01) YV0A70(B27) YV0A30(A47) YV0A31(A47)
YV0A34(A31) YV0AX3(A02) YV0AX2(A02) YV0A31(A57) YV0A30(A57)
YV0AX0(A16) YV0BB0(A02) YV0AX1(A14) YV0A30(A37) YV0A31(A37)
YV0A34(A21) YV0BB1(A01) YV0AX0(A27) YV0BA0(A01) YV0BA0(A12)
YV0BB0(A12) YV0A72(A12) YV0BB0(A22) YV0A71(A12) YV0A71(A22)
YV0A52(A5B) YV0BB0(A32) YV0BB3(A01) YV0BB4(A01) YV0A72(A25)
YV0BA3(A12) YV0A30(A69) YV0A34(A43) YV0A31(A69) YV0AD0(A37)
YV0AD1(A36) YV0AX0(A39) YV0BA4(A12) YV0BM0(A02) YV0BB4(A12)
YV0BB1(A16) YV0BB2(A17) YV0AX2(A15) YV0BB4(A23) YV0AX3(A15)
YV0AX0(A09) YV0BB3(A13) YV0BB5(A01) YV0BB5(A11) YV0BB0(A44)
YV0BB3(A24) YV0BM0(A12) YV0BB3(A34) YV0BB0(A55) YV0BB0(A65)
YV0BB4(A34) YV0BB4(A44) YV0BB4(A54) YV0BB5(A32) YV0BB5(A42)
YV0BB0(A76) YV0BB3(A54) YV0BB4(A64) YV0A30(B02) YV0A31(B01)
YV0A34(B01) YV0BB4(A84) YV0BB4(A74) YV0BB2(A28)) YV0BA0(A24)
YV0BM1(A01) YV0BM2(A01) YV0BM3(A01) YV0BM0(A22) YV0BM0(A32)
YV0BM0(A42) YV0BB5(A52) YV0BB5(A82) YV0BL0(A01) YV0BL1(A01)
YV0BZ0(A01) YV0BZ1(A01) YV0BM1(A11) YV0BM2(A11) YV0BM3(A11)
YV0A30(C02) YV0A31(C04) YV0A30(C14) YV0A31(C14) YV0BM1(A22)
YV0BM2(A22) YV0BM3(A23) YV0BM0(A63) YV0BZ1(A12) YV0BZ0(A12)
YV0BL2(A01) YV0BL3(A01) YV0BL0(A12) YV0BL1(A13) YV0BM1(A33)
YV0BM2(A33) YV0BM3(A33) YV0BA0(A35) YV0AD0(B01) YV0AD1(B01)
YV0BM0(A73) YV0BL3(A11) YV0BM4(A01) YV0BM0(A84) YV0BM1(A43)
YV0BM2(A43) YV0BM3(A43) YV0BM1(A53)YV0BM2(A53) YV0BM3(A53)

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3.Test Item

3.1 Diagnostic test in Tiny Linux

4.Relevant Document

None.

5.Definition : 1. If the model name contains P, please test HDCP function.

Ex: EAH***/HTDP/512M/A

2. If you test HDMI already, it's unnecessary to test HDCP. The HDMI contains HDCP function.

6. Operation Procedures:

6.1 Attention

- 6.1.1 To avoid damaging devices or M/B, please turn off A/C power then unplug VGA card.
- 6.1.2 While unplugging the card, do it in upright direction to avoid damaging slot & golden finger.
- 6.1.3 If any problem happens, please inform test supervisor. Base on ATX power specification, +5V standby voltage is existed while AC power is connecting. For safety reason, please turn off main power during break time or idling test.
- 6.1.4 Some test items should be tested under Windows environment. (Please install windows test environment by yourself).

6.2 Fixture connect

6.2.1 Install Intel CPU & CPU Fan.

6.2.2 Install DDR Memory to DIMM1~4.

6.2.3 Plug USB Mouse and Keyboard into MB.

6.2.4 Plug VGA Card GTX1050 into PCIE X16.

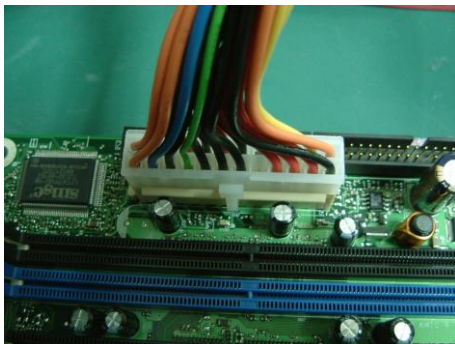
6.2.5 Connect power con of VGA card & Power supply via bundled power cable.

6.2.6 Connect SATA HDD with SATA CON.



6.2.7 Plug 4PIN 12V power cable into CON12V.

6.2.8 Plug ATX Power on Power Connector.



6.3 Diagnostic test in Tiny Linux.

6.3.1 Please connect LCD monitor & card's first DVI CON via a DVI cable.

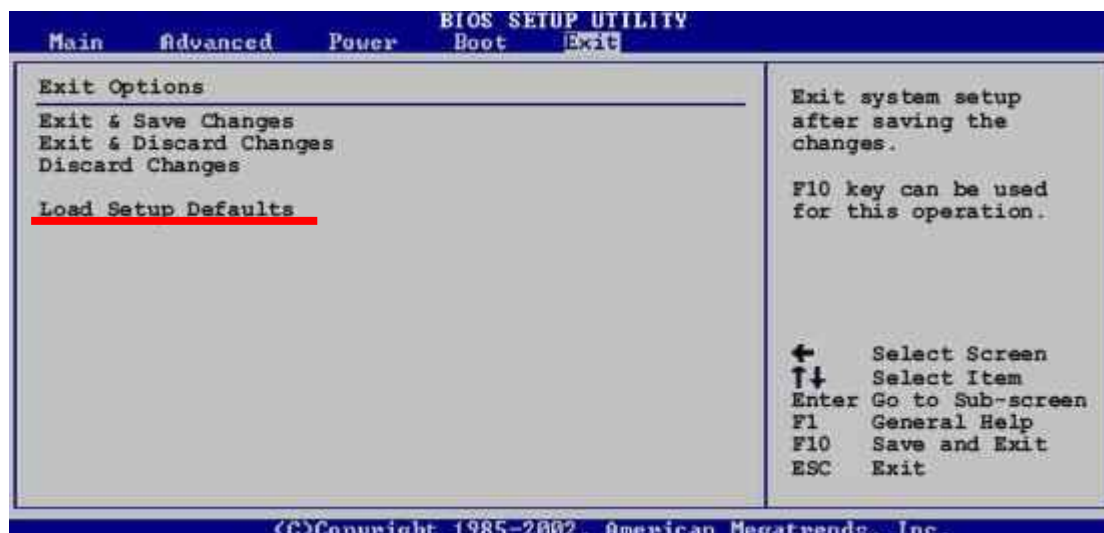


6.3.2 Press Power Switch then turn on power button.

6.3.3 Press "Del" to enter CMOS setup menu while boot screen is displaying.

EXIT -> Load Setup Defaults.

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6.3.4 System will reboot, select Linux to run test program.

Path:GTX1050\yv0a52a0.sh or yv0a52a2.sh or yv0a52a4.sh or yv0ax0a0.sh or yv0ax3a0.sh

Path:GTX1050l\ yv0a51a0.sh

Path:GTX10503\ yv0a70a0.sh or yv0a70a1.sh or yv0a70b0.sh or yv0a70a2.sh or yv0a70b1.sh

Path:GTX10504\ yv0a31a0.sh or yv0a31a5.sh

Path:GTX10505\ yv0a30a0.sh or yv0a30a5.sh

Path:GTX10506\ yv0a81a0.sh or yv0a81a1.sh

Path:GTX10507\ yv0a82a0.sh

Path:GTX10508\ yv0a82a1.sh

Path:GTX10509\ yv0aa0a0.sh or yv0aa0b0.sh or yv0aa0b1.sh or yv0aa0a1.sh or yv0ba0a0.sh or yv0ba0a1.sh or yv0ba0a2.sh or yv0bl0a0.sh or yv0bl0a1.sh or yv0ba0a3.sh

Path:GTX1050a\ yv0a50a0.sh

Path:GTX1050b\ yv0a54a0.sh or yv0a54a1.sh or yv0a54a2.sh or yv0ax1a0.sh or yv0ax2a0.sh

Path:GTX1050c\ yv0a80a0.sh or yv0a80a1.sh

Path:GTX1050d\ yv0ad0a0.sh or yv0ad0a1.sh

Path:GTX1050e\ yv0ad1a0.sh or yv0ad1a1.sh

Path:GTX1050f\ yv0a84a0.sh or yv0a84a1.sh

Path:GTX1050g\ yv0a82a2.sh

Path:GTX1050h\ yv0a82a3.sh

Path:GTX1050i\ yv0a30a1.sh or yv0a30a4.sh

Path:GTX1050j\ yv0a31a1.sh or yv0a31a4.sh

Path:GTX1050k\ yv0a50a1.sh or yv0a50a2.sh

Path:GTX1050l\ yv0a51a1.sh or yv0a51a2.sh

Path:GTX1050m\ yv0a80a2.sh

Path:GTX1050n\ yv0a82a4.sh

Path:GTX1050o\ yv0a84a2.sh

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Path:GTX1050p\ yv0a81a2.sh
 Path:GTX1050q\ yv0a82a5.sh
 Path:GTX1050r\ yv0a80a3.sh
 Path:GTX1050s\ yv0a81a3.sh
 Path:GTX1050t\ yv0a32a0.sh or yv0a33a0.sh
 Path:GTX1050u\ yv0a56a0.sh or yv0a57a0.sh
 Path:GTX1050v\ yv0a30a2.sh or yv0a31a2.sh or yv0a34a0.sh or yv0a34a1.sh or
 yv0a34a3.sh or yv0a30a3.sh or yv0a31a3.sh or yv0a34a2.sh or yv0a30a6.sh or
 yv0a31a6.sh or yv0a34a4.sh or yv0a30b0.sh or yv0a31b0.sh or yv0a34b0.sh or
 yv0a30c0.sh or yv0a31c0.sh or yv0a30c1.sh or yv0a31c1.sh
 Path:GTX1050w\ yv0a71a0.sh or yv0a72a0.sh or yv0a72a1.sh or yv0a71a1.sh or
 yv0a71a2.sh or yv0a72a2.sh
 Path:GTX1050x\ yv0aa1a0.sh or yv0aa2a0.sh or yv0ba1a0.sh or yv0ba2a0.sh or
 yv0ba3a1.sh or yv0ba4a1.sh or yv0bl2a0.sh or yv0bl3a0.sh or yv0bl3a1.sh
 Path:GTX1050y\ yv0a70b2.sh
 Path:GTX1050z\ yv0bb0a0.sh or yv0bb0a1.sh or yv0bb0a2.sh or yv0bb0a3.sh or
 yv0bm0a0.sh or yv0bb0a4.sh or yv0bm0a1.sh or yv0bb0a5.sh or yv0bb0a6.sh or
 yv0bb0a7.sh or yv0bm0a2.sh or yv0bm0a3.sh or yv0bm0a4.sh or yv0bm0a6.sh
 or yv0bm0a7.sh or yv0bm0a8.sh
 Path:GTX1050a1\ yv0ax1a1.sh or yv0ax2a1.sh
 Path:GTX1050a2\ yv0ad0a2.sh or yv0ad1a2.sh
 Path:GTX1050a3\ yv0bb1a0.sh or yv0bb2a0.sh or yv0bb3a0.sh or yv0bb4a0.sh or
 yv0bb1a1.sh or yv0bb2a1.sh or yv0bb3a1.sh or yv0bb5a0.sh or yv0bb5a1.sh or
 yv0bb4a2.sh or yv0bb3a2.sh or yv0bb3a3.sh or yv0bb4a3.sh or yv0bb4a4.sh or
 yv0bb5a3.sh or yv0bb5a4.sh or yv0bb3a5.sh or yv0bb4a8.sh or yv0bm1a0.sh or
 yv0bm2a0.sh or yv0bm3a0.sh or yv0bb5a5.sh or yv0bb4a7.sh or yv0bb5a8.sh or
 yv0bm1a1.sh or yv0bm2a1.sh or yv0bm3a1.sh or yv0bm1a2.sh or yv0bm2a2.sh
 or yv0bm3a2.sh or yv0bm1a3.sh or yv0bm2a3.sh or yv0bm3a3.sh or
 yv0bm1a4.sh or yv0bm2a4.sh or yv0bm3a4.sh or yv0bm1a5.sh or yv0bm2a5.sh
 or yv0bm3a5.sh
 Path:GTX1050a4\ yv0ax0a2.sh or yv0ax0a4.sh
 Path:GTX1050a5\ yv0ad0a3.sh or yv0ad1a3.sh or yv0ad0b0.sh or yv0ad1b0.sh
 Path:GTX1050a6\ yv0ax0a3.sh
 Path:GTX1050a7\ yv0bb4a1.sh or yv0bb4a5.sh or yv0bb4a6.sh or yv0bb2a2.sh or
 yv0bm4a0.sh
 Path:GTX1050a8\ yv0bz0a0.sh or yv0bz1a0.sh or yv0bz1a1.sh or yv0bz0a1.sh
 Path:GTX1050a9\ yv0bl1a0.sh or yv0bl1a1.sh
 Path:GTX1050b1\ yv0a52a1.sh or yv0a52a3.sh or yv0a55a0.sh or yv0ax0a1.sh or
 yv0a52a5.sh or yv0ax3a1.sh or yv0ax3a2.sh

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Run the test program refer to the prompt.

6.3.5 Memory test

If you want to test memory, please run **./mats -e xx** or **./mats -c xx**

Where, -c xx----- only xx% of frame buffer is detected.

Path:GTX1050\

6.3.7 BIOS flash

If you want to check Vbios infor, Please run **biosinfo**

Please use the following command to flash bios and notice the size of character.

Path: Path:GTX1050\

Command: **nvflash xxx.rom -6 -A** or **iromflsh xxx.rom**

Notice:

If you can not flash bios because of the bios locked, please use **nvflash.exe /r** or **nvflash.exe -r** to unlock bios. And then, please use **nvflash.exe /w** or **nvflash.exe -w** to lock bios after updating latest bios.

6.3.8 Please connect LCD monitor & card's the other DVI CON via a DVI cable.

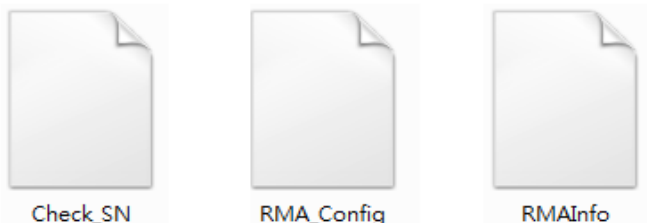
(Please refer to step 6.3.2&6.3.4)



6.4 Result upload to server (need connect to internet)

6.4.1 Download and copy RMA upload tool to diag folder by model (It don't need copy after 2018.5 uploaded model).

The tool of RMA upload tool include 3 files:

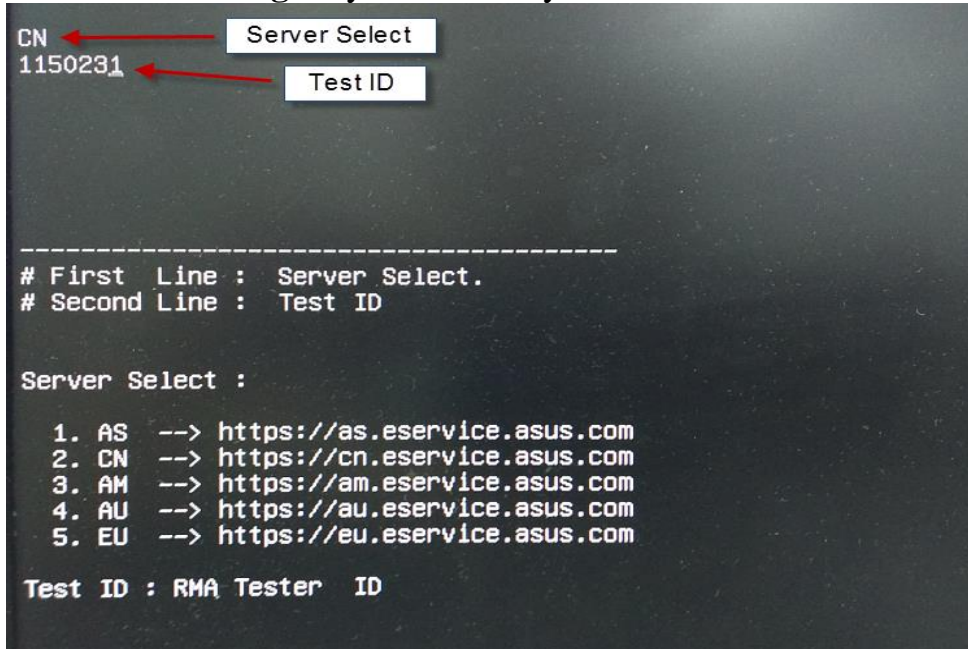


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6.4.2 Edit **RMA_Config** file:

First line confirms the address of local sever, Second line is Tester ID.

The **RMA_Config** only need modify one time.



```

CN ← Server Select
1150231 ← Test ID

-----
# First Line : Server Select.
# Second Line : Test ID

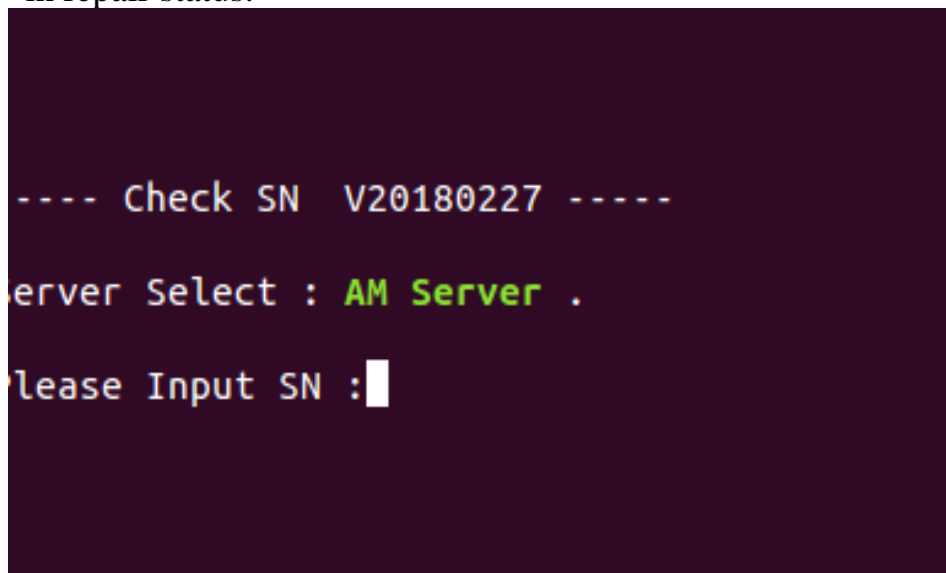
Server Select :

1. AS --> https://as.eservice.asus.com
2. CN --> https://cn.eservice.asus.com
3. AM --> https://am.eservice.asus.com
4. AU --> https://au.eservice.asus.com
5. EU --> https://eu.eservice.asus.com

Test ID : RMA Tester ID

```

6.4.3 Run **Check_SN** and type in your VGA SN which need to be testing. The SN must be in repair status.



```

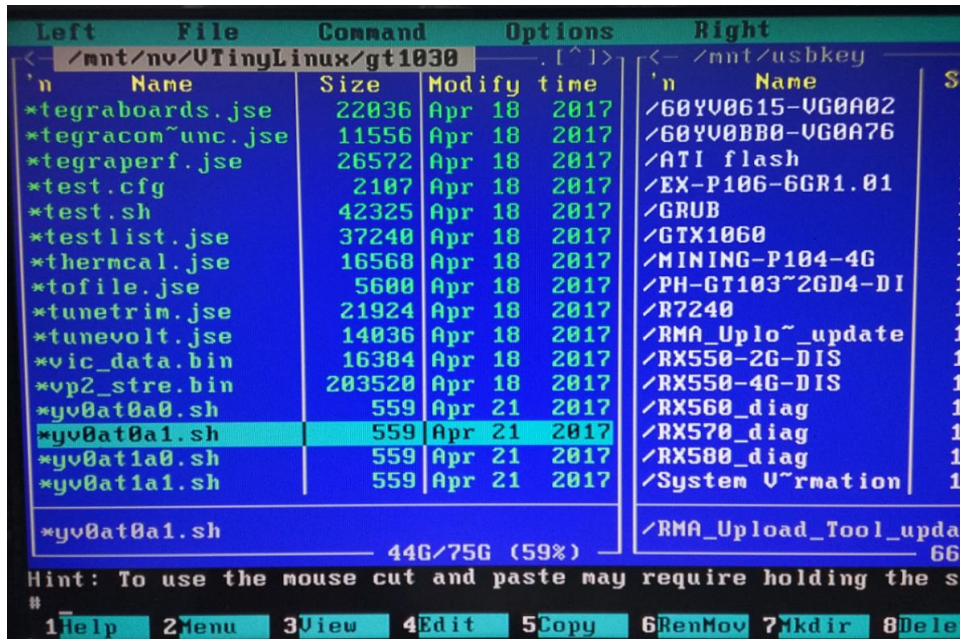
---- Check SN V20180227 ----

Server Select : AM Server .

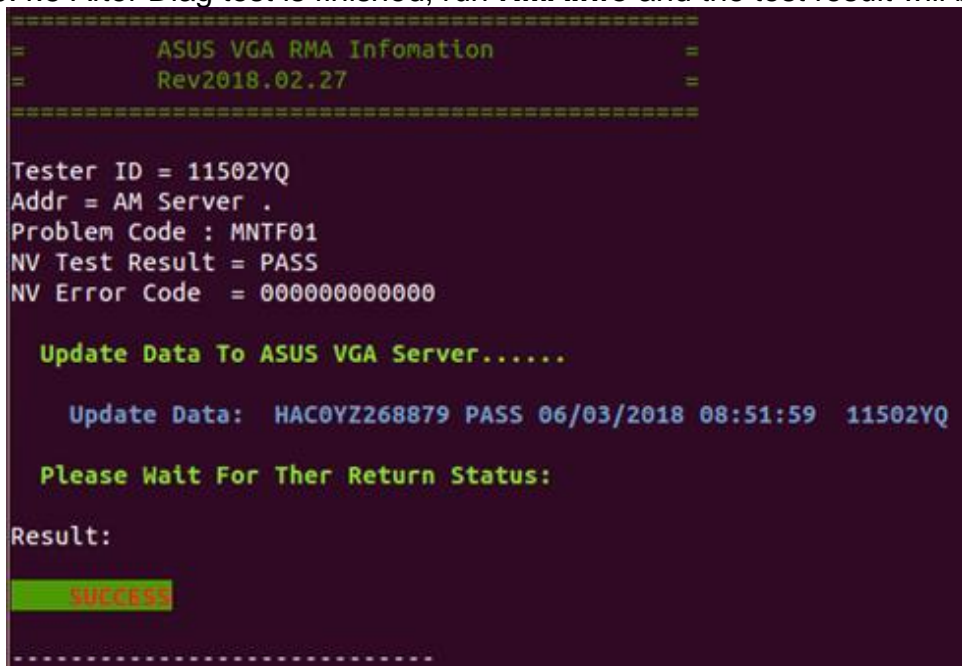
Please Input SN :

```

6.4.4 Run **Diag test** and the diag name is same as the PN of your VGA card.



6.4.5 After Diag test is finished, run **RMAInfo** and the test result will be sent to server.



6.5 If test OK, please NTF test.

NO#	Equipment Description	ASUS Part#	Supplier	Quantity	Remarks
1	LCD Monitor with HDMI/Display Port 4096 x 2304 @ 60 Hz		local	1	
2	USB MOUSE		local	1	
3	KEYBOARD		local	1	
4	ATX POWER SUPPLY(a minimum power of 500W)		local	1	
5	Intel CPU Intel(R) I5 7500 CPU		local	1	
6	Heat sink + FAN		local	1	
7	DDR4 2400 4G		local	2	
8	TEST HDD		local	1	
9	SATA HDD Cable		local	1	
10	HDMI cable (Buying a local HDMI cable by yourself)		local	1	
11	ASUS Prime B250-plus MB		local	1	
12	DVI port cable		local	1	
13	Display port cable (Buying a local Display port cable by yourself)		local	1	