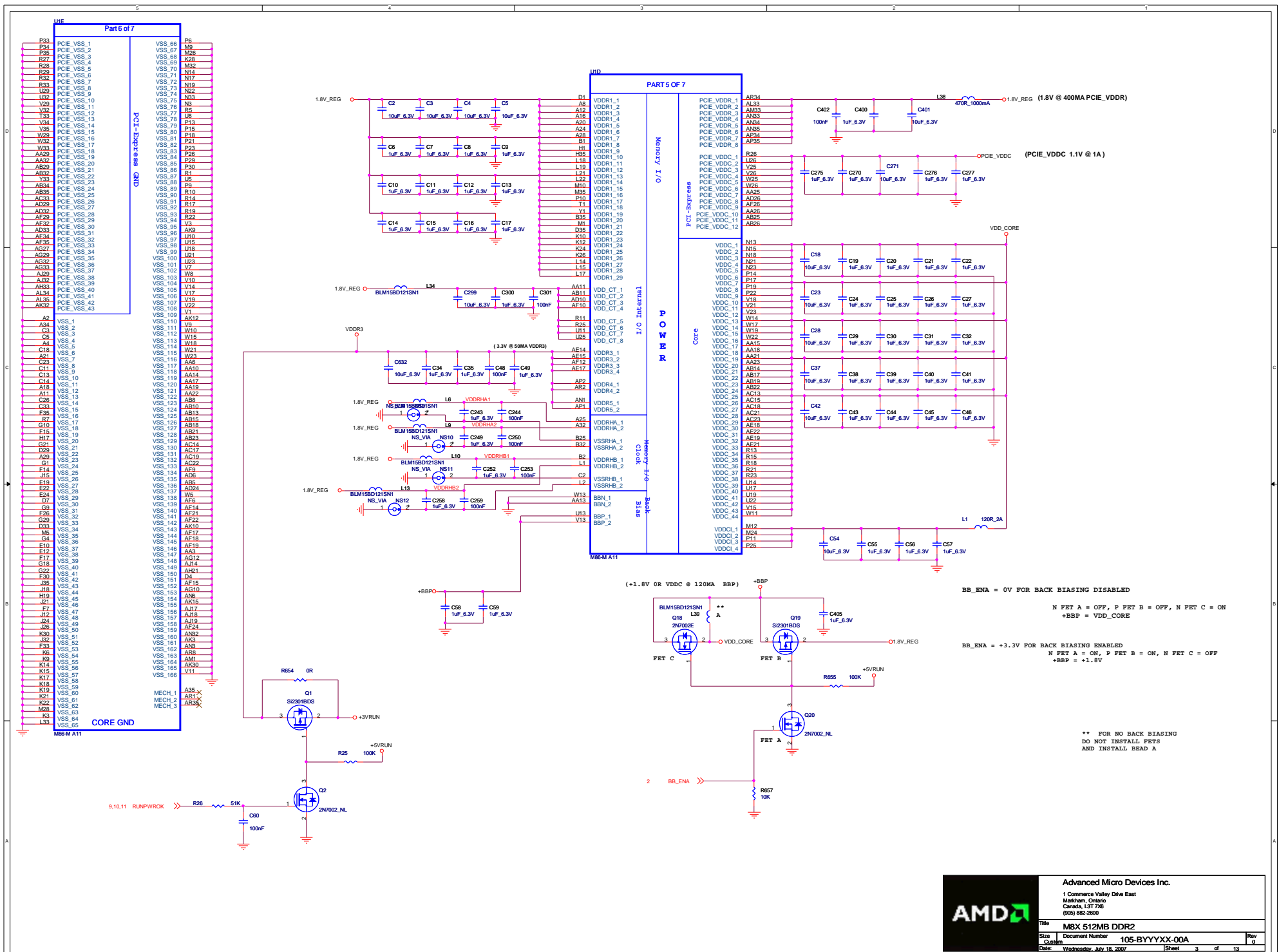
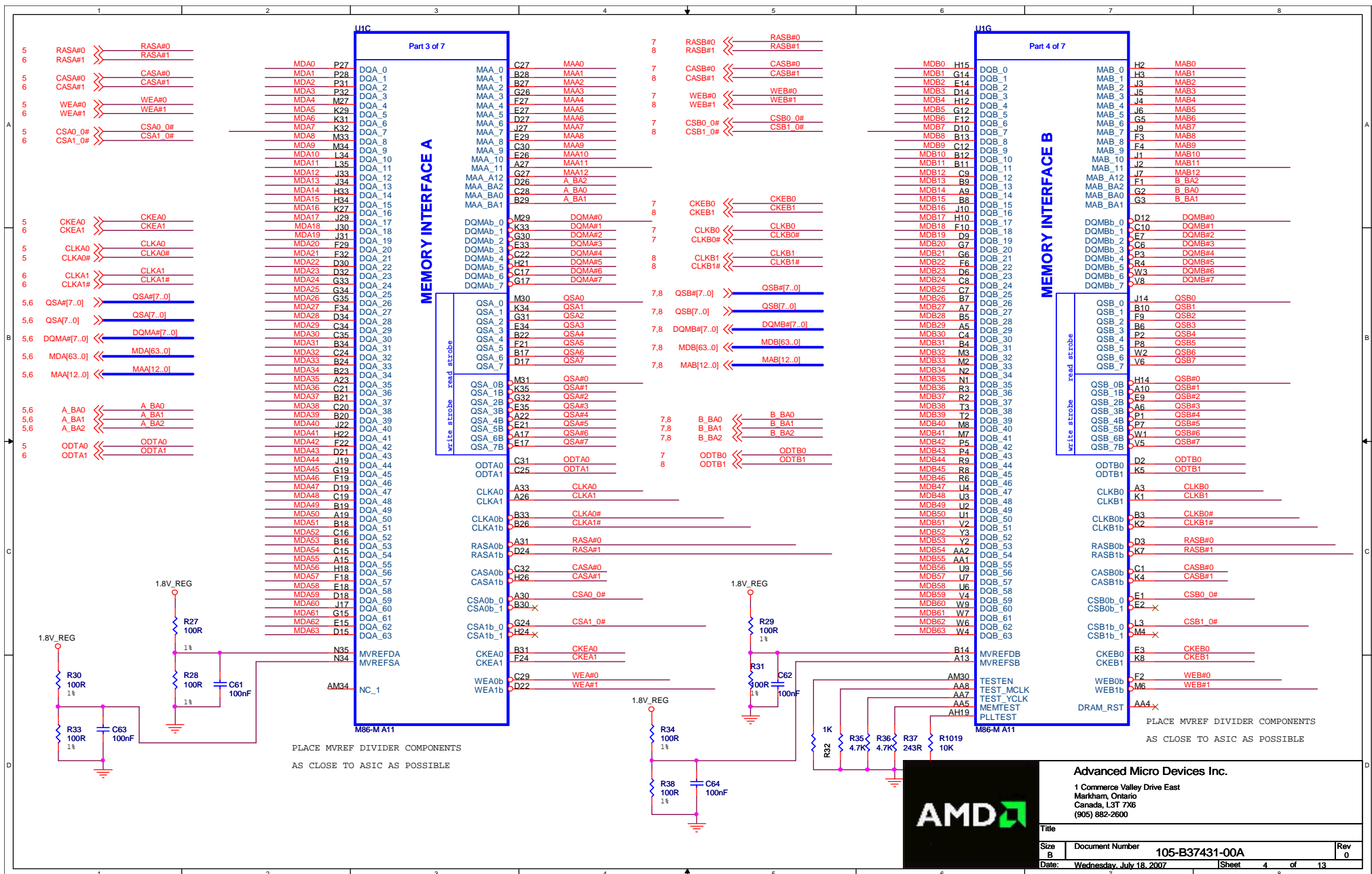


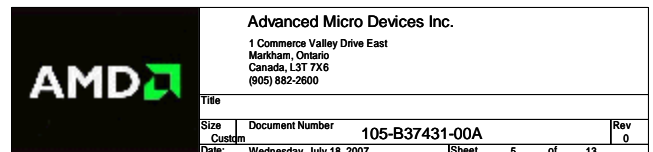
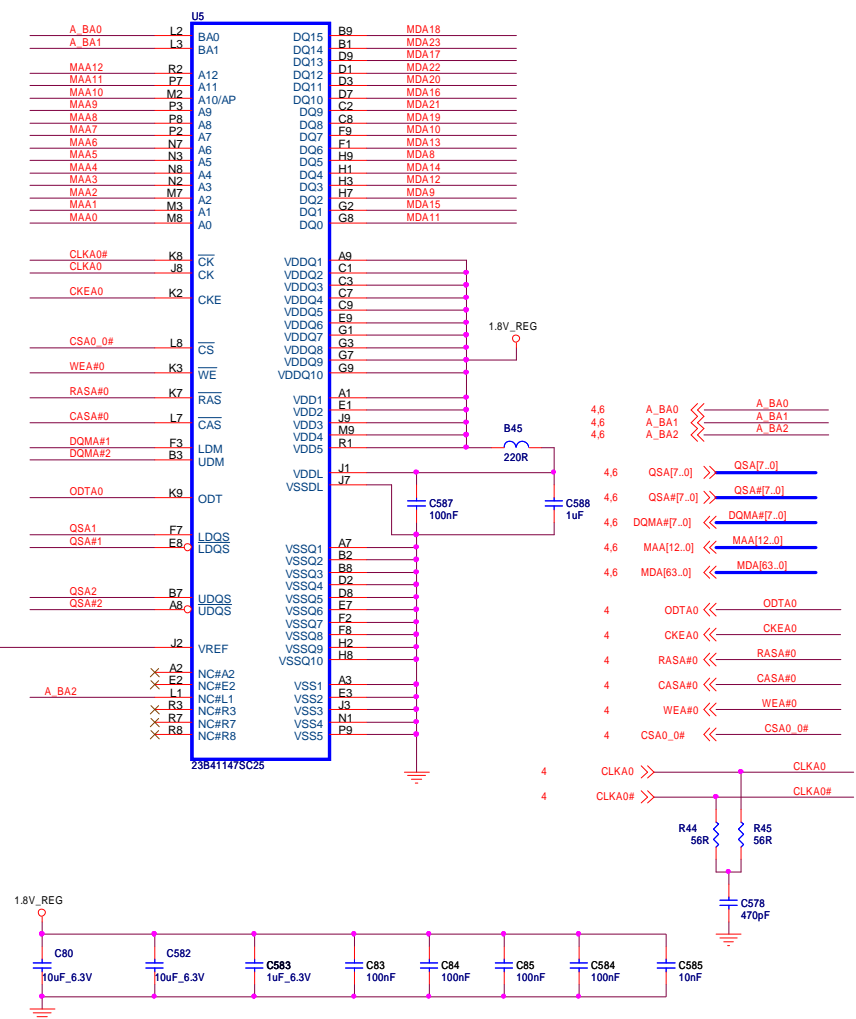
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Markham, Ontario
Canada, L3T 7X6
(905) 882-2600

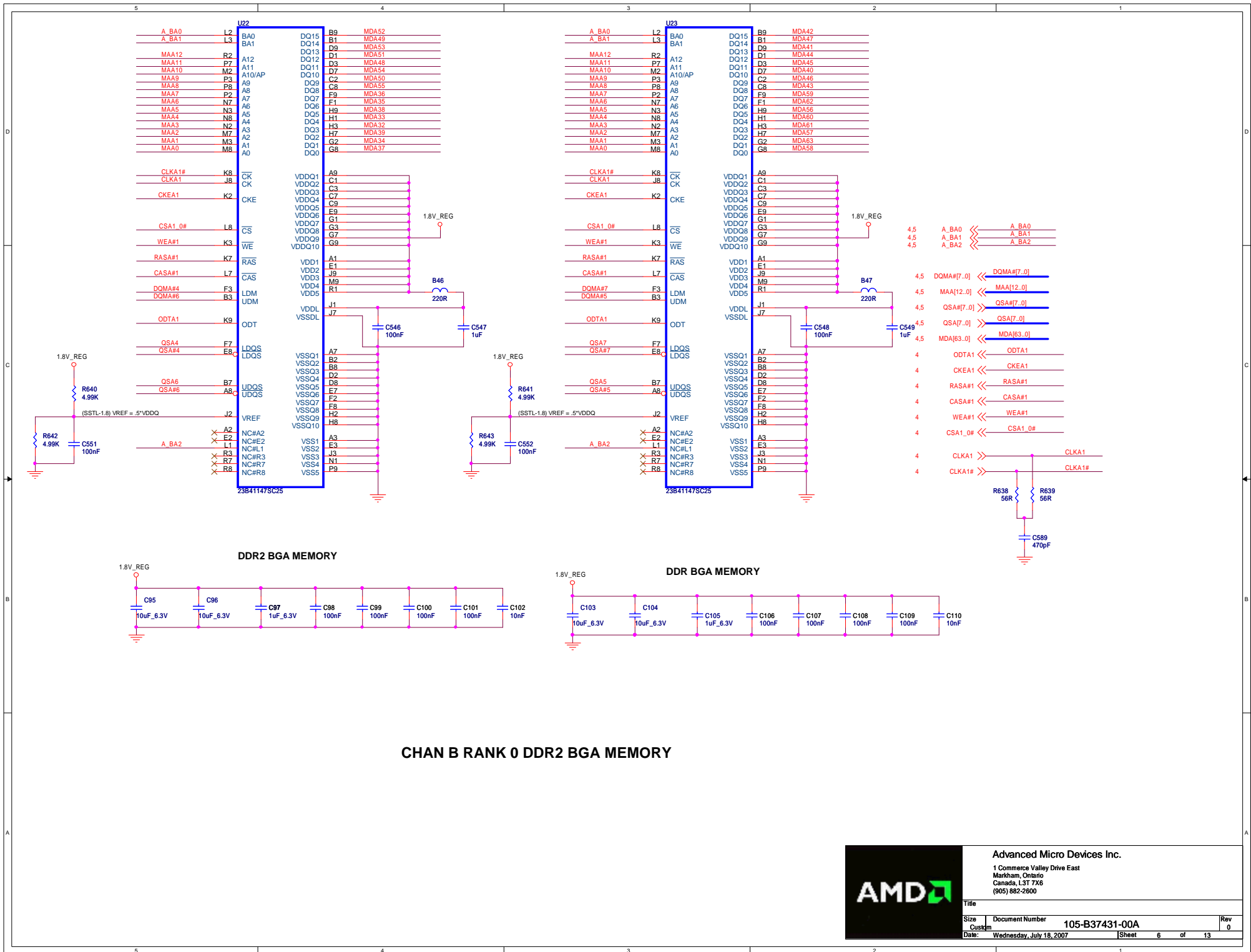
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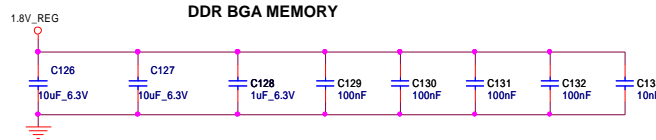
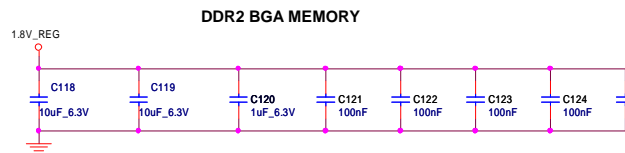
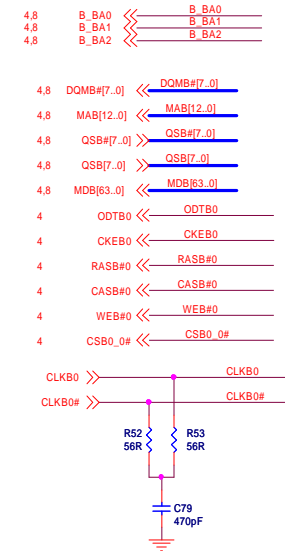
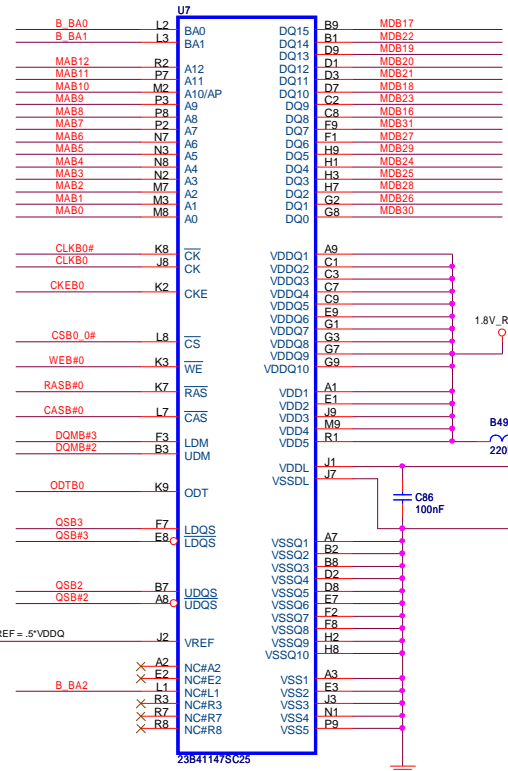
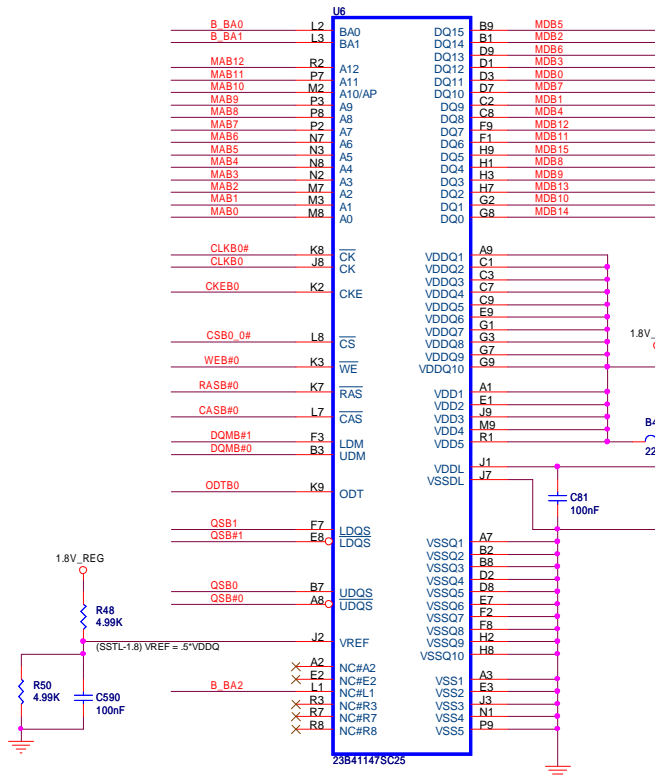











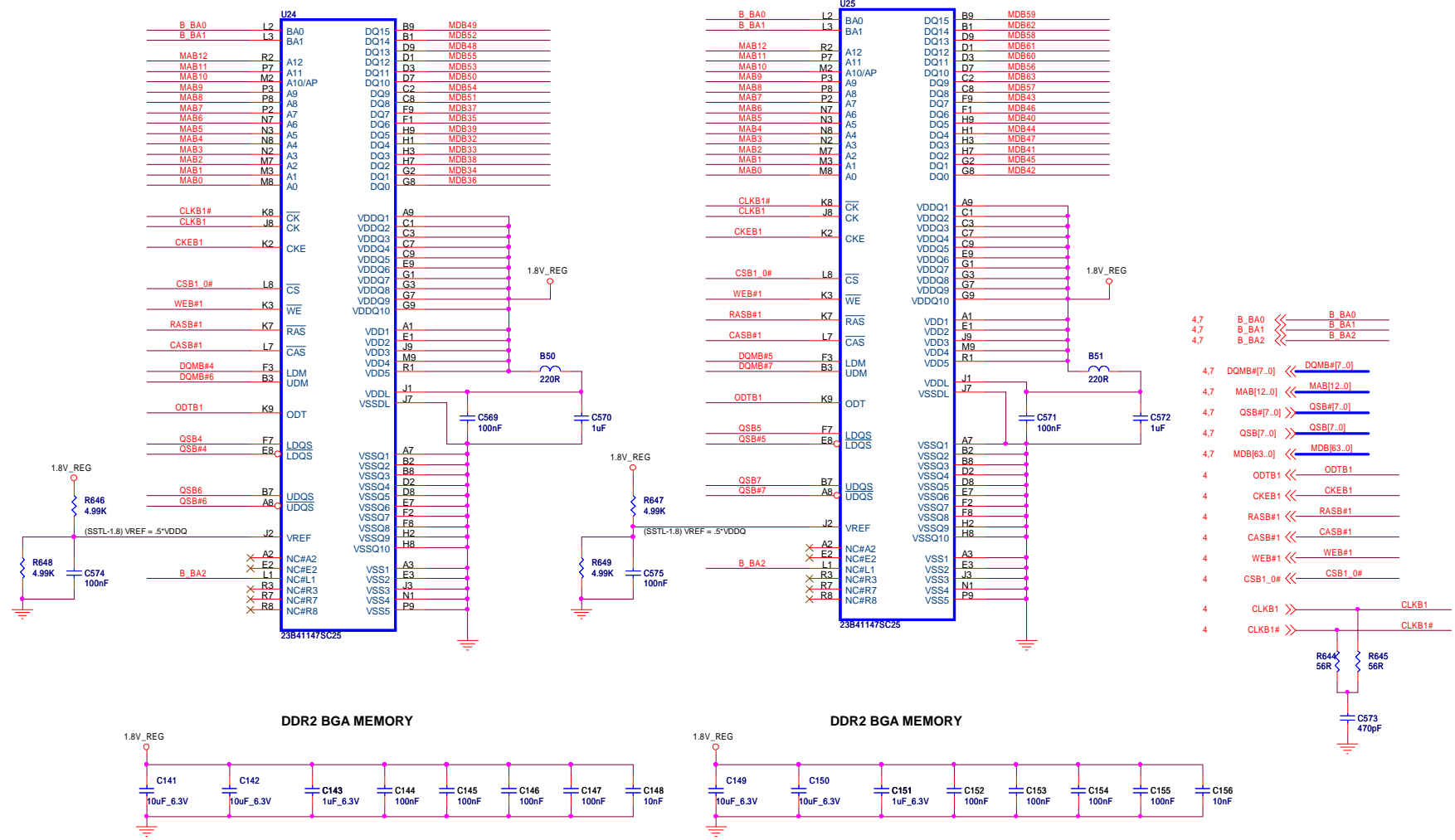


CHAN C RANK0 DDR2 BGA MEMORY



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PLACE CAPS ON THIS PAGE AS CLOSE TO CONNECTOR AS POSSIBLE

G_PWR_SRC

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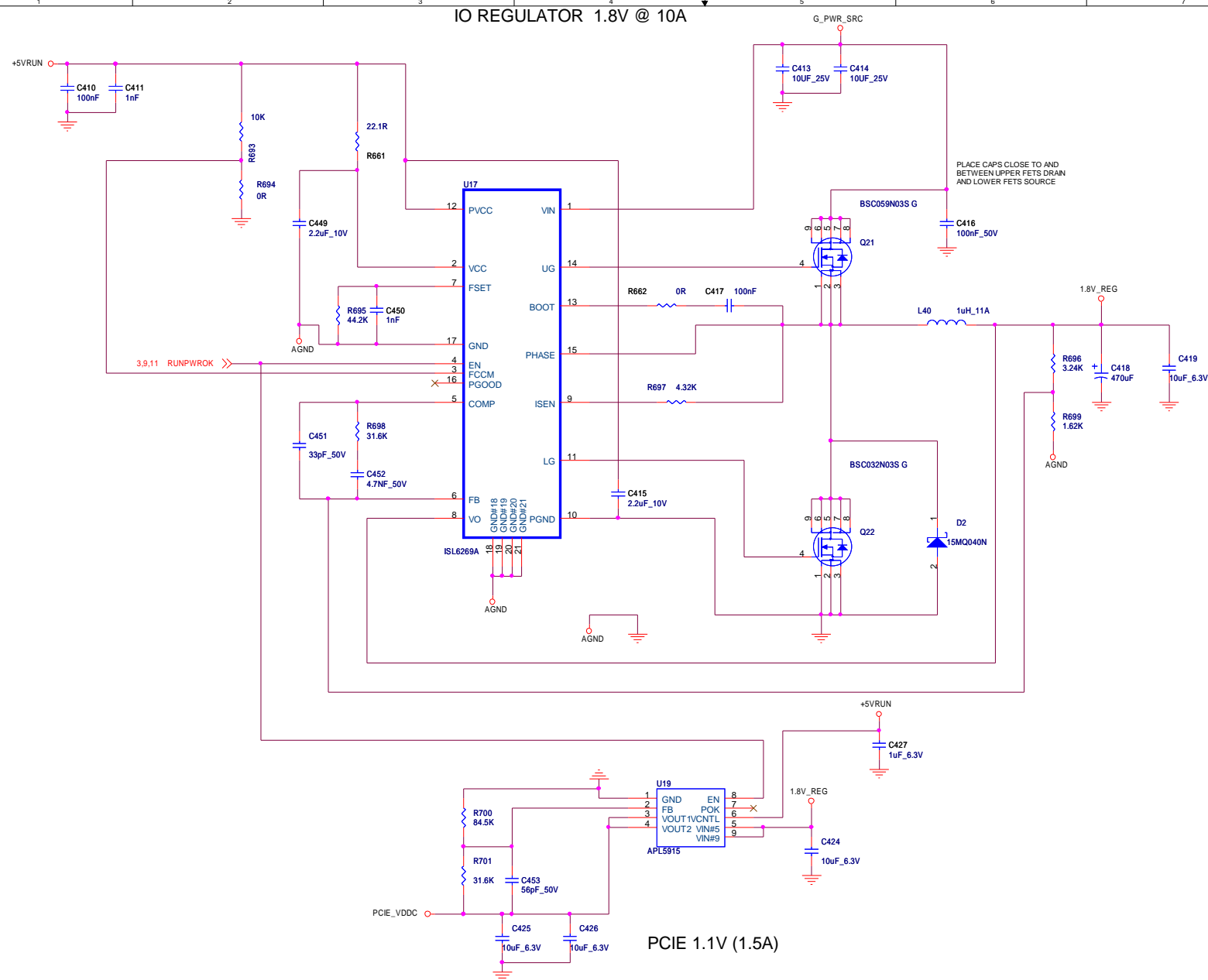
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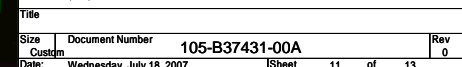
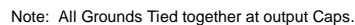
IO REGULATOR 1.8V @ 10A

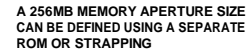
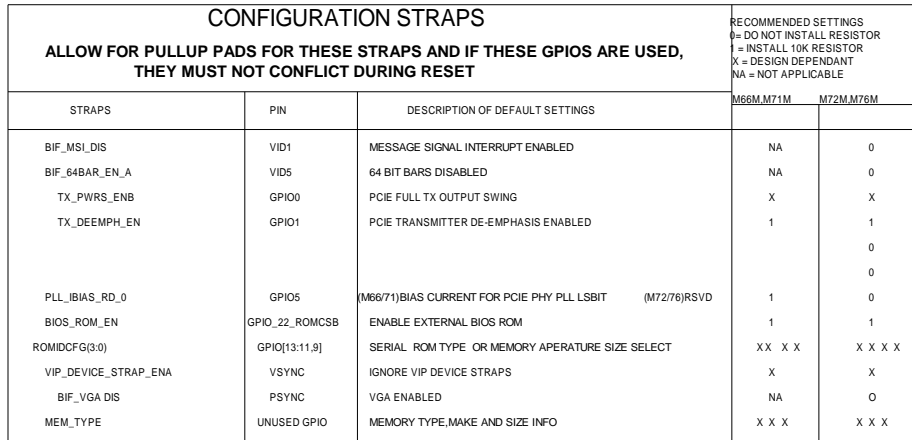


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ATI RESERVED CONFIGURATION STRAPS													
ALLOW FOR PULLUP PADS FOR THESE STRAPS AND IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET													
VID0	VID2	VID3	VID4	VID6	VID7	HSYNC	DVALID	GPIO2	GPIO3	GPIO5	GPIO6	GPIO8	VHAD0
PULLUP PADS ARE NOT REQUIRED FOR THESE STRAPS BUT IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET													
GPIO_28_TDO	GENERICC	GPIO21_BB_EN	GPIO_23_CLKREQB (DRIVES LOW DURING RESET)					H2SYNC	V2SYNC				

