

# ANT SWITCH

A

B

C

D

E

F

A

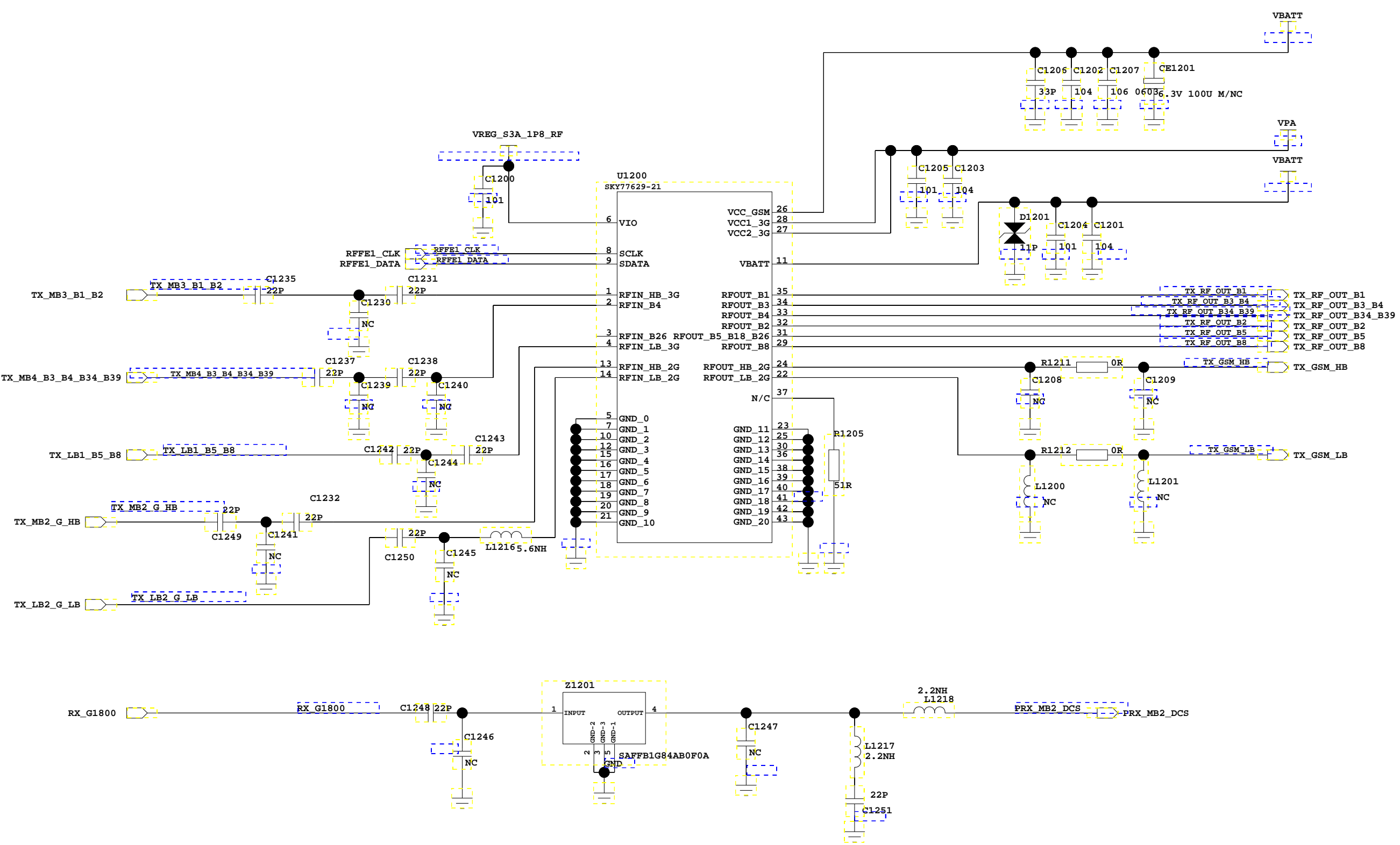
B

C

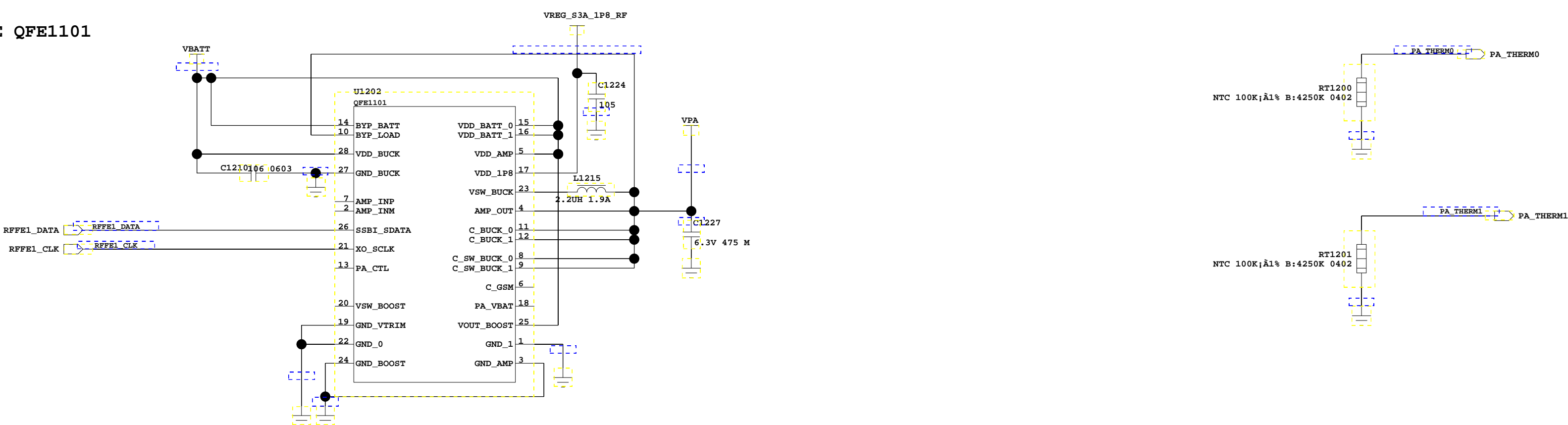
D

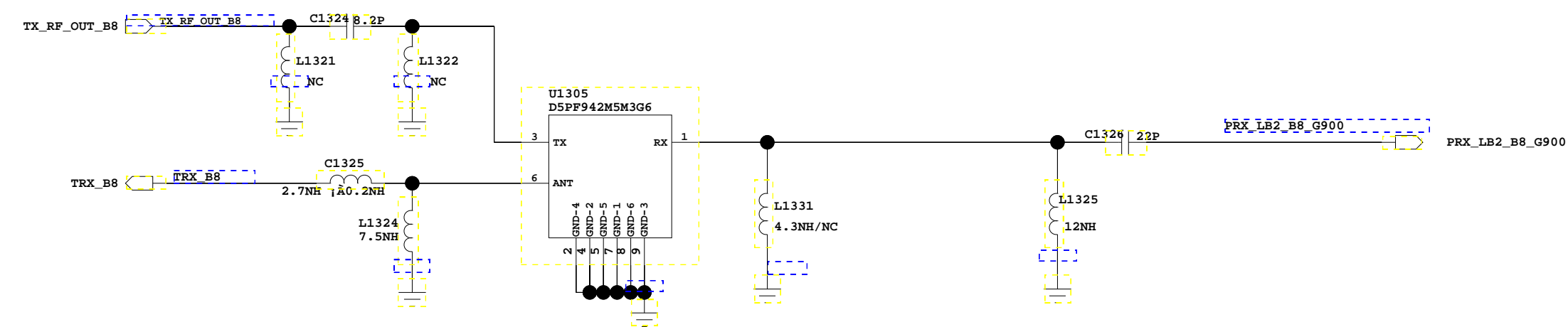
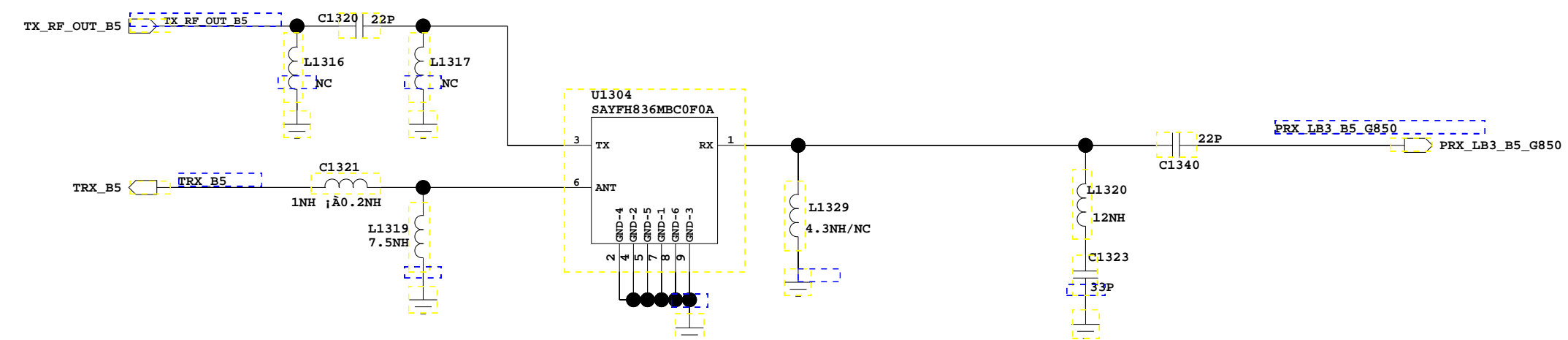
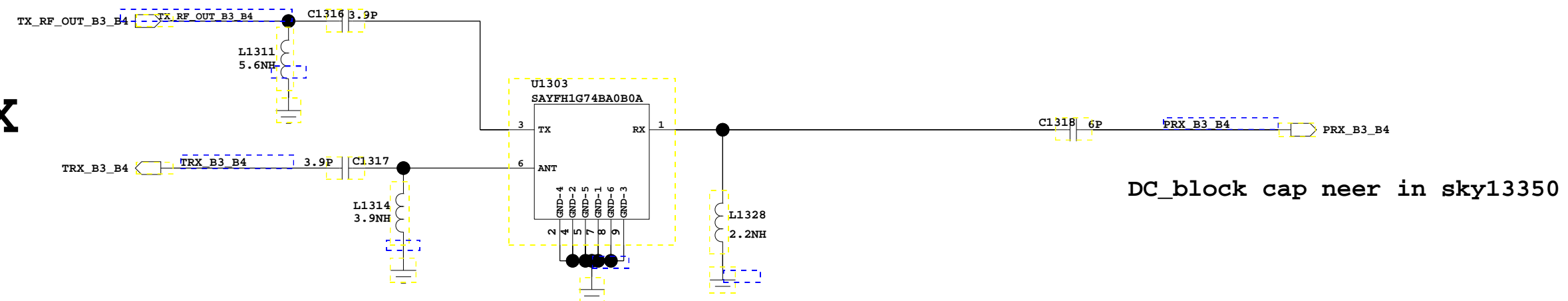
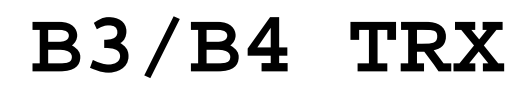
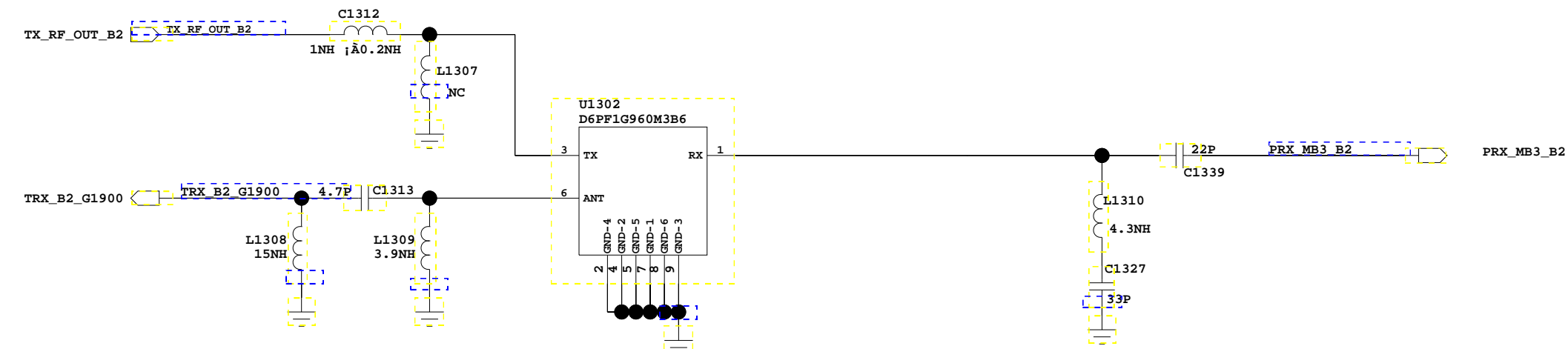
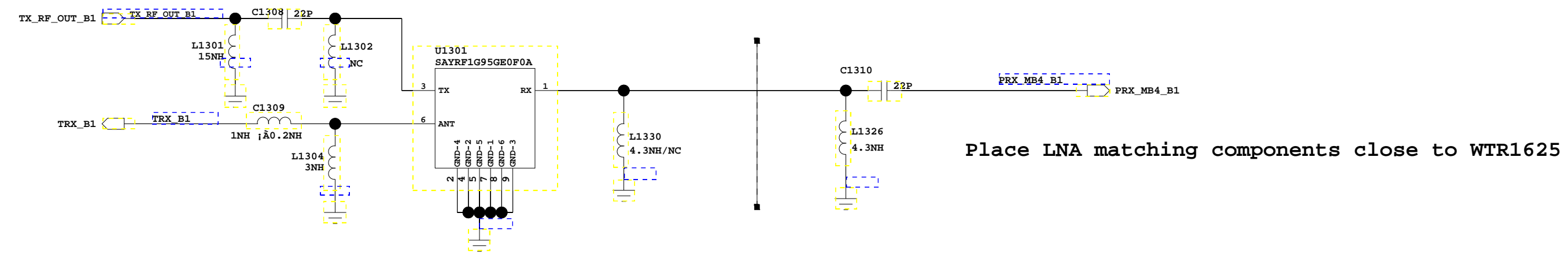
E

F

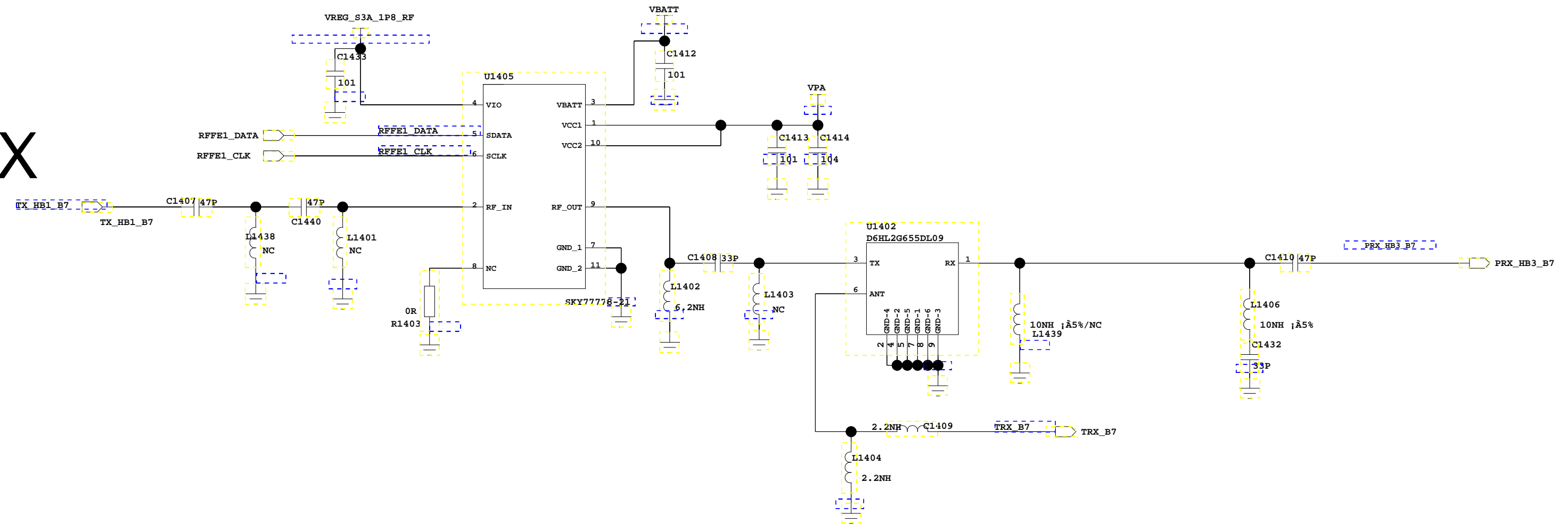


USE QFE1101

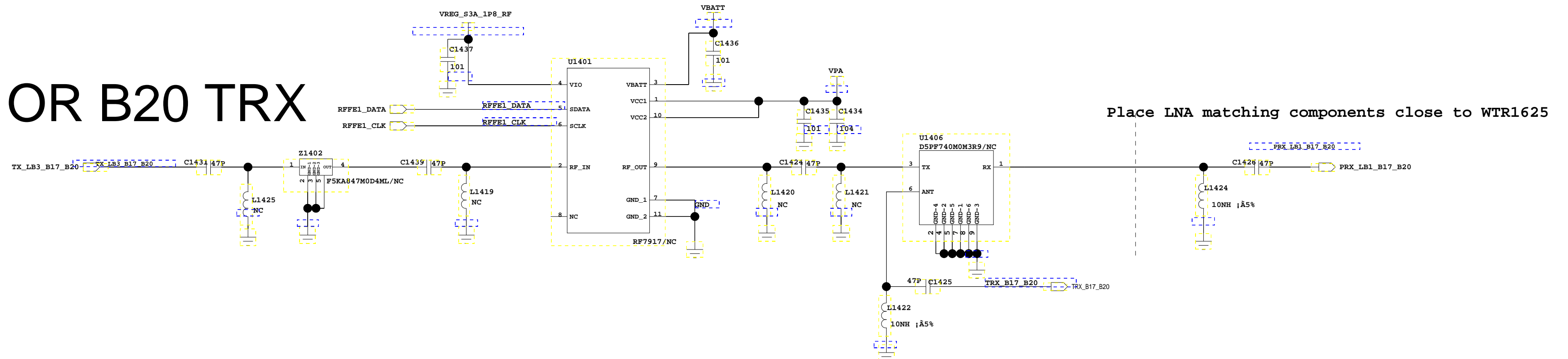




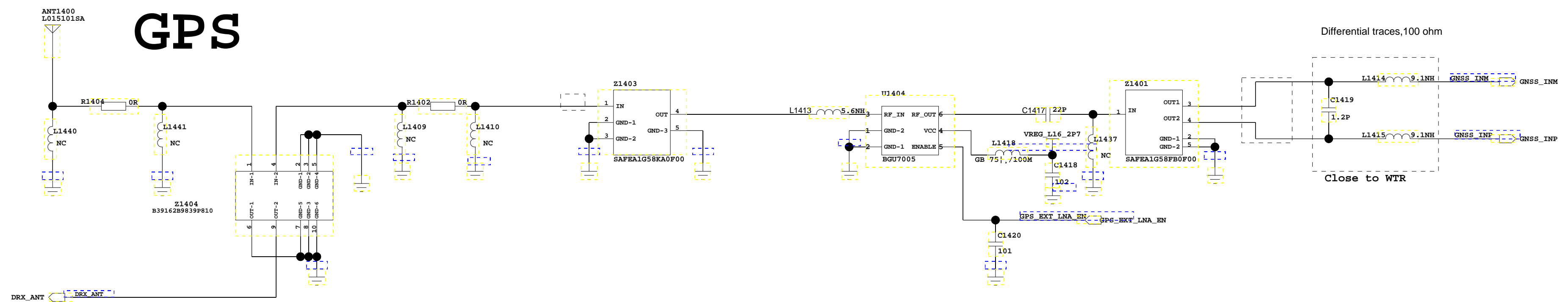
# B7 TRX

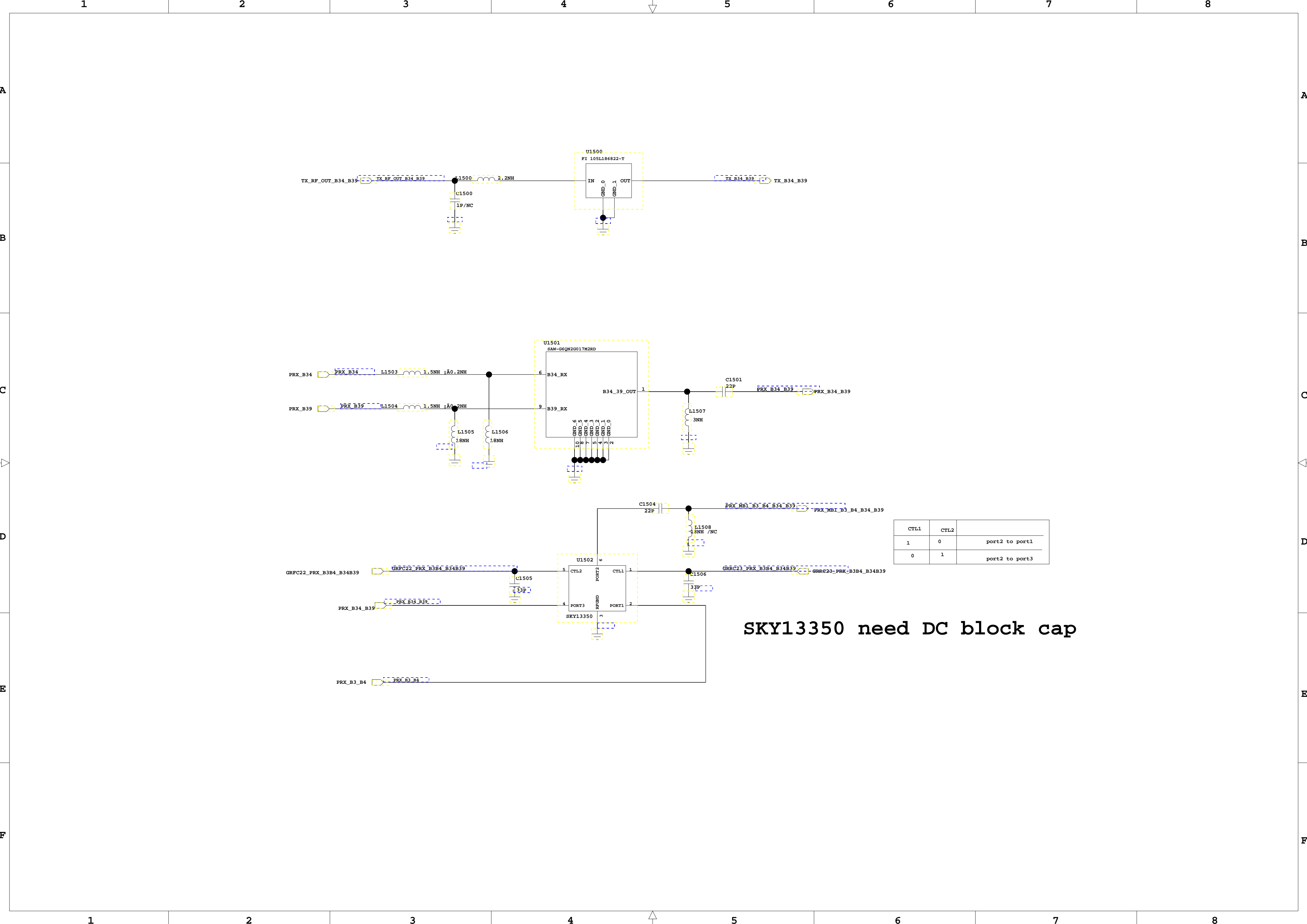


# B17 OR B20 TRX

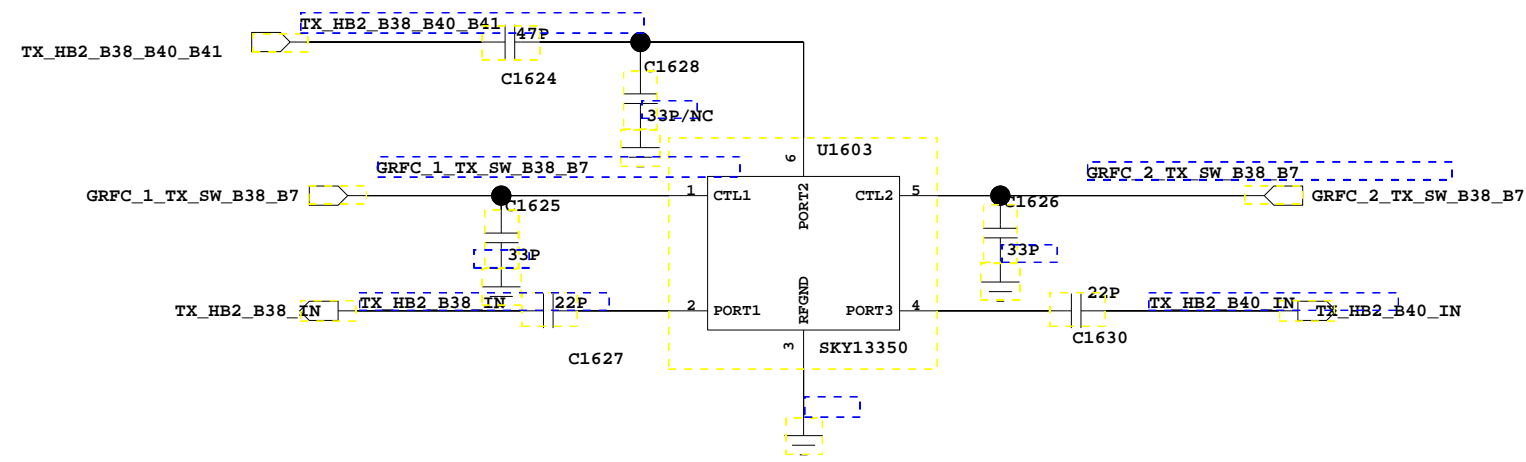
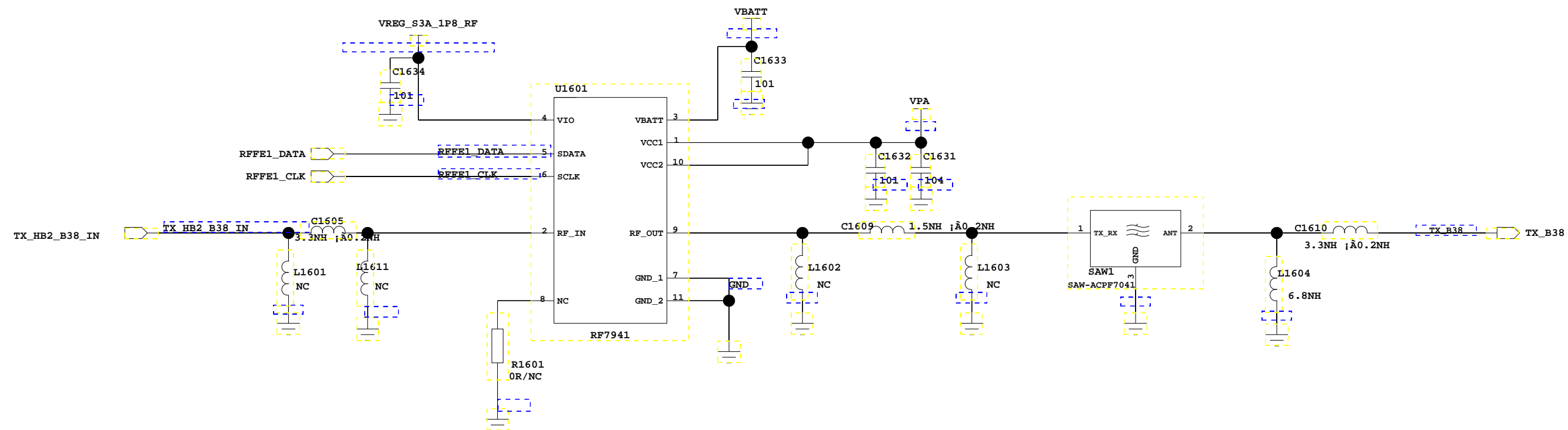


# GPS

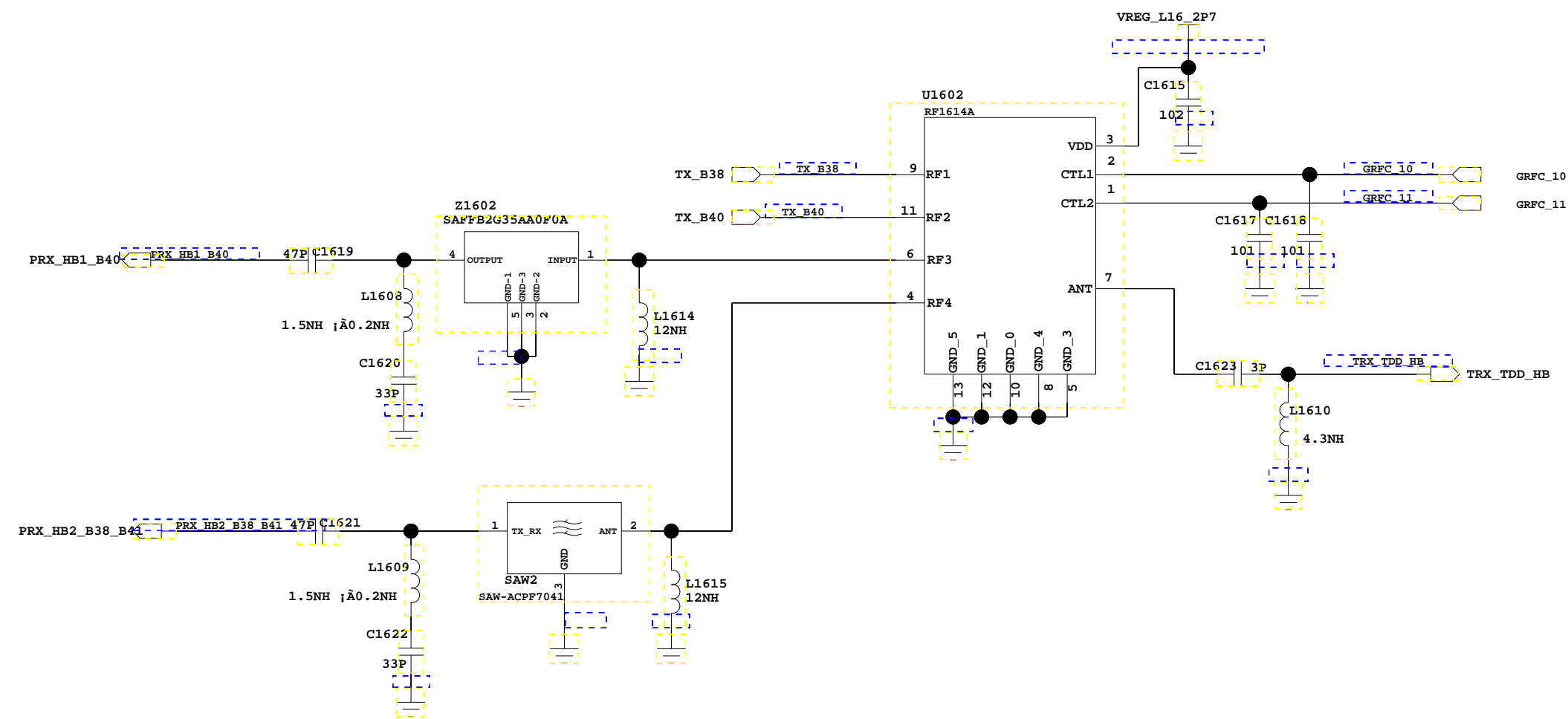
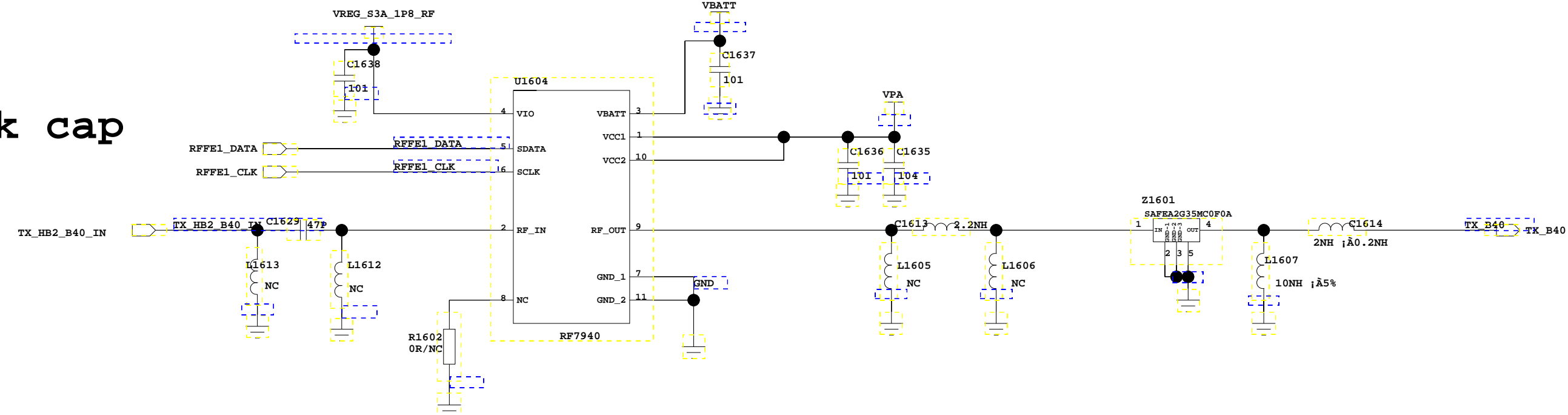


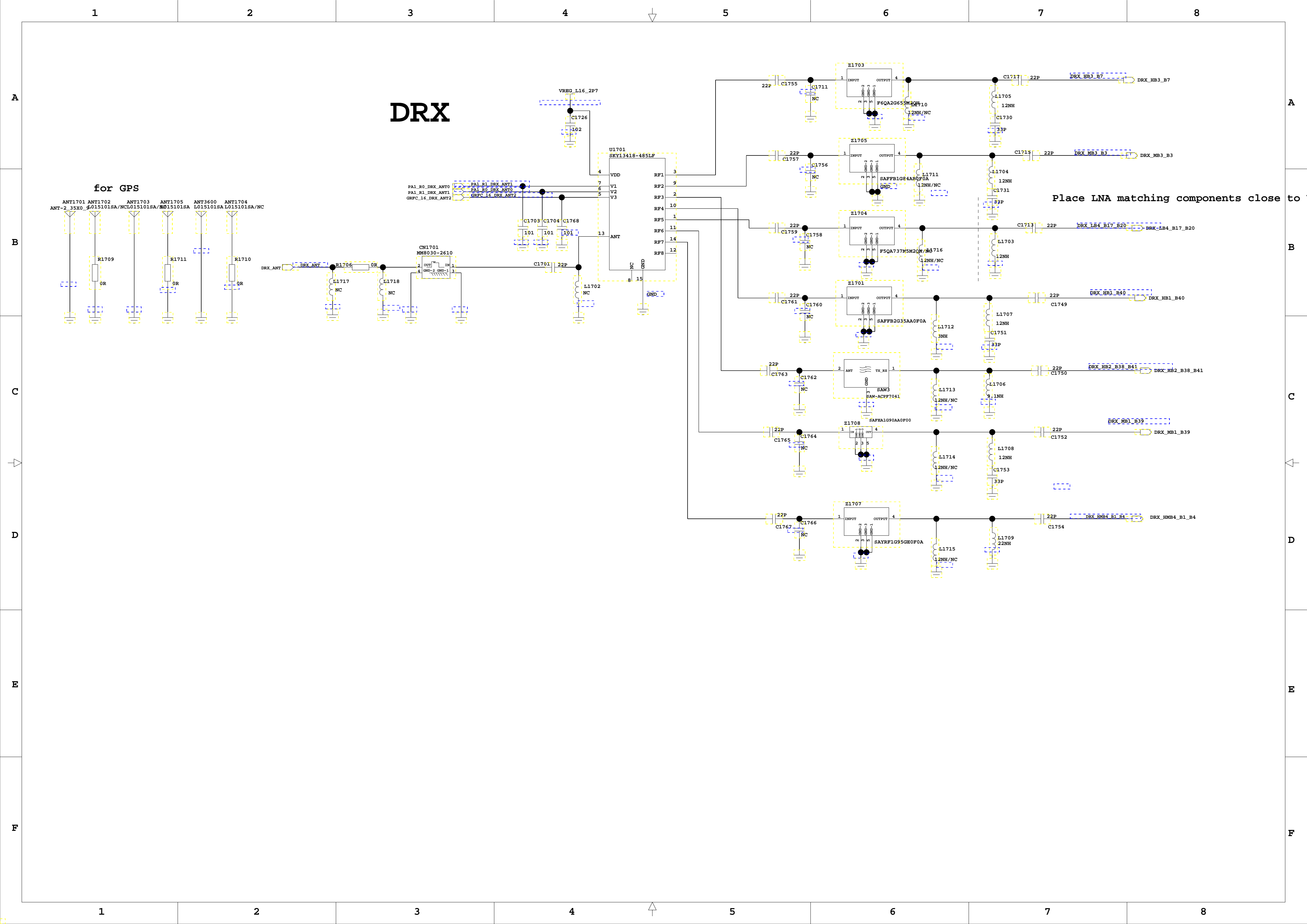


# TDD LTE TRX

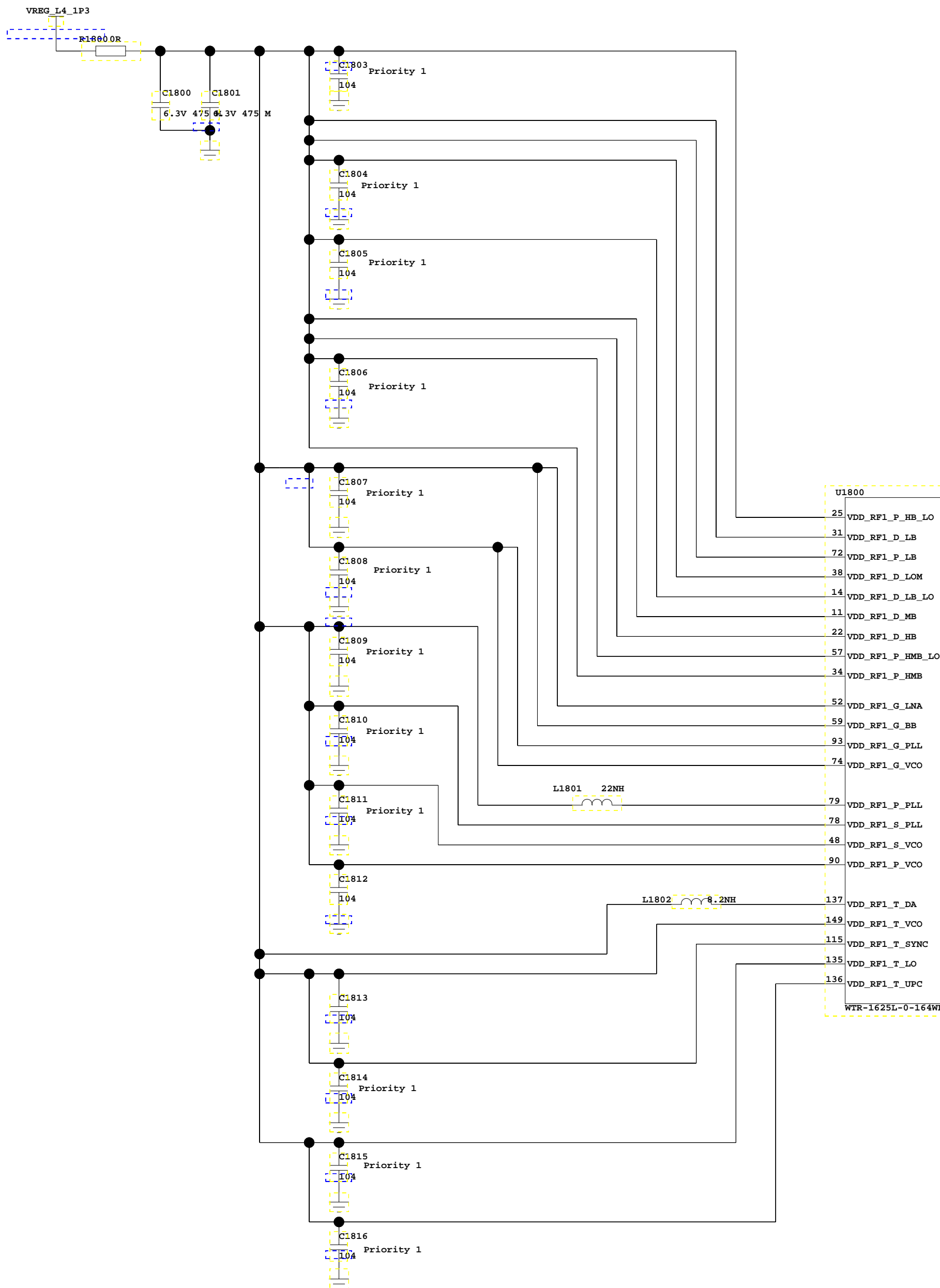


SKY13350 need DC block cap

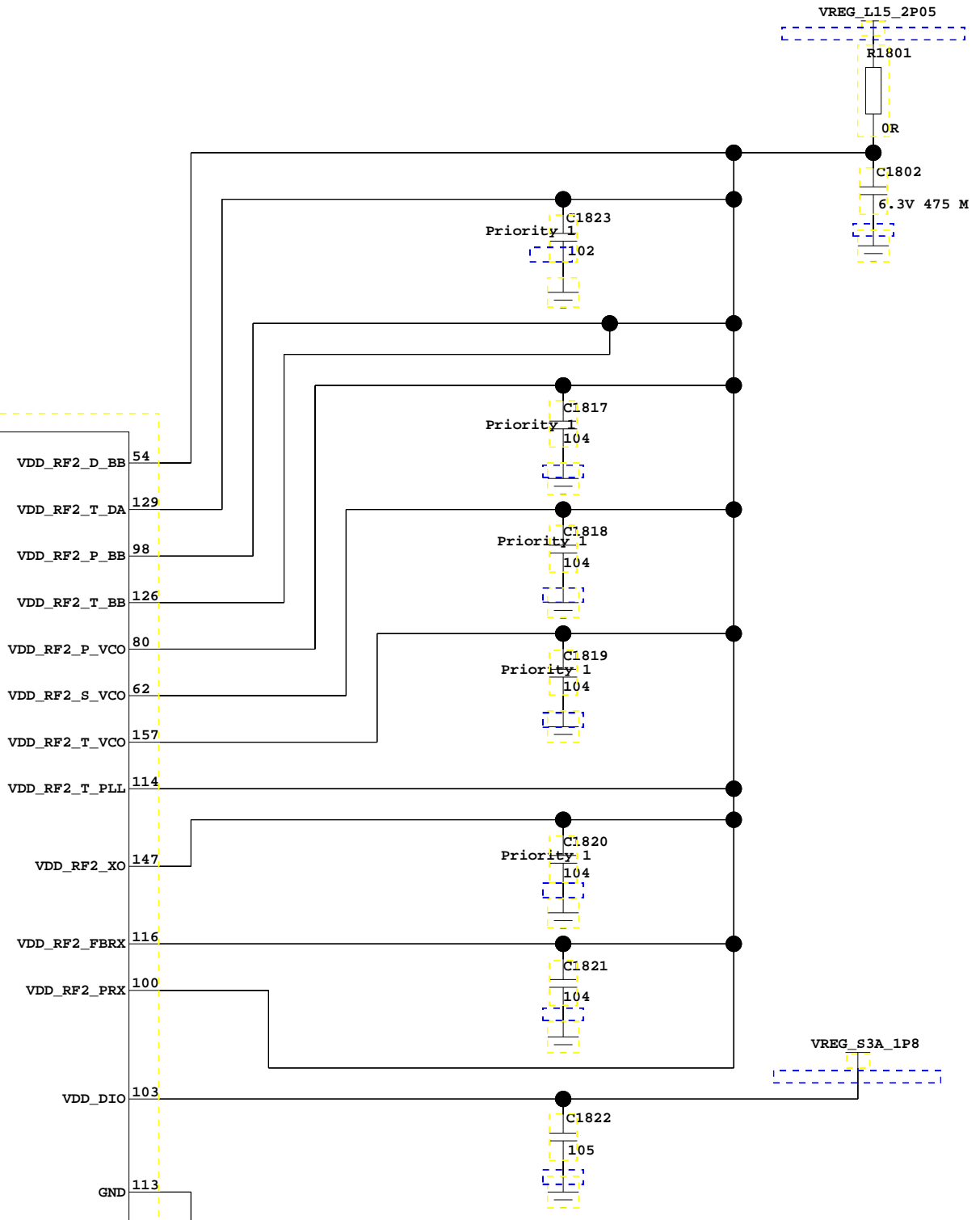




WTR POWER DISTRIBUTION 1.3V

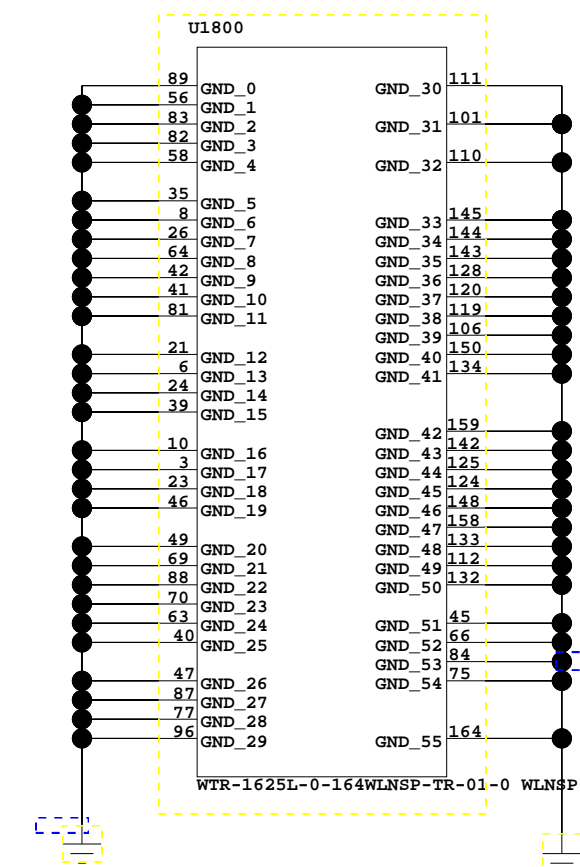
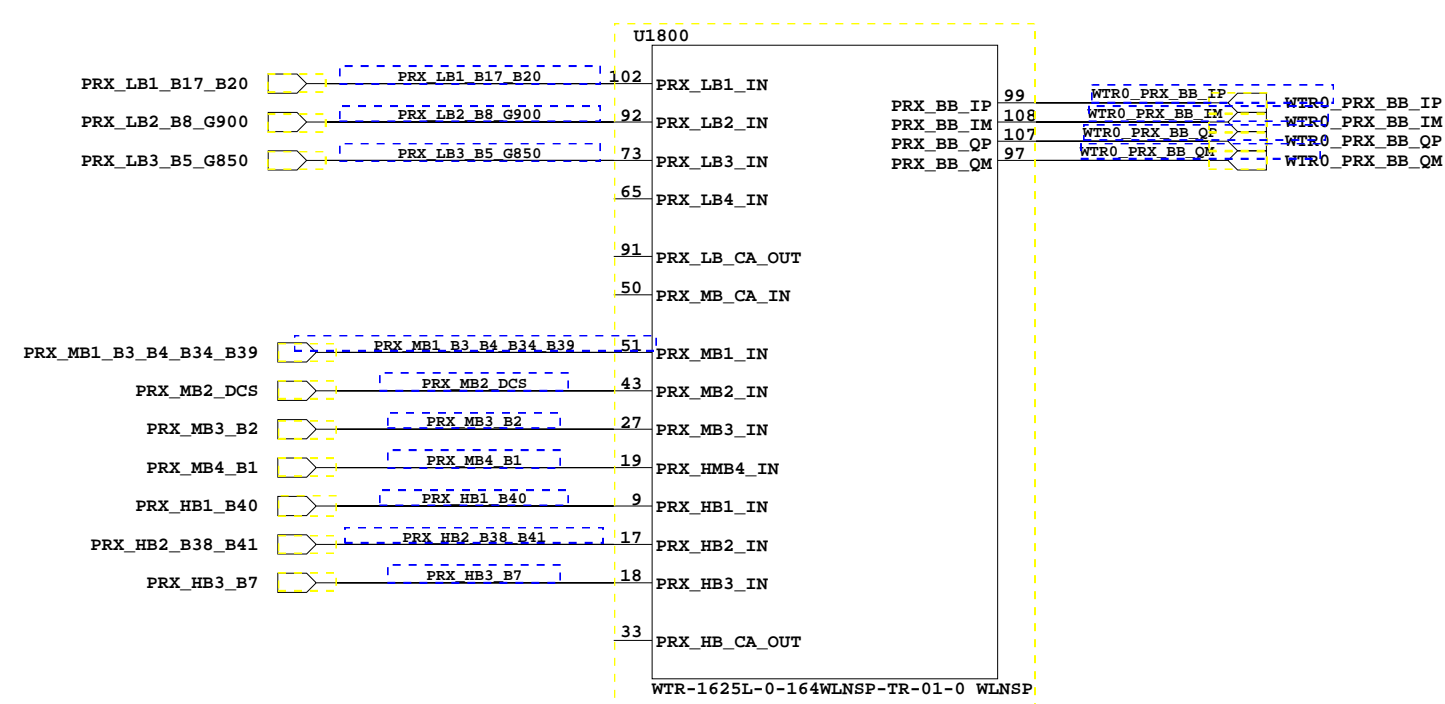
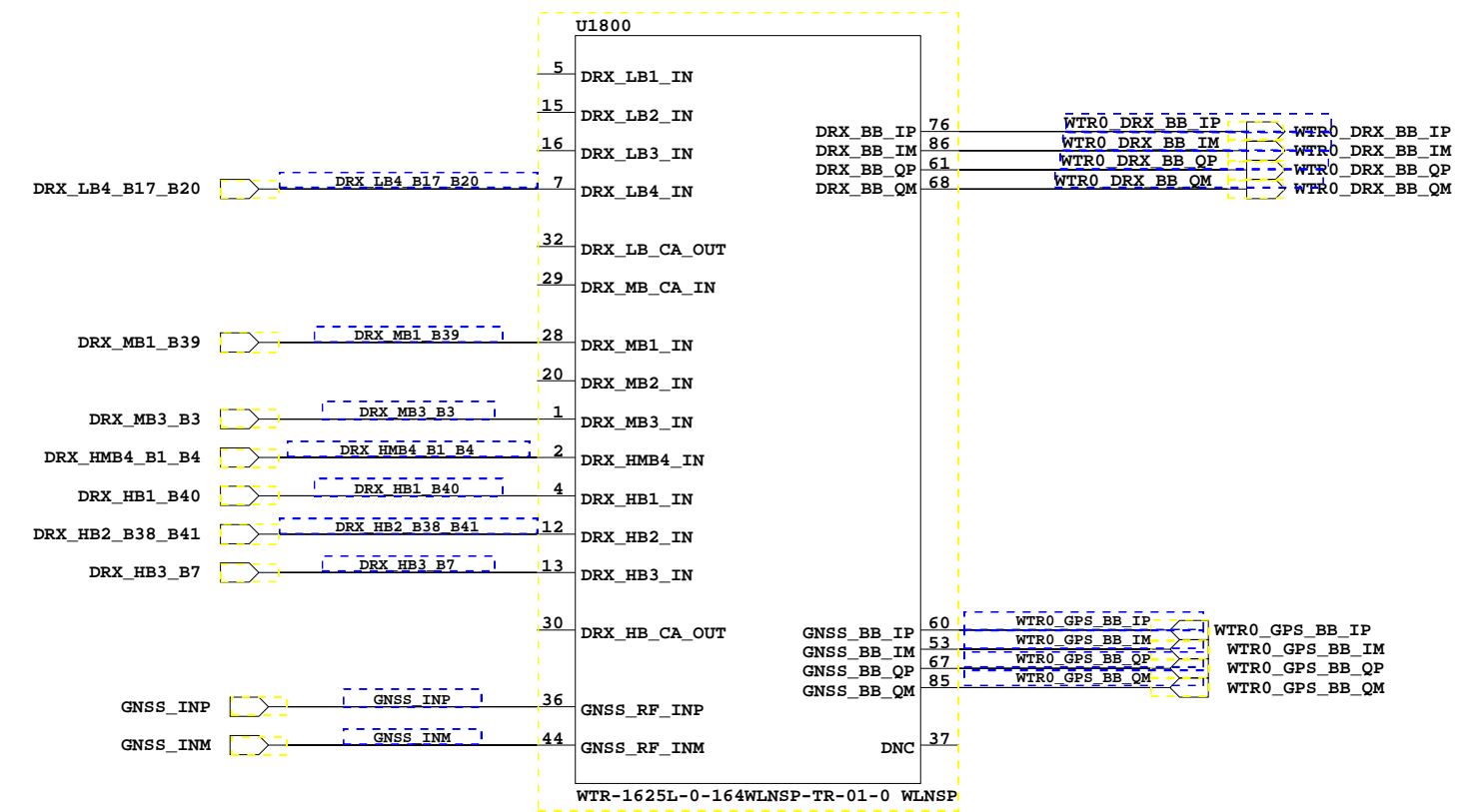
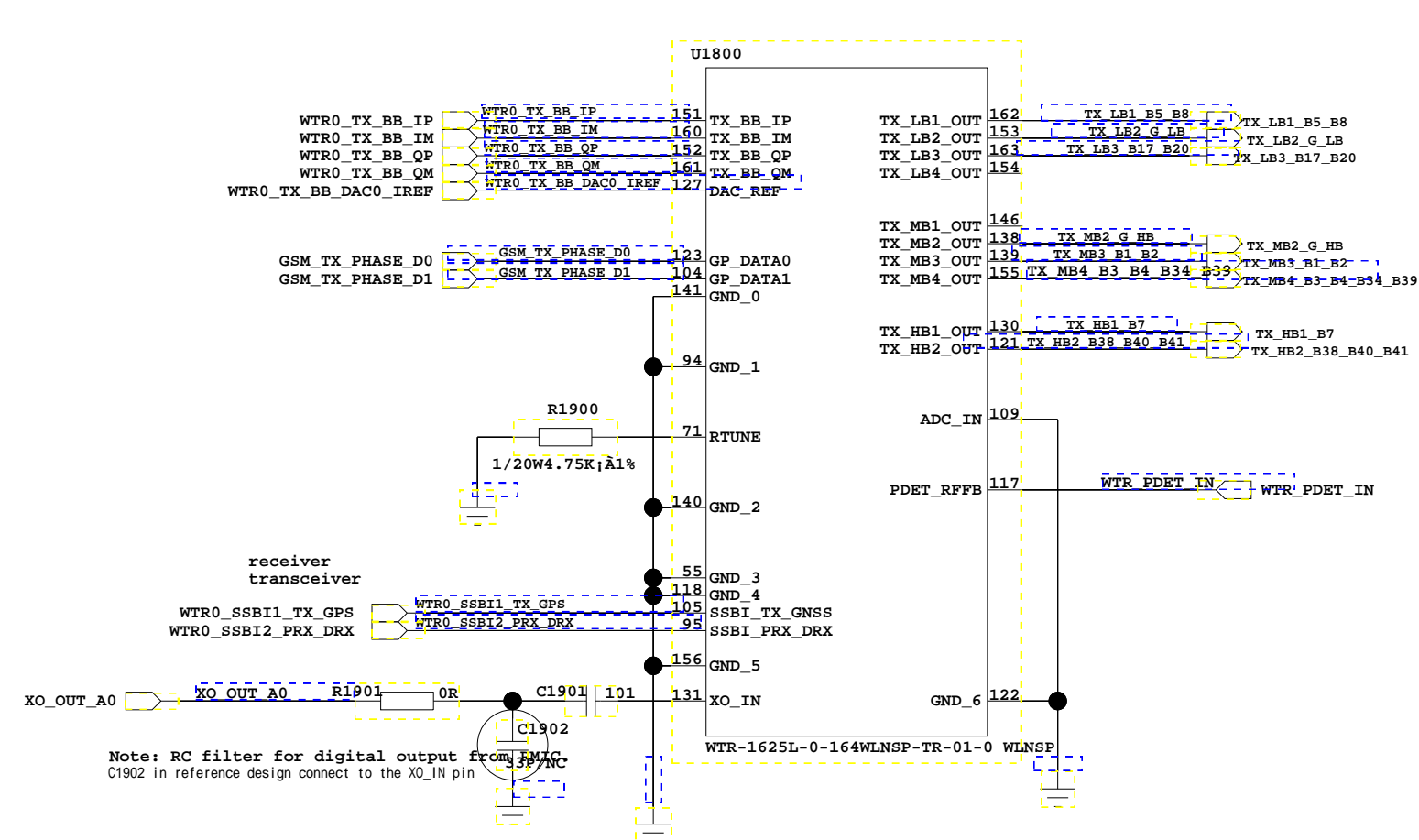


WTR POWER DISTRIBUTION 2.05V(Rev.B)

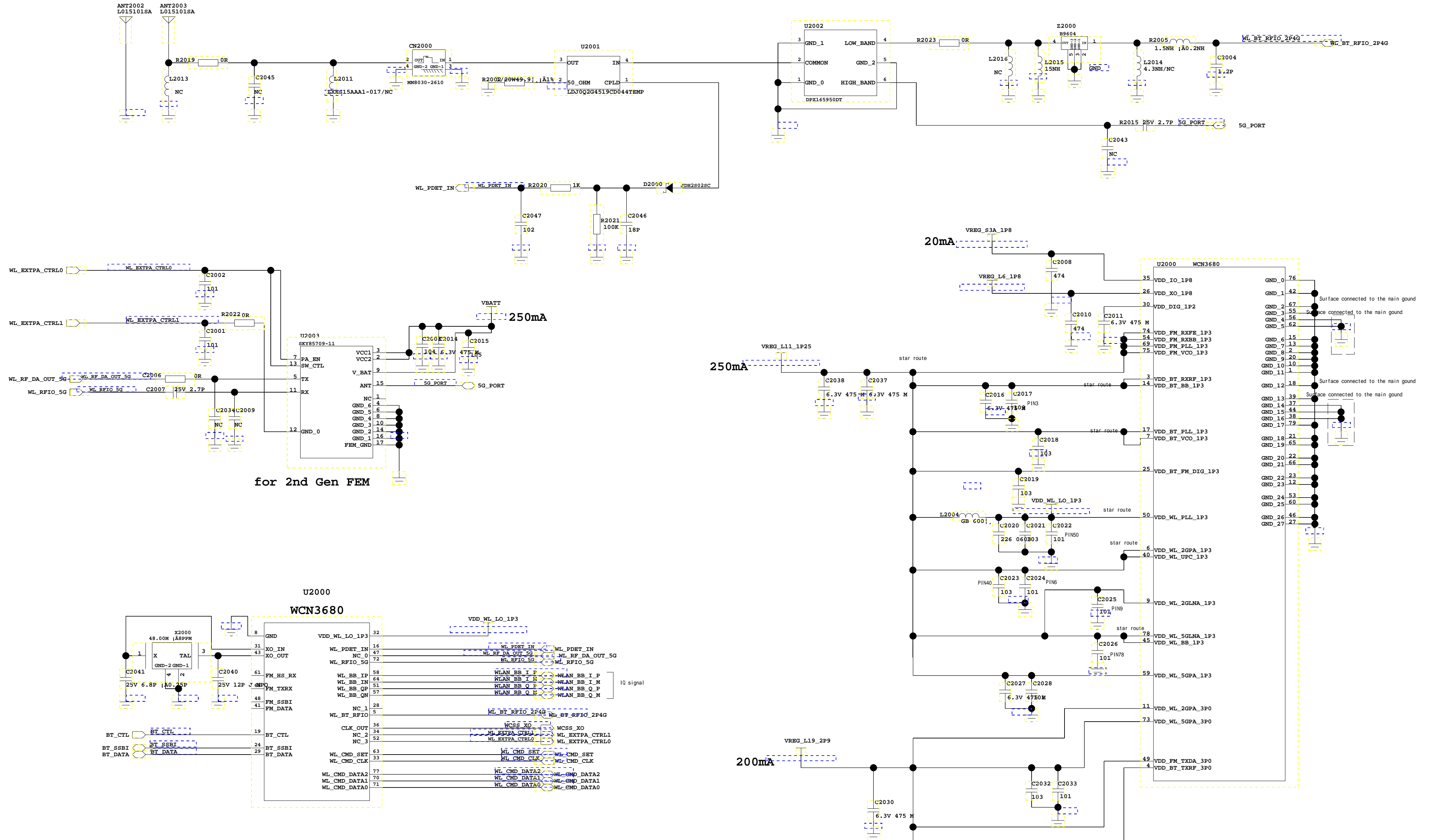


VREG\_S3A\_1P8

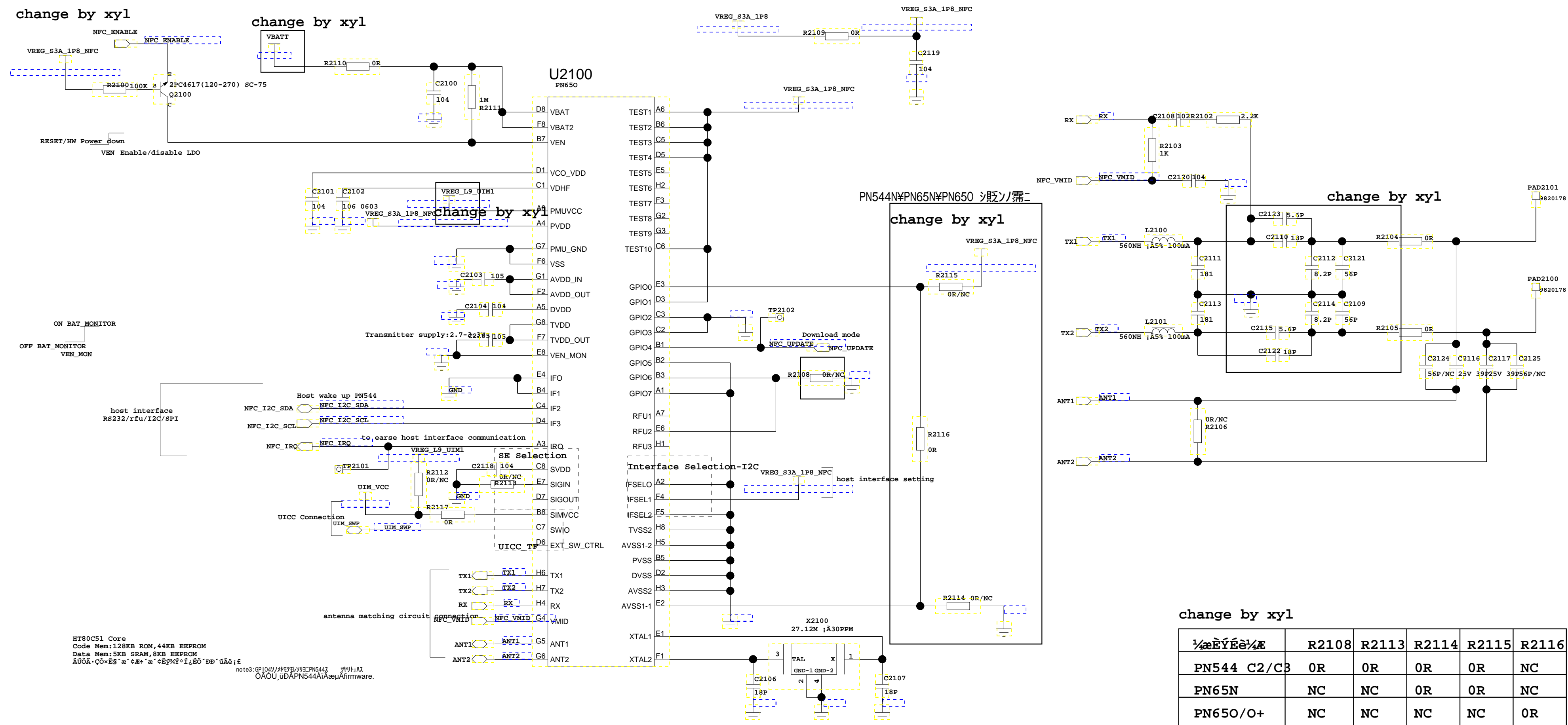




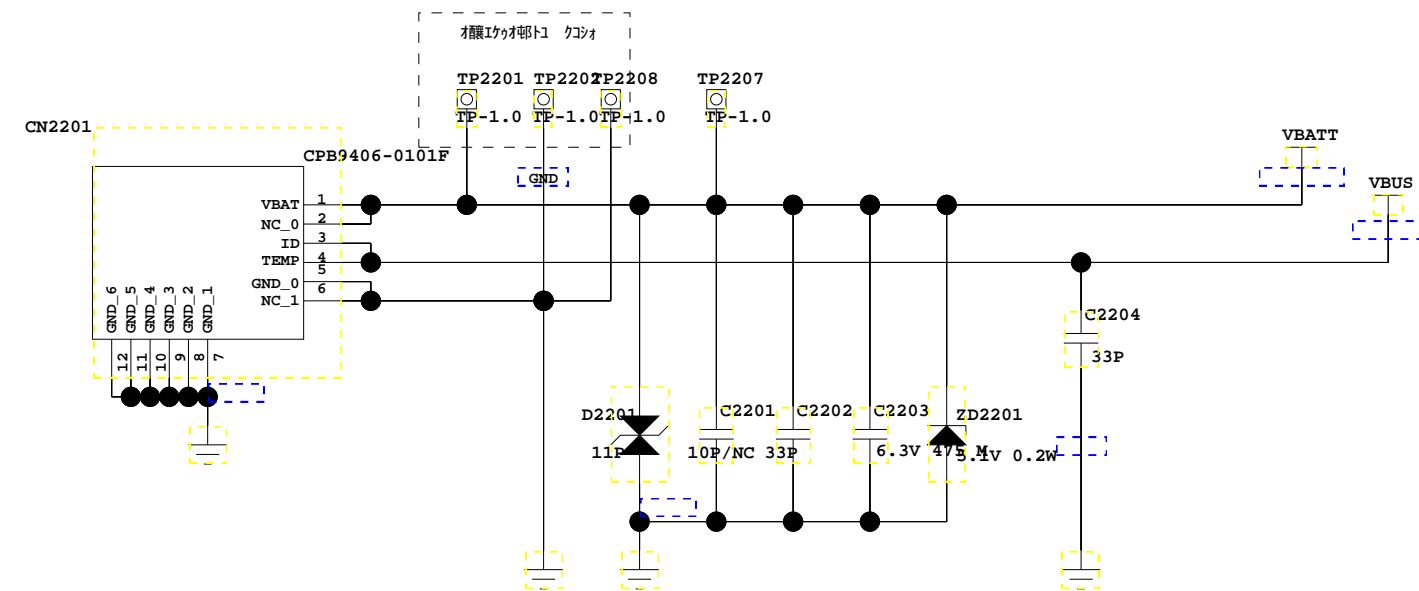
# WIFI+BT



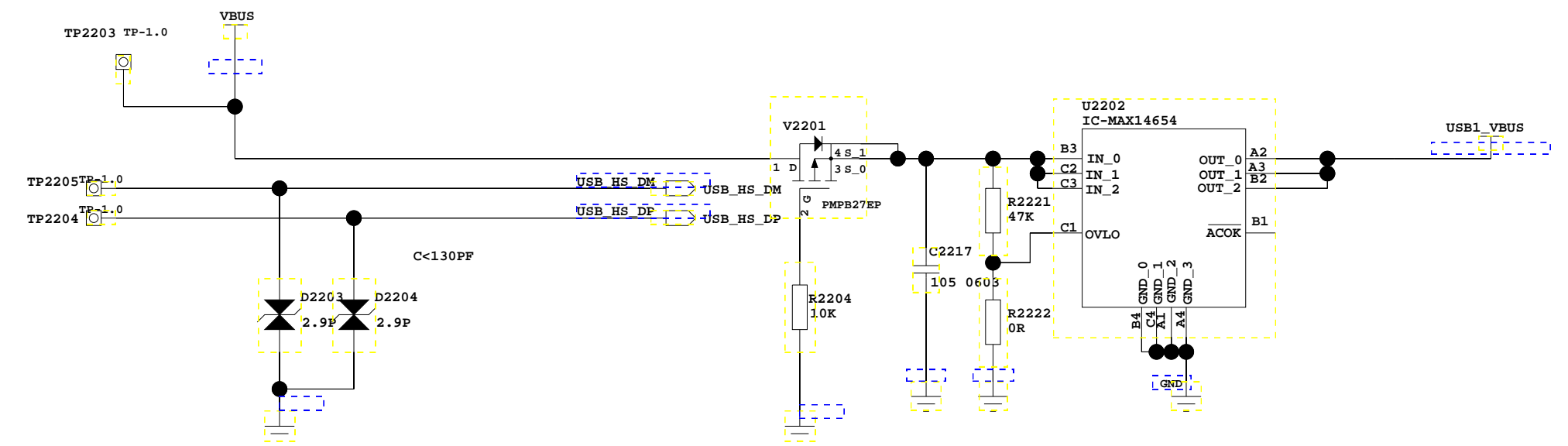
If 2.4 GHz WLAN spurs are observed around 2.6 to 2.7 GHz,  
replace R1612 with 20 nH inductor.



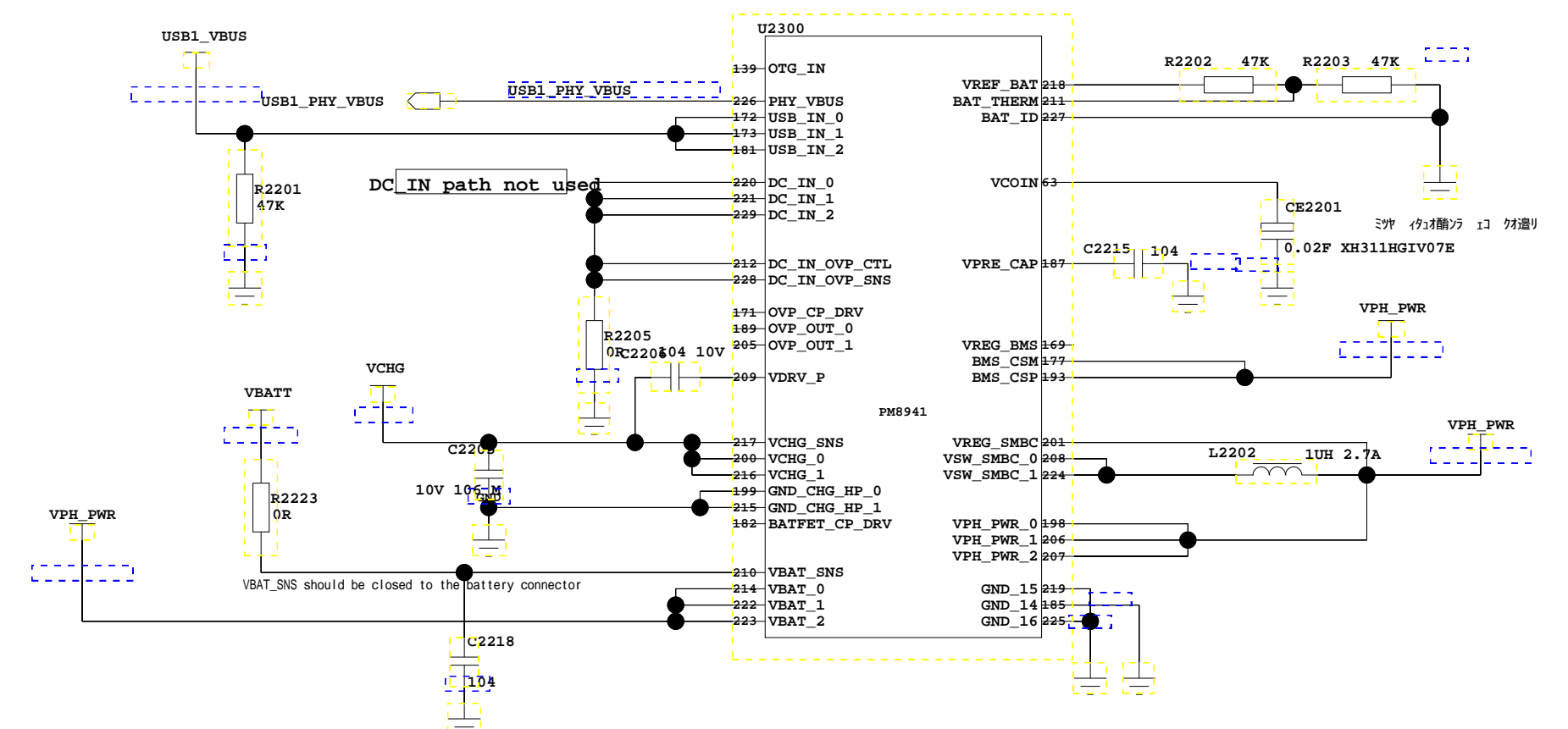
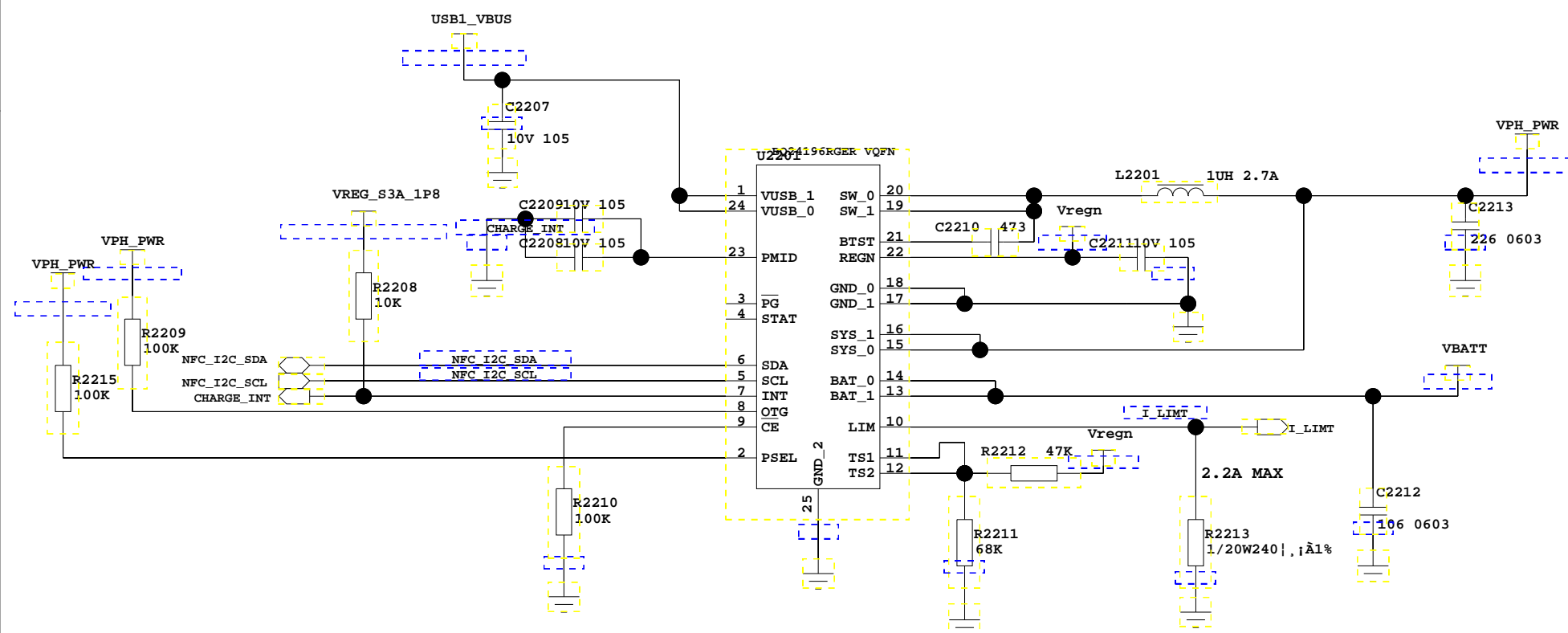
# Battery Connector



# USB OVP

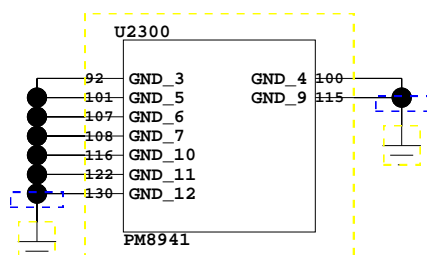
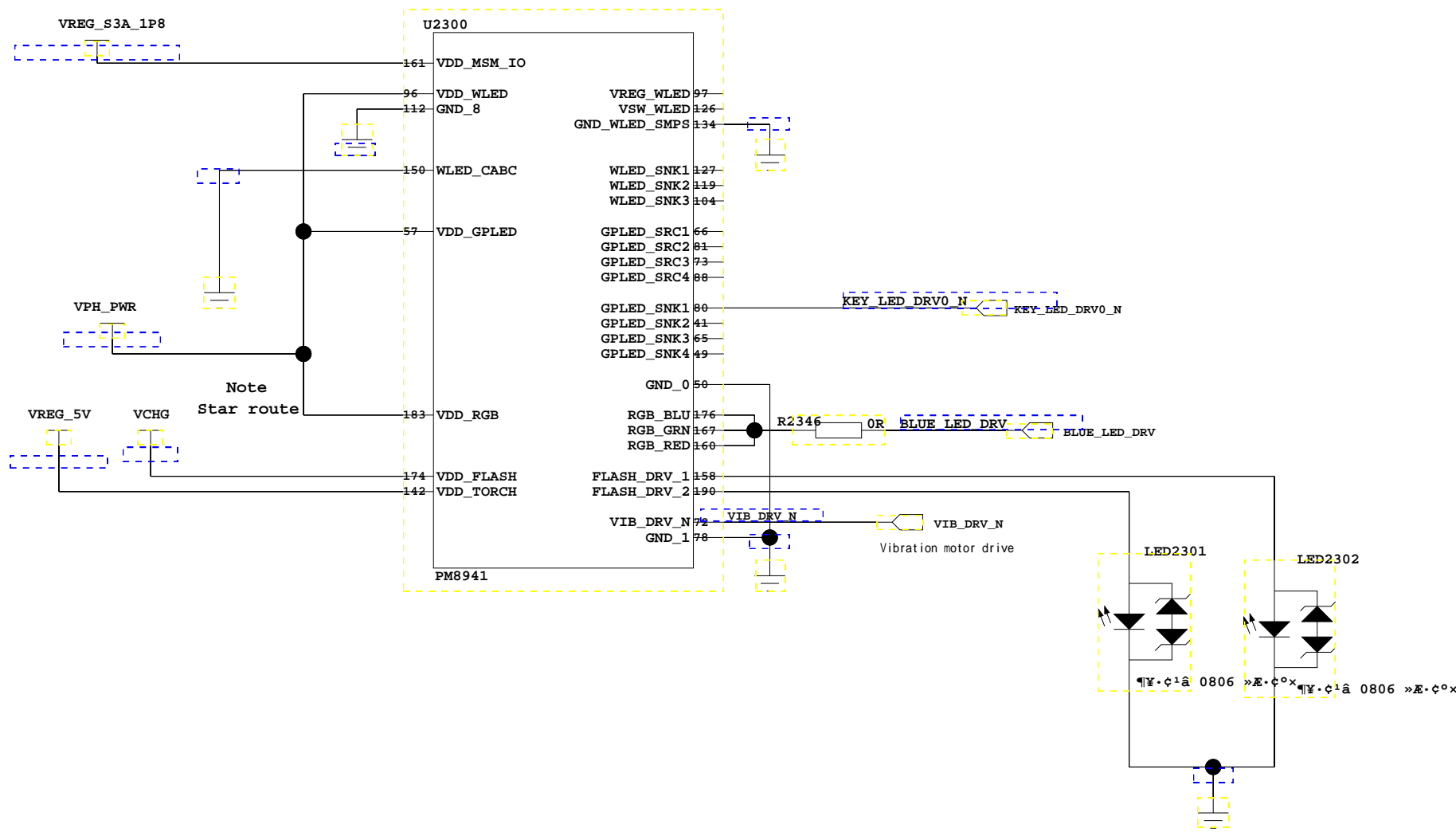


# Circuit of Charge

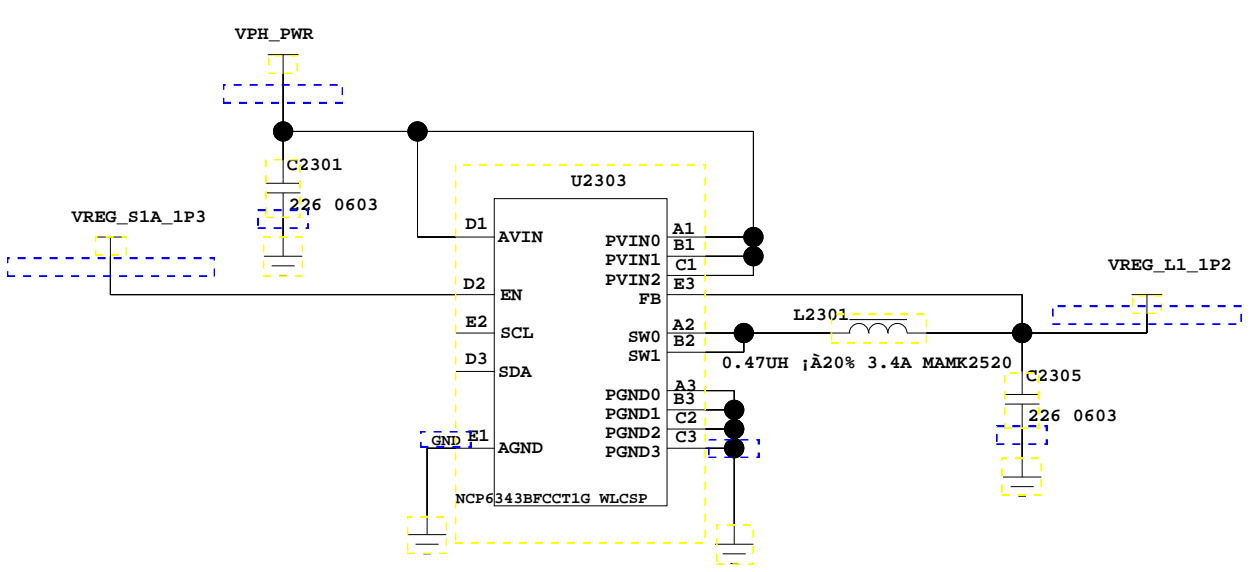


C220C15F510uF is required on VCOIN pin to support SMPL upto 2sec.  
Connect pin 185 of PM8941 to GND directly using a dedicated via

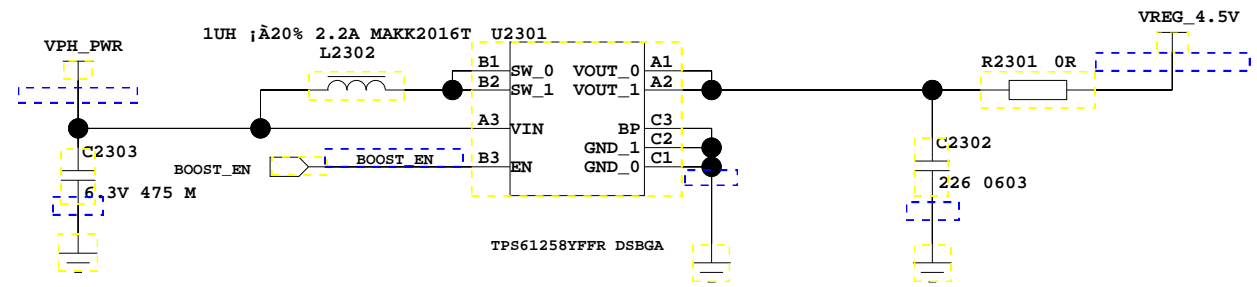
PM8941 LED AND GROUND



LD01 helper 指示灯



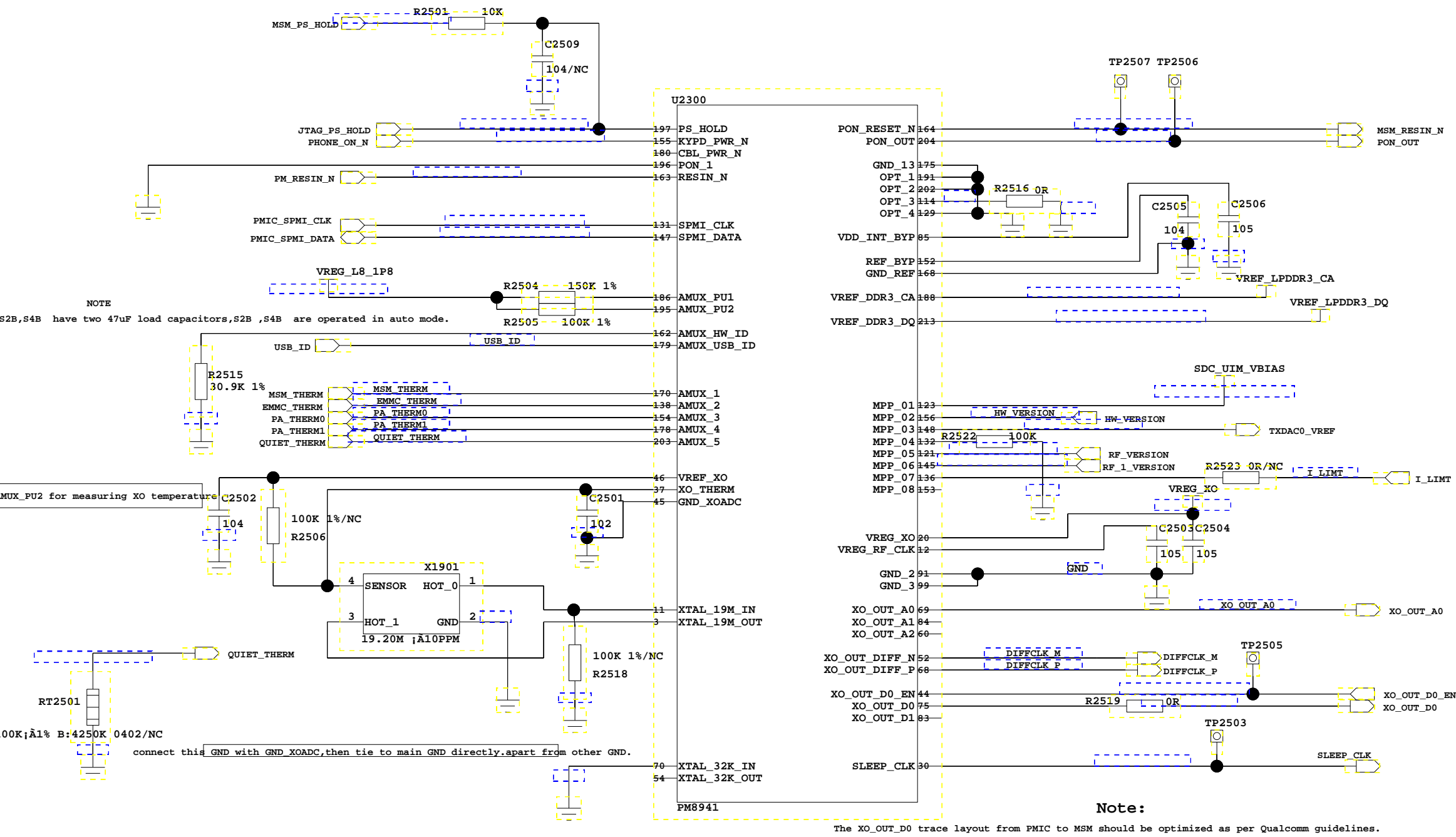
4.5V BOOST



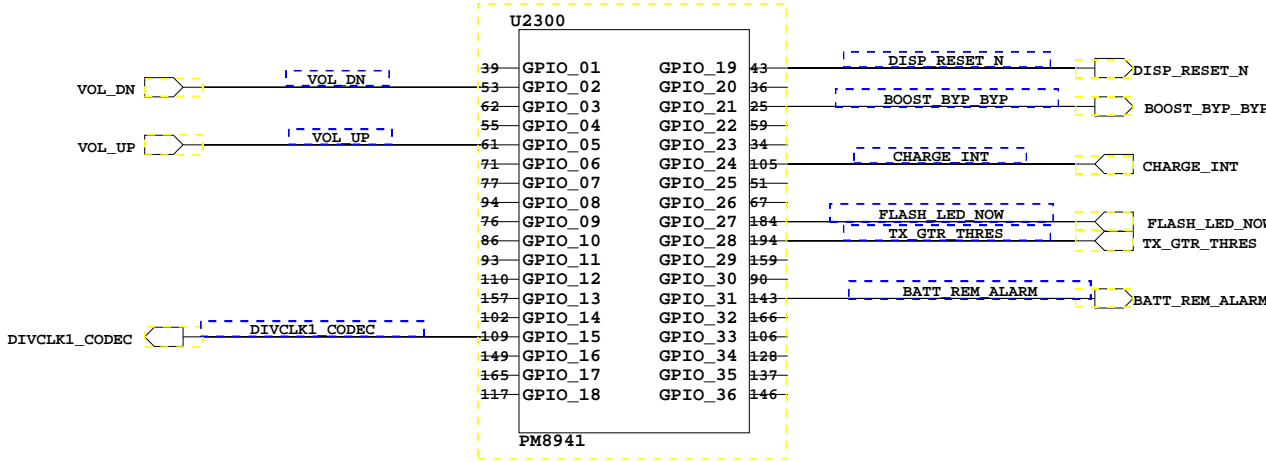




PM8941 : AMUX, CLK AND MPPTS



PM8941 : GPIOS



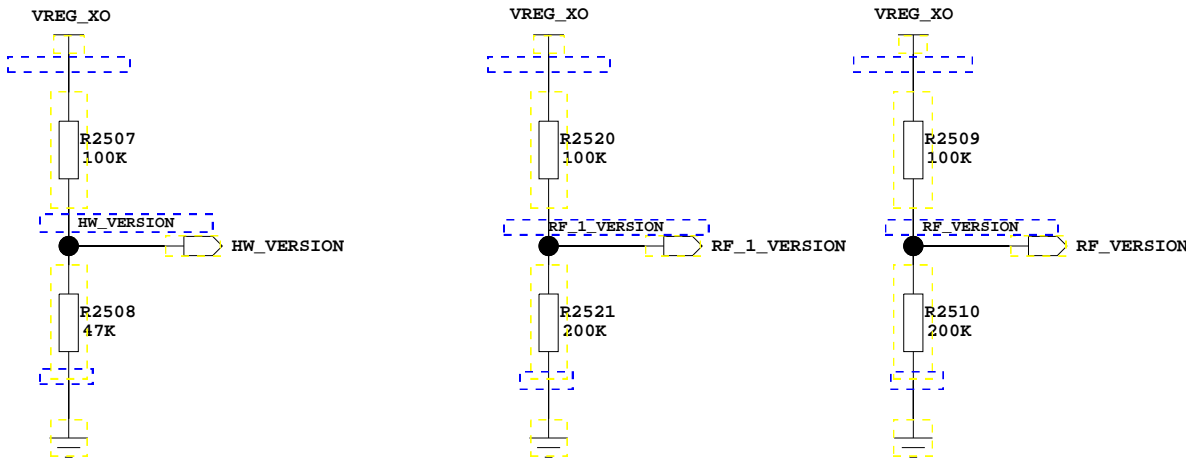
reset circuit

1/モジュール内 2/外付



HW\_VERSION

	EVb	EVT	DVT
マテリアル			
R2507	100K	100K	100K
マテリアル			
R2508	47K	100K	200K
ボルト値	0.576V	0.9V	1.2V

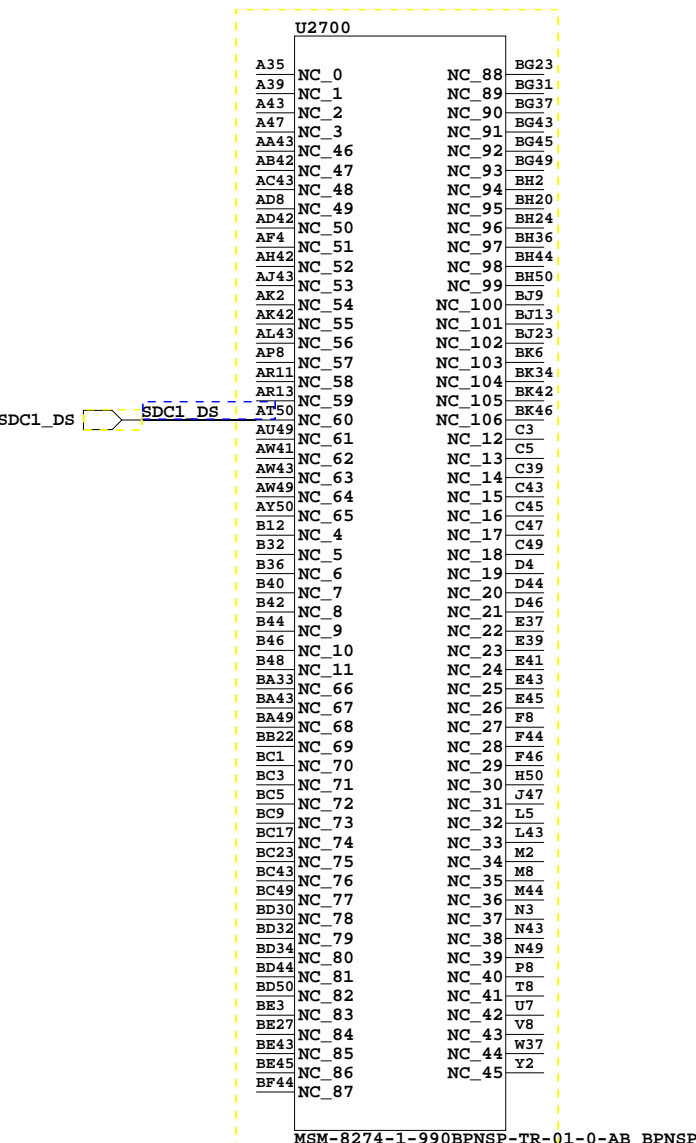
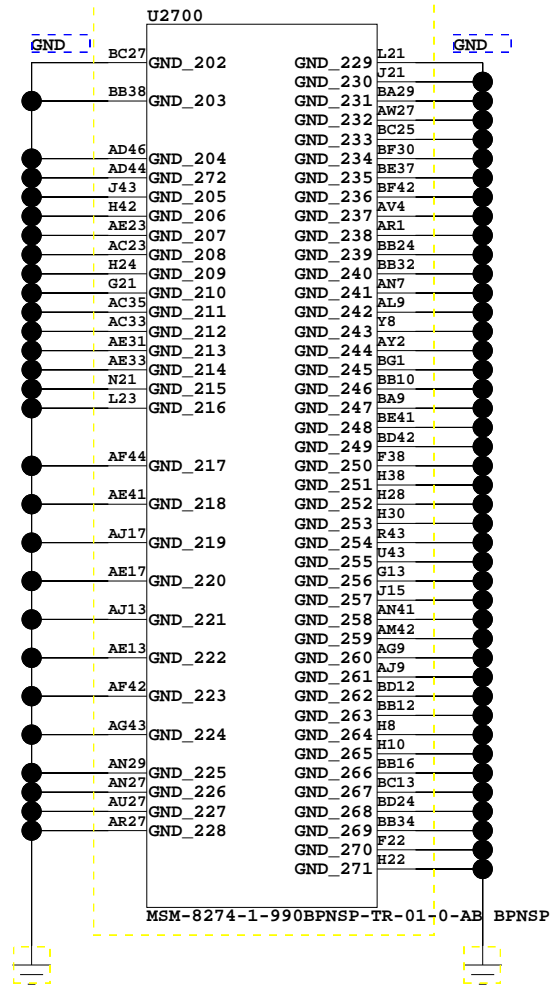
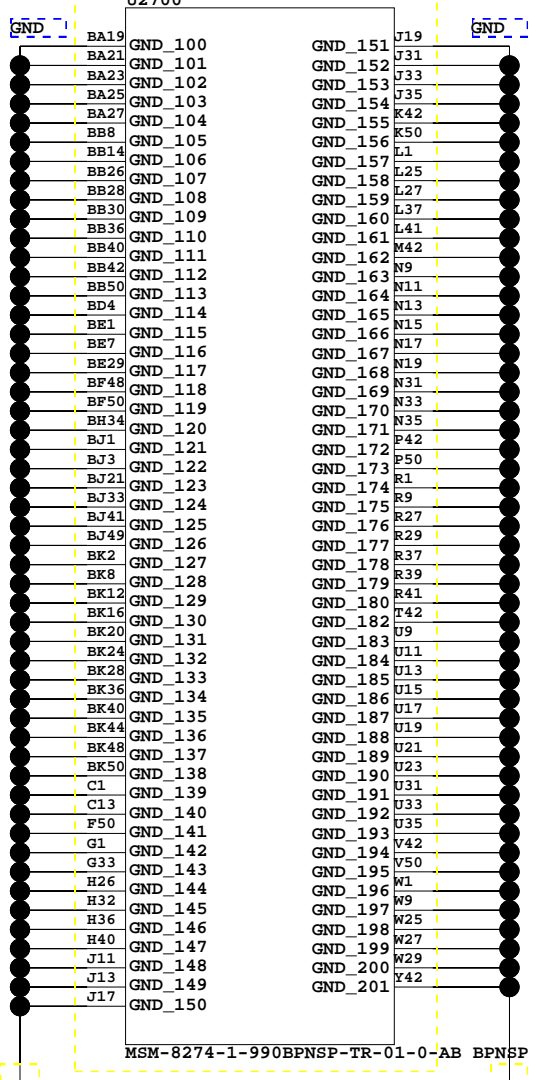
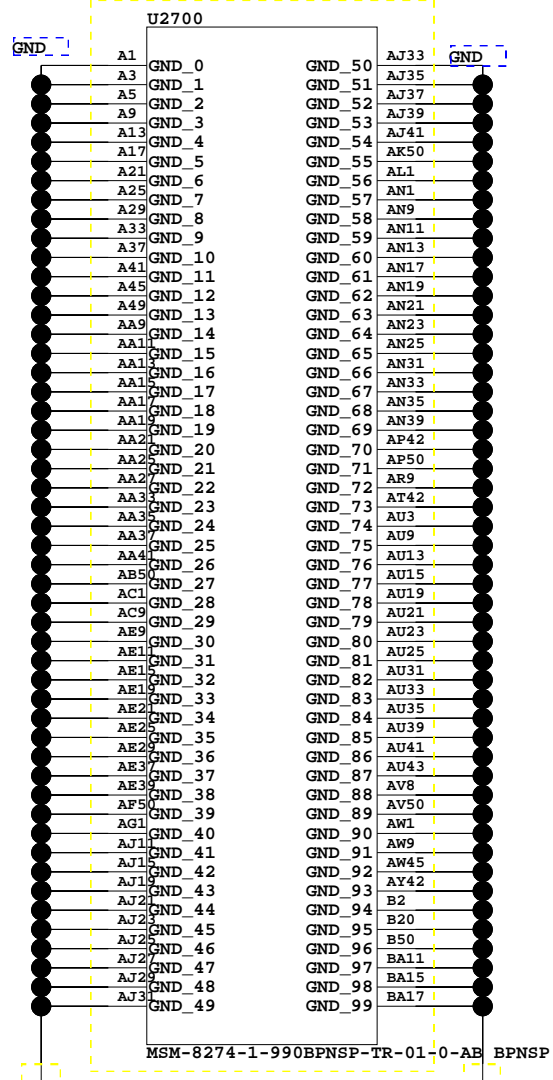
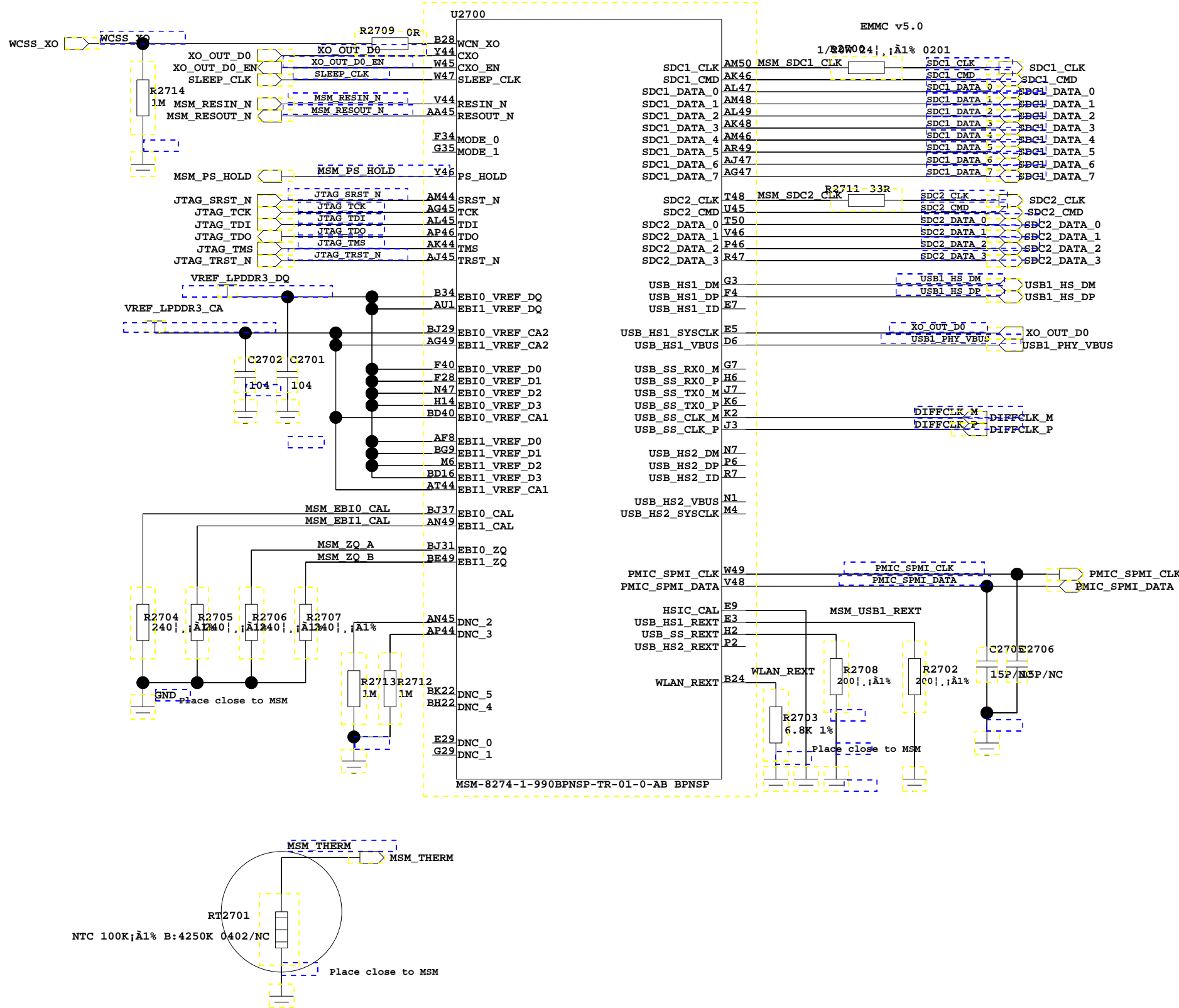




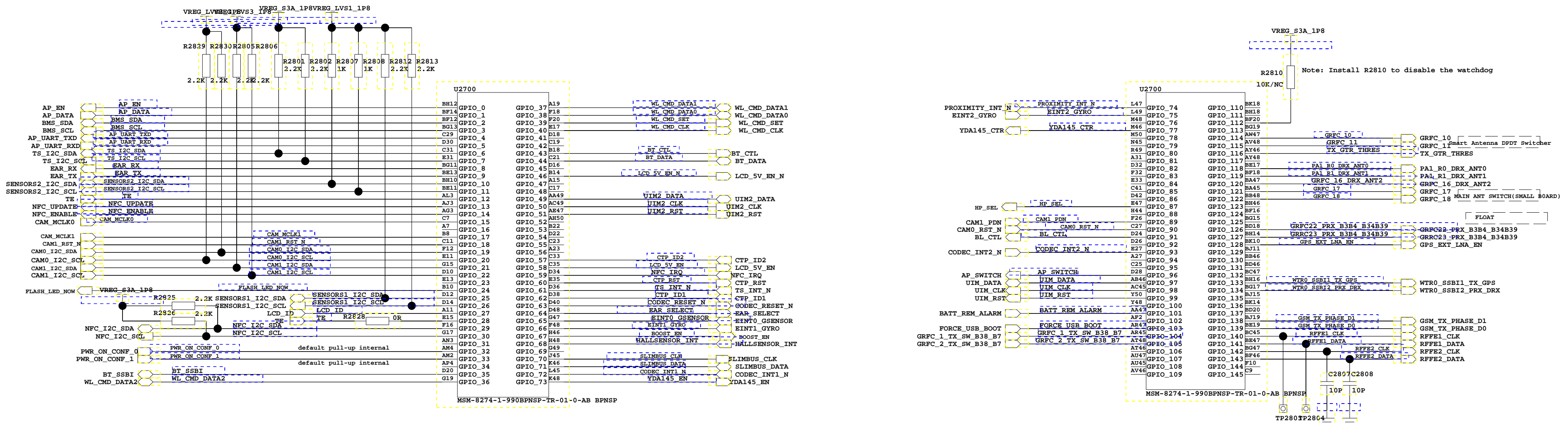


MSM8974 : CONTROL

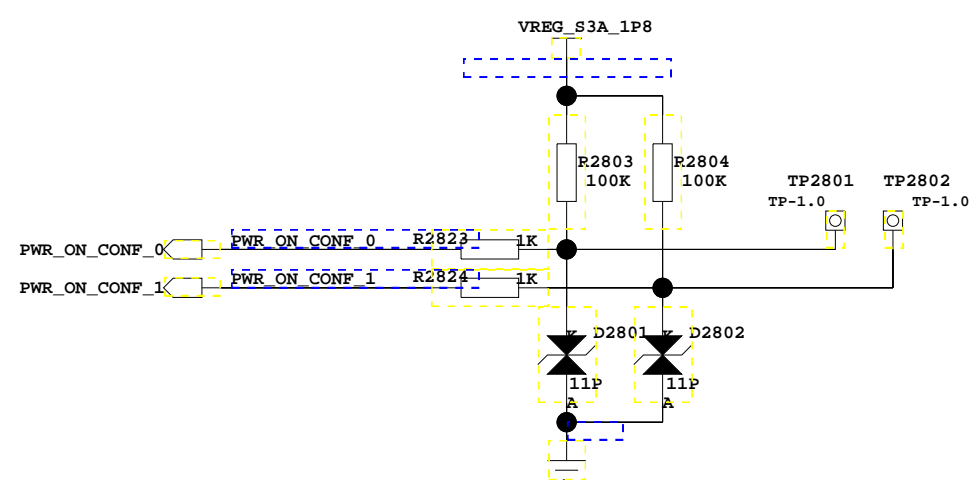
MSM8974 : GND & NC



## MSM8974 : GPIOs



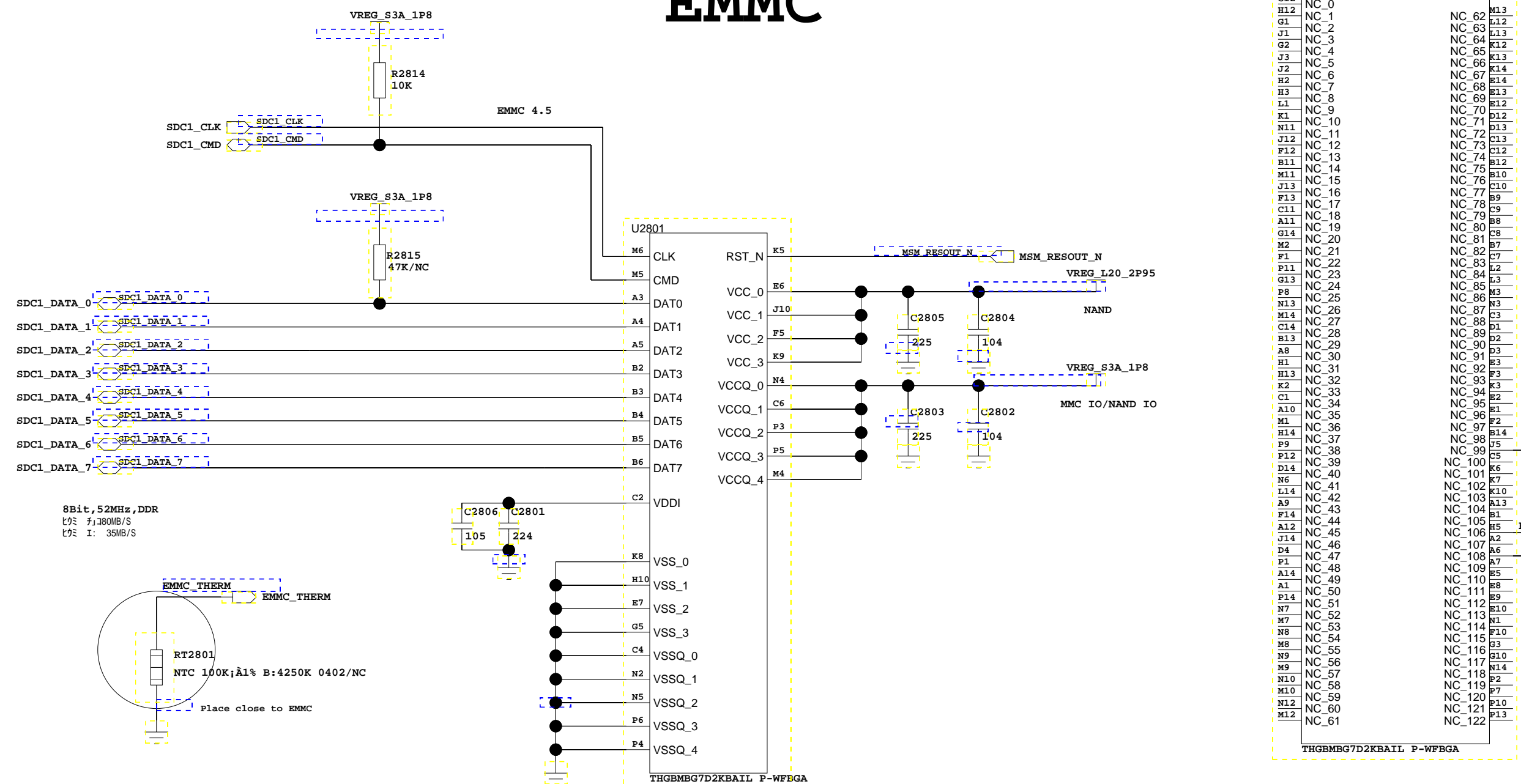
# Test Config



設定項目表

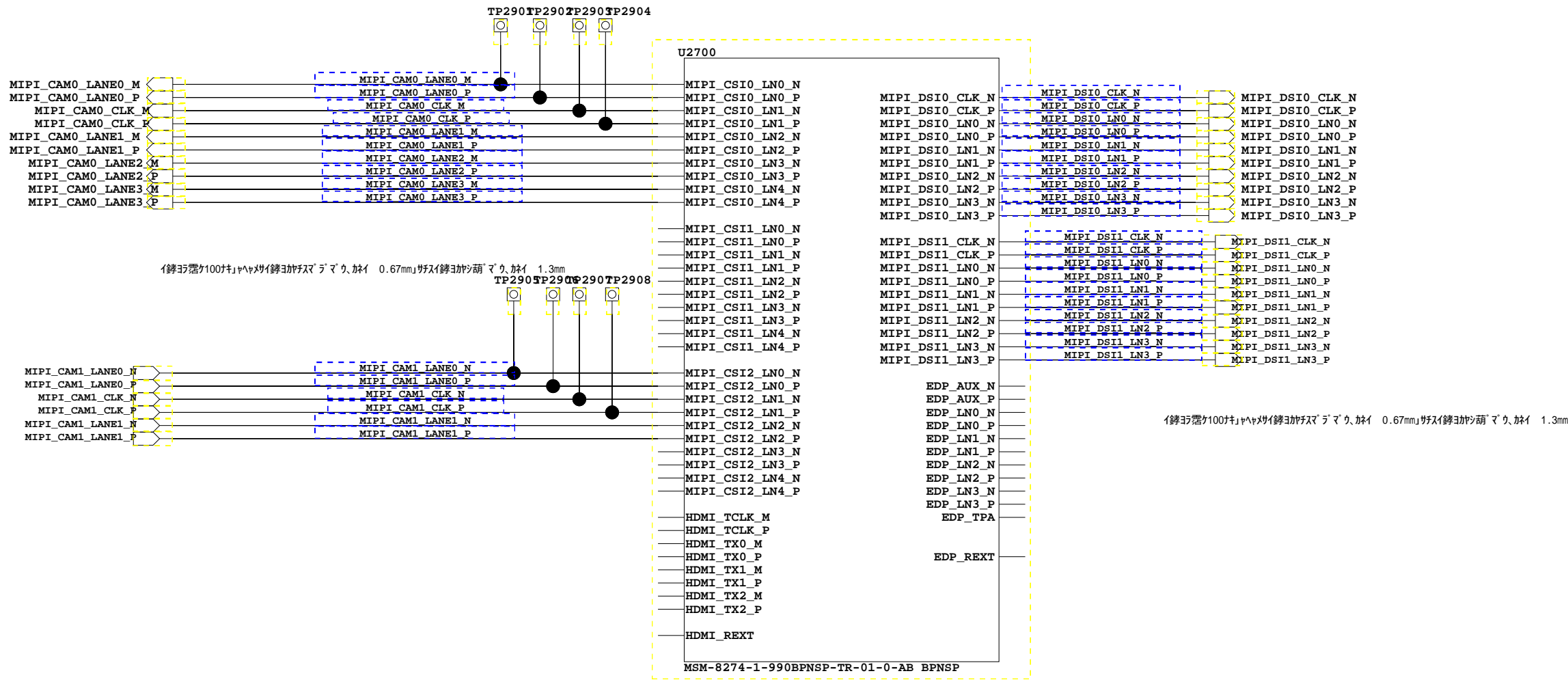
CONF_0	CONF_1	Mode
0	0	Factory Mode
0	1	WLAN Final Test
1	0	RF mode
1	1	Ordinary Power On

# EMMC

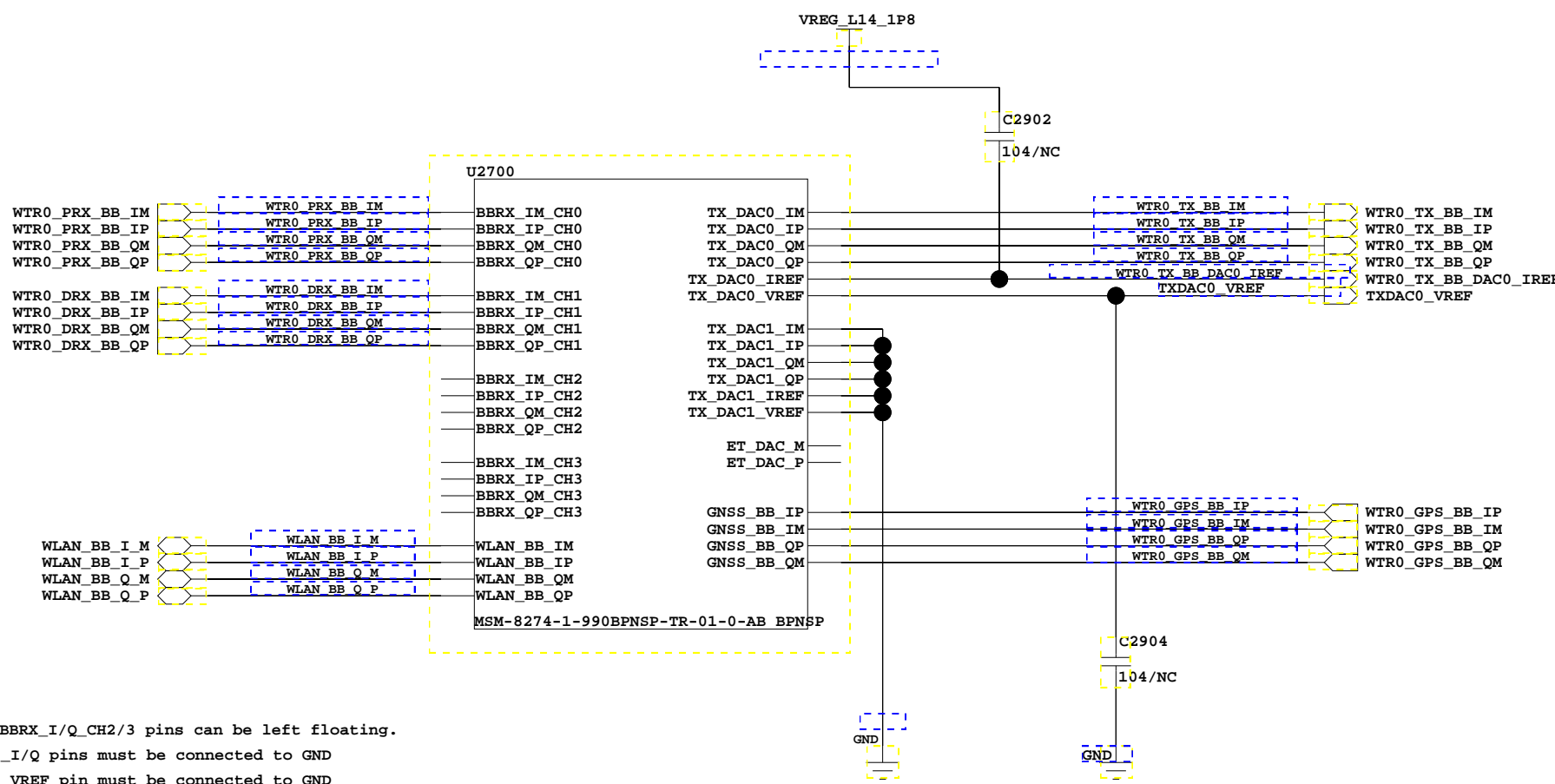


and the chip should has no special requirement on 1.8V/2.95V power on sequence (which is fixed by our pmic, some eMMC has special requirement on it, most has no requirement on it).

# MSM8974: MIPI



# MSM8974 : RF, GPS AND WLAN

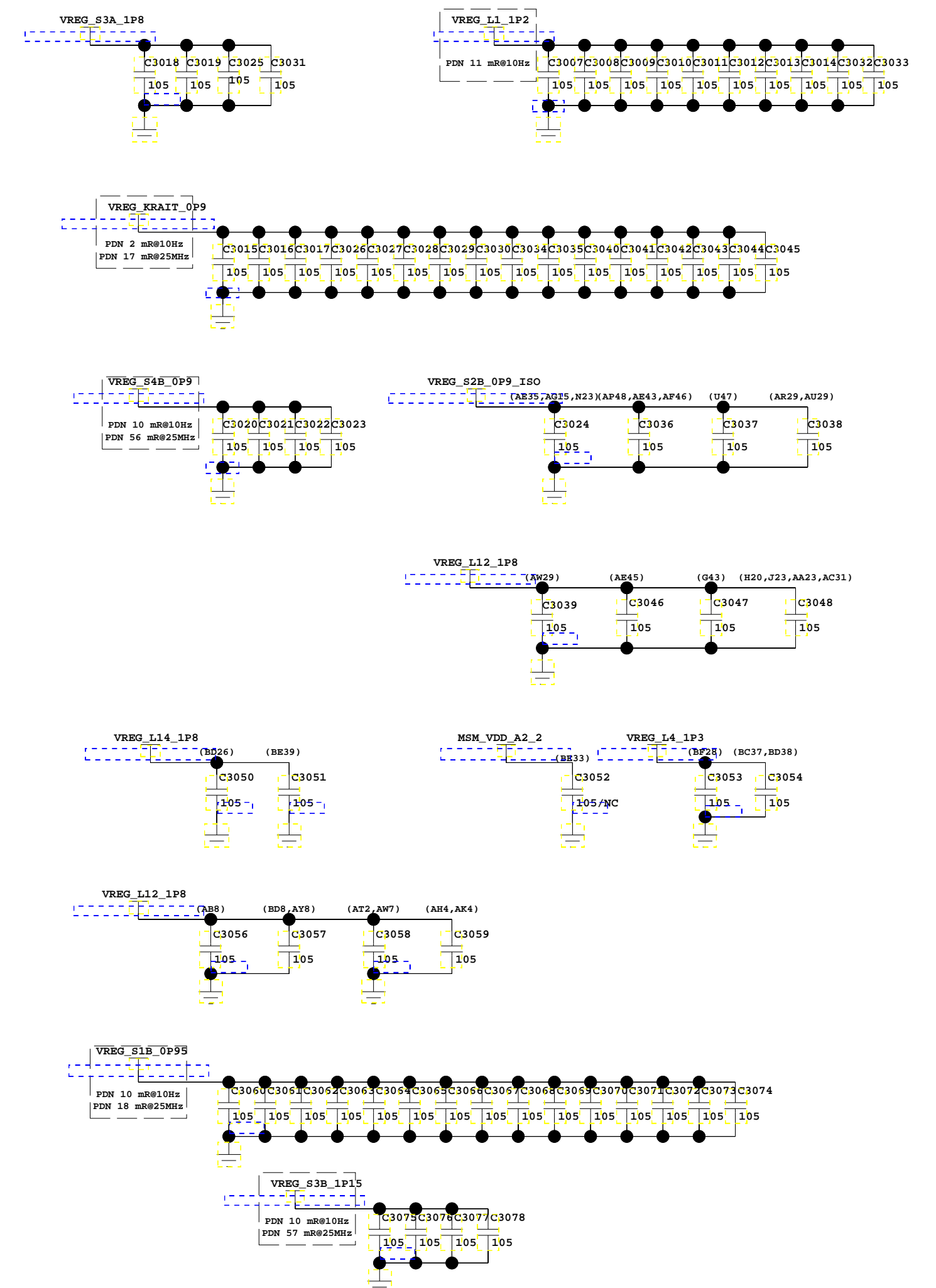


Note: For CSFB - unused BBRX\_I/Q\_CH2/3 pins can be left floating

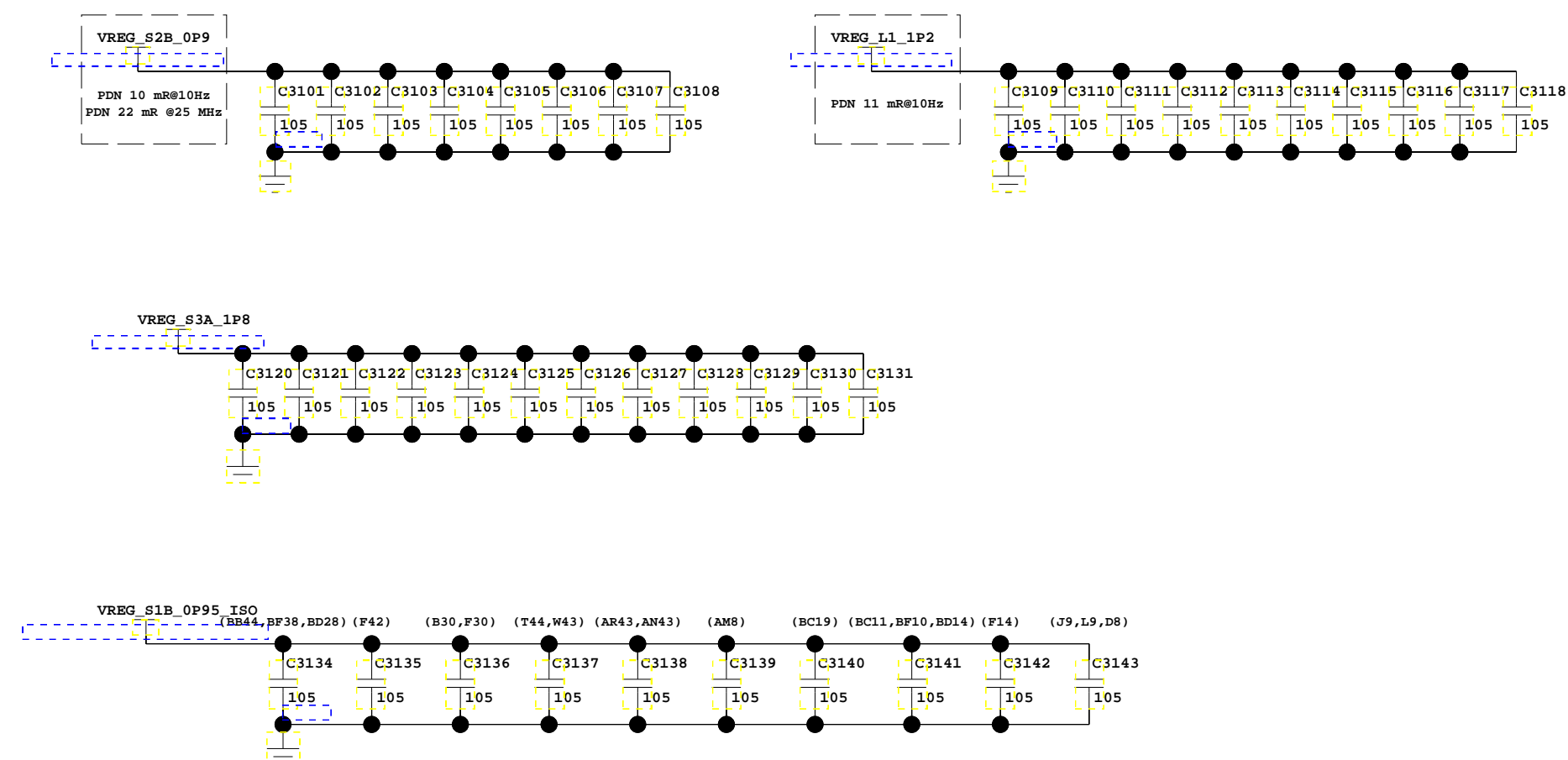
- Unused TXDAC1\_I/Q pins must be connected to GND
- Unused TXDAC1\_VREF pin must be connected to GND
- TXDAC1\_IREF pin must be connected to VREG\_L14\_1P8.

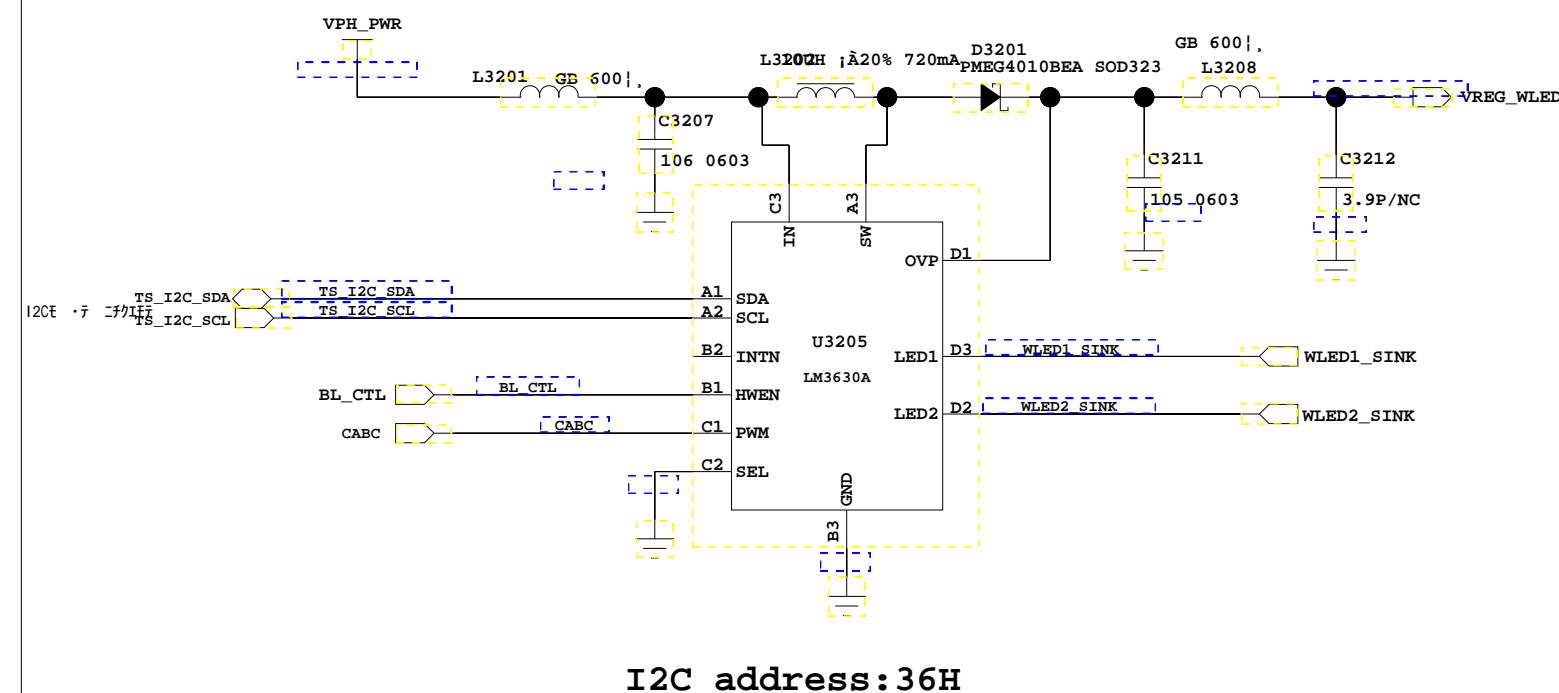
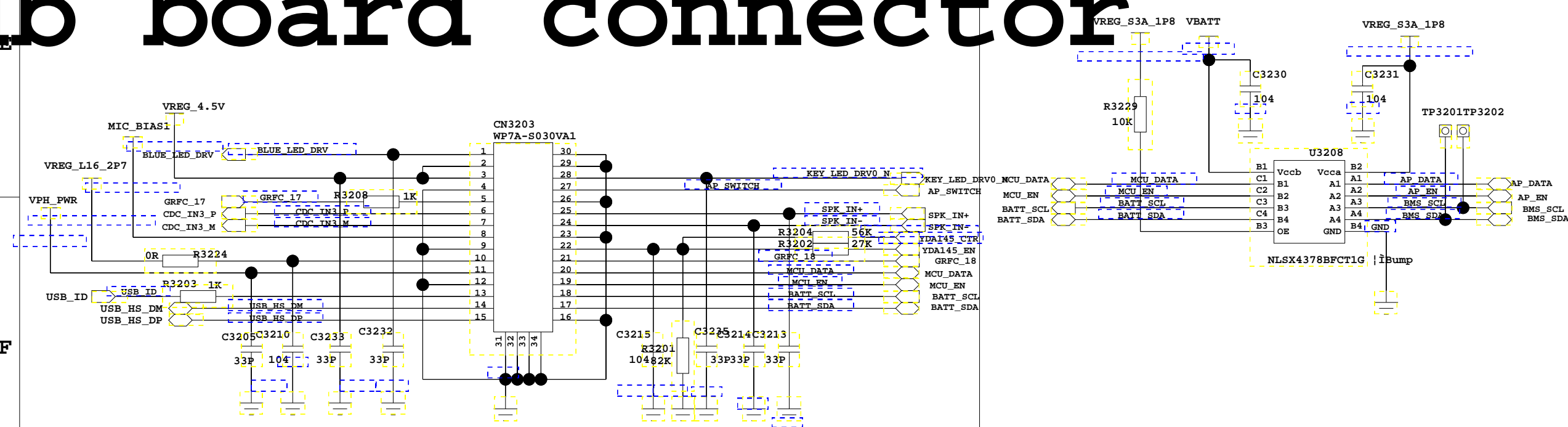
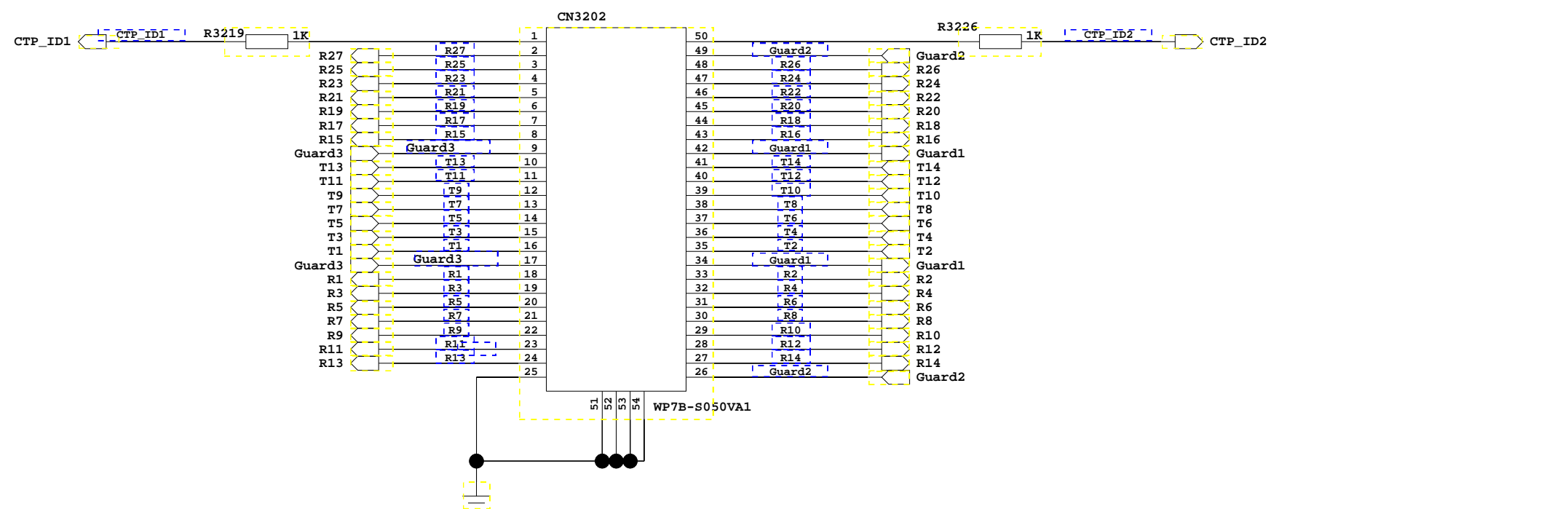
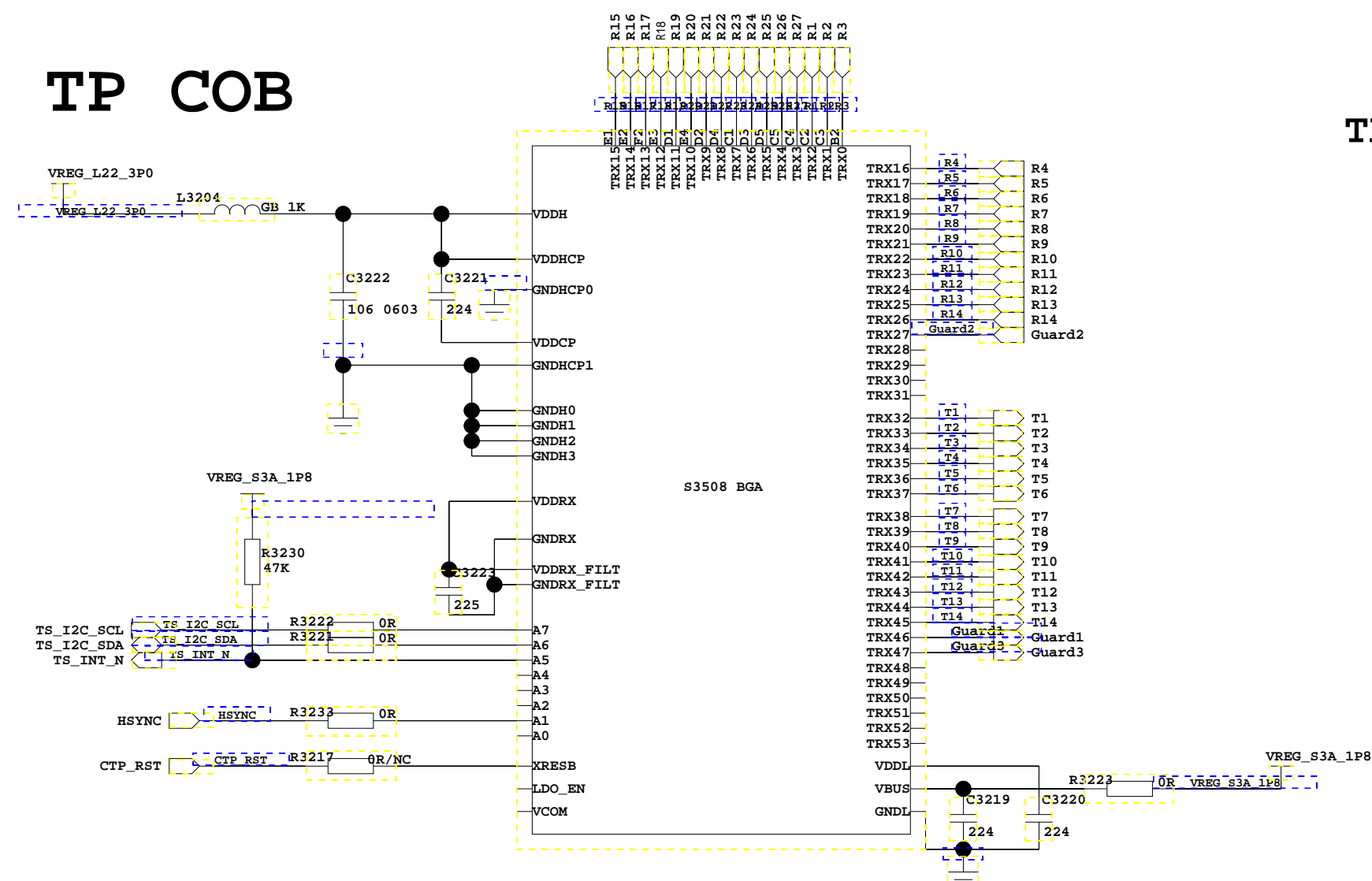
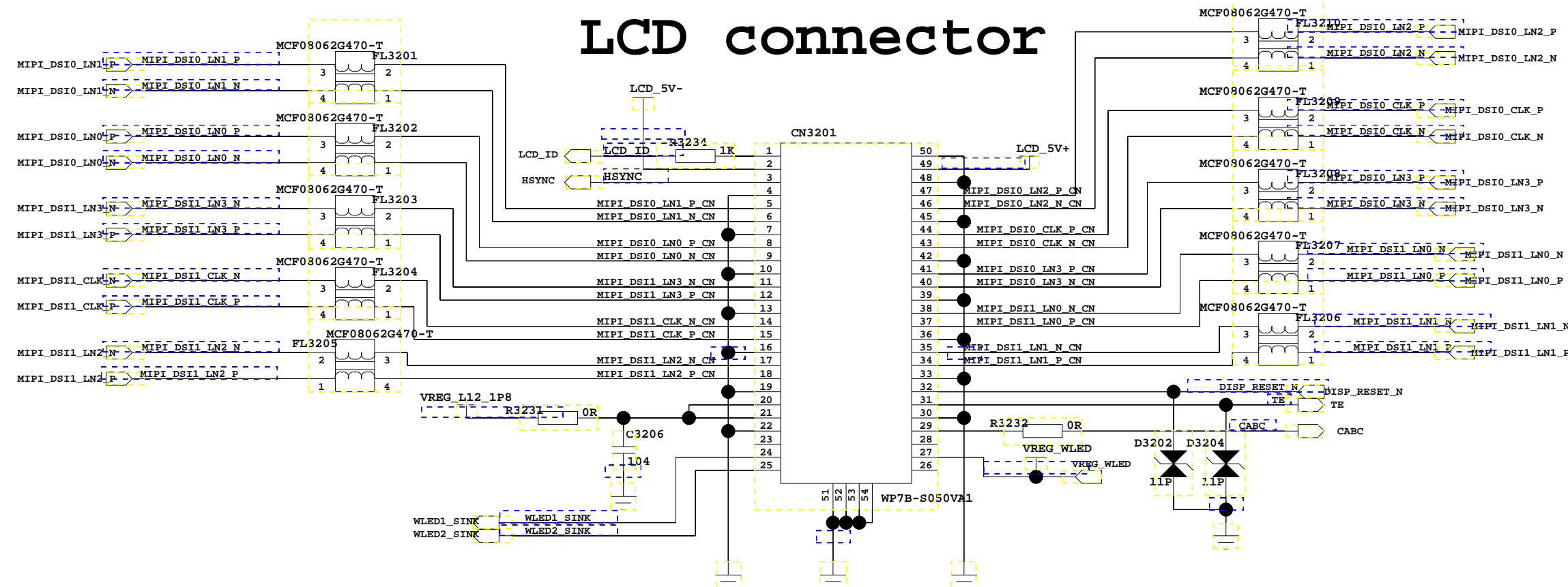
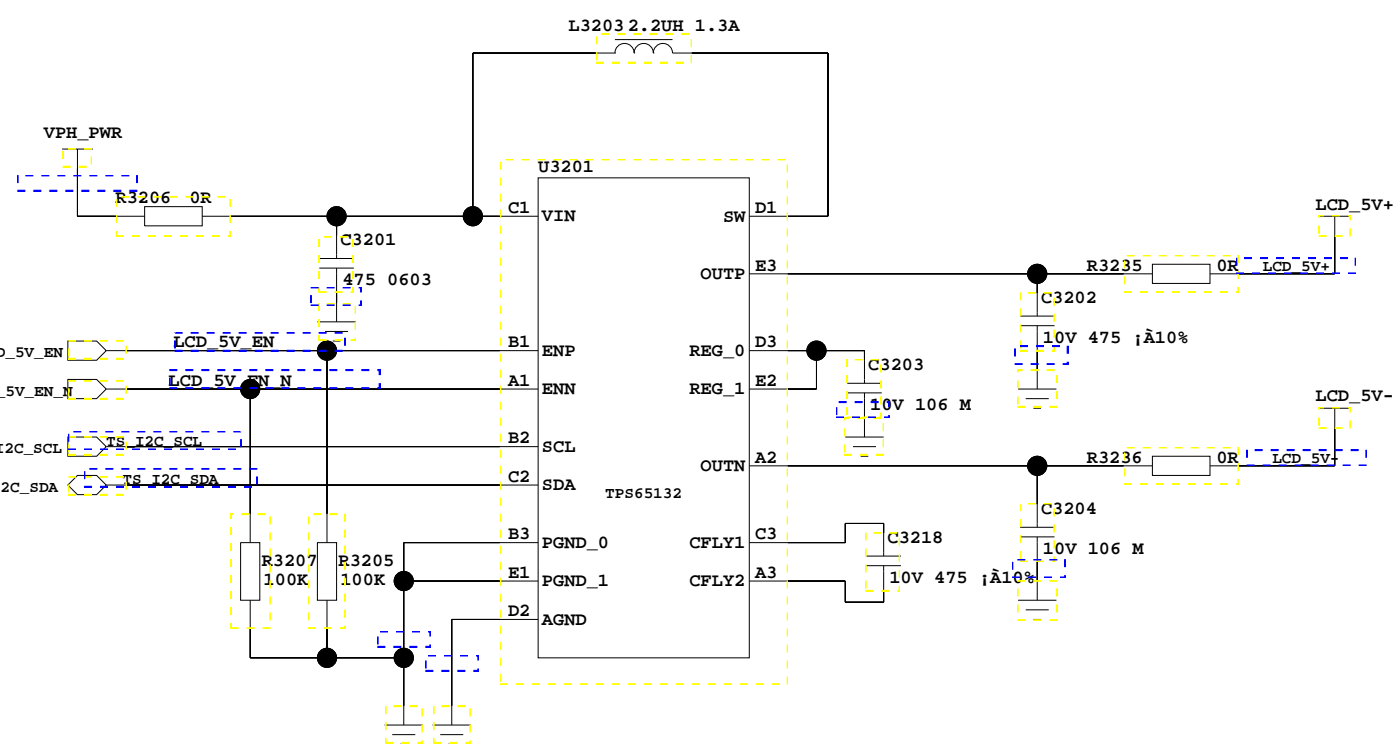


## MSM8974 : DECOUPLING CAPS



## MSM8974 : DECOUPLING CAPS





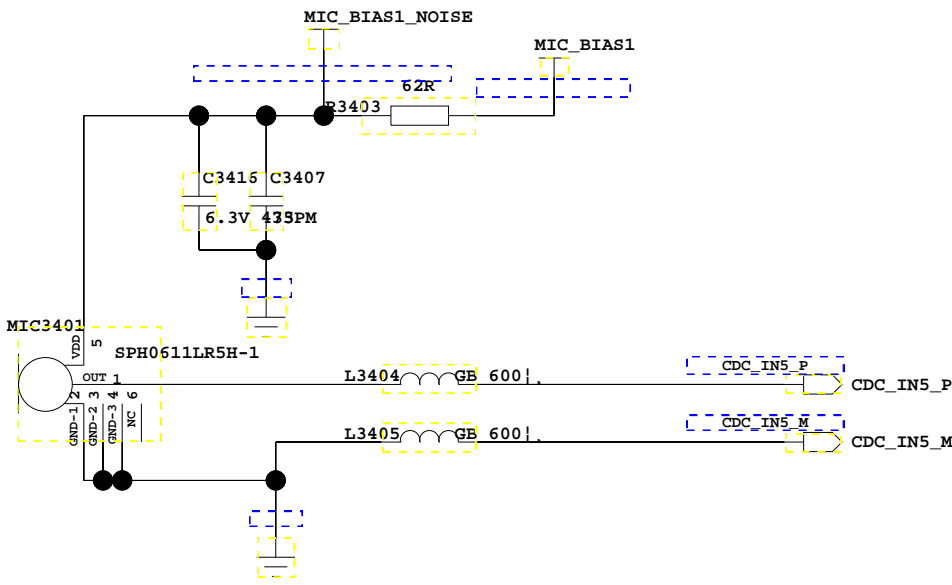
I2C address:36H



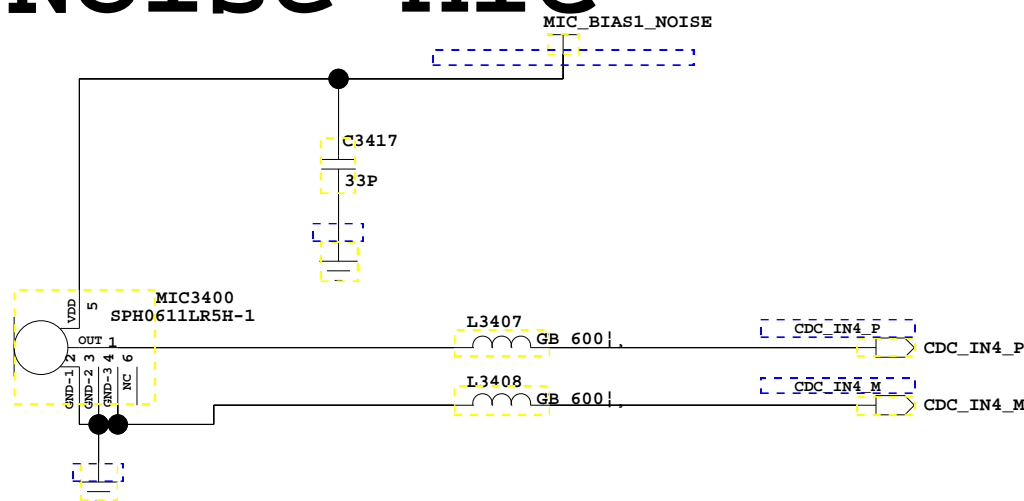


RECEIVER

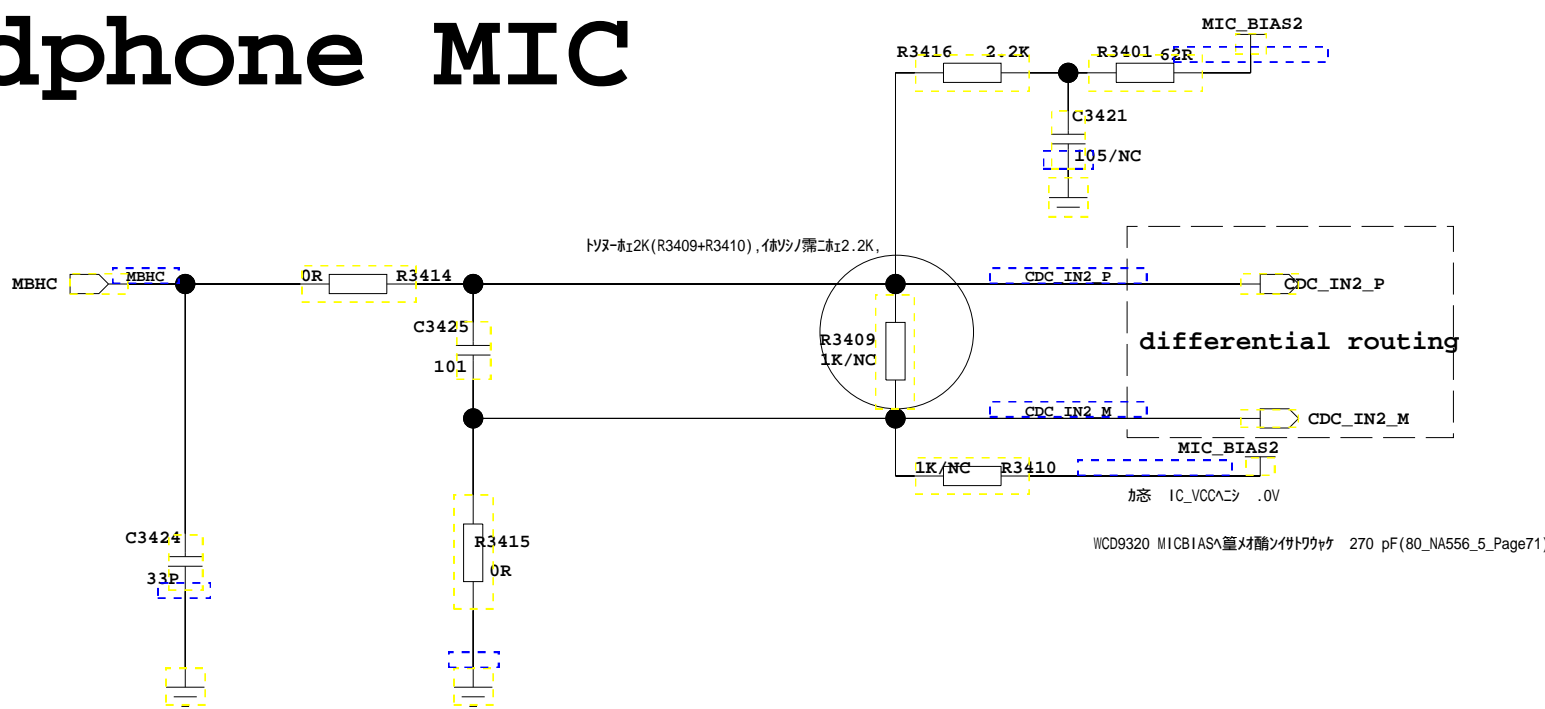
ANC MIC



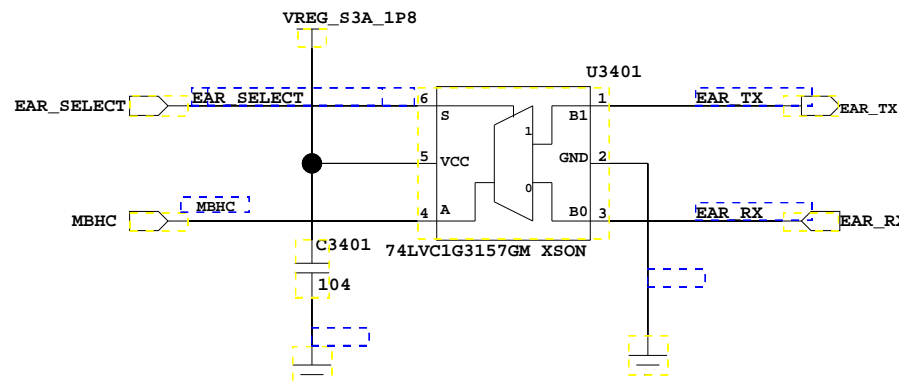
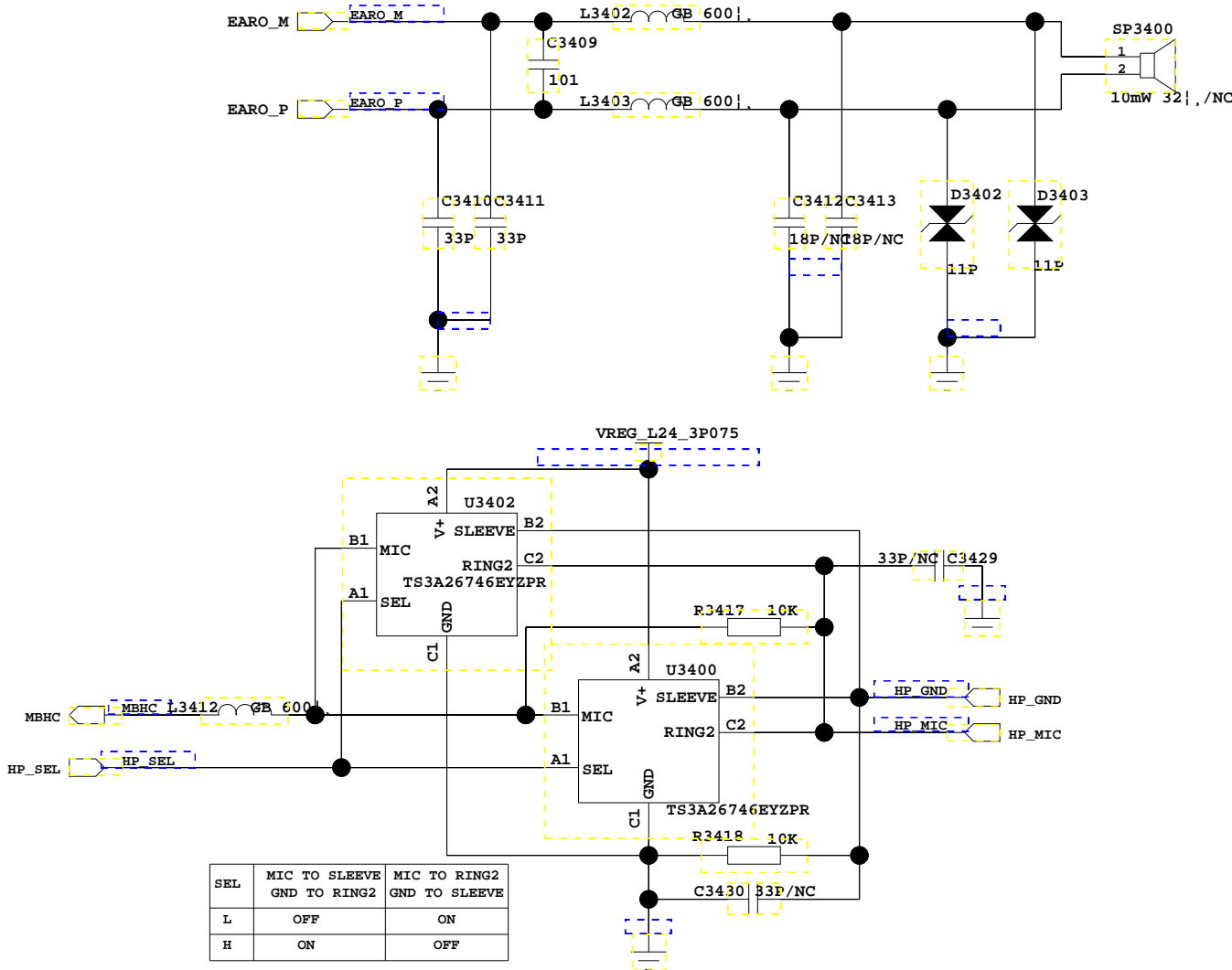
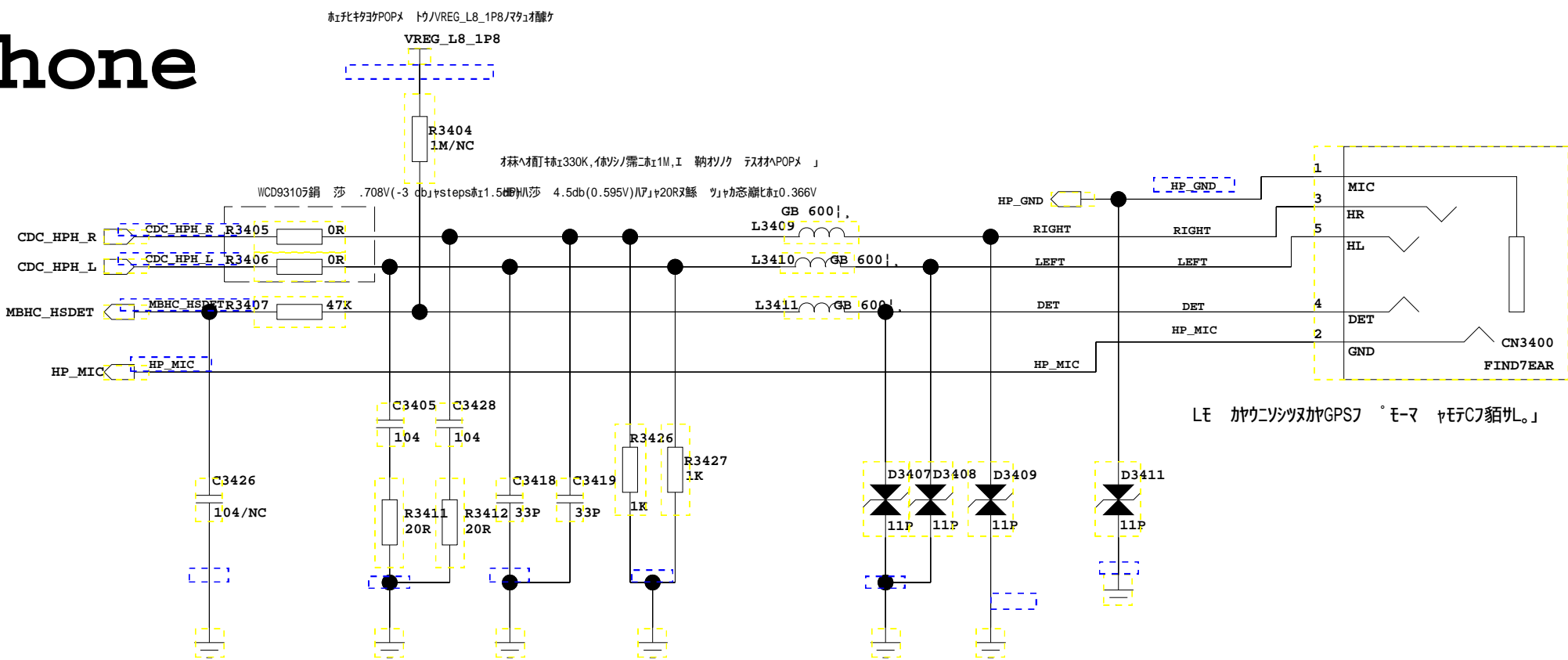
Noise MIC



Headphone MIC

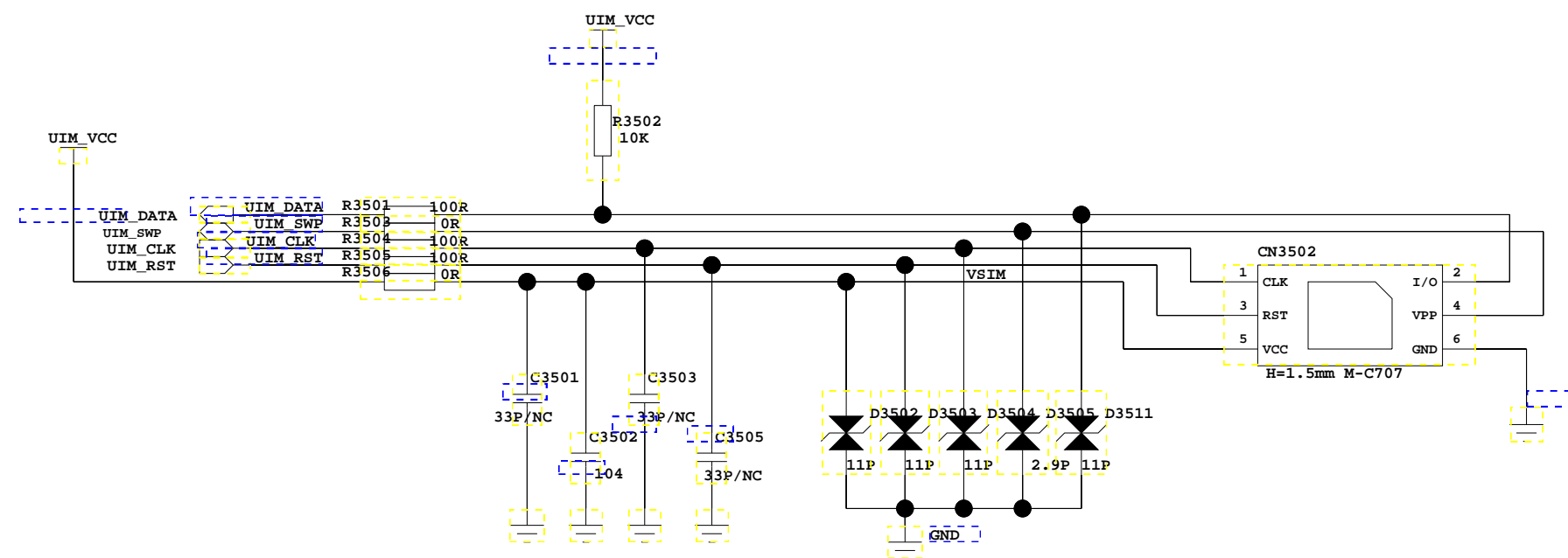


Headphone

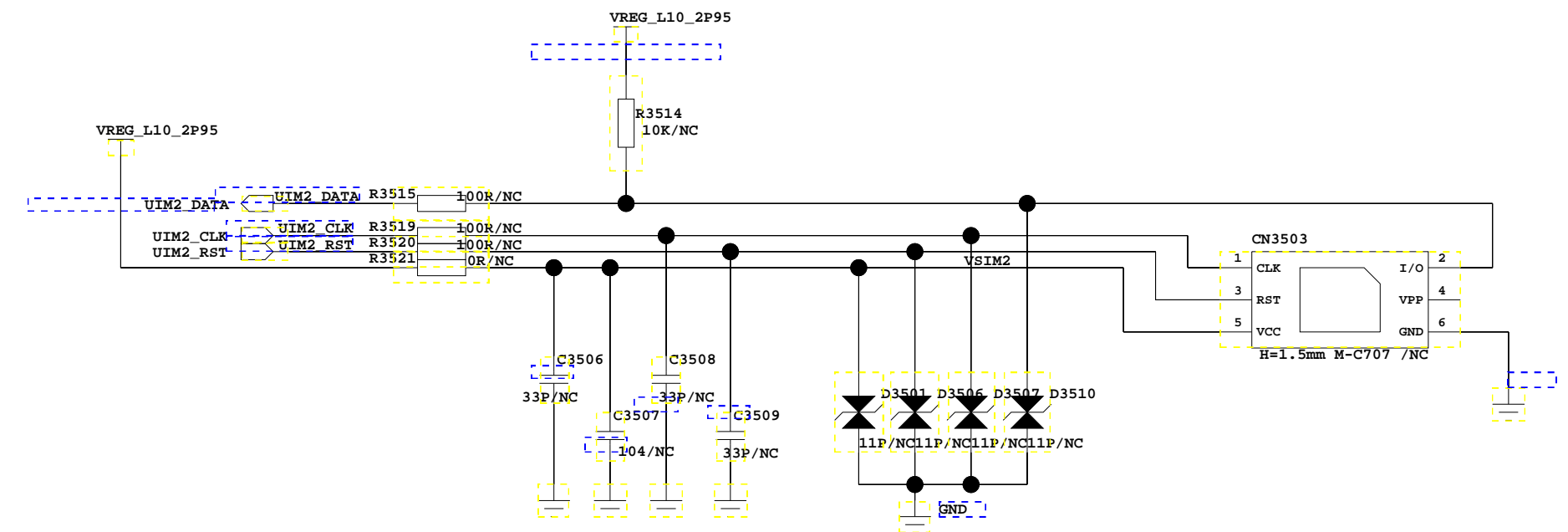




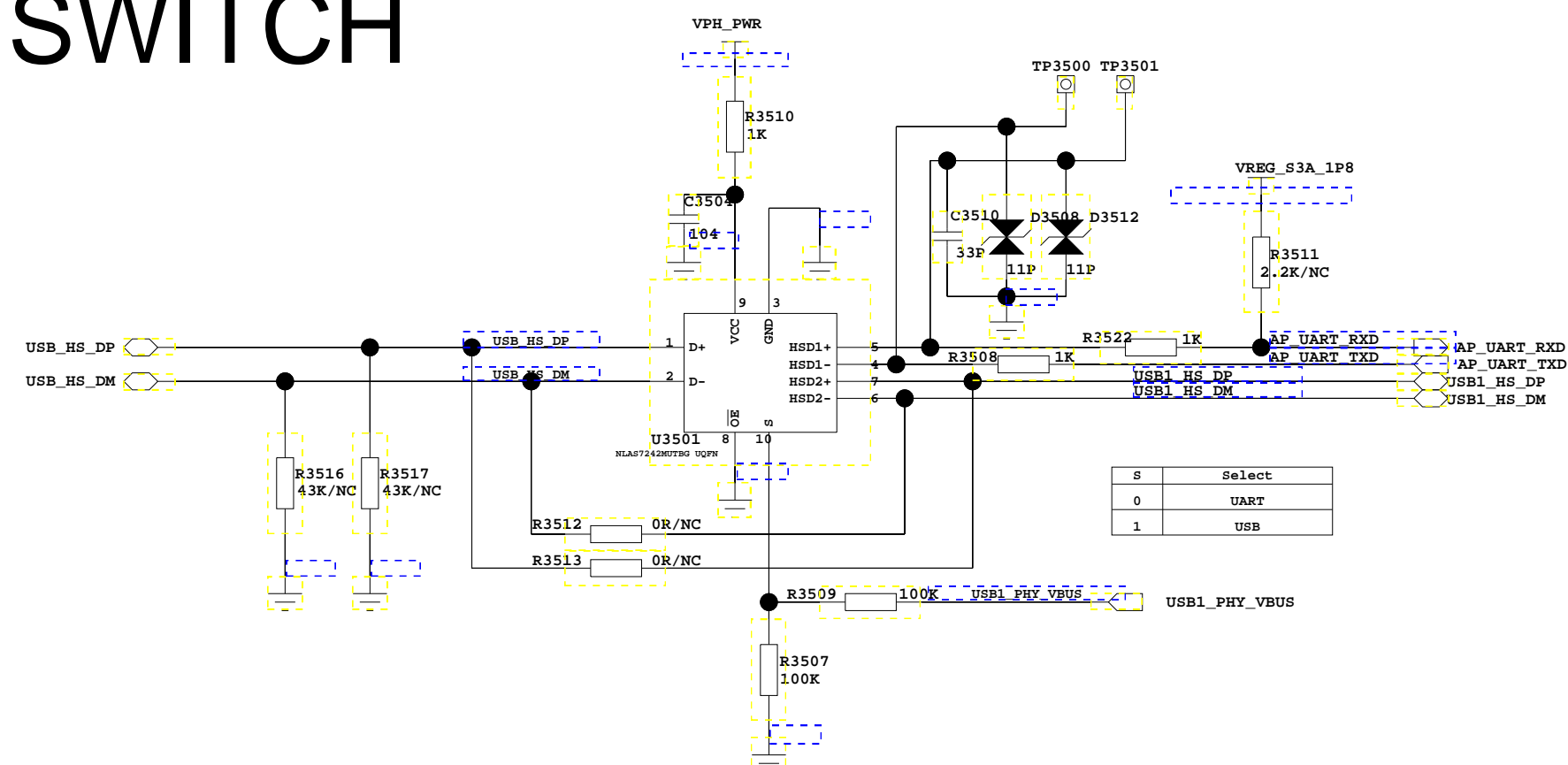
# SIM CARD



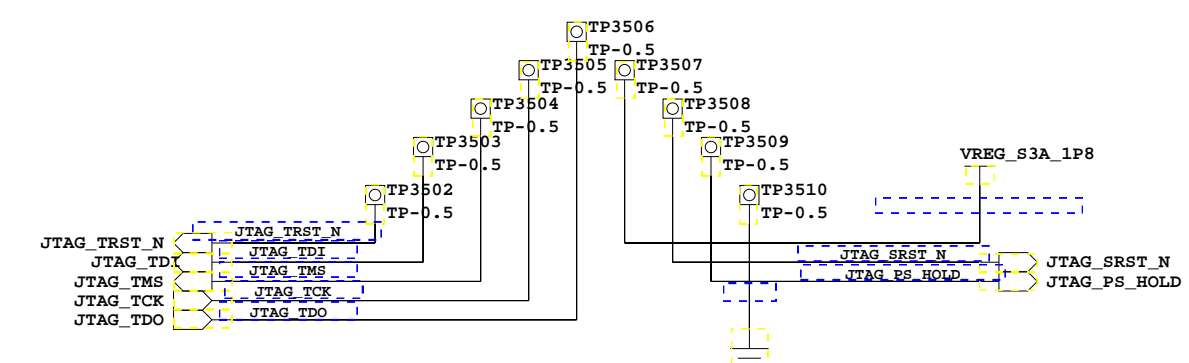
# SIM CARD 2



# USB SWITCH



# MSM8974 JTAG DEBUG



S	Select
0	UART
1	USB

A

B

C

D

E

F

A

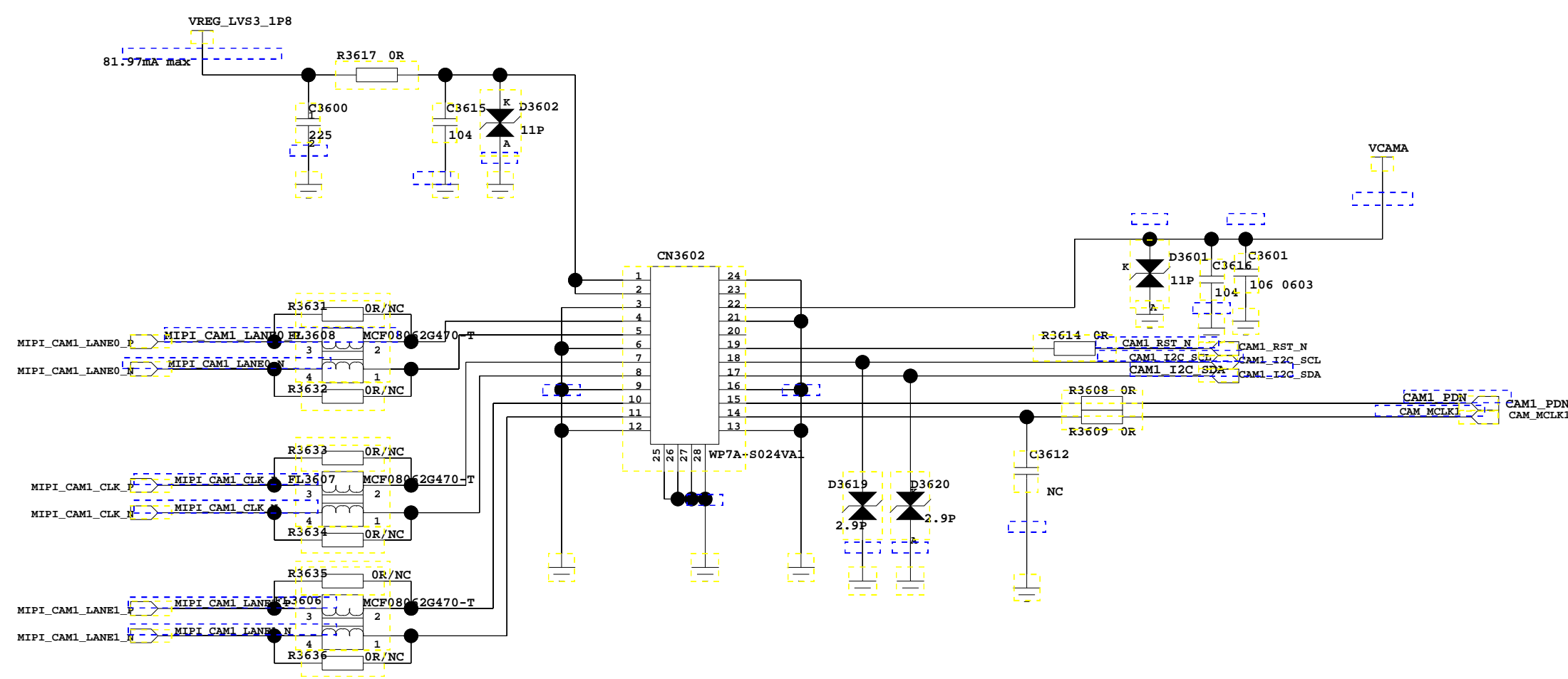
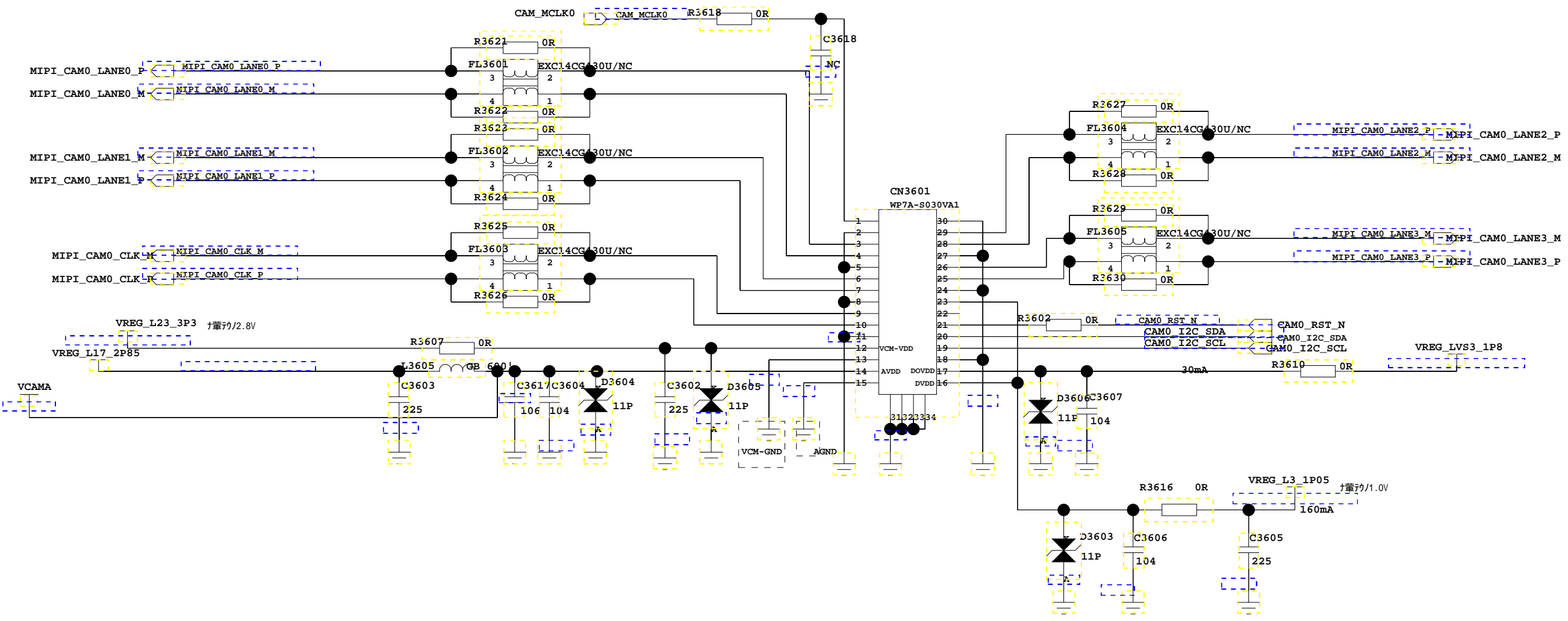
B

C

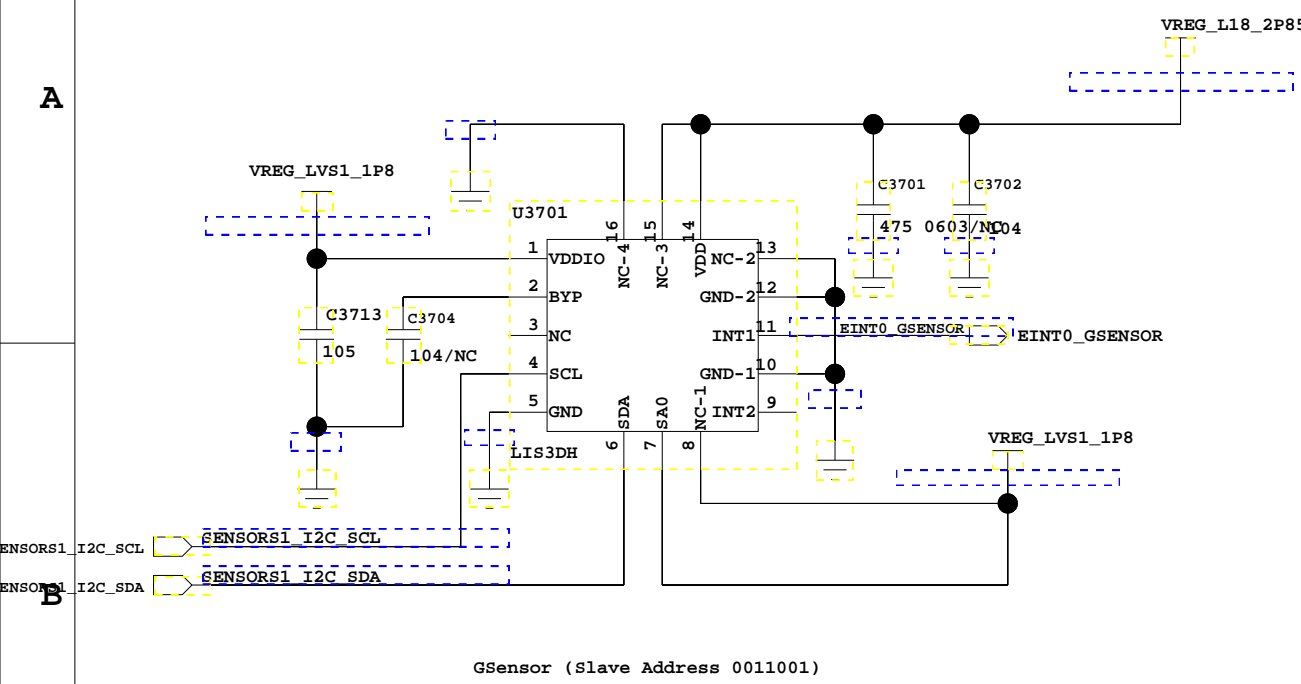
D

E

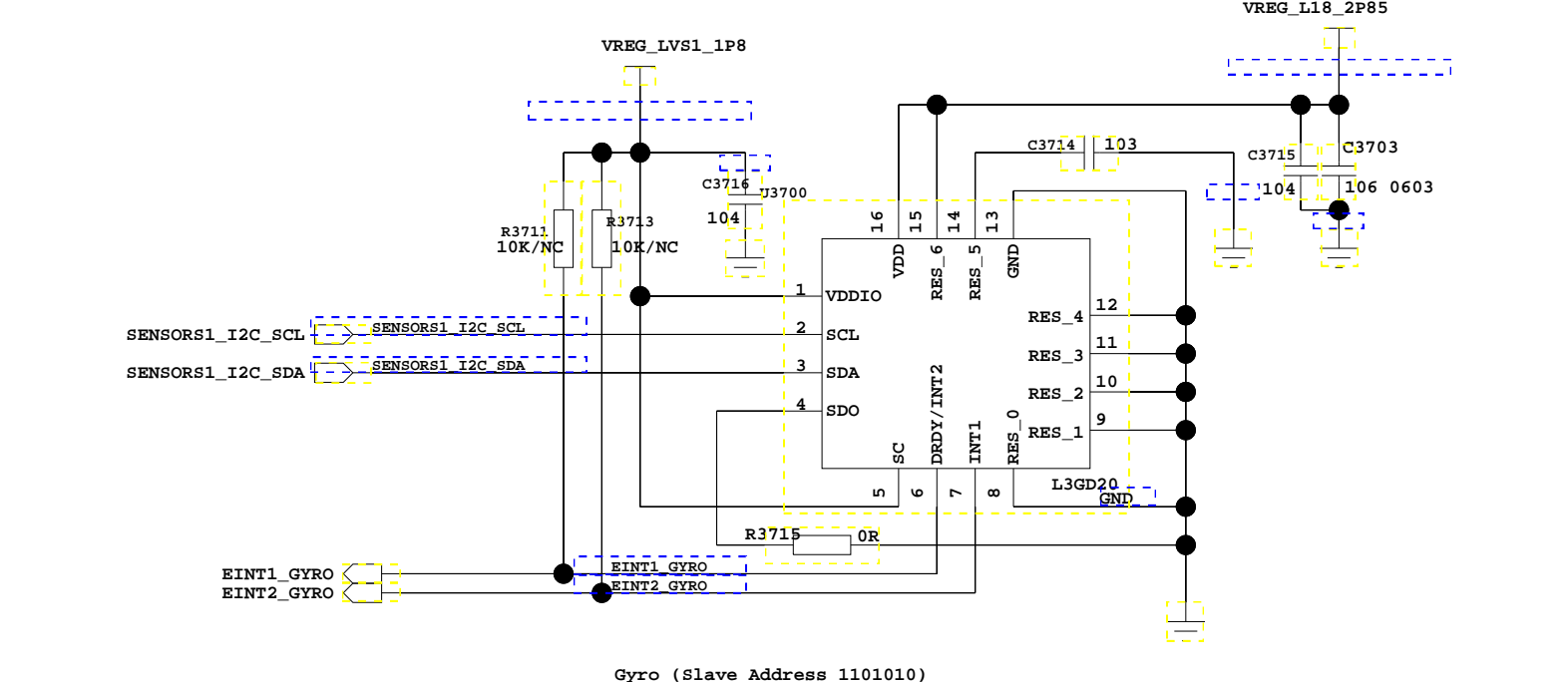
F



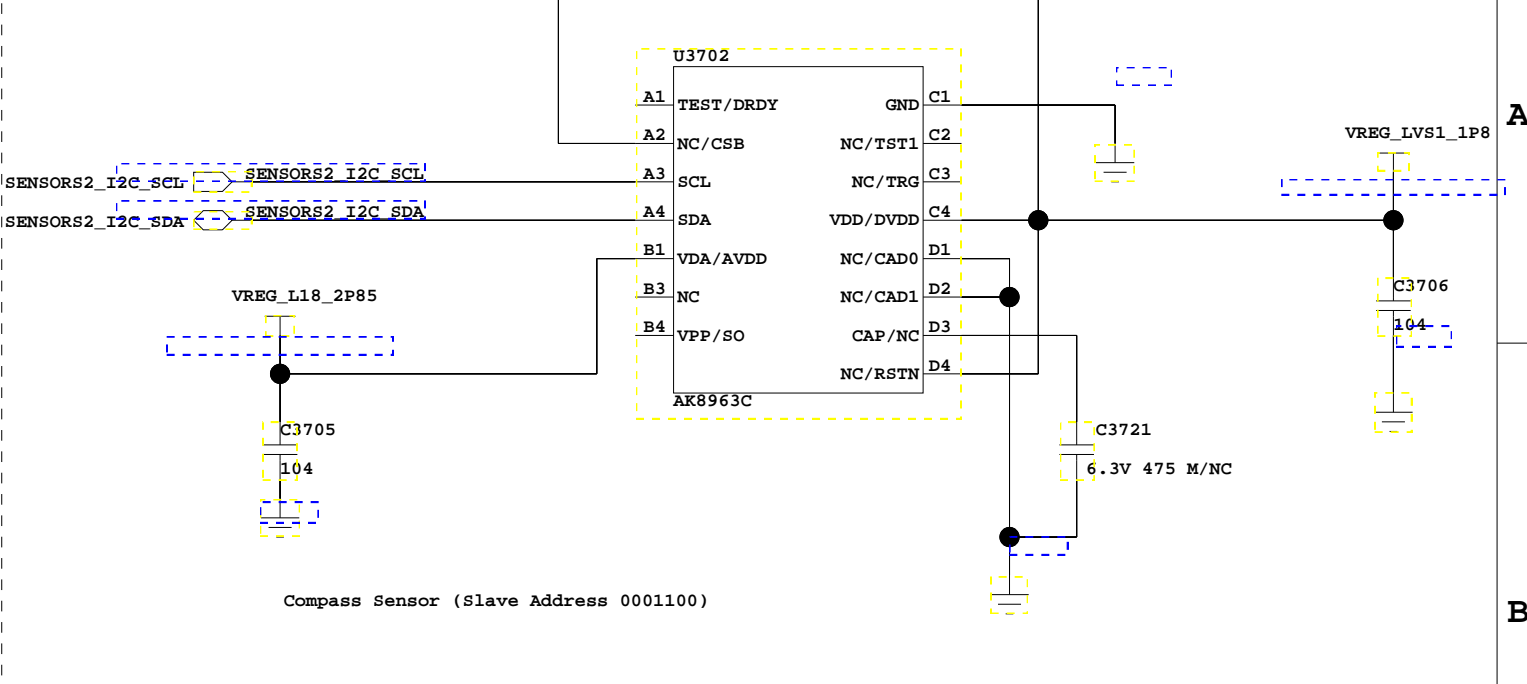
# G-sensor



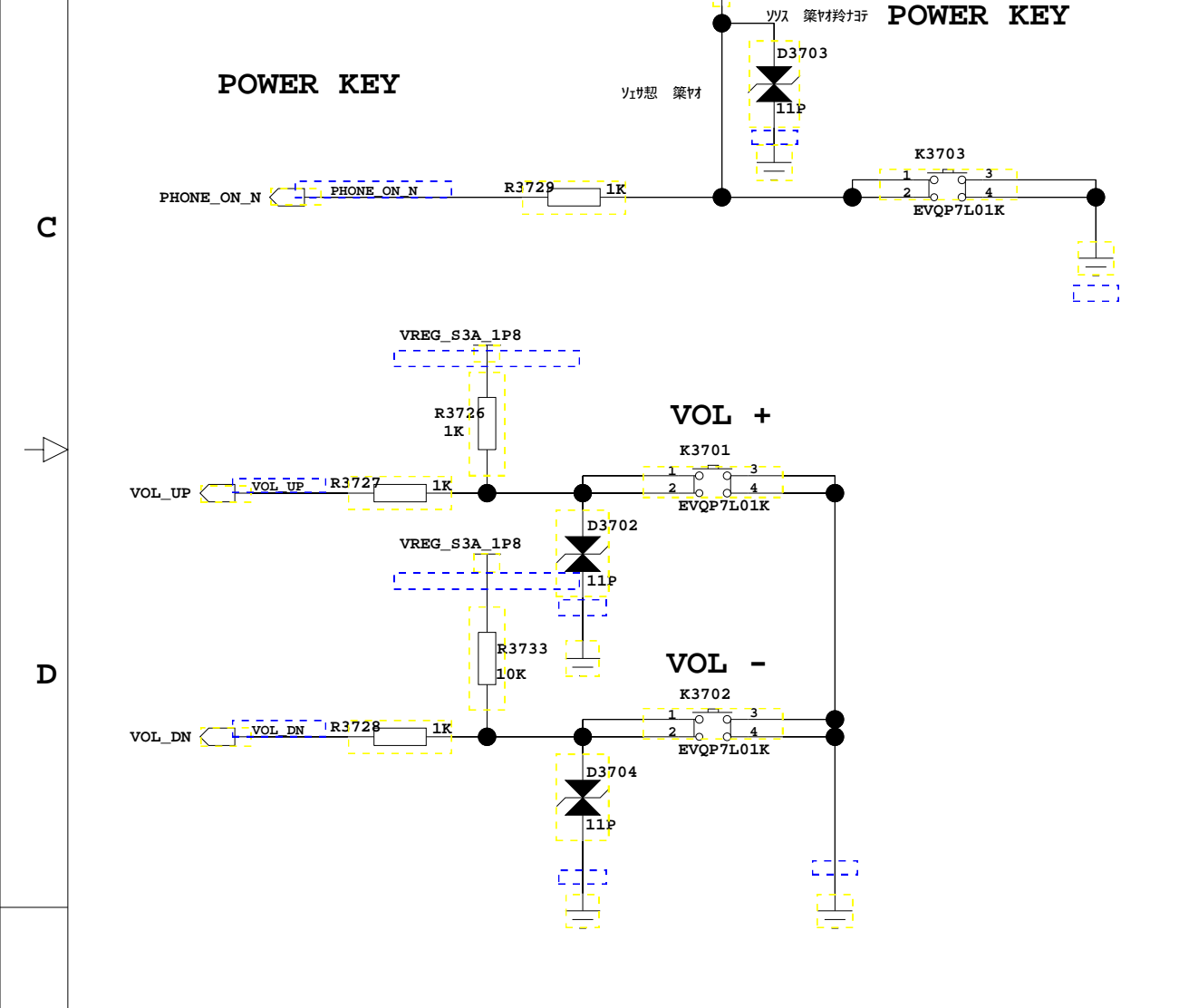
# GYRO



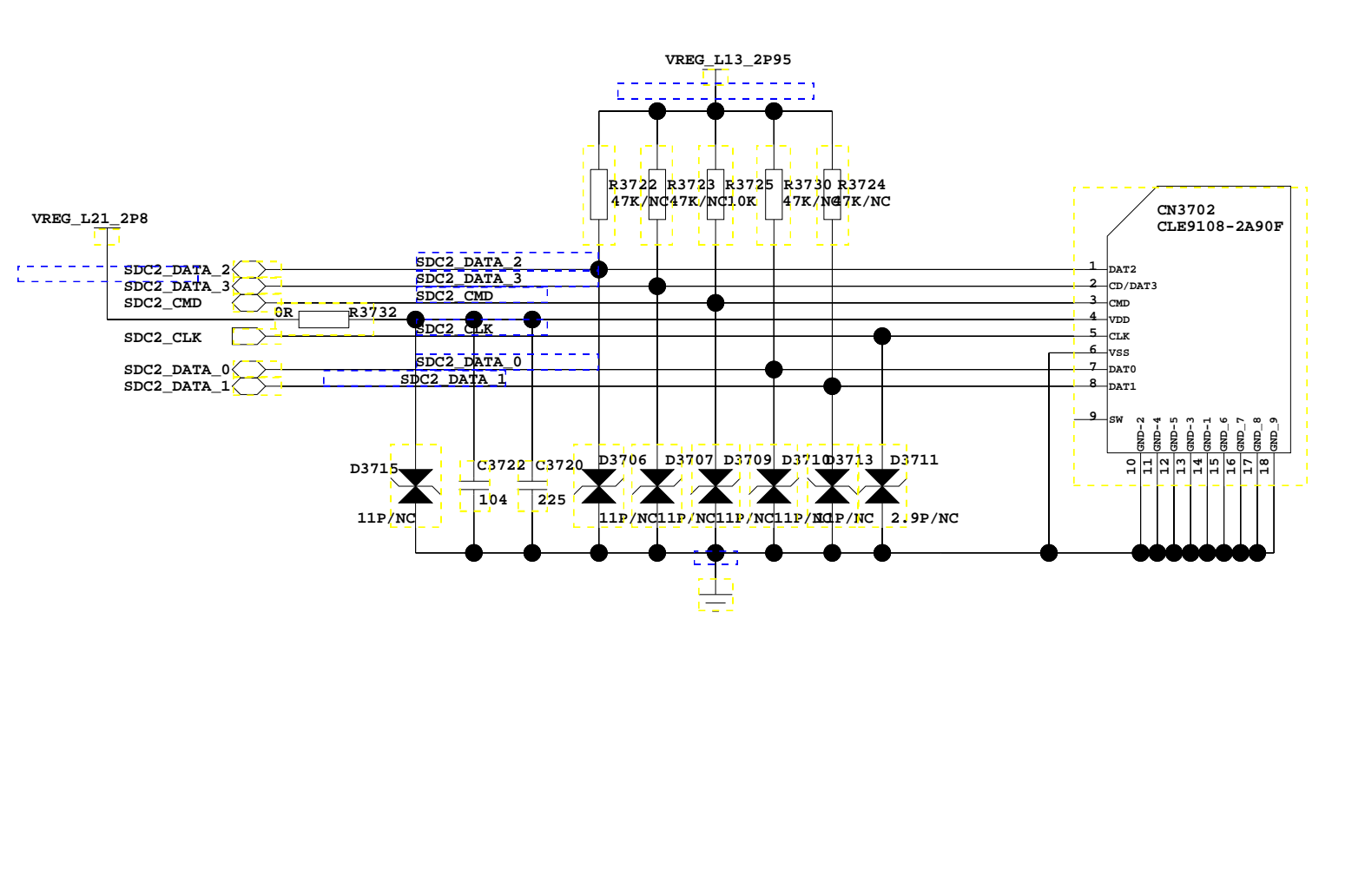
# M-Sensor



# KEY



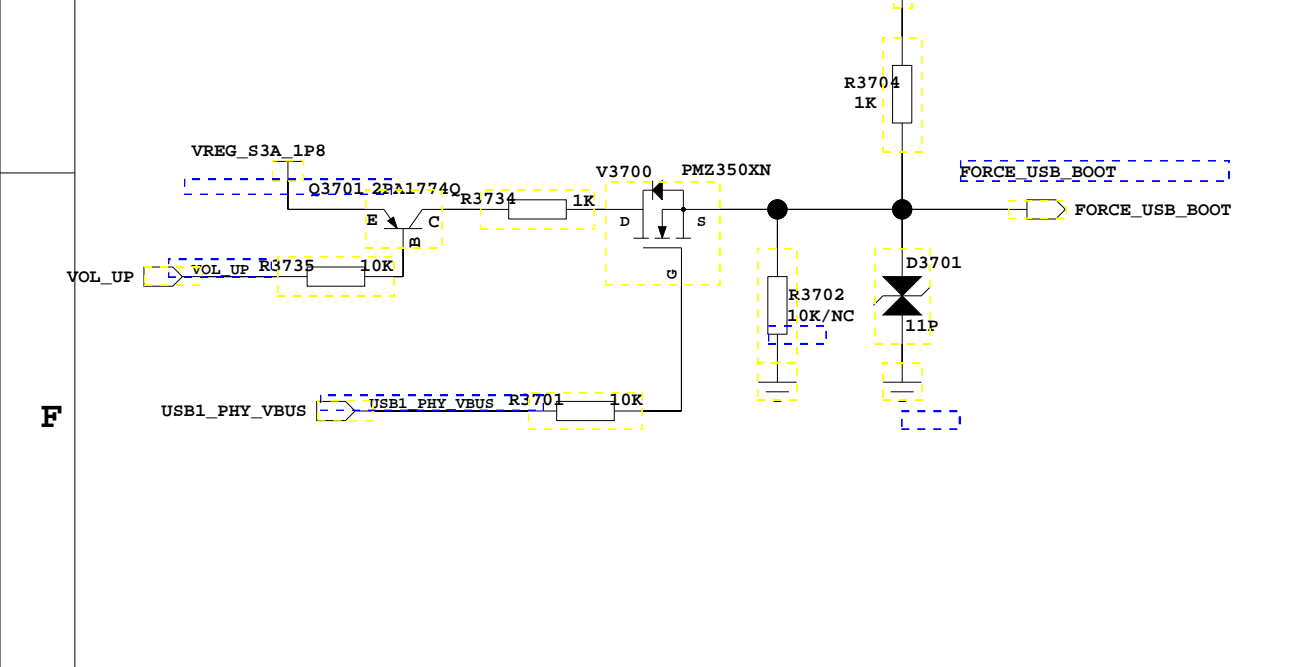
# TF CARD



# Pressure Sensor



# UPDATE



# FPC BTB:ALS+PS/HALL SENSOR

