

P2132-A01

NVG GK208 DDR3 64BIT

DVI-I/DVI-D + HDMI + VGA

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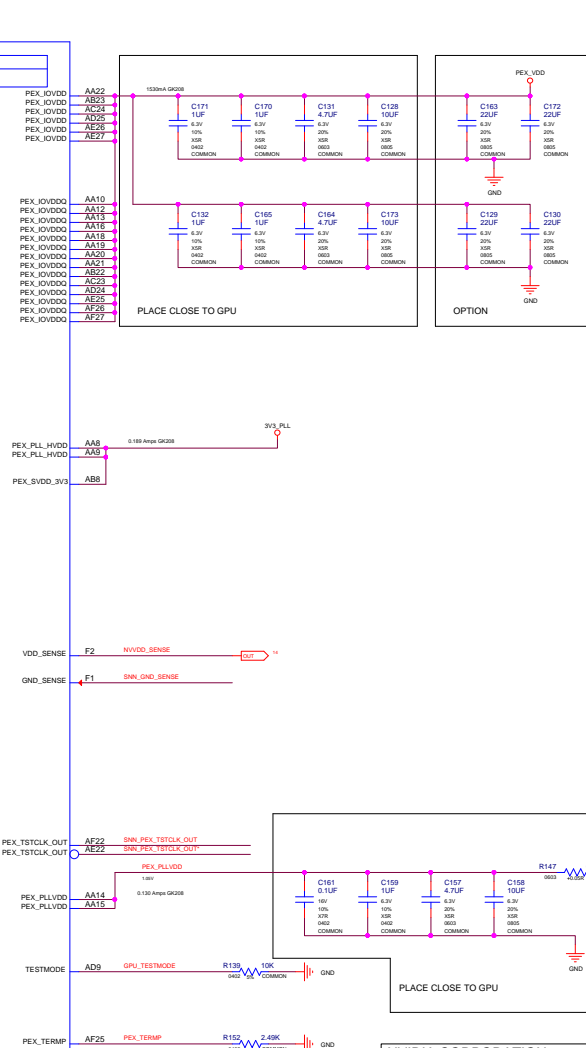
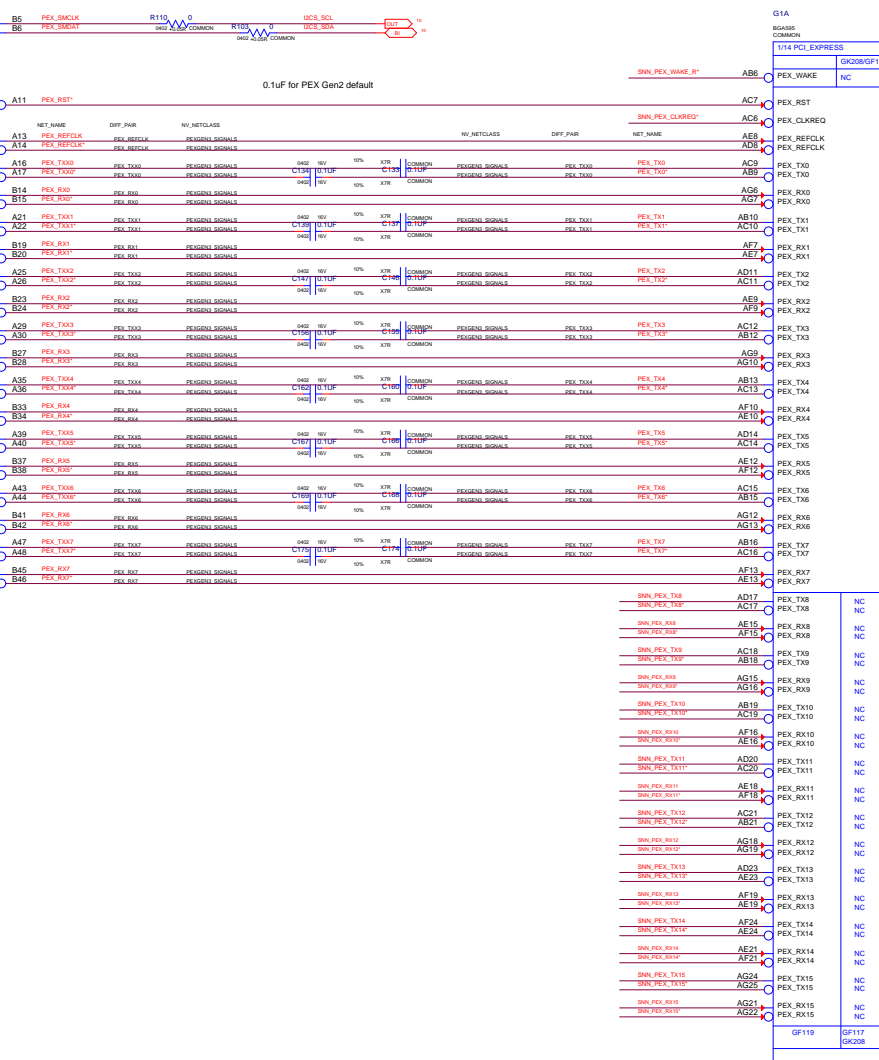
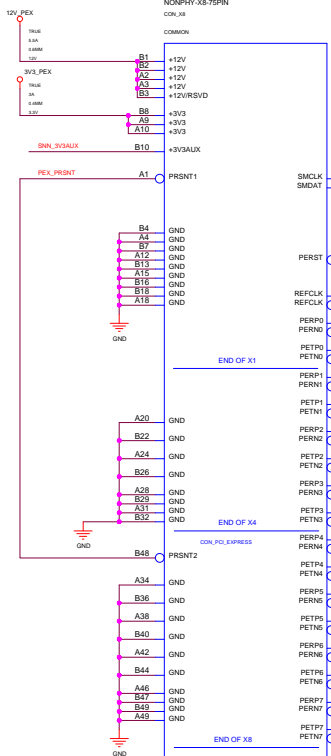
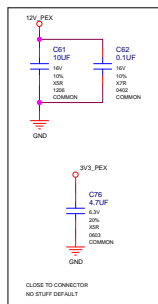
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PCI EXPRESS



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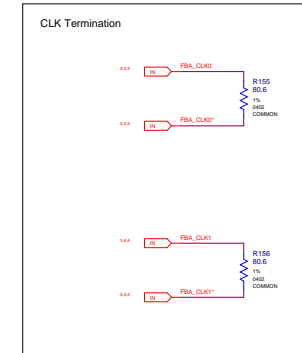
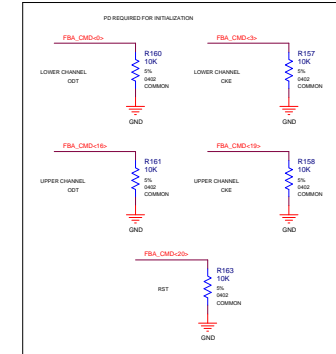
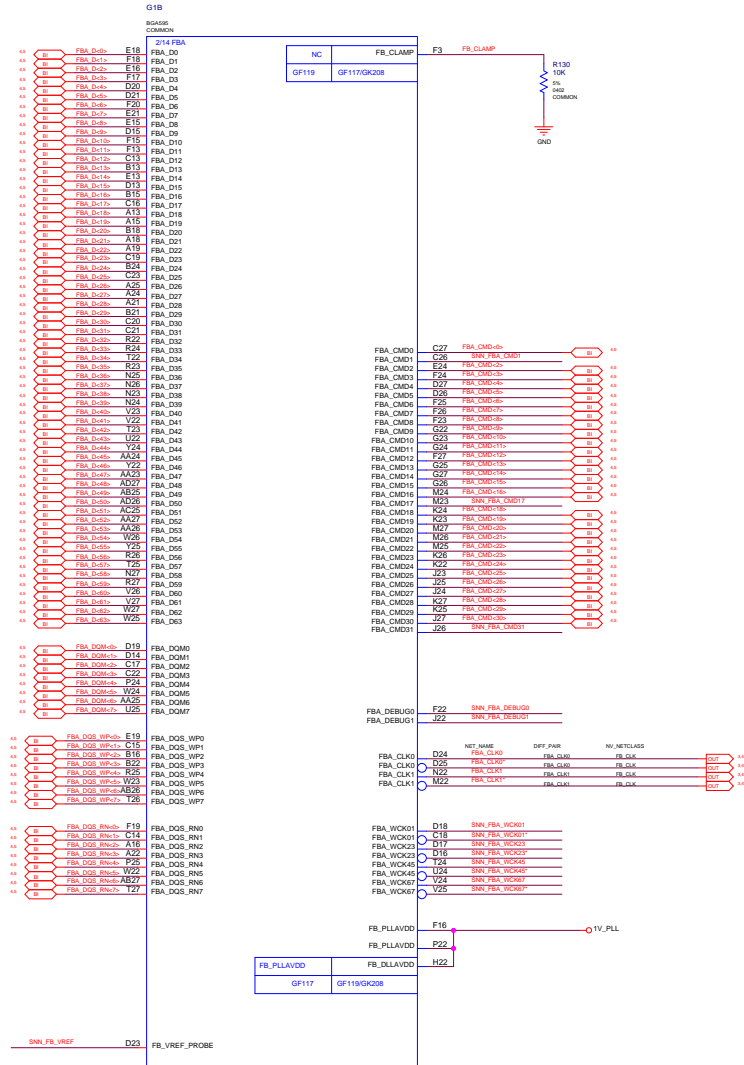
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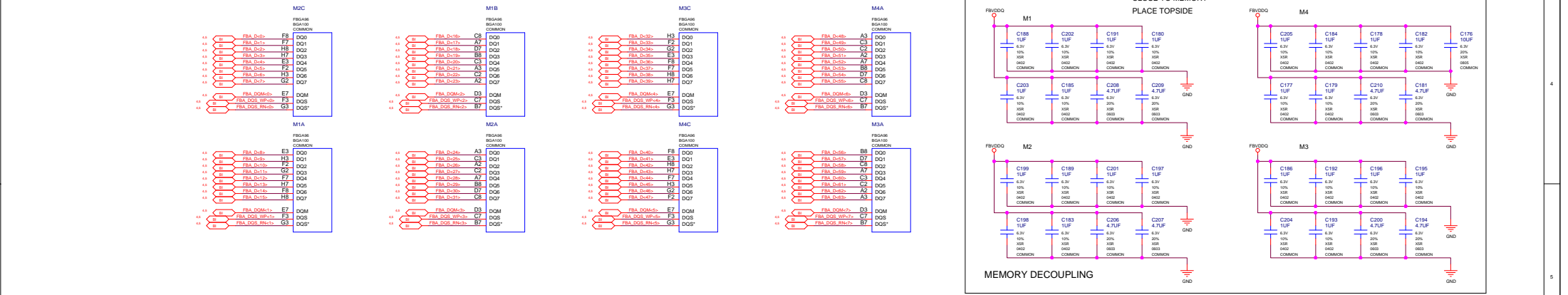
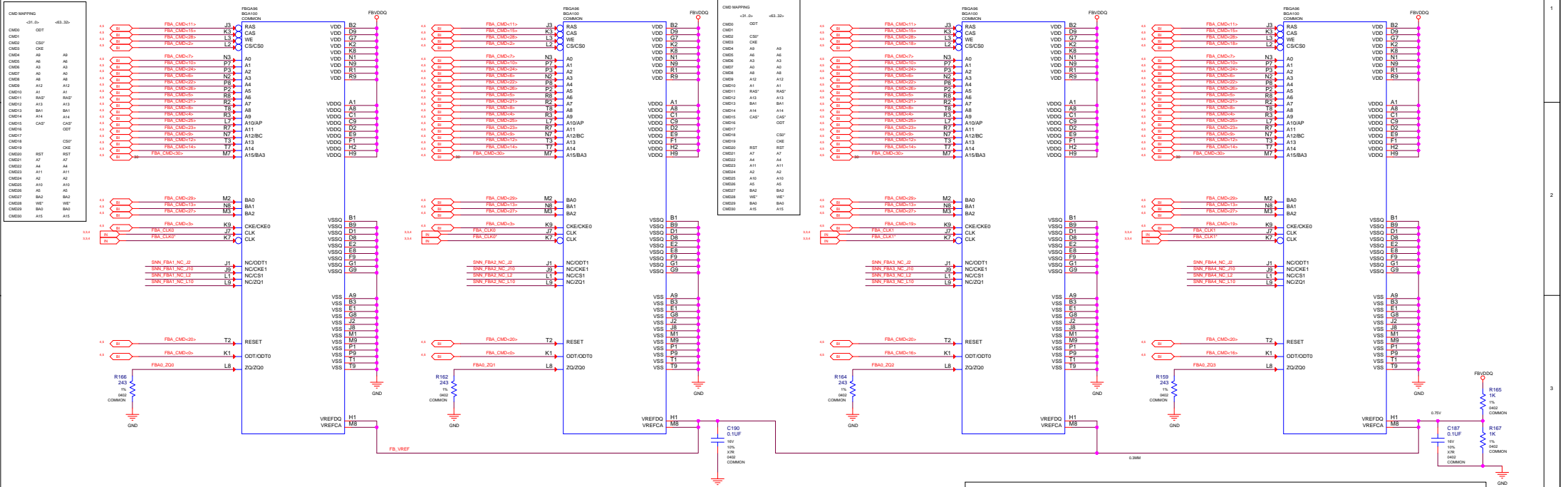
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GPU FRAME BUFFER: PARTITION A



DDR3 Memories



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DDR3 Memories

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P2132-001

BOB REV

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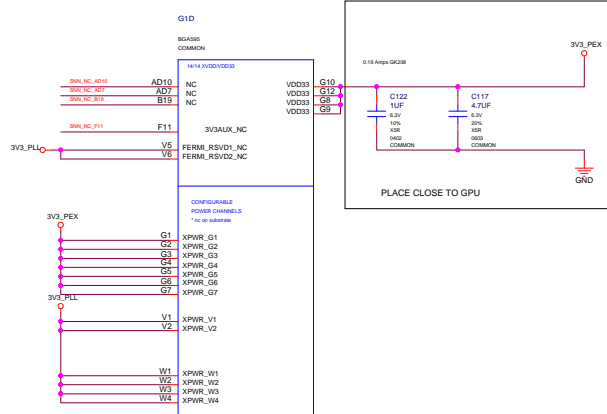
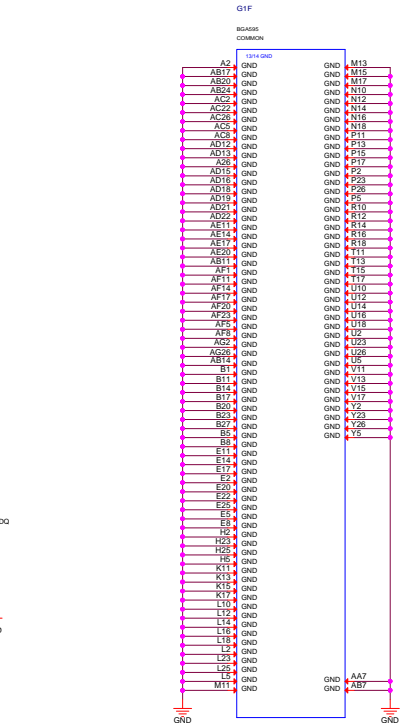
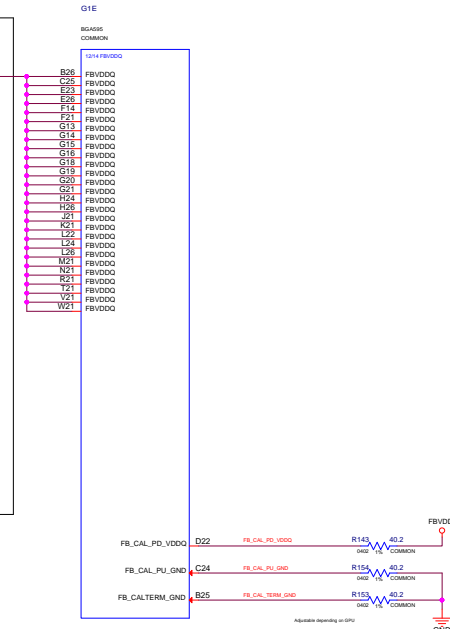
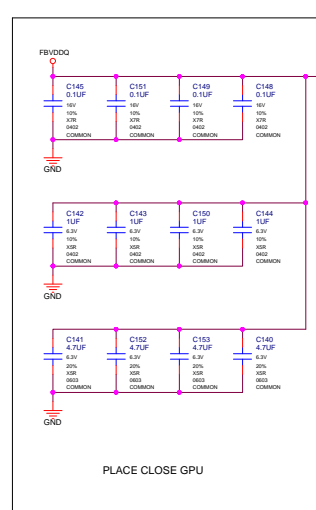
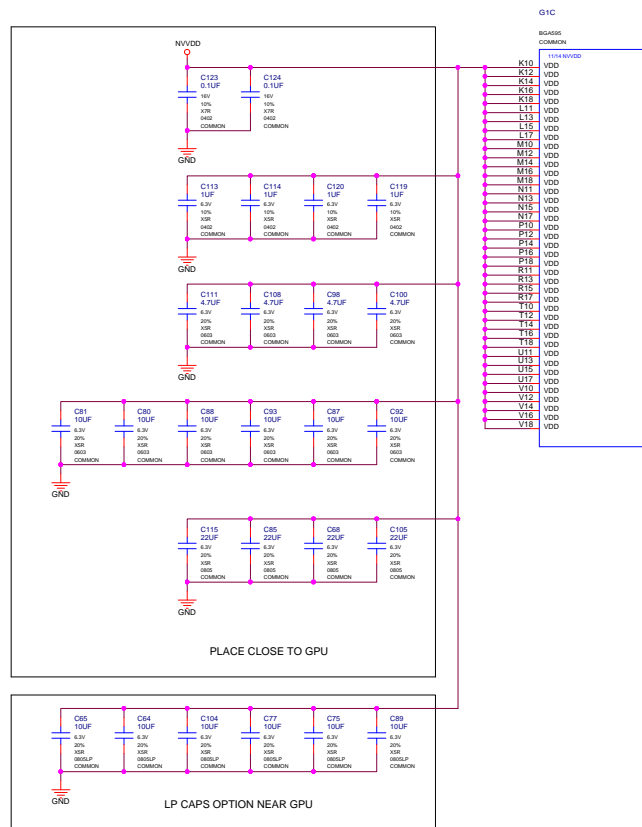
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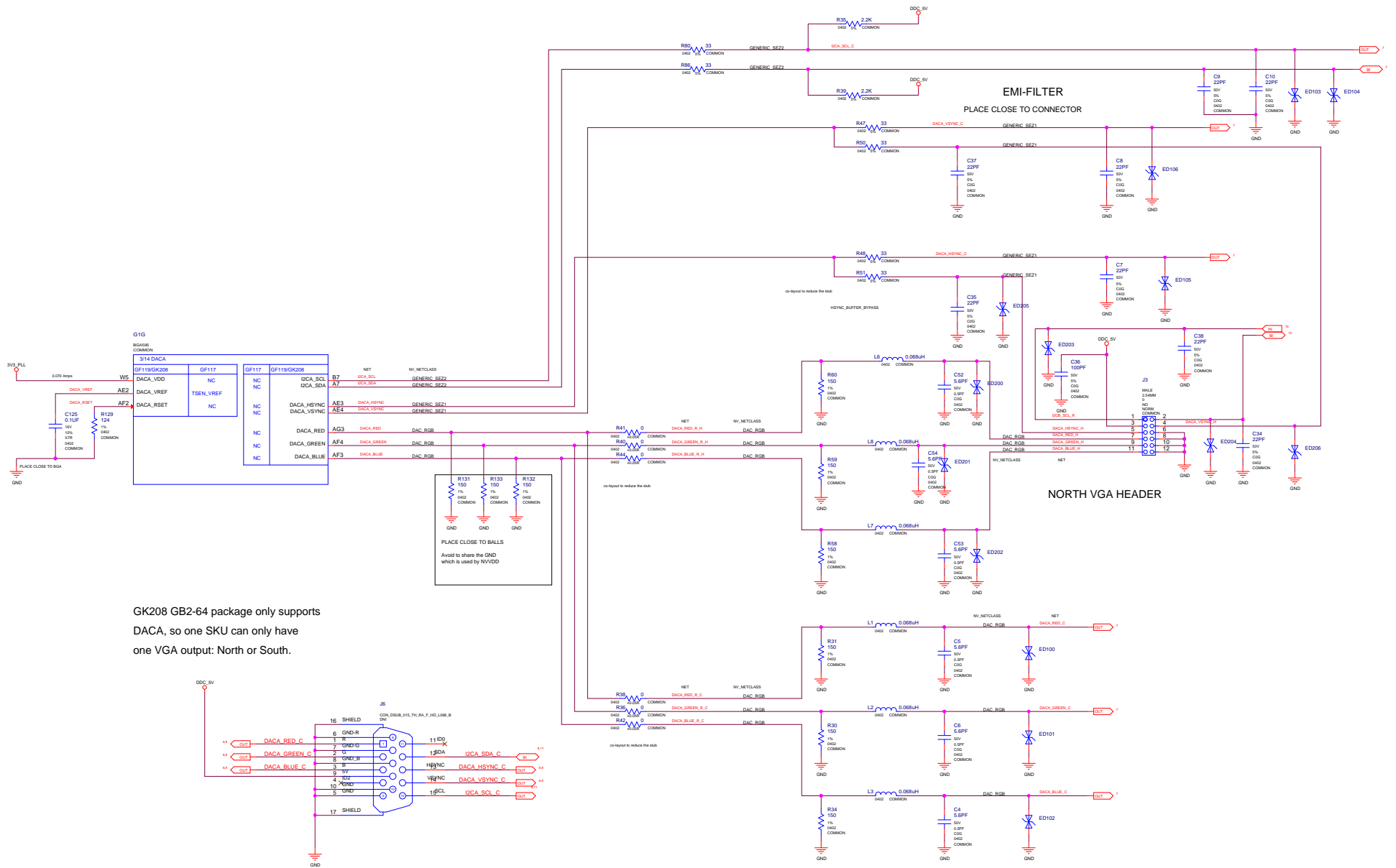
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DECOUPLING FOR CORE POWER_TOP



DACA VGA

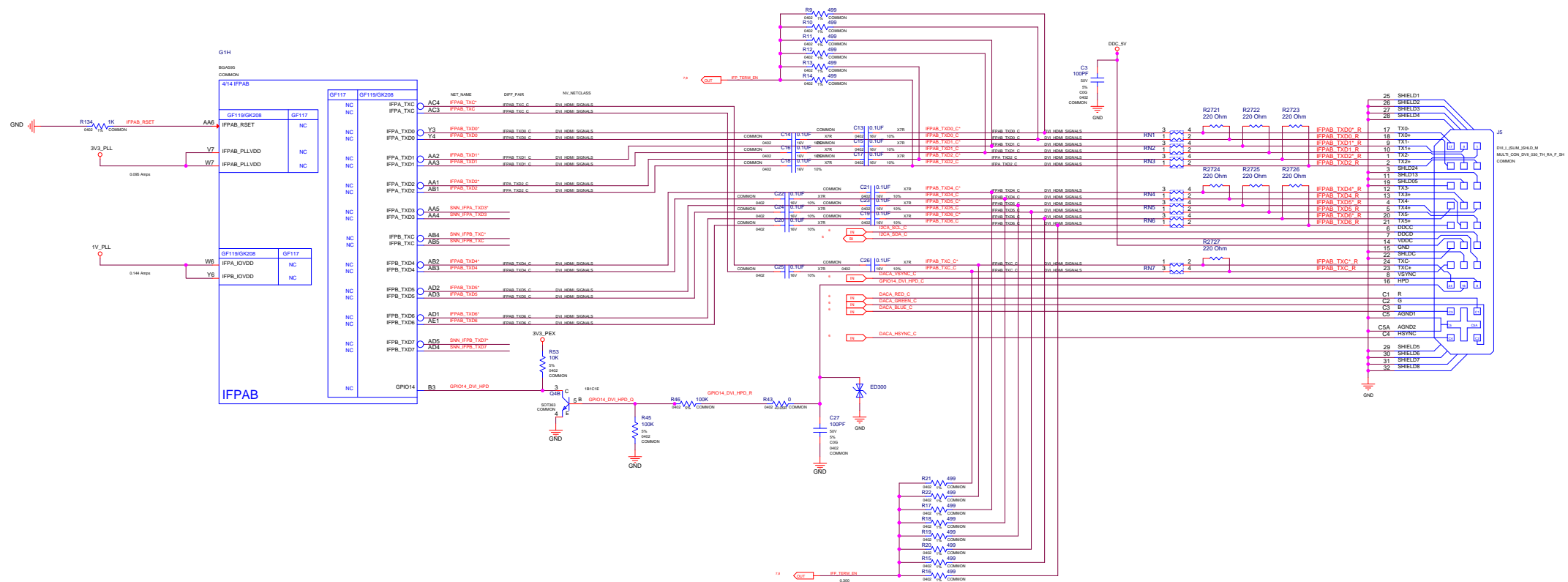



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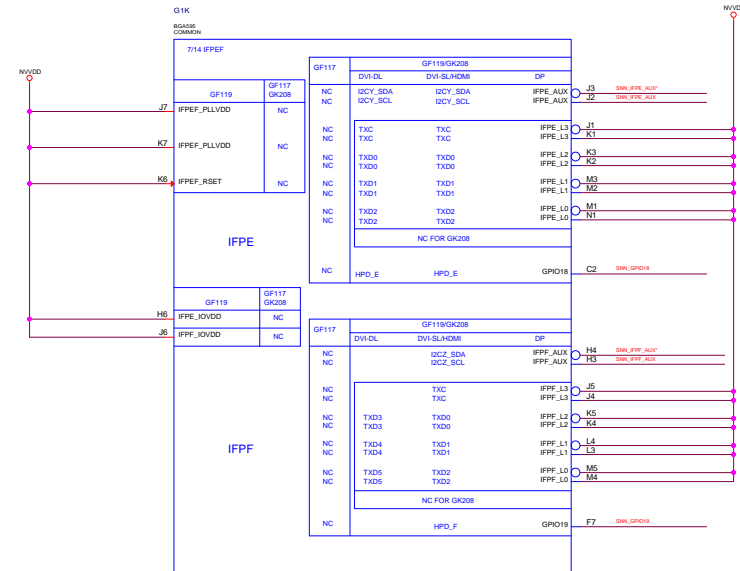
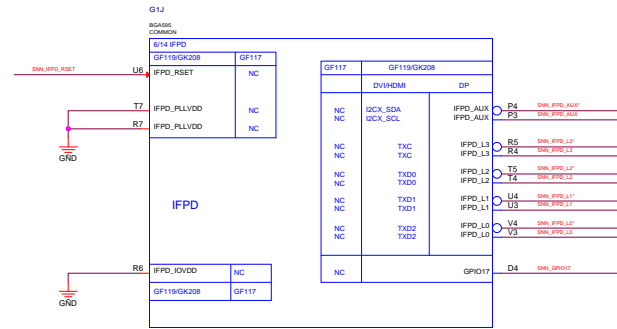
IFPAB TMDS



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UNUSED IFPD, IFPE & IFPF



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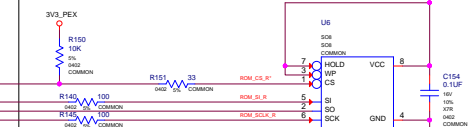
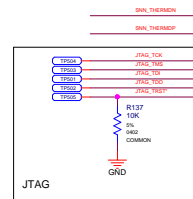
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| A | B | C | D | E | F | G | H |
|---|---|---|---|---|---|---|---|
|---|---|---|---|---|---|---|---|



| SMART FAN STRAP | | |
|-----------------|---------------|---------------|
| XTAL_SSIN | XTAL_OUTBUFF | Inverted PWMs |
| 0 0 | 0 (100% HIGH) | |
| 0 1 | 33 (67% HIGH) | |
| 1 0 | 50 (50% HIGH) | |
| 1 1 | 67 (33% HIGH) | |



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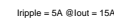
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NVDD Power Sequencing

Bootup Voltage set to 0.9000V

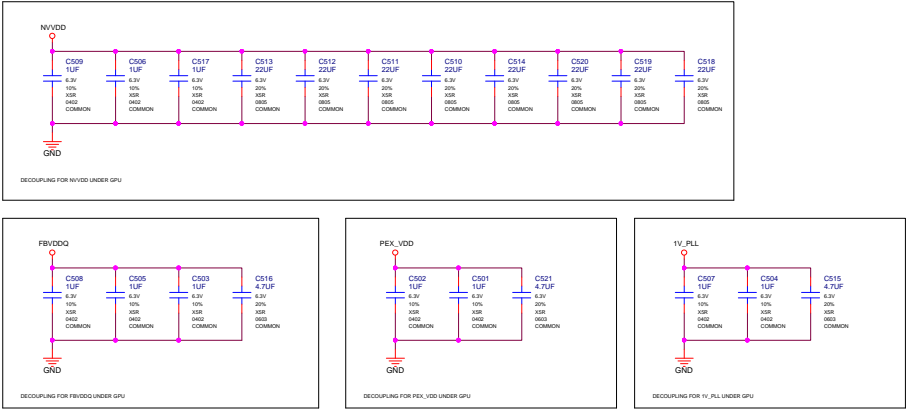
$$0.8V = 0.6V * (1 + 1.1k\Omega)$$

values for Rb1, Rb2
Rb3, Rb4 and Rb5
will be calculated based
on the needed voltages



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DECOUPLING FOR CORE POWER_BOTTOM



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