

P682: GT216, GB1-128, DDR2 1024MB 64Mx16, DL-DVI,VGA, HDMI

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Page 21: POWER SUPPLY: NVVDD DUAL PHASE SWITCHER

0A

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Page 2 Remove JTAG resistor; Disconnecter SMCLK to I2CS_SCL, SMDAT to I2CS_SDA

Page 3 Add FBA_CMD18 for A13

Page 4 Add FBA_CMD18 for A13

Page 6 Add FBC_CMD18 for A13

Page 7 Add FBC_CMD18 for A13

Page 11 Add R1101~R1107 for EMI

Page 12 Add R1201~R1204 for EMI, Change J2 footprint

Page 13 Remove J1 slim VGA

Page 16 Change Crystal Footprint to 2-PIN SMD; Remove Info ROM function

Page 17 Remove JTAG (J3) Connector

Page 19 Cahnge PEXVDD solution to UP7706

Page 20 Change FBVDDQ PWM solution to UP6101

Page 21 Change NVVDD PWM solution to UP6161

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Page 17 Modify PIN4 for co-lay connector (4PIN and 2PIN)

Page 18 Remove HDCP

Page 19 change IFPPLLVD to 3V3

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Page 19 add PEX_PLLVDD

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Page 16 Add R1601,R1602

Page 19 Remove 1V8

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Page 5 Add FBA_CMD18's Terminations

Page 6 Add FBC_CMD18's Terminations

Page 21 Remove C926,C931

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Only change PCB ver:

REV	VARIANT	NPVN	ASSEMBLY
B	BASE	600-10682-base-100	BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU000	600-10682-0000-100	GT216-200, 600/1500MHz
2	SKU001	600-10682-0001-100	GT216-200, 600/1500MHz 512MB 32Mx16 BGA84 500MHz DCR2 DVI-I(DP)HDMI
3	SKU002	600-10682-0002-100	GT216-300, 600/1500MHz 512MB 32Mx16 BGA84 500MHz DCR2 DVI-I(DP)HDMI
4	SKU003	600-10682-0003-100	GT216-100, 600/1500MHz 256MB 32Mx16 BGA84 500MHz DCR2 DVI-I(DP)HDMI
5	SKU011	600-10682-0011-100	GT216-300, 600/1500MHz 512MB 32Mx16 BGA84 500MHz DCR2 DVI-I(DV)IHHDMI
6	SKU012	600-10682-0012-100	GT216-200, 600/1500MHz 512MB 32Mx16 BGA84 500MHz DCR2 DVI-I(DV)IHHDMI
7	SKU013	600-10682-0013-100	GT216-100, 600/1500MHz 512MB 32Mx16 BGA84 500MHz DCR2 DVI-I(DV)IHHDMI
8	SKU004	600-10682-0004-100	GT216-200, 600/1500MHz 1024MB 64Mx16 BGA84 500MHz DCR2 DVI-I(DP)HDMI
9	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
10	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
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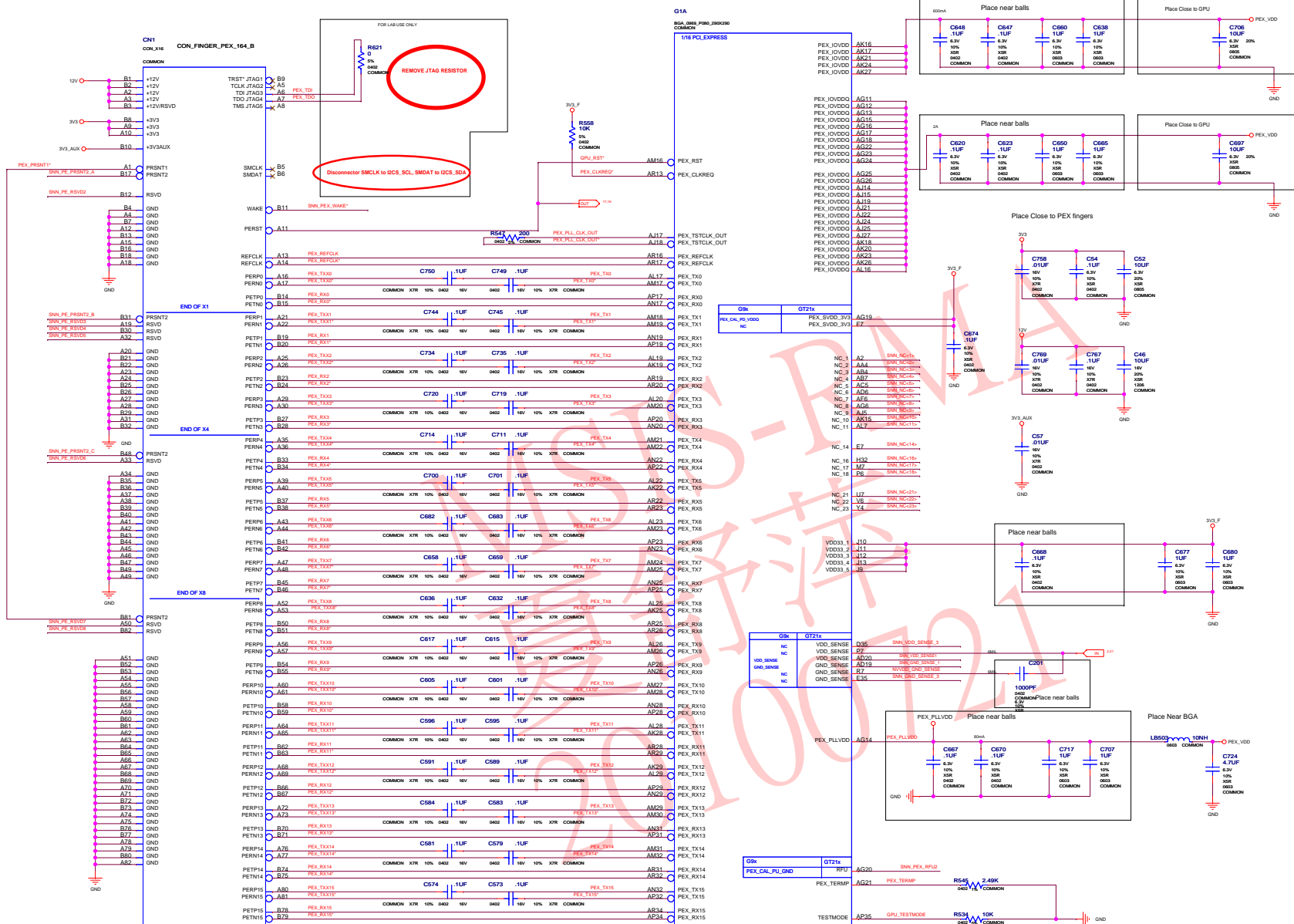
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NV_PN	600-10682-base-100 A
ID	
NAME	
PAGE	
DATE	15 MAY 2009

16X PEX INTERFACE



	NET	DFFPAIR	NV_IMPEDANCE	NV_CRITICAL
SI	PEX_REFCLK PEX_REFCLK*	PEX_REFCLK	SOCKET	1
		PEX_REFCLK	SOCKET	1

10	PEX 100	PEX 120	PEX 140	PEX 160	PEX 180	PEX 200	PEX 220	PEX 240	PEX 260	PEX 280	PEX 300	PEX 320	PEX 340	PEX 360	PEX 380	PEX 400	PEX 420	PEX 440	PEX 460	PEX 480	PEX 500	PEX 520	PEX 540	PEX 560	PEX 580	PEX 600	PEX 620	PEX 640	PEX 660	PEX 680	PEX 700	PEX 720	PEX 740	PEX 760	PEX 780	PEX 800	PEX 820	PEX 840	PEX 860	PEX 880	PEX 900	PEX 920	PEX 940	PEX 960	PEX 980	PEX 1000	PEX 1020	PEX 1040	PEX 1060	PEX 1080	PEX 1100	PEX 1120	PEX 1140	PEX 1160	PEX 1180	PEX 1200	PEX 1220	PEX 1240	PEX 1260	PEX 1280	PEX 1300	PEX 1320	PEX 1340	PEX 1360	PEX 1380	PEX 1400	PEX 1420	PEX 1440	PEX 1460	PEX 1480	PEX 1500	PEX 1520	PEX 1540	PEX 1560	PEX 1580	PEX 1600	PEX 1620	PEX 1640	PEX 1660	PEX 1680	PEX 1700	PEX 1720	PEX 1740	PEX 1760	PEX 1780	PEX 1800	PEX 1820	PEX 1840	PEX 1860	PEX 1880	PEX 1900	PEX 1920	PEX 1940	PEX 1960	PEX 1980	PEX 2000	PEX 2020	PEX 2040	PEX 2060	PEX 2080	PEX 2100	PEX 2120	PEX 2140	PEX 2160	PEX 2180	PEX 2200	PEX 2220	PEX 2240	PEX 2260	PEX 2280	PEX 2300	PEX 2320	PEX 2340	PEX 2360	PEX 2380	PEX 2400	PEX 2420	PEX 2440	PEX 2460	PEX 2480	PEX 2500	PEX 2520	PEX 2540	PEX 2560	PEX 2580	PEX 2600	PEX 2620	PEX 2640	PEX 2660	PEX 2680	PEX 2700	PEX 2720	PEX 2740	PEX 2760	PEX 2780	PEX 2800	PEX 2820	PEX 2840	PEX 2860	PEX 2880	PEX 2900	PEX 2920	PEX 2940	PEX 2960	PEX 2980	PEX 3000	PEX 3020	PEX 3040	PEX 3060	PEX 3080	PEX 3100	PEX 3120	PEX 3140	PEX 3160	PEX 3180	PEX 3200	PEX 3220	PEX 3240	PEX 3260	PEX 3280	PEX 3300	PEX 3320	PEX 3340	PEX 3360	PEX 3380	PEX 3400	PEX 3420	PEX 3440	PEX 3460	PEX 3480	PEX 3500	PEX 3520	PEX 3540	PEX 3560	PEX 3580	PEX 3600	PEX 3620	PEX 3640	PEX 3660	PEX 3680	PEX 3700	PEX 3720	PEX 3740	PEX 3760	PEX 3780	PEX 3800	PEX 3820	PEX 3840	PEX 3860	PEX 3880	PEX 3900	PEX 3920	PEX 3940	PEX 3960	PEX 3980	PEX 4000	PEX 4020	PEX 4040	PEX 4060	PEX 4080	PEX 4100	PEX 4120	PEX 4140	PEX 4160	PEX 4180	PEX 4200	PEX 4220	PEX 4240	PEX 4260	PEX 4280	PEX 4300	PEX 4320	PEX 4340	PEX 4360	PEX 4380	PEX 4400	PEX 4420	PEX 4440	PEX 4460	PEX 4480	PEX 4500	PEX 4520	PEX 4540	PEX 4560	PEX 4580	PEX 4600	PEX 4620	PEX 4640	PEX 4660	PEX 4680	PEX 4700	PEX 4720	PEX 4740	PEX 4760	PEX 4780	PEX 4800	PEX 4820	PEX 4840	PEX 4860	PEX 4880	PEX 4900	PEX 4920	PEX 4940	PEX 4960	PEX 4980	PEX 5000	PEX 5020	PEX 5040	PEX 5060	PEX 5080	PEX 5100	PEX 5120	PEX 5140	PEX 5160	PEX 5180	PEX 5200	PEX 5220	PEX 5240	PEX 5260	PEX 5280	PEX 5300	PEX 5320	PEX 5340	PEX 5360	PEX 5380	PEX 5400	PEX 5420	PEX 5440	PEX 5460	PEX 5480	PEX 5500	PEX 5520	PEX 5540	PEX 5560	PEX 5580	PEX 5600	PEX 5620	PEX 5640	PEX 5660	PEX 5680	PEX 5700	PEX 5720	PEX 5740	PEX 5760	PEX 5780	PEX 5800	PEX 5820	PEX 5840	PEX 5860	PEX 5880	PEX 5900	PEX 5920	PEX 5940	PEX 5960	PEX 5980	PEX 6000	PEX 6020	PEX 6040	PEX 6060	PEX 6080	PEX 6100	PEX 6120	PEX 6140	PEX 6160	PEX 6180	PEX 6200	PEX 6220	PEX 6240	PEX 6260	PEX 6280	PEX 6300	PEX 6320	PEX 6340	PEX 6360	PEX 6380	PEX 6400	PEX 6420
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[illegible][illegible]

	SEN_LINE_WIDTH	VOLTAGE
27	20	1.2V
	20	0V
27	20	1.2V
392	3V3	120mV 3.3V 3A
12V	12V	120mV 12V 5.5A



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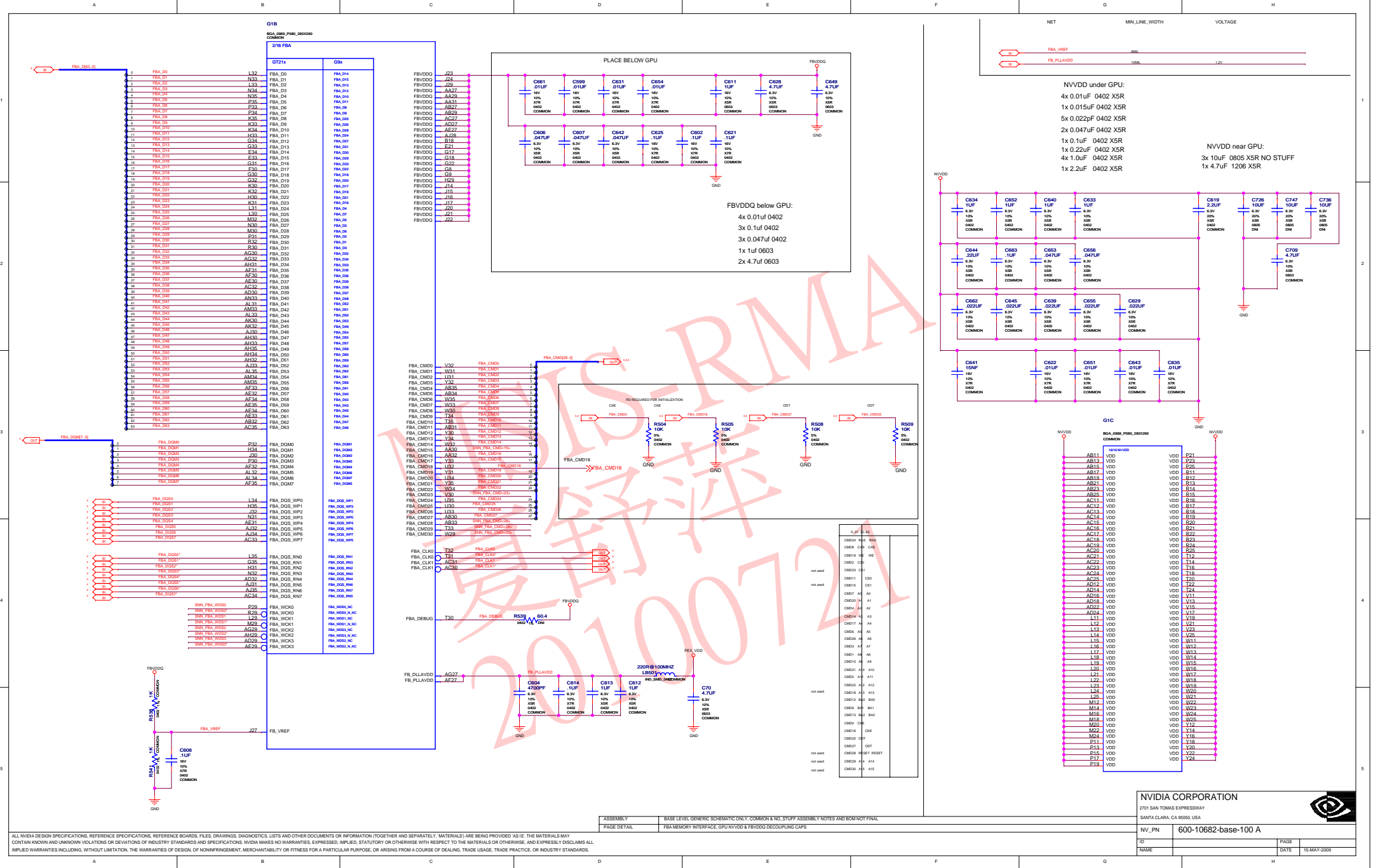
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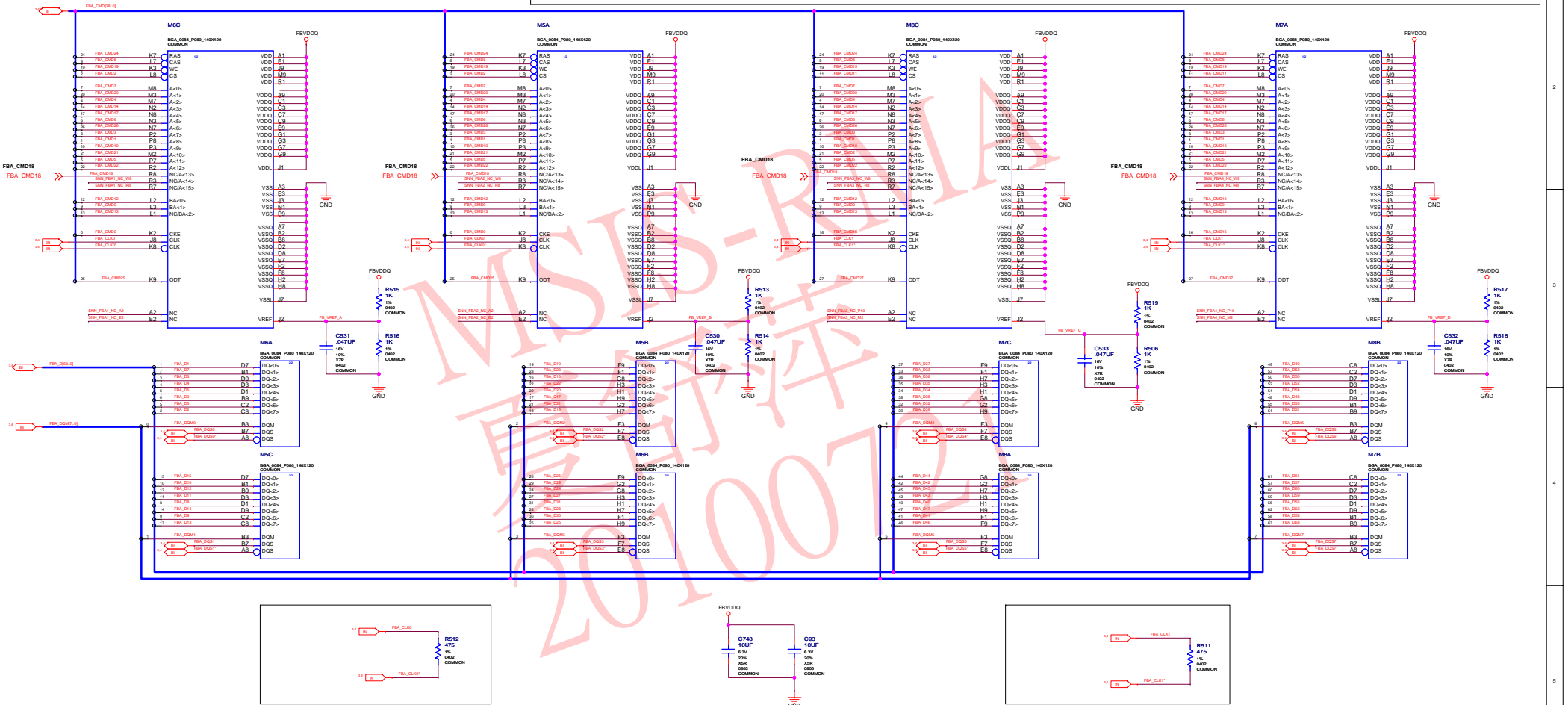
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Partition A Memories

GT21x CMD MAP



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ASSEMBLY: BASE LEVEL GENERIC SCHEMATIC ONLY. COMMON & NC: BY OFF-ASSEMBLY NOTES AND BOM NOT FINAL.
PAGE: DETAIL: FBA 10032003 (0003) MEMORIES, FBA COMMAND BUS PUTS, FBA CLK TERMS

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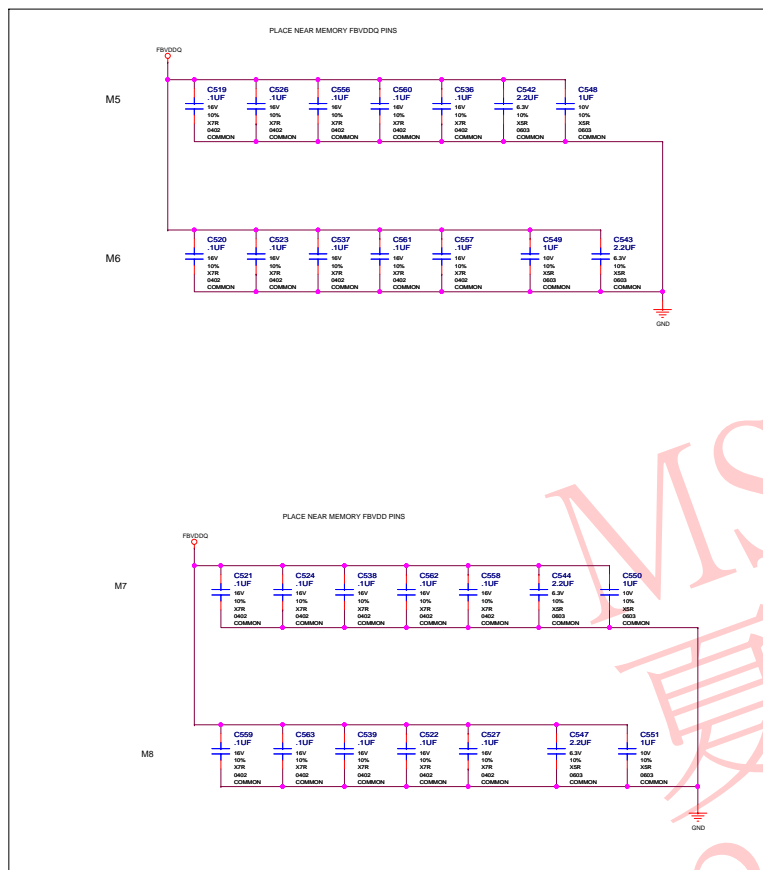


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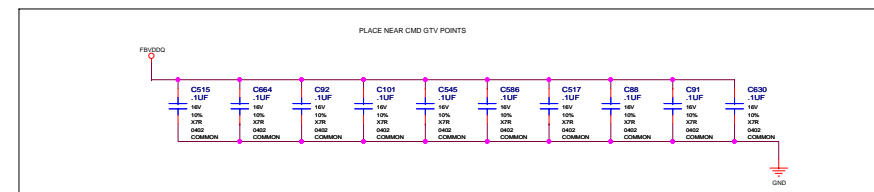
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FRAME BUFFER: PARTITION A DECOUPLING

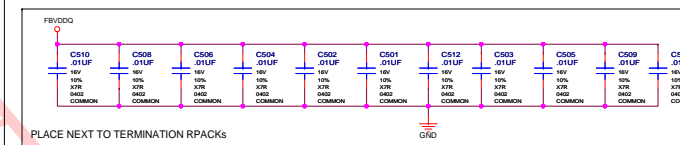
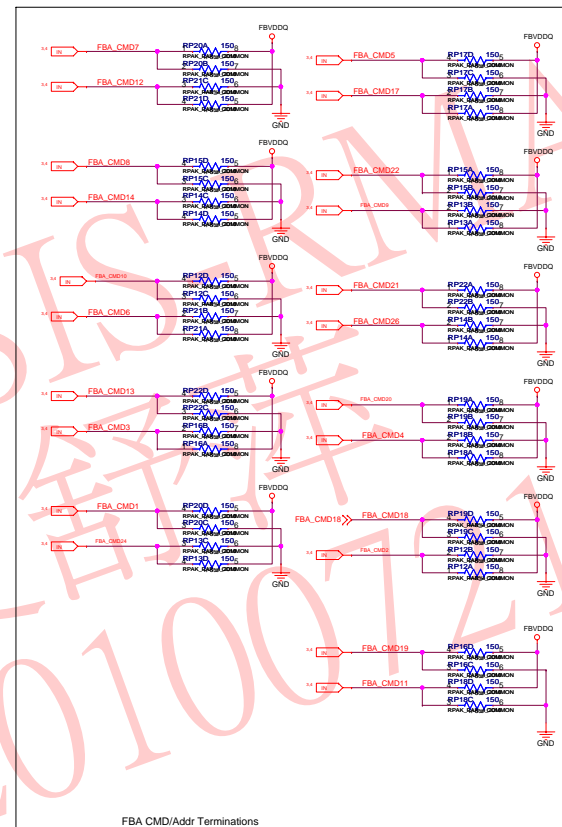
Decoupling for FBA



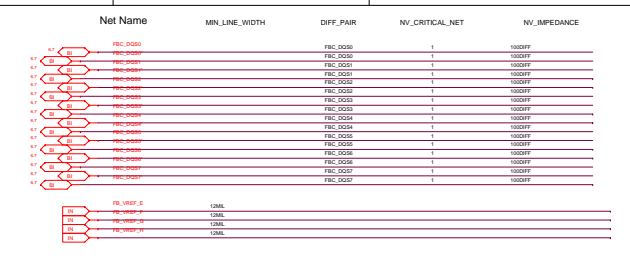
Return path coupling GND/FBVDDQ




FB A Addr/CMD termination



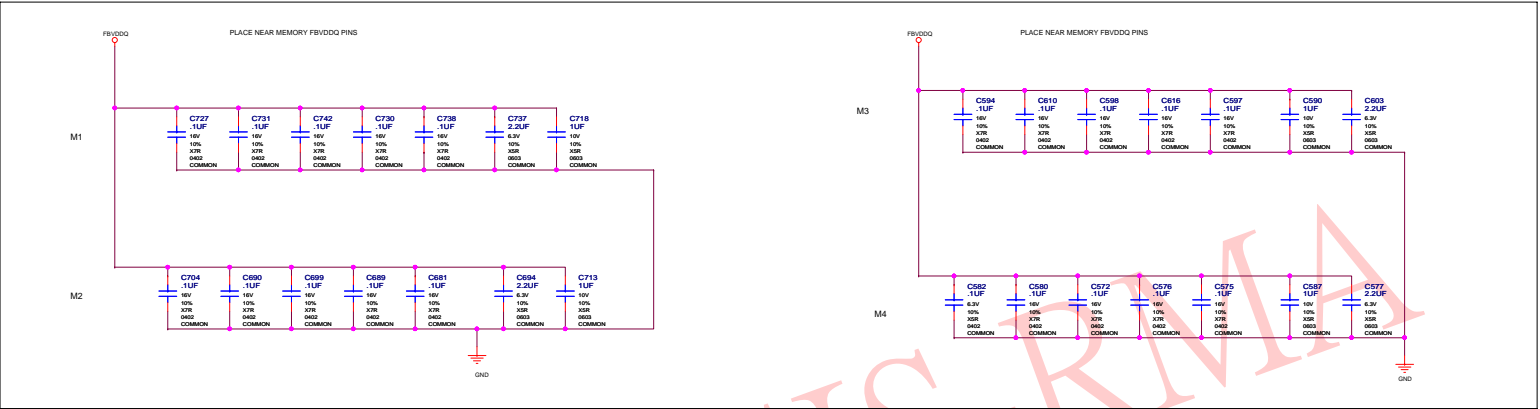
GT21x CMD MAP



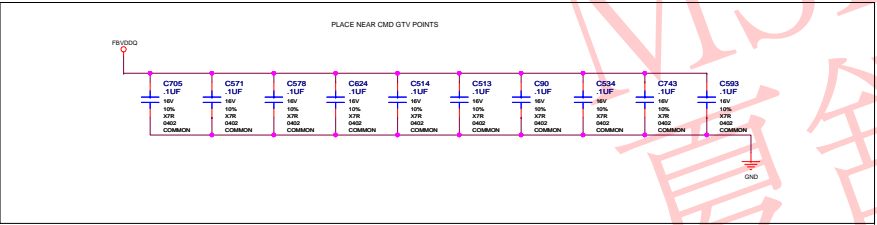
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NAME		DATE	15-MAY-2009

FRAMEBUFFER: PARTITION C DECOUPLING

Decoupling for FBC

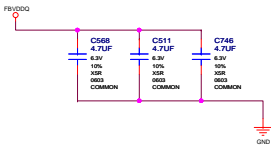


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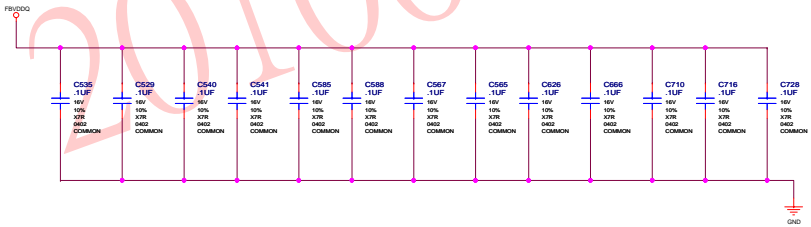


Capacitors bounding the FB area

on each side and in middle



Return path coupling GND/FBVDDQ for FBA/FBC



01E

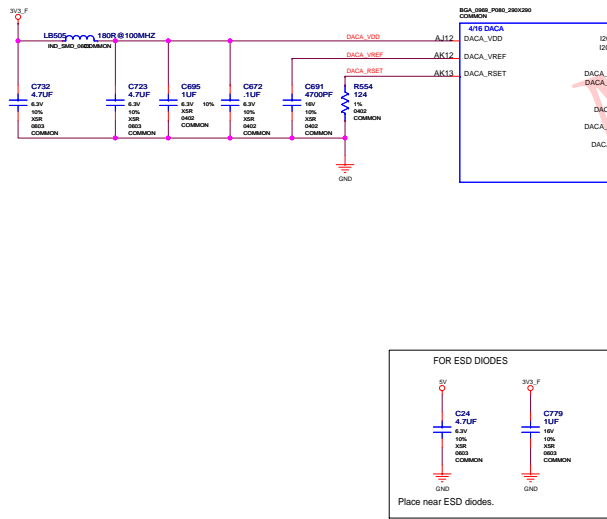
BGA_0068_P006_200200

COMMON

10% GND

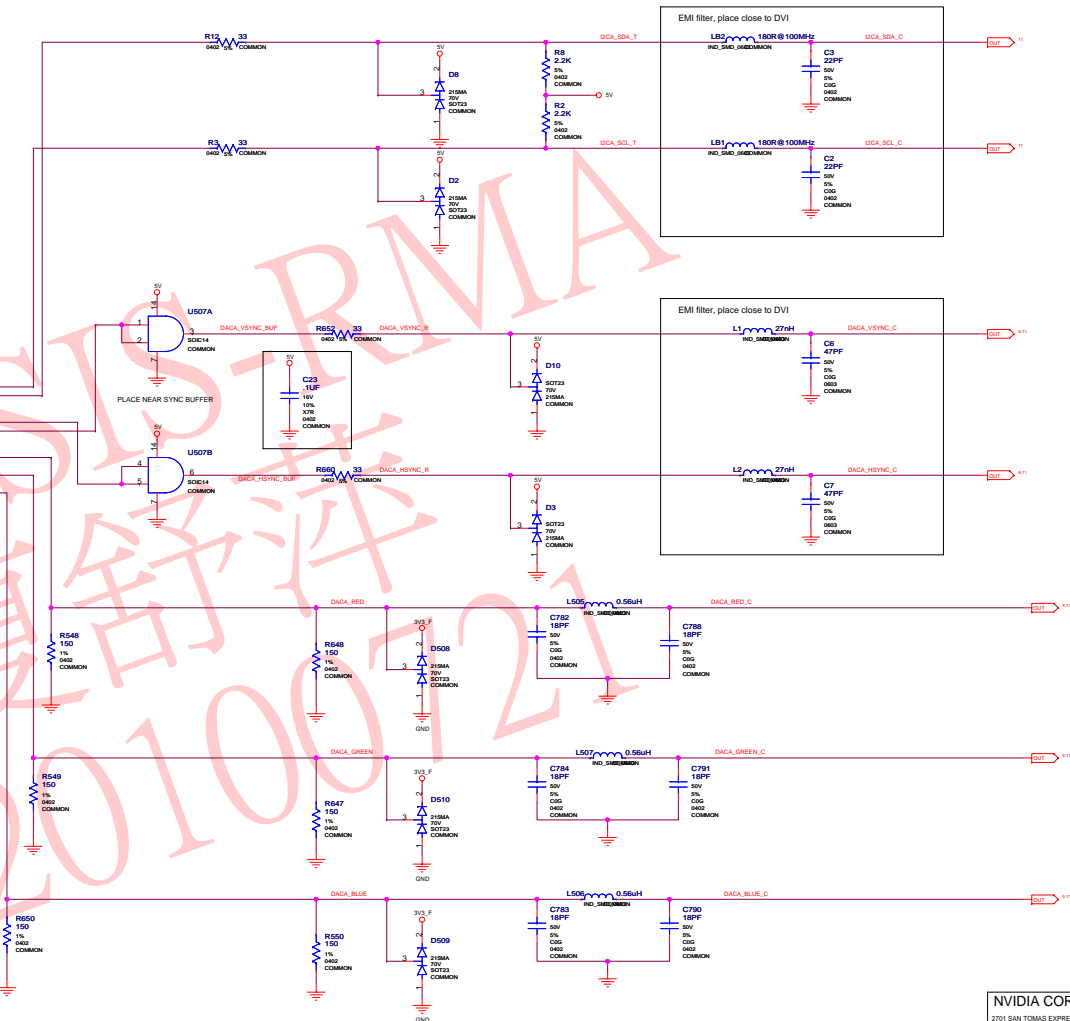
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AA13	GND	E17	GND
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AA16	GND	E20	GND
AA17	GND	E21	GND
AA18	GND	E22	GND
AA19	GND	E23	GND
AA20	GND	E24	GND
AA21	GND	E25	GND
AA22	GND	E26	GND
AA23	GND	E27	GND
AA24	GND	E28	GND
AA25	GND	E29	GND
AA26	GND	E30	GND
AA27	GND	E31	GND
AA28	GND	E32	GND
AA29	GND	E33	GND
AA30	GND	E34	GND
AA31	GND	E35	GND
AA32	GND	E36	GND
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AA34	GND	E38	GND
AA35	GND	E39	GND
AA36	GND	E40	GND
AA37	GND	E41	GND
AA38	GND	E42	GND
AA39	GND	E43	GND
AA40	GND	E44	GND
AA41	GND	E45	GND
AA42	GND	E46	GND
AA43	GND	E47	GND
AA44	GND	E48	GND
AA45	GND	E49	GND
AA46	GND	E50	GND
AA47	GND	E51	GND
AA48	GND	E52	GND
AA49	GND	E53	GND
AA50	GND	E54	GND
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AA79	GND	E83	GND
AA80	GND	E84	GND
AA81	GND	E85	GND
AA82	GND	E86	GND
AA83	GND	E87	GND
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AA85	GND	E89	GND
AA86	GND	E90	GND
AA87	GND	E91	GND
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AA167	GND	E171	GND
AA168	GND	E172	GND
AA169	GND	E173	GND
AA170	GND	E174	GND
AA171	GND	E175	GND
AA172	GND	E176	GND
AA173	GND	E177	GND
AA174	GND	E178	GND
AA175	GND	E179	GND
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AA177	GND	E181	GND
AA178	GND	E182	GND
AA179	GND	E183	GND
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AA367	GND	E371	GND
AA368	GND	E372	GND
AA369	GND	E373	GND
AA370	GND	E374	GND

NET NAME	INV_CRITICAL_NET	IMPEDANCE	MIN_LINE_WIDTH
B DATA_VDD			3.20 20M
B DATA_VREF			20M
B DATA_XBIST			20M
B DATA_RED	1	50OH	
B DATA_GREEN	1	50OH	
B DATA_BLUE	1	50OH	
B DATA_RED_C	1	50OH	
B DATA_GREEN_C	1	50OH	
B DATA_BLUE_C	1	50OH	
B DATA_HSTING	2	50OH	
B DATA_VSTING	2	50OH	
B DATA_HSTING_C	2	50OH	
B DATA_VSTING_C	2	50OH	
B DATA_HSTING_BUF	2	50OH	
B DATA_VSTING_BUF	2	50OH	
B DATA_HSTING_C	2	50OH	
B DATA_VSTING_C	2	50OH	



Primary Display (DACA), DVI-I

DACA RGB-FILTER



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACA (PRIMARY DVI-I)

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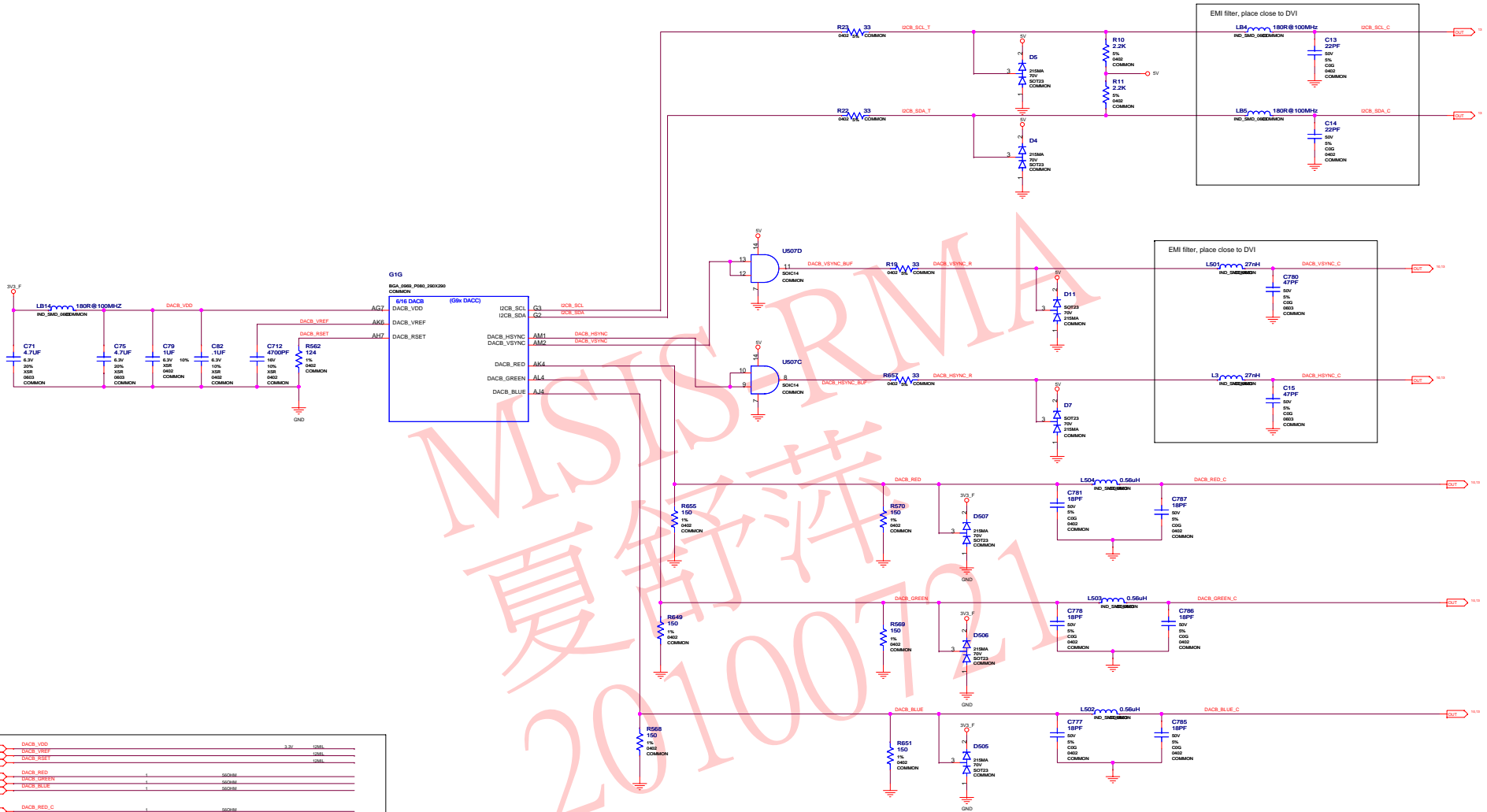
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NAME		DATE	15-MAY-2009

DACB, SECONDARY VGA

[illegible]

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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACB, SECONDARY DVI-I

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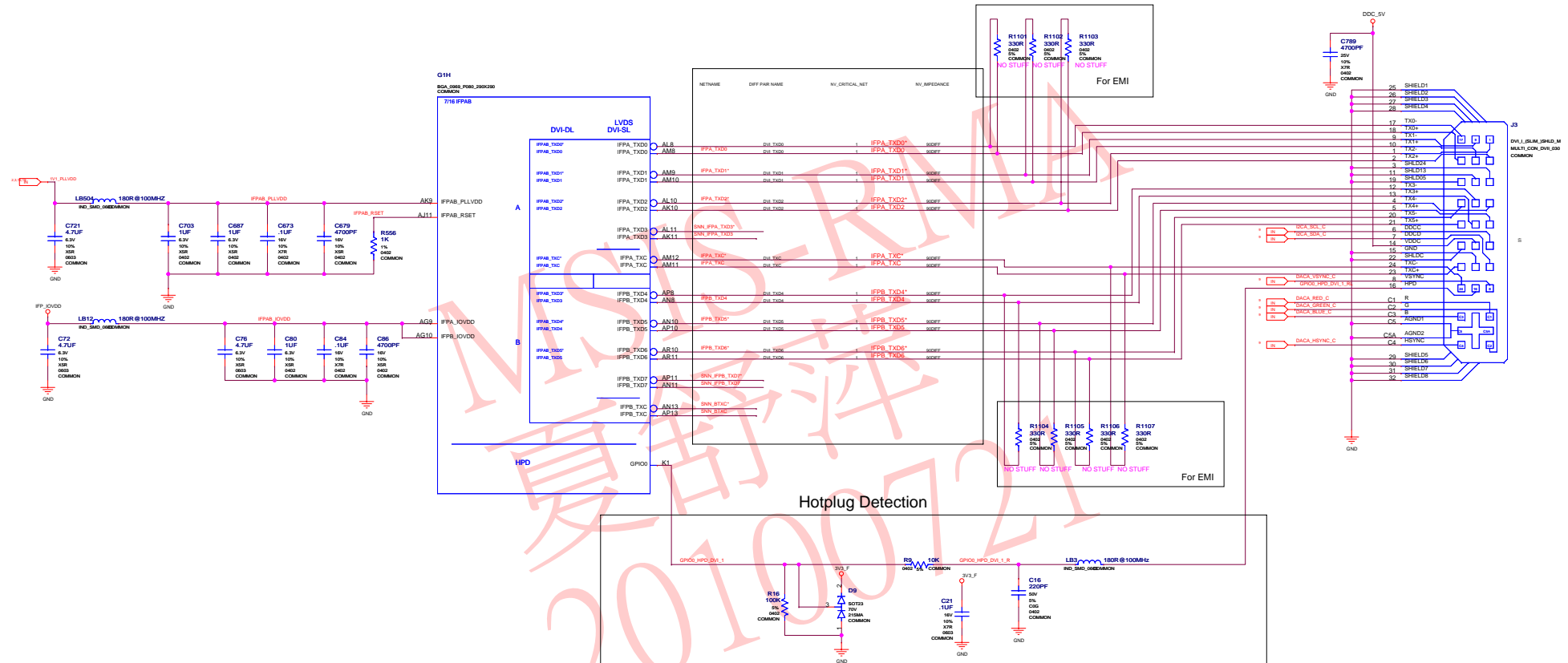
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ID	PAGE
NAME	DATE 15-MAY-2009

TMD5 LINK A/B: DVI-I DUALLINK, SOUTH



	NETNAME	MIN_LENGTH	VOLTAGE
IS	PPAS_PLLVDD	120MIL	1.00V
IS	PPAS_IOVDD	120MIL	3.3V
IS	PPAS_ISET	120MIL	

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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	TMD5 LINK A/B: DVI-I (SOUTH)

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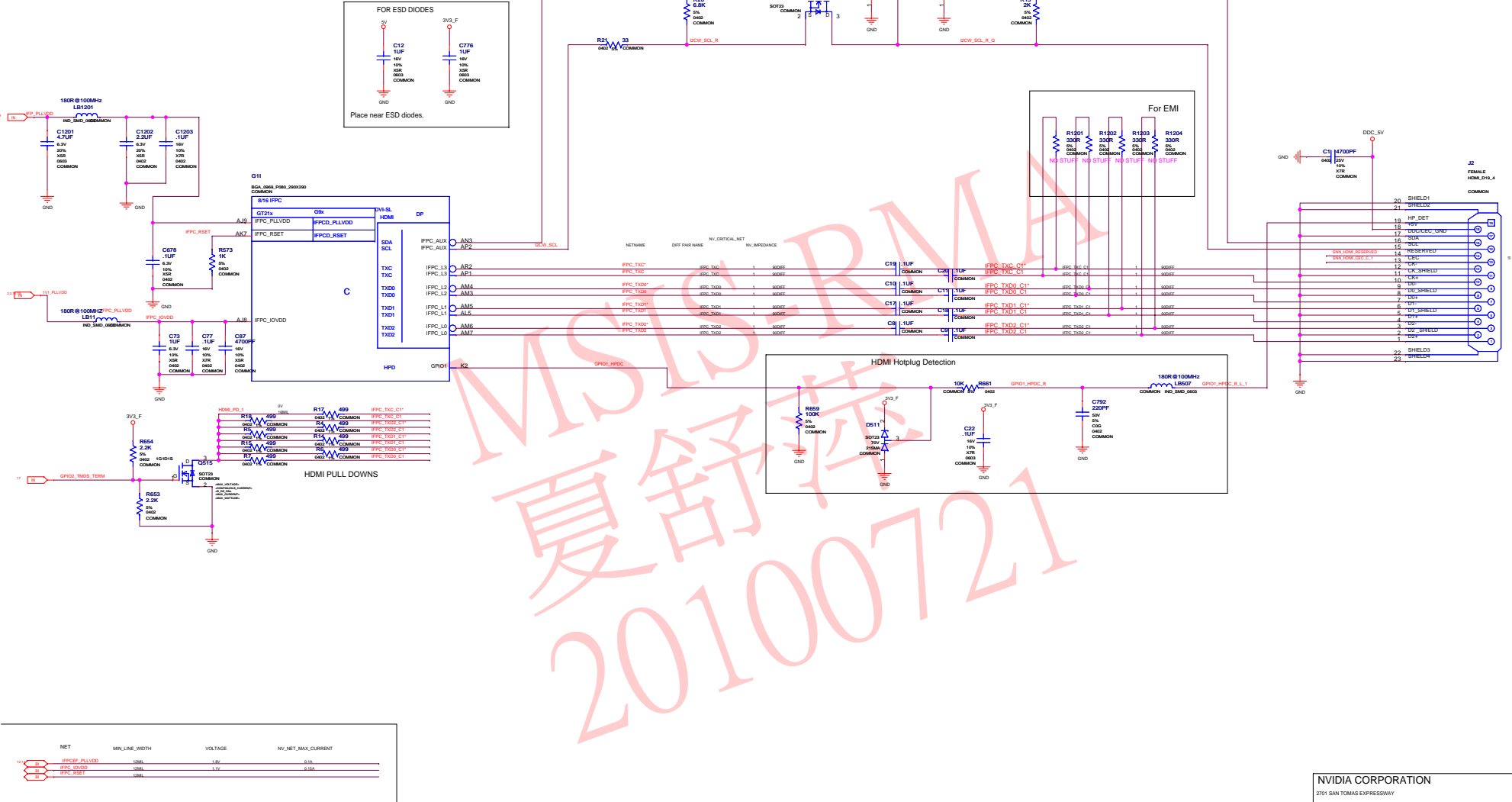
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NAME		DATE	15-MAY-2009

LINK C: HDMI
HDMI (NORTH)

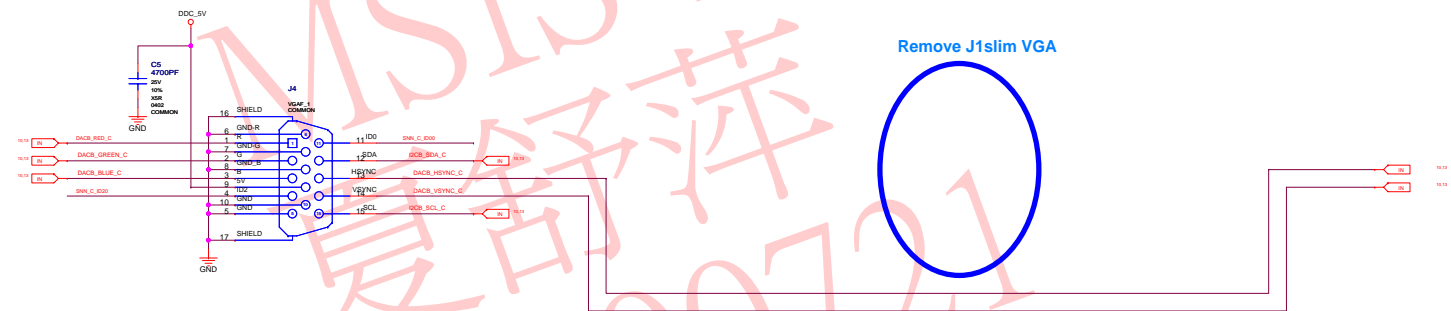
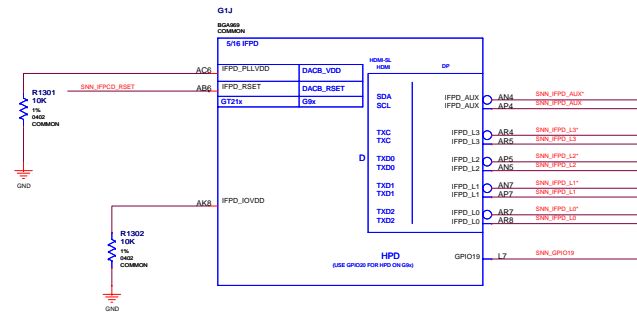


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PAGE DETAIL: LINK C: HDMI (NORTH)

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DATE	15 MAY 2009

LINK D (UNUSED) DISPLAYPORT, MID



Remove J1slim VGA

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	LINK D: DISPLAYPORT (MID)

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ID

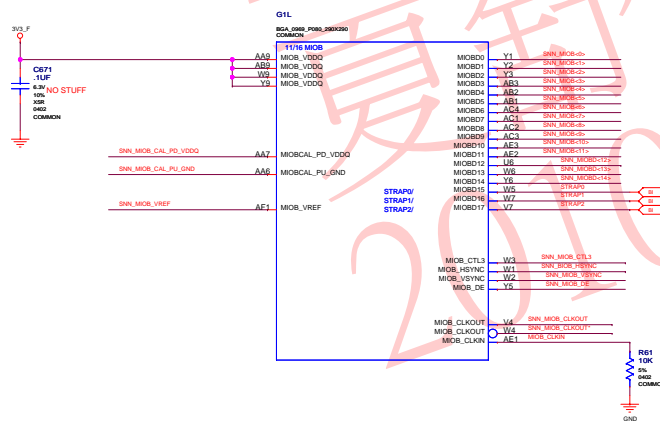
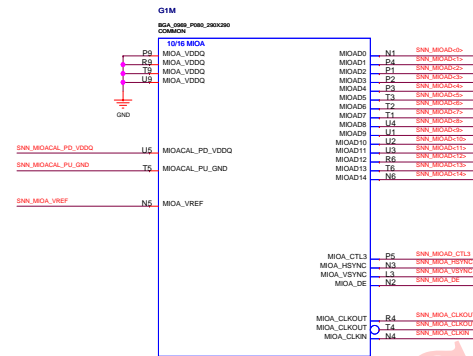
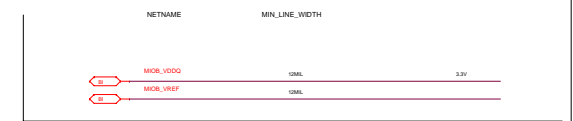
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MIOA/B SLI

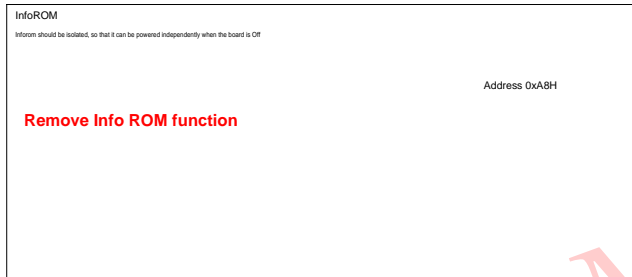


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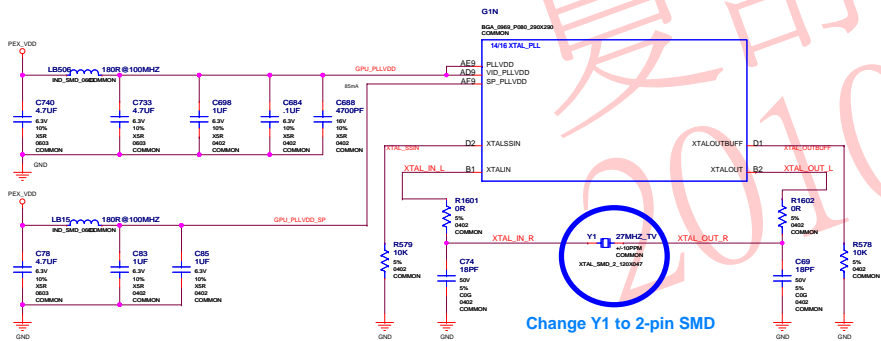
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	MIOA & MIOB, SLI CONNECTOR

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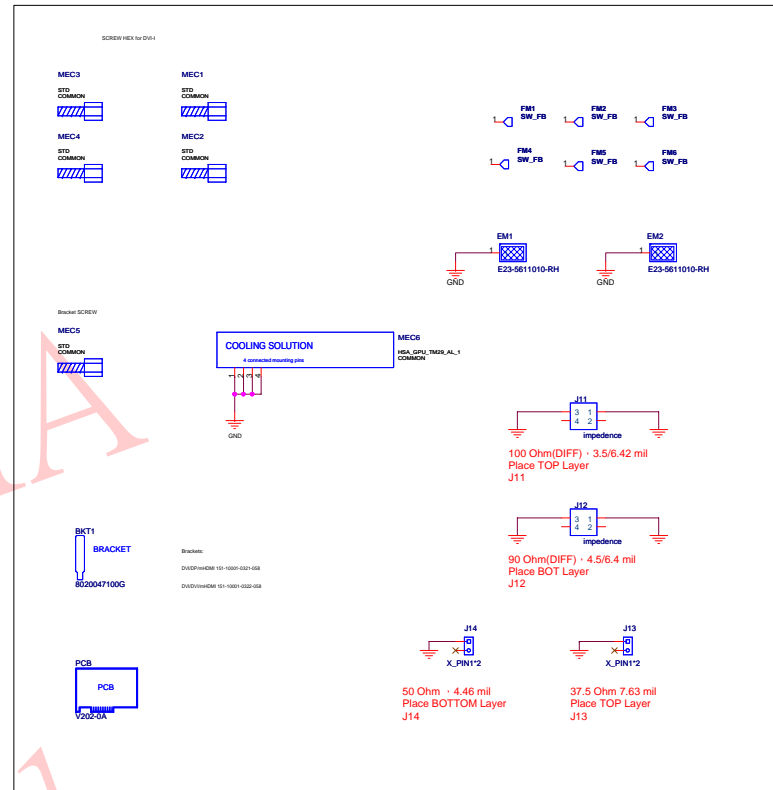
INFO_ROM/XTAL/PLLVDD



XTAL/GPU_PLLVDD



MECHANICALS & THERMALS



NETNAME	MIN_LINE_WIDTH
GPU_PLLVDD	120mil
GPU_PLLVDD_SP	120mil
XTAL_IN	100mil
XTAL_OUT	120mil
XTAL_SSIN	100mil
XTAL_OUTBUFF	100mil

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ASSEMBLY	BASE LEVEL: GENERIC BOARDMATE ONLY; COMMON & NO. 31000 ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	XTAL, MECHANICALS, THERMALS

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NAME	DATE
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GPIO & JTAG

THERMAL SENSOR

I2C: 0x98H

OVERTEMP LATCH

NETNAME	MIN_LINE_WIDTH	VOLTAGE
THERM_DC	10MIL	
THERM_DA_R	10MIL	
THERM_DA_B	10MIL	
THERM_DC_R	10MIL	
THERM_DC_B	10MIL	
GPIOX_FAN_PWM_ALERT	8MIL	
GPIOX_FAN_PWM_R	8MIL	
GPIOX_FAN_PWM_G_L	8MIL	
GPIOX_FAN_PWM_G	8MIL	
GPIOX_FAN_PWM_B	8MIL	
GPIOX_FAN_PWM_ALERT_B	8MIL	
GPIOX_FAN_PWM_R	8MIL	

Remove JTAG Connector

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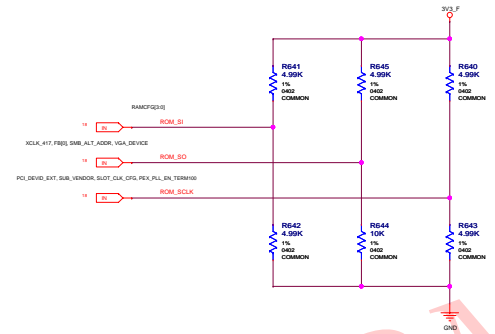
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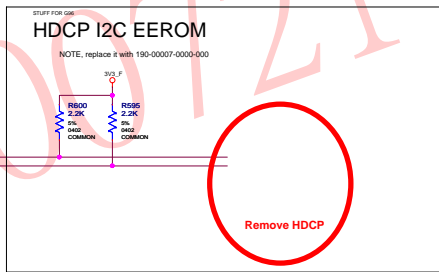
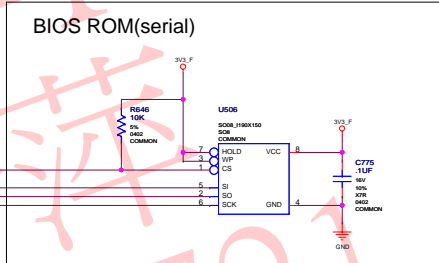
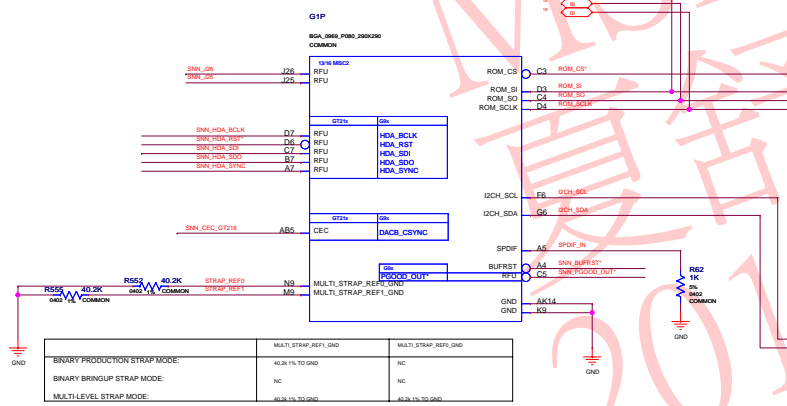
ID		PAGE	6
NAME		DATE	15-MAY-2009

ASSEMBLY	BASE LEVEL GENERIC BOARD/PCB ONLY, COMMON & NO. 3 INUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	EXTERNAL THERMAL SENSOR, FAN CONTROL, GPIO, JTAG

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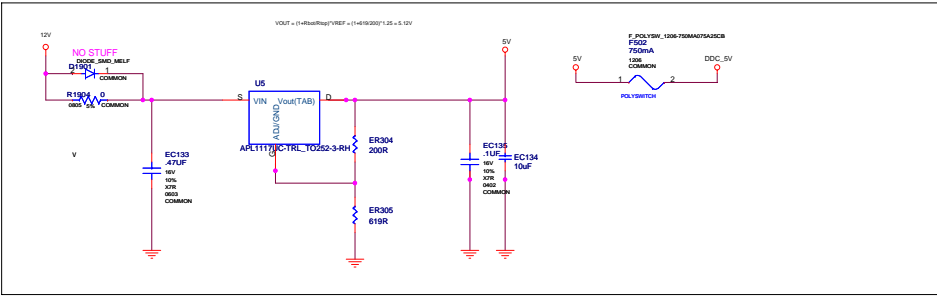


Strap0	USER0[0]	END0 1111	45A PU
Strap1	3040_FAC0F0E3[0]	DESKTOP 0000	5A PU
Strap2	PCI_DEV0[0]	07210-000 0A31-0001	10B PU
RCM_S1	RANCF0E3[0]	0001 108B 3040V16 DOR2 Samsung 0002 108B 3040V16 DOR2 Corsair 0001 108B 3040V16 DOR2 Hynix 0001 108B 3040V16 DOR2 Samsung 0010 108B 3040V16 DOR2 Corsair 0011 108B 3040V16 DOR2 Hynix	1001 3040 3040V16 DOR2 Samsung 1010 3040 3040V16 DOR2 Corsair 1001 3040 3040V16 DOR2 Hynix 1001 3040 3040V16 DOR2 Samsung 1110 3040 3040V16 DOR2 Corsair 1111 3040 3040V16 DOR2 Hynix
RCM_SCLK	3 PCI_DEV0_DEV	0110	35A PU
RCM_S0	2 SCLK_VEND0R (1 - BIOS not present) 1 SLOTT_CLK_CFG (0 - common clk, MCHCFG) 0 PEK_PLA_EN_TERRM (0 - disable p0 term) 3 CLK_H7 (0 - POR 177MHz) 2 TR_32_BM4_SIZE (0 - POR 256MB) 1 BM0_A1_A0CR (0 - POR 1, maxGRPS) 0 VISA_DEV0[0] (0 - disable, 1 - POR VISA device)	0001	5A PU
		0 PD 5A 8 PD 10A 2 PD 15A 3 PD 20A 6 PD 25A 9 PD 30A 6 PD 35A 7 PD 45A	8 PU 8 PU 10 PU 11 PU 12 PU 13 PU 14 PU 15 PU

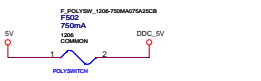


Power Supply: 5V, 5V_DDC, 1V8, EXT PWR

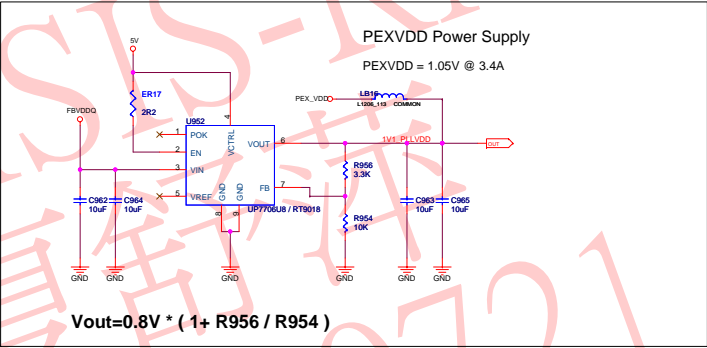
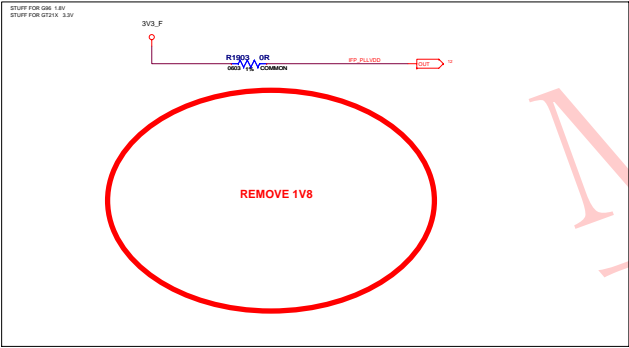
5V REGULATOR



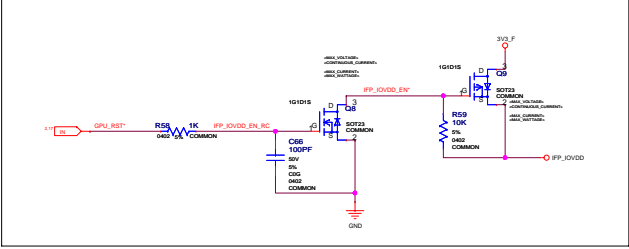
5V DDC



IFP PLL Supply



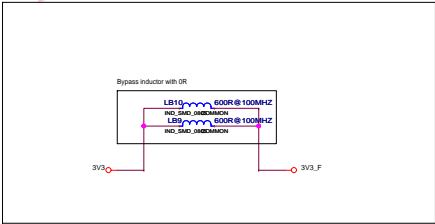
IFP_IOVDD BACKDRIVE PREVENTION



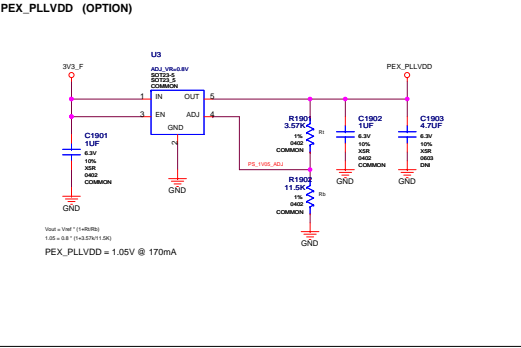
12V filter



3.3V filter



NETNAME	MAX_CURRENT	MIN_LINE_WIDTH	VOLTAGE
5V_FUSED	0.5A	120MIL	5V
5V_500V	0.5A	120MIL	5V
5V_DDC	0.5A	120MIL	5V
5V	0.5A	120MIL	5V
PEX_1V8_ADJ		120MIL	1.8V
1V8	0.15A	120MIL	1.8V
GND		120MIL	0V
3V3_F	AA	200MIL	3.3V
IFP_IOVDD	0.3A	120MIL	3.3V
12V_F	AA	200MIL	12V



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ASSEMBLY	BASE LEVEL: GENERIC SCHEMATIC ONLY; COMMON & VIO; 3D/2D ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	POWER SUPPLY LINEAR 5V, DDC5V, IFP_PLLVDD, IFP_IOVDD, 3V3 FILTER, 12V FILTER, 12V EXT

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FBVDDQ: 1.8-2.0V @ 12.7A



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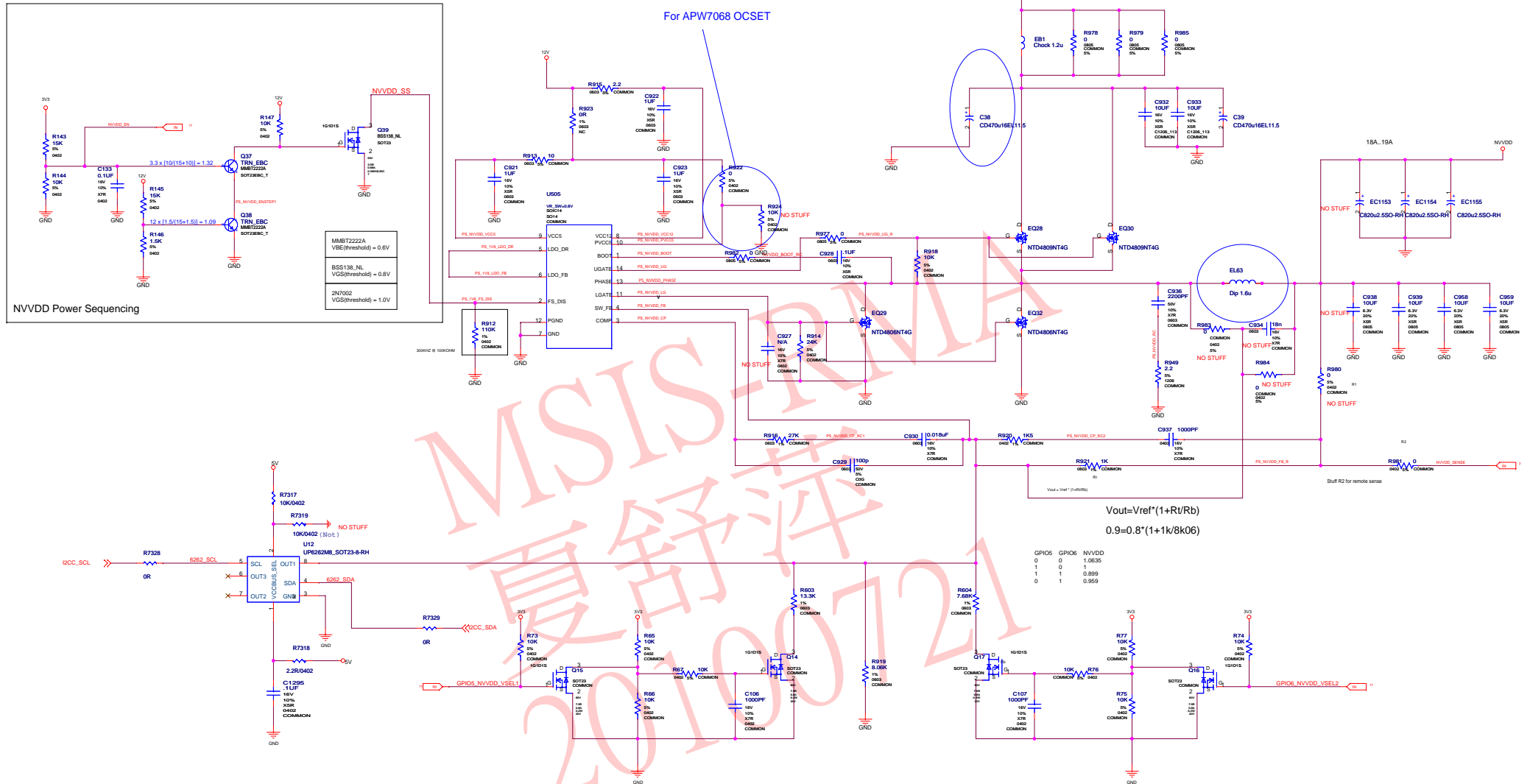
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	POWER SUPPLY: FBVDDQ SINGLE PHASE SWITCHER, PEX_VDD LINEAR

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POWER SUPPLY: NVVDD



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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	POWER SUPPLY: NVVDD DUAL PHASE SWITCHER

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