

## NV15, 1/2MX32 DDR, RGB, TV\_OUT, VIDEO\_IN, AGP4X.

PCI DEVICE ID 0X0=0X150.

## Design Notes:

B00:FIXED TMDS CLKs CONNECTIONS.

B00:ADDED 4MX32 SUPPORT.

B00:Corrected memory configuration resistors.

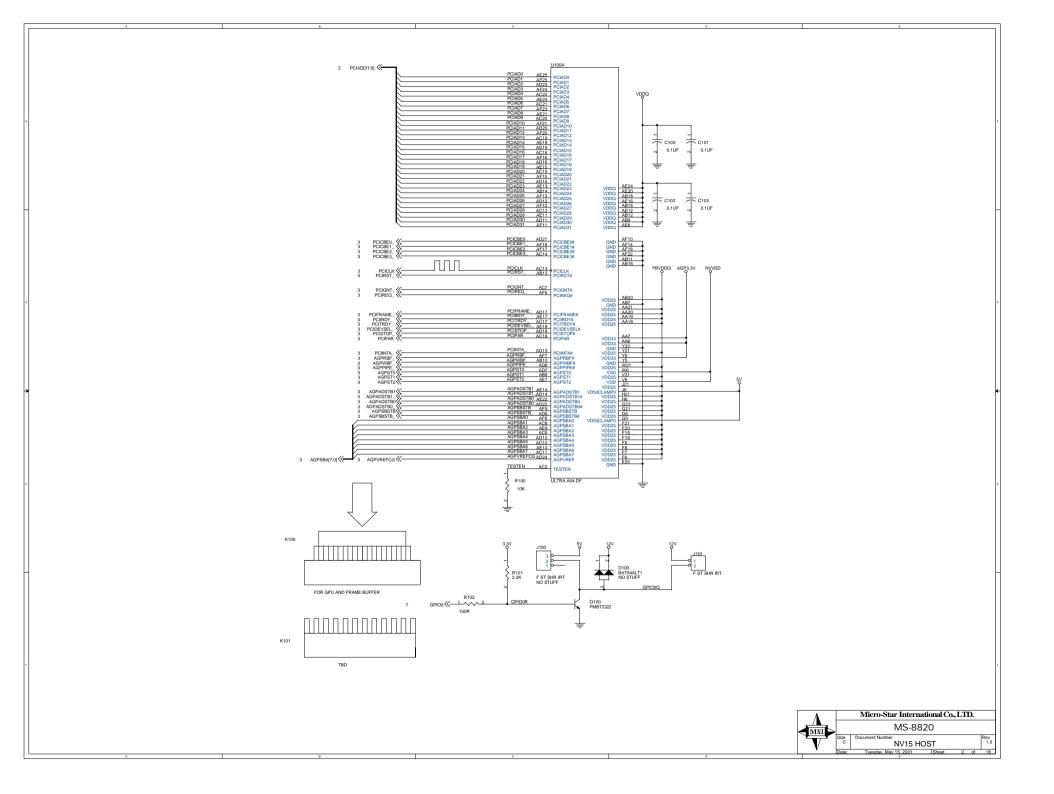
B00:Changed C809 to 4.7UF.

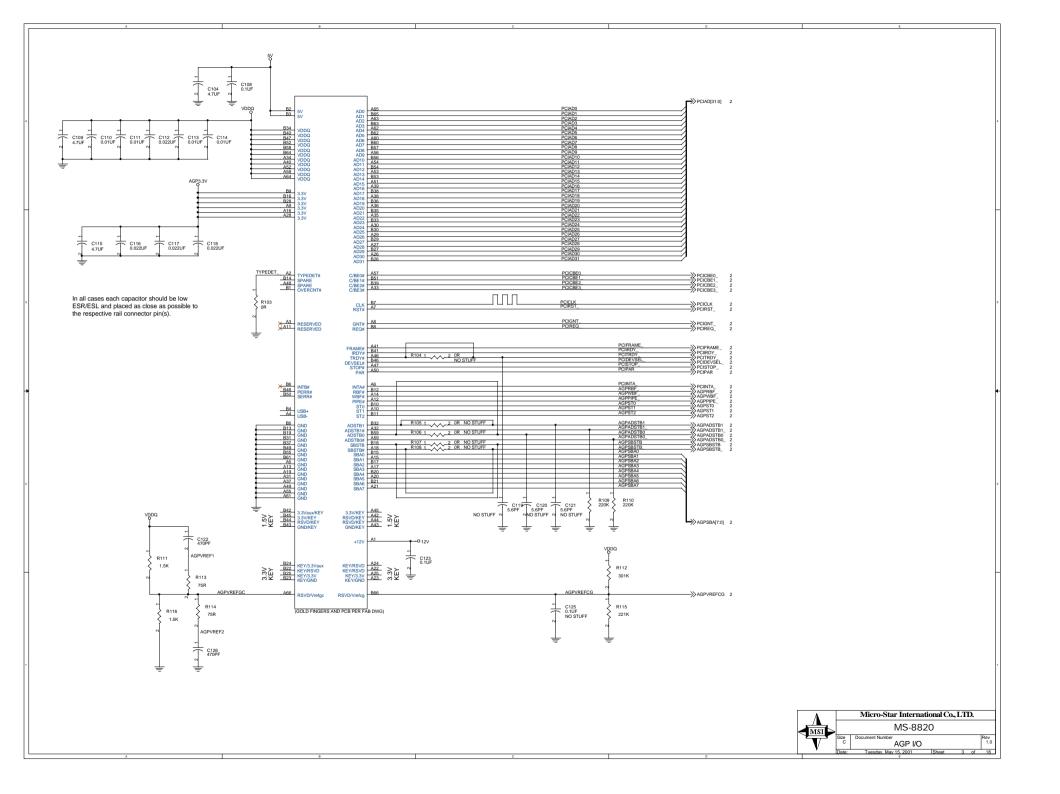
B00: Changed R811 from 100R to 105R

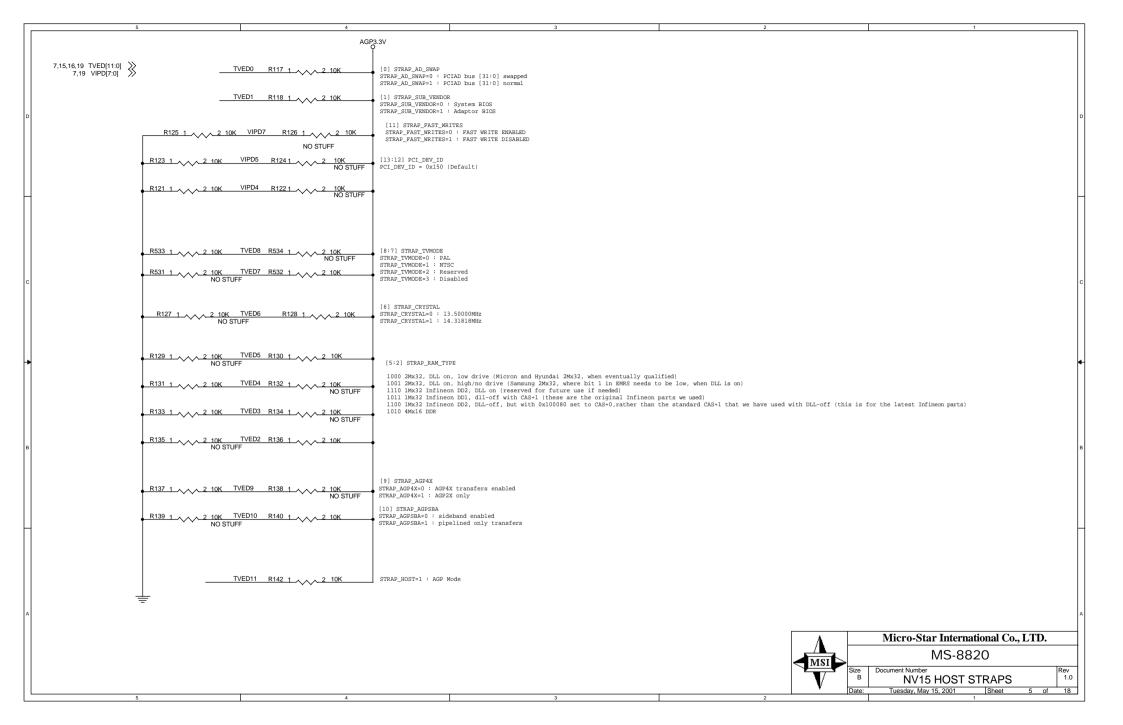
B00:C128 changed to no stuff

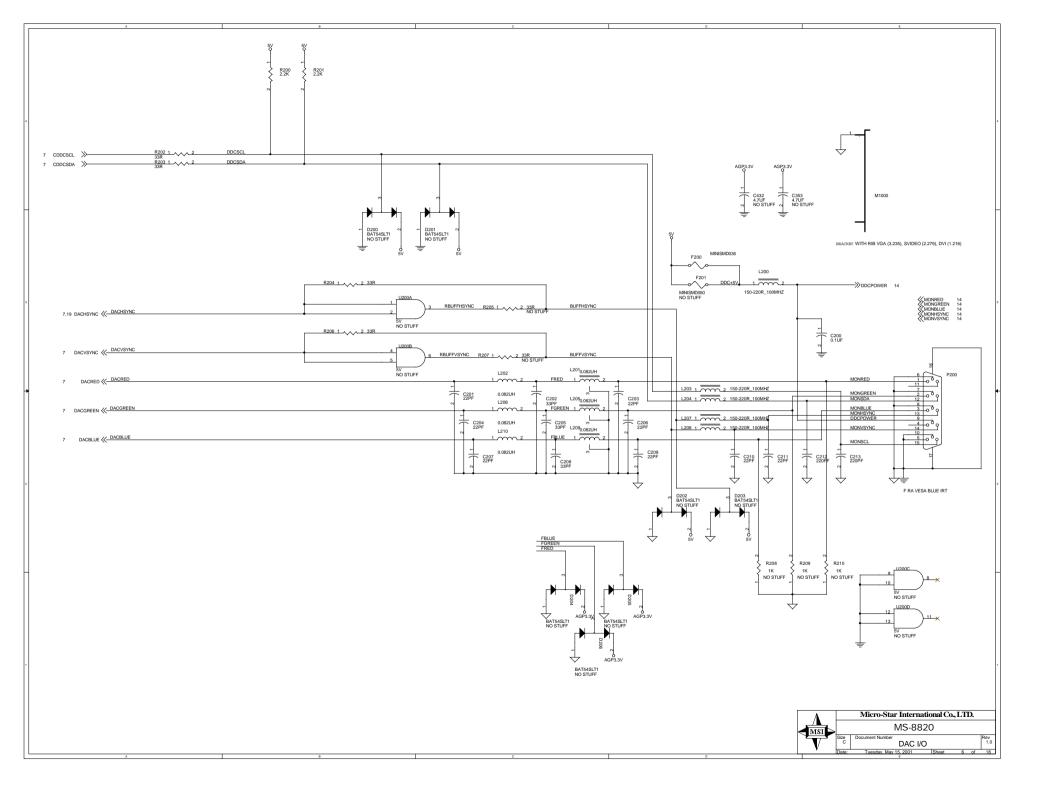
B00:Removed 4mx32 function (R876, R877= NO STUFF)

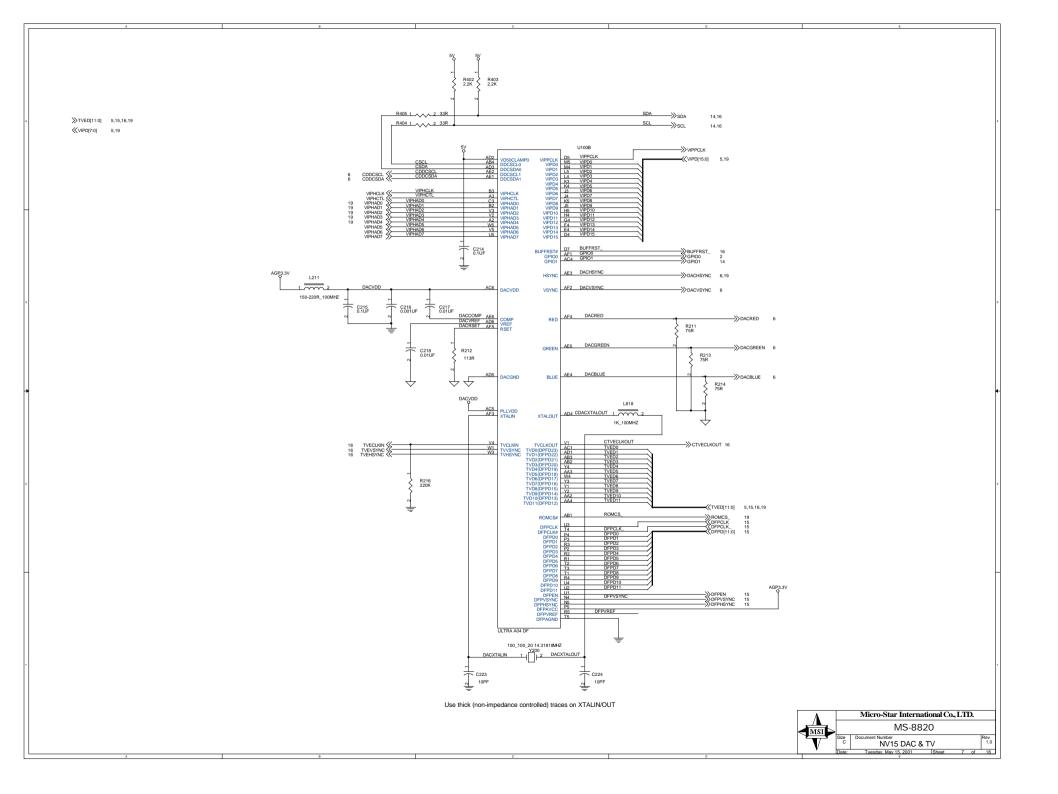
	Λ.		Micro-Star International Co., LTD.	
	MSI		MS-8820	
		Size	Document Number	Rev
	<b>V</b>	В	Based on P-22-B00	1.0
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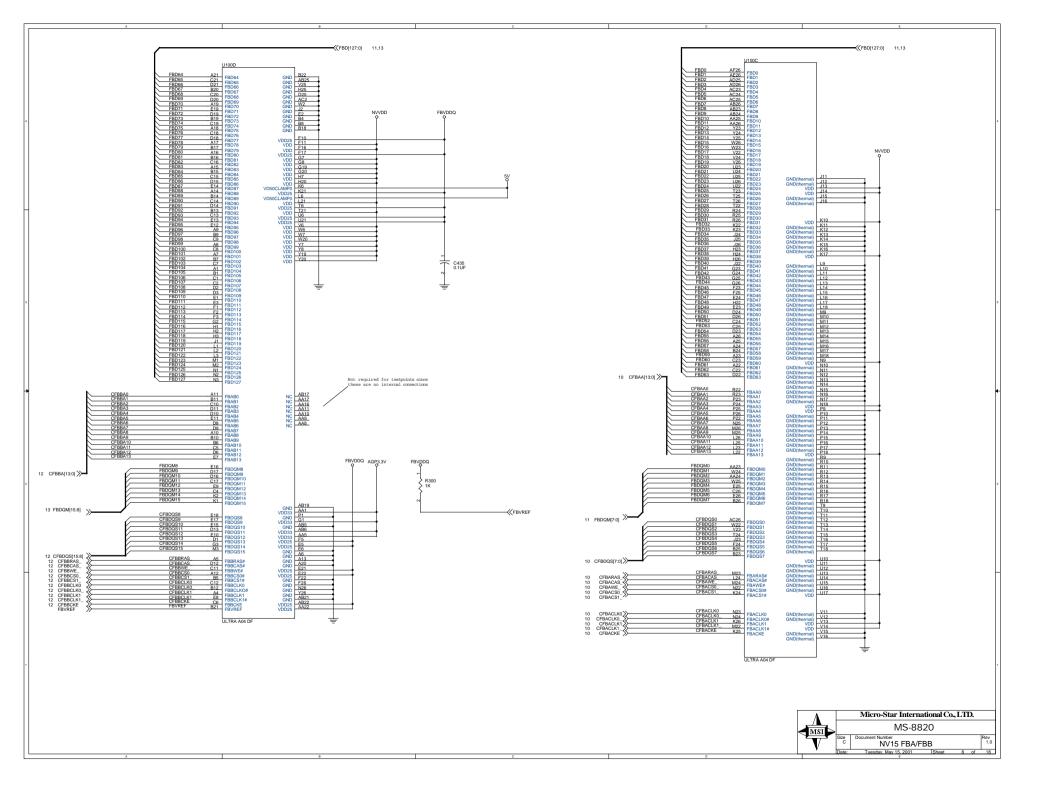


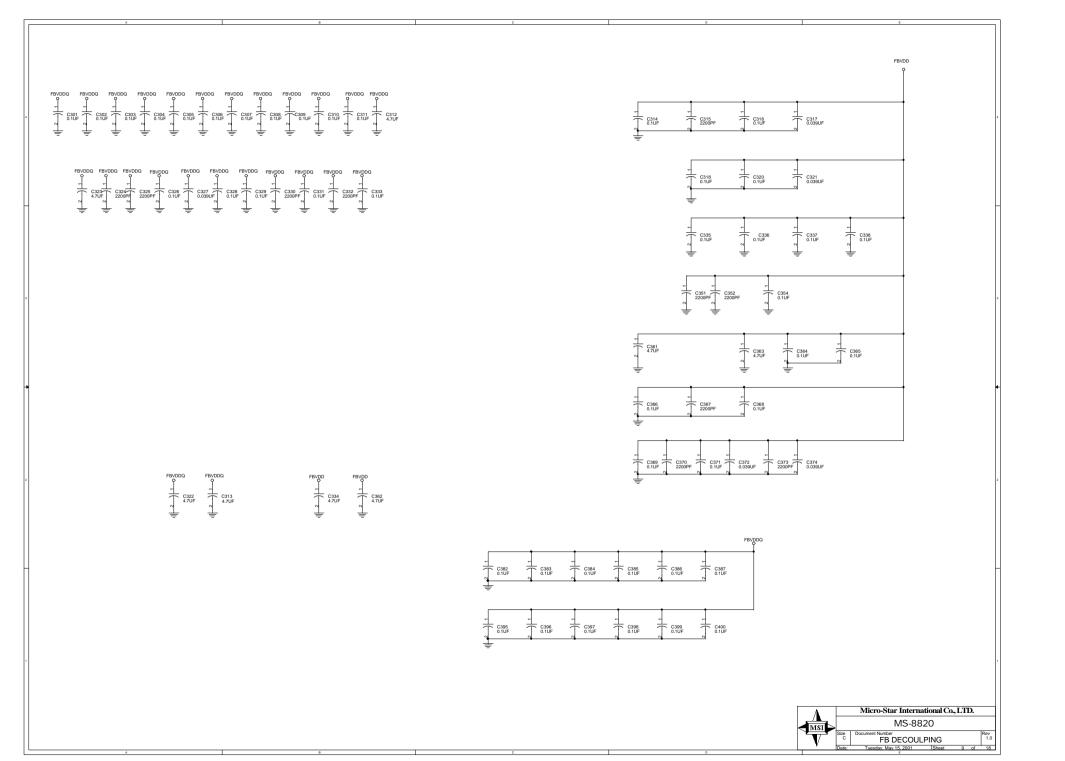


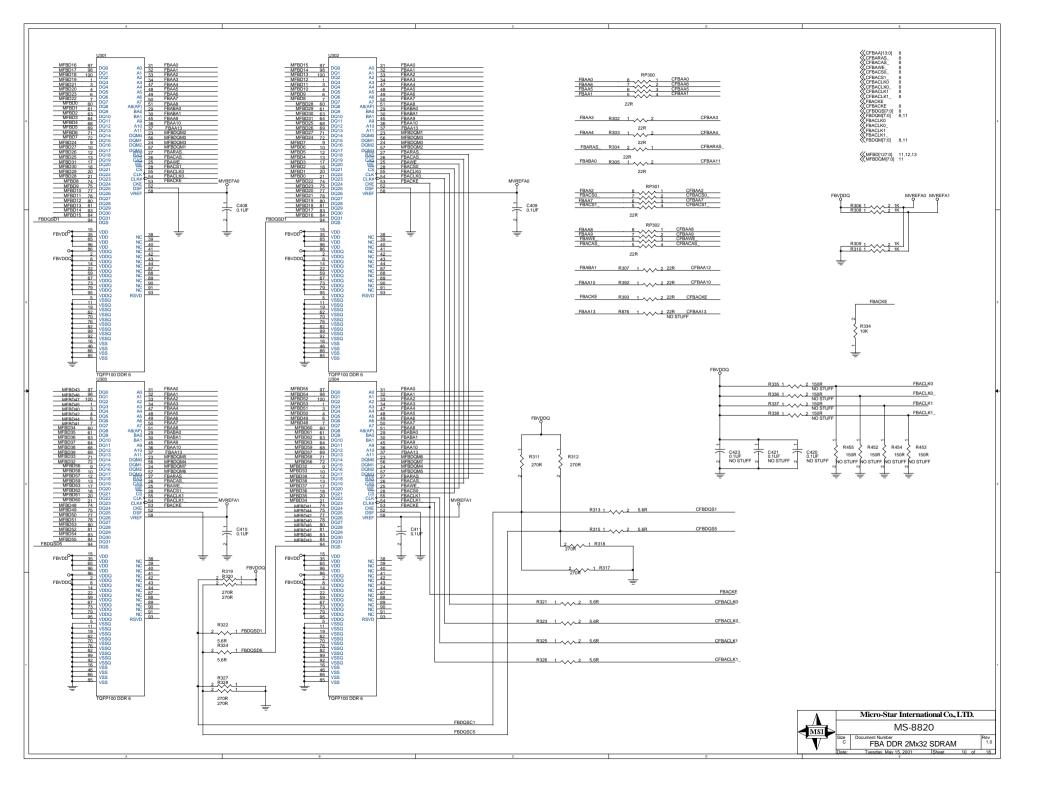


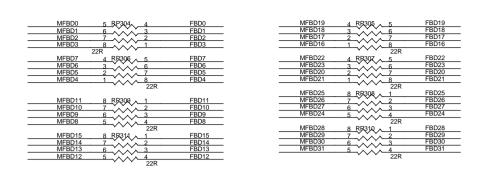












MFBD[127:0] 10,12,13
MFBDQM[7:0] 10

MFBD32	4 RR312 4 5	FBD32
MFBD33	3 4 4 6	FBD33
MFBD39	2 1 7	FBD39
MFBD38	1 44 8	FBD38
WII DDOO		1 0000
MFBD37	4_RP3145	FBD37
MFBD36_	3 4 4 6	FBD36
MFBD35	2 4 4 7	FBD35
MFBD34	1 4 4 8	FBD34
	22R	
MFBD51	8 RR316 A 1	FBD51
MFBD50	7 ~ ~ ~ 2	FBD50
MFBD49	6 4 4 3	FBD49
MFBD48	5 4	FBD48
	22R	
MFBD55	8 RR318 1	FBD55
MFBD54	7 0 0 2	FBD54
MFBD52	6 ~ ~ ~ ~ 3	FBD52
MFBD53	5 4	FBD53
	22R	
	LL.	

MFBD41	4 RR313 _ 5	FBD41
MFBD44	3 0 0 6	FBD44
MFBD42	2 7 7	FBD42
MFBD40	1 4 4 8	FBD40
	22R	
MFBD45	4 RR315 _ 5	FBD45
MFBD47	3 4 4 6	FBD47
MFBD46	2 0 0 7	FBD46
MFBD43	1 4 4 4 8	FBD43
	22R	
MFBD59	8 RR317 1	FBD59
MFBD57		EDDEZ
IVIFDUST	7^^2	FBD57
MFBD57 MFBD58	$\frac{7}{6}$	FBD58
MFBD58	6 3	FBD58
MFBD58	6 3 3 5	FBD58
MFBD58 MFBD56	6 3 5 4 22R	FBD58 FBD56
MFBD58 MFBD56 MFBD60	6 3 5 4 22R	FBD58 FBD56 FBD60
MFBD58 MFBD56 MFBD60 MFBD61	6 3 3 4 22R 8 RR318 1 7 2	FBD58 FBD56 FBD60 FBD61

MFBDQM2	R339 1 2 22R	FBDQM2
MFBDQM3	R340 1 2 22R	FBDQM3
MFBDQM0	R341 1 2 22R	FBDQM0
MFBDQM1	R342 1 22R	FBDQM1
MFBDQM4	R343 1 2 22R	FBDQM4
MFBDQM6	R344 1 2 22R	FBDQM6
MFBDQM5	R345 1 2 22R	FBDQM5
MFBDQM7	R346 1 ^ ^ 2 22R	FBDQM7

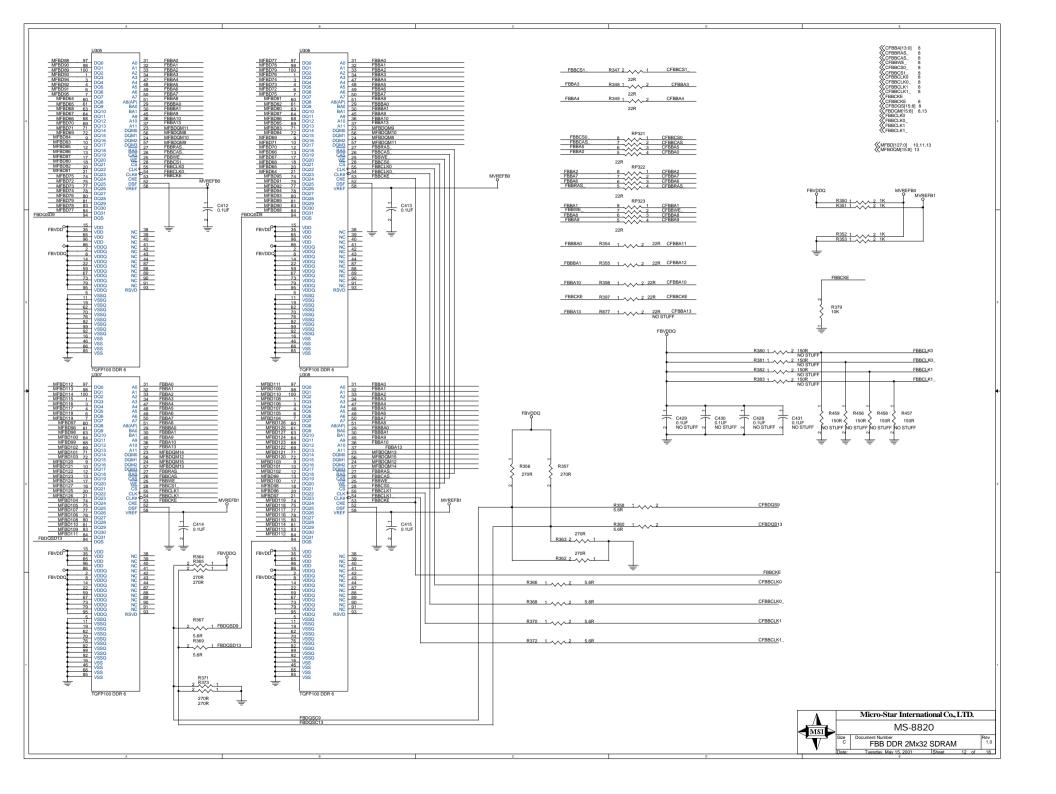
**₹**MSI Document Number

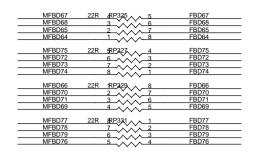
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FBA DDR 2Mx32 SDRAM TERM

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MFBD95	22R 4 RR324 5	FBD95
MFBD91	3 4 4 6	FBD91
MFBD92	2 4 4 7	FBD92
MFBD94	1 4 4 8	FBD94
MFBD84	22R 5 RP326 4	FBD84
MFBD83	6 0 0 3	FBD83
MFBD85	7 ^ 2	FBD85
MFBD86	8 0 0 1	FBD86
MFBD81	22R 8 RR328 1	FBD81
MFBD82	7 ~~~ 2	FBD82
MFBD80	6 0 0 3	FBD80
MFBD87	5 4	FBD87
	~~~	
MFBD88	22R 1 RP330 8	FBD88
MFBD90	2 4 4 7	FBD90
MFBD89	3 4 4 6	FBD89
MFBD93	4 4 4 5	FBD93

MFBD[127:0] 10,11,12 MFBDQM[15:8] 12

FBD[127:0] 8,11 FBDQM[15:8] 8

MFBD97	22R 5 RP332, 4	FBD97
MFBD96	6 0 0 3	FBD96
MFBD98	7 ~ ~ ~ 2	FBD98
MFBD100	8 4 4 1	FBD100
MFBD108	22R 5 RP334 <sub>A</sub> 4	FBD108
MFBD110	6 0 0 3	FBD110
MFBD109	7 ^ ^ 2	FBD109
MFBD111	8 4 4 1	FBD111
MFBD103	22R 8 RR336 1	FBD103
MFBD101	7 ~~~ 2	FBD101
MFBD102	6 00 0 3	FBD102
MFBD99	5 0 0 4	FBD99
MFBD104	22R 1 RP338 <sub>4</sub> 8	FBD104
MFBD105	2 4 4 7	FBD105
MFBD107	3 4 4 6	FBD107
MFBD106	4 4 4 5	FBD106

MFBD115	22R 4 RR333 4 5	FBD115
MFBD114	3 4 4 6	FBD114
MFBD113	2 0 7	FBD113
MFBD112	1 000 8	FBD112
MFBD119	22R 4 RP335 5	FBD119
MFBD118	3 4 4 6	FBD118
MFBD117	2 4 4 7	FBD117
MFBD116	1 000 8	FBD116
MFBD123	22R 8 RR337 1	FBD123
MFBD122	7	FBD122
MFBD121	6 000 3	FBD121
MFBD120	5 0 0 4	FBD120
MFBD126	22R 8 RR339 1	FBD126
MFBD125	7 . 4 4 2	FBD125
MFBD127	6 0 0 3	FBD127
MFBD124	5 0 0 4	FBD124

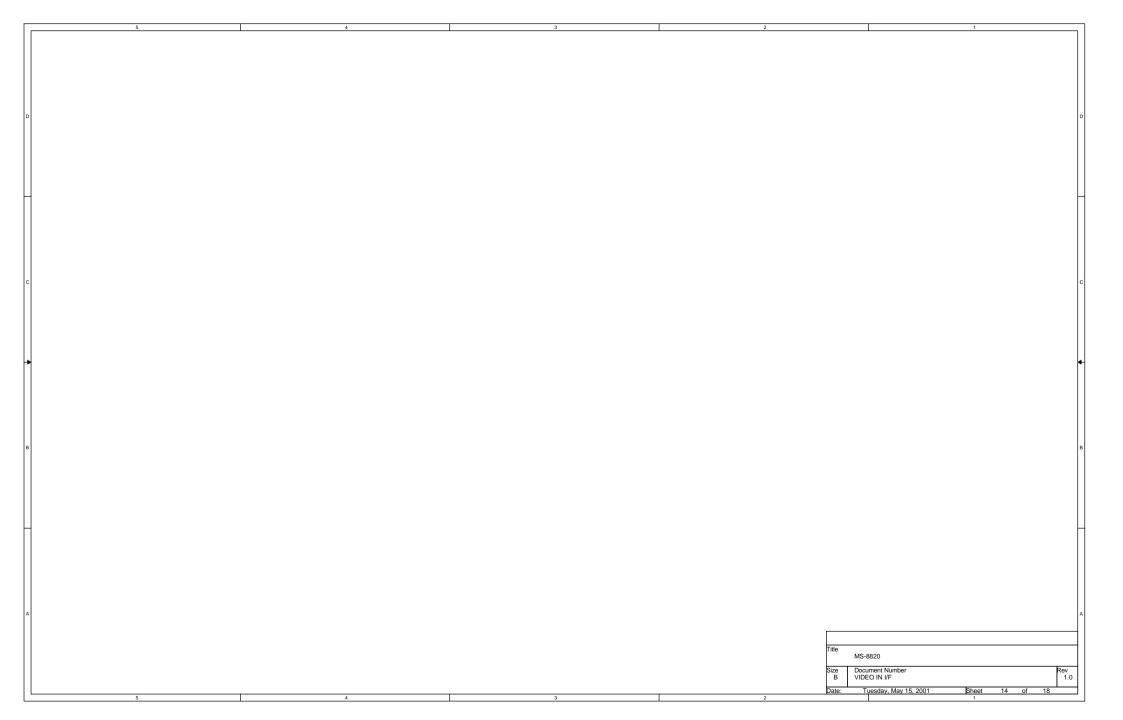
MFBDQM11	R384 1 ^ ^ 2 22R	FBDQM11
MFBDQM10	R385 1 2 22R	FBDQM10
MFBDQM9	R386 1 A A A 2 22R	FBDQM9
MFBDQM8	R387 1 4 4 4 2 22R	FBDQM8
MFBDQM14	R388 1 A A A 2 22R	FBDQM14
MFBDQM15	R389 1 A A A 2 22R	FBDQM15
MFBDQM12	R390 1 A A A 2 22R	FBDQM12
-		
MFBDQM13	R391 1 A A 2 22R	FBDQM13

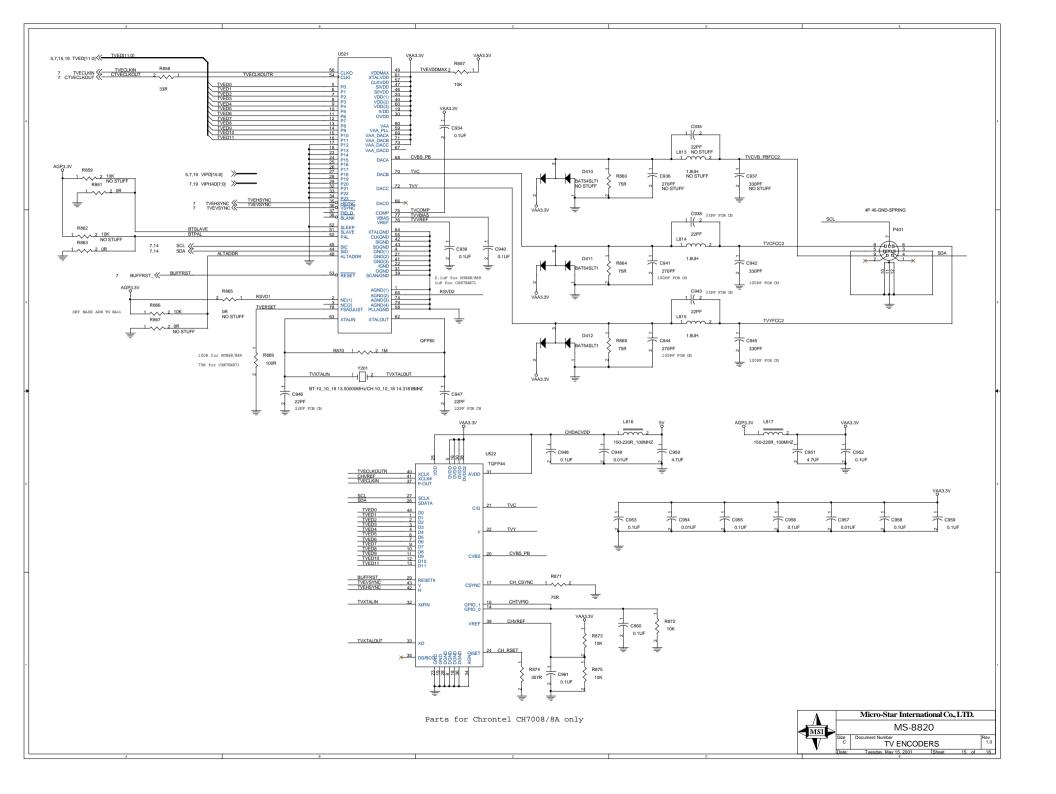


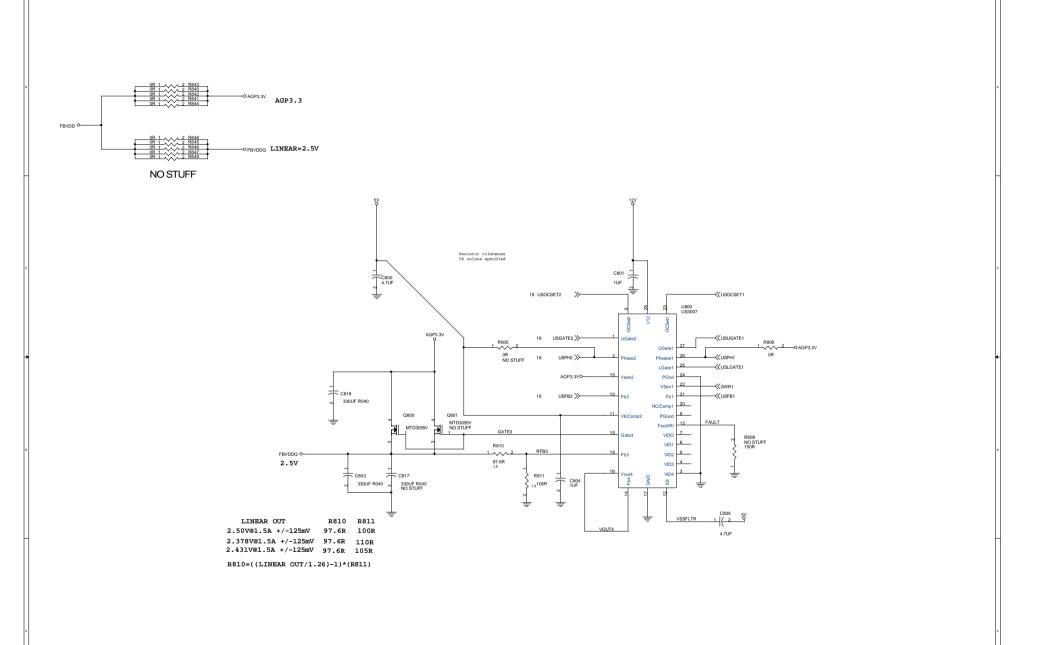
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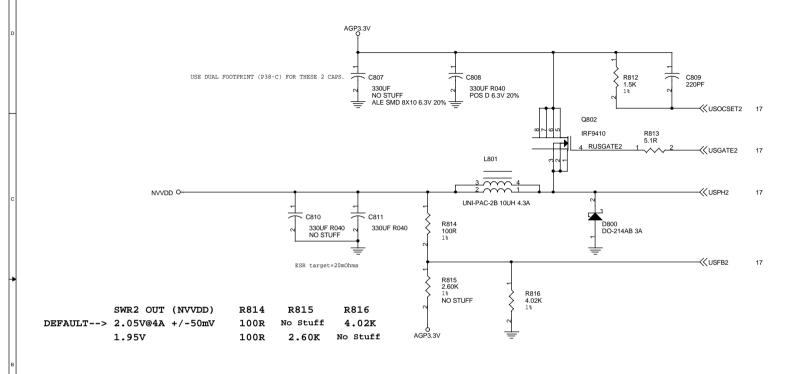
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R816=100/((2.05V/2)-1)

R815=130/(2-1.95v)

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