TABLE OF CONTENTS

AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: TOC

DATE: Wed Sep 09 16:50:44 2015 REV: 1.0

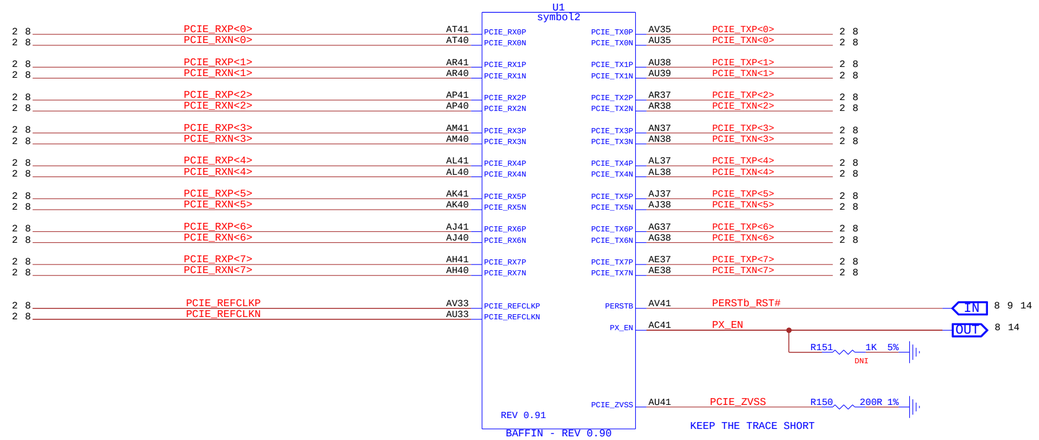
SHEET NUMBER: 1 OF 17

DOCUMENT NUMBER: 105_C985xx_00B

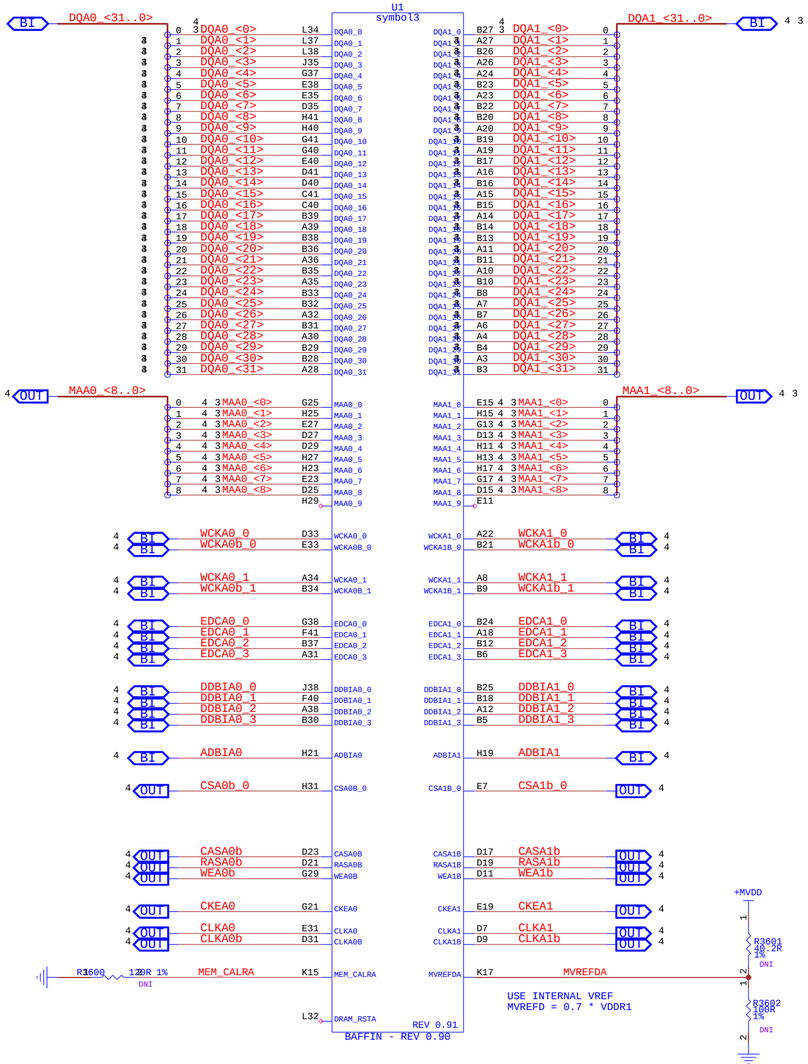
NOTES: NOTE

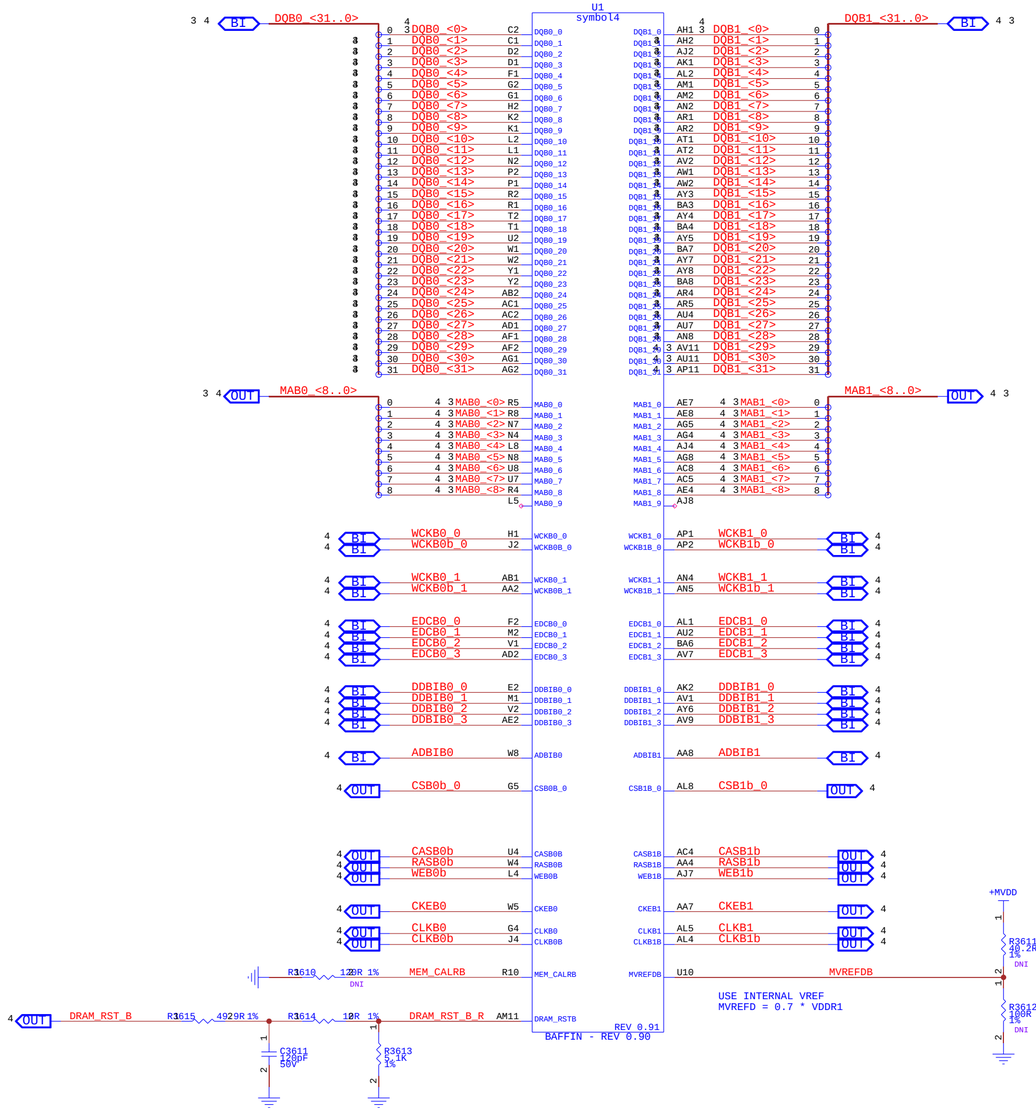
TITLE:

BAFFIN GL Pro 4x32 G5 TYPE A



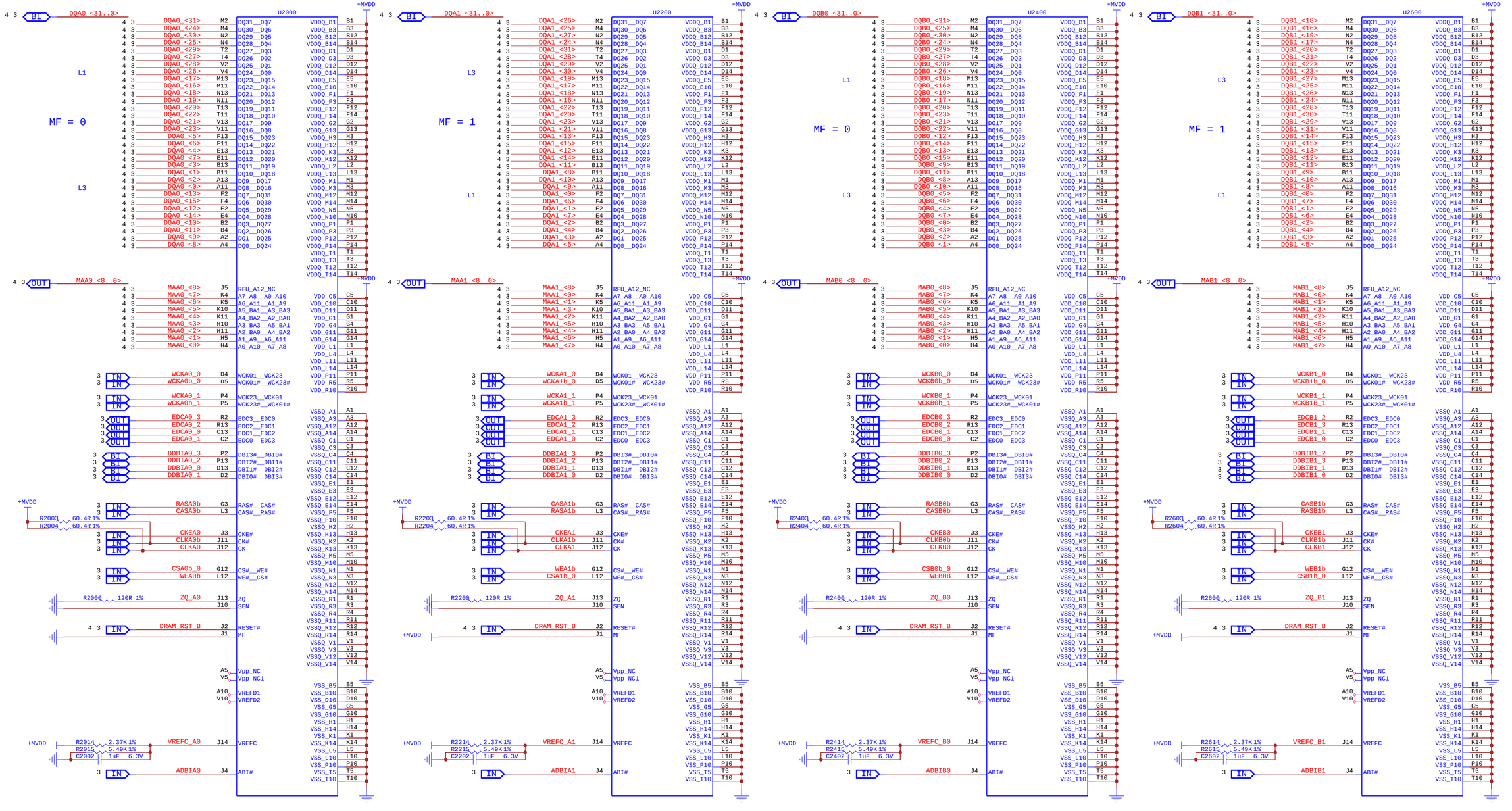
AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC. © 2015 Advanced Micro Devices This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representations or warranties of any kind regarding this schematic and design, including, not limited to, any implied warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information included herein.
SHEET: BAFFIN PCIE	
DATE: Mon Dec 14 13:53:01 2015 REV: 1.0	
SHEET NUMBER: 2 OF 17	TITLE: BAFFIN GL Pro 4x32 G5 TYPE A
DOCUMENT NUMBER: 105_C985xx_00B	
NOTES: NOTE	





AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	
SHEET: BAFFIN MEMORY	
DATE: Mon Dec 14 13:52:57 2015	REV: 1.0
SHEET NUMBER: 3 OF 17	
DOCUMENT NUMBER: 105_C985xx_00B	
NOTES: NOTE	

TITLE:
BAFFIN GL Pro 4x32 G5 TYPE A



AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: GDDR5 x32 CHAB

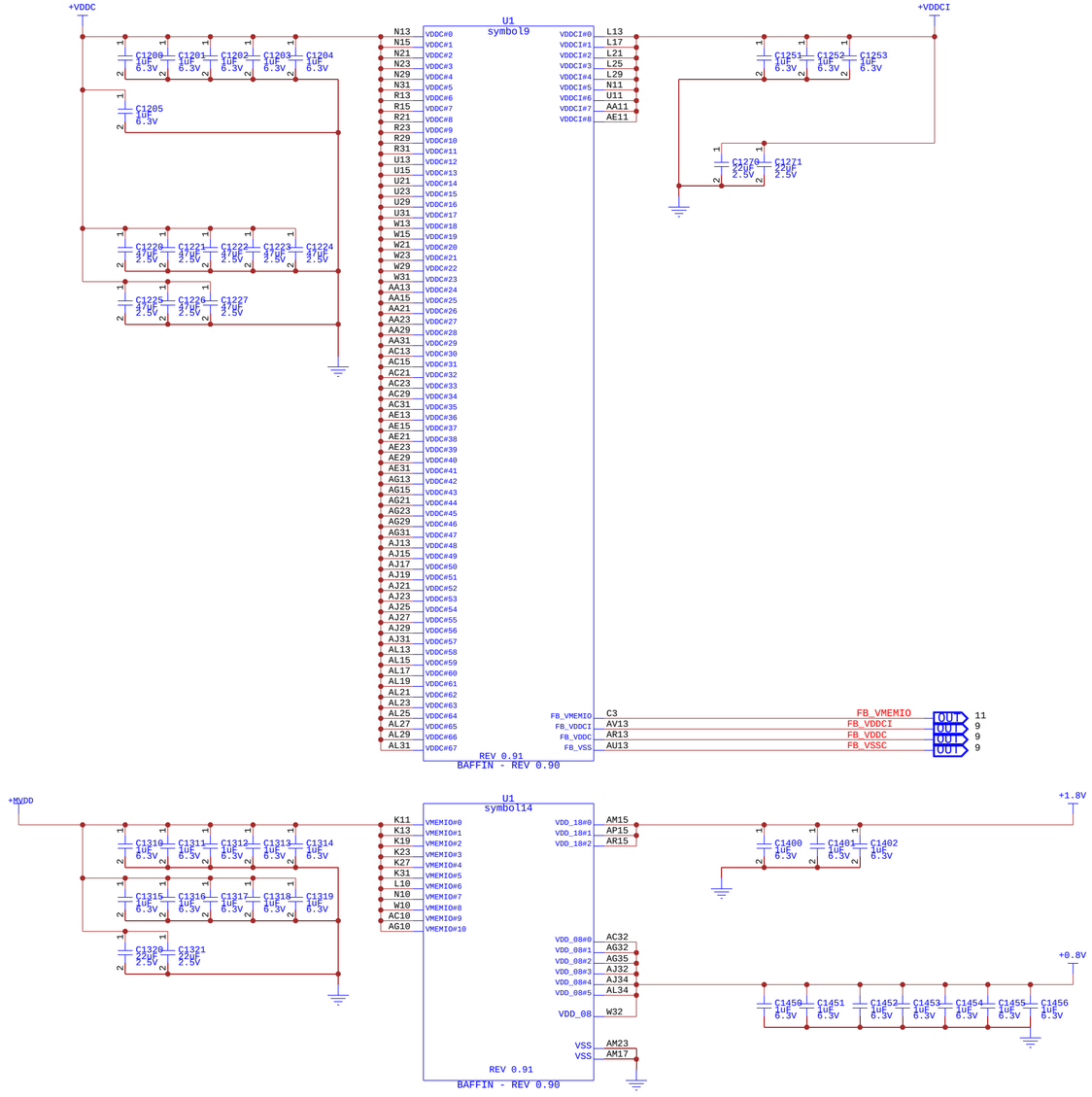
DATE: Mon Dec 14 13:53:00 2015 REV: 1.0

SHEET NUMBER: 4 OF 17

DOCUMENT NUMBER: 105_C985xx_00B

NOTES: NOTE

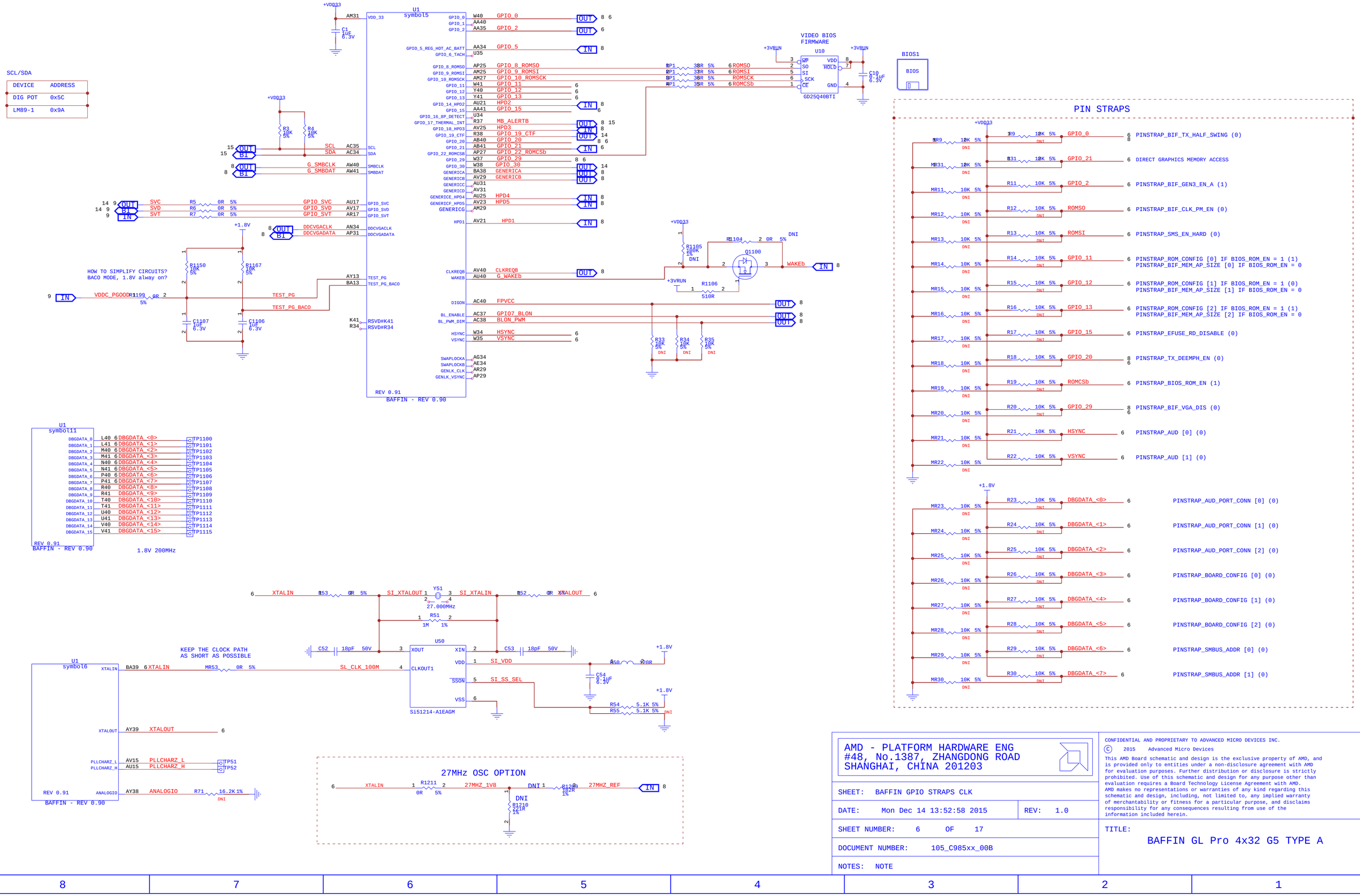
TITLE:
BAFFIN GL Pro 4x32 G5 TYPE A

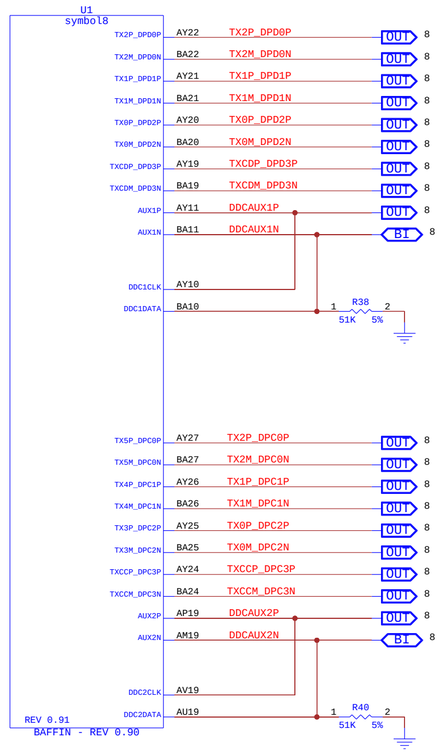


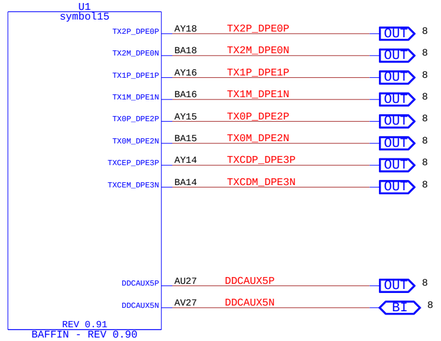
AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: BAFFIN POWER GND	
DATE: Mon Dec 14 13:53:00 2015	REV: 1.0
SHEET NUMBER: 5 OF 17	
DOCUMENT NUMBER: 105_C985xx_00B	
NOTES: NOTE	

TITLE:
BAFFIN GL Pro 4x32 G5 TYPE A

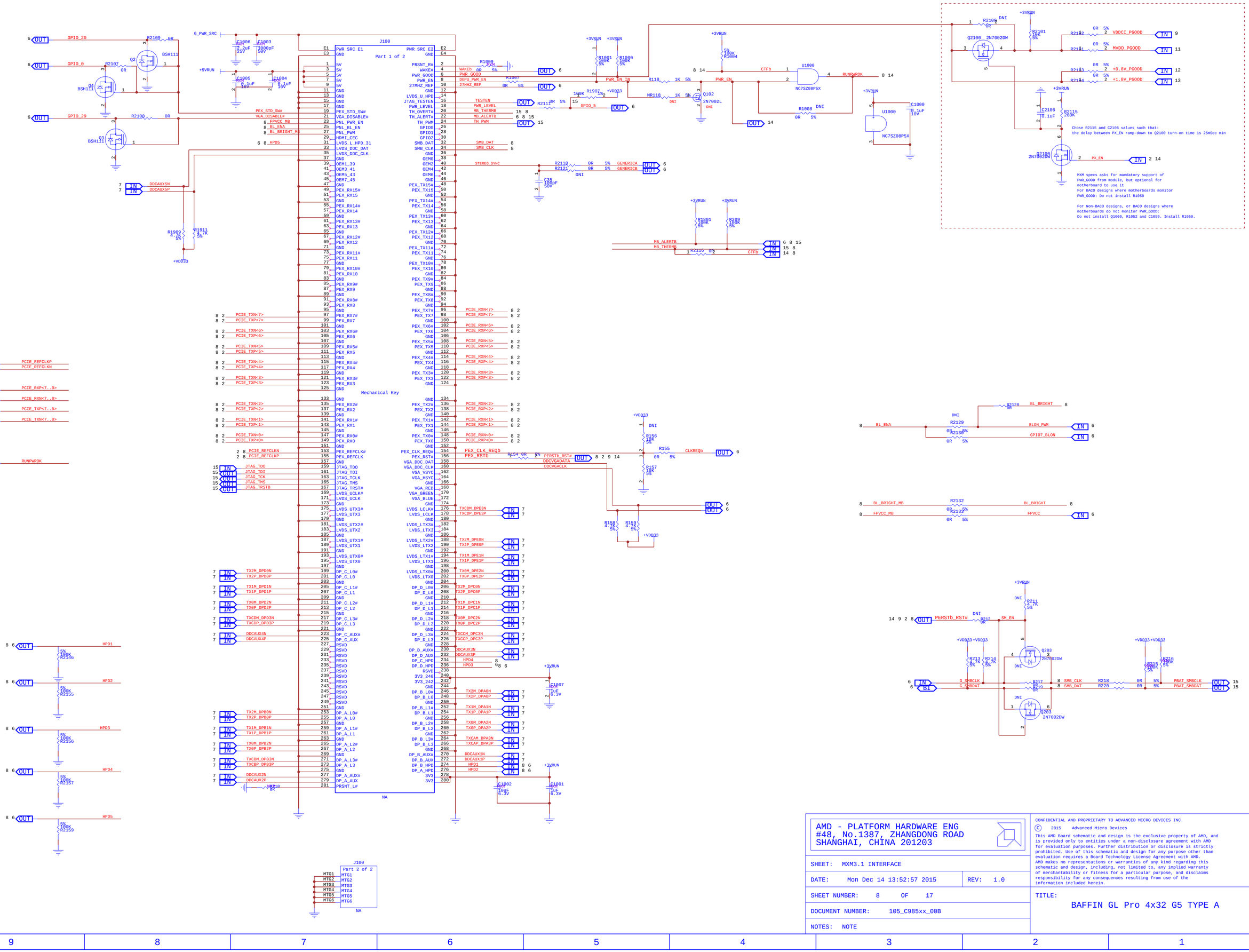

SCL/SDA
DEVICE ADDRESS
DIG POT 0x5C
LM89-1 0x9A
U1 Symbol5
BAFFIN - REV 0.90
REV 0.91
VIDEO BIOS FIRMWARE
U10
GD25Q40BTI
BIOS1
PIN STRAPS
PINSTRAP_BIF_TX_HALF_SWING (0)
DIRECT GRAPHICS MEMORY ACCESS
PINSTRAP_BIF_GEN3_EN_A (1)
PINSTRAP_BIF_CLK_PM_EN (0)
PINSTRAP_SMS_EN_HARD (0)
PINSTRAP_ROM_CONFIG [0] IF BIOS_ROM_EN = 1 (1)
PINSTRAP_BIF_MEM_AP_SIZE [0] IF BIOS_ROM_EN = 0
PINSTRAP_ROM_CONFIG [1] IF BIOS_ROM_EN = 1 (0)
PINSTRAP_BIF_MEM_AP_SIZE [1] IF BIOS_ROM_EN = 0
PINSTRAP_ROM_CONFIG [2] IF BIOS_ROM_EN = 1 (1)
PINSTRAP_BIF_MEM_AP_SIZE [2] IF BIOS_ROM_EN = 0
PINSTRAP_EFUSE_RD_DISABLE (0)
PINSTRAP_TX_DEEMPH_EN (0)
PINSTRAP_BIOS_ROM_EN (1)
PINSTRAP_BIF_VGA_DIS (0)
PINSTRAP_AUD [0] (0)
PINSTRAP_AUD [1] (0)
PINSTRAP_AUD_PORT_CONN [0] (0)
PINSTRAP_AUD_PORT_CONN [1] (0)
PINSTRAP_AUD_PORT_CONN [2] (0)
PINSTRAP_BOARD_CONFIG [0] (0)
PINSTRAP_BOARD_CONFIG [1] (0)
PINSTRAP_BOARD_CONFIG [2] (0)
PINSTRAP_SMBUS_ADDR [0] (0)
PINSTRAP_SMBUS_ADDR [1] (0)
HOW TO SIMPLIFY CIRCUITS?
BACO MODE, 1.8V alway on?
U1 symbol11
1.8V 200MHz
KEEP THE CLOCK PATH
AS SHORT AS POSSIBLE
U50
Si151214-A1EAGM
27MHz OSC OPTION
AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203
SHEET: BAFFIN GPIO STRAPS CLK
DATE: Mon Dec 14 13:52:58 2015
REV: 1.0
SHEET NUMBER: 6 OF 17
DOCUMENT NUMBER: 105_C985xx_00B
NOTES: NOTE
TITLE:
BAFFIN GL Pro 4x32 G5 TYPE A
CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC.
© 2015 Advanced Micro Devices
This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representations or warranties of any kind regarding this schematic and design, including, not limited to, any implied warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information included herein.





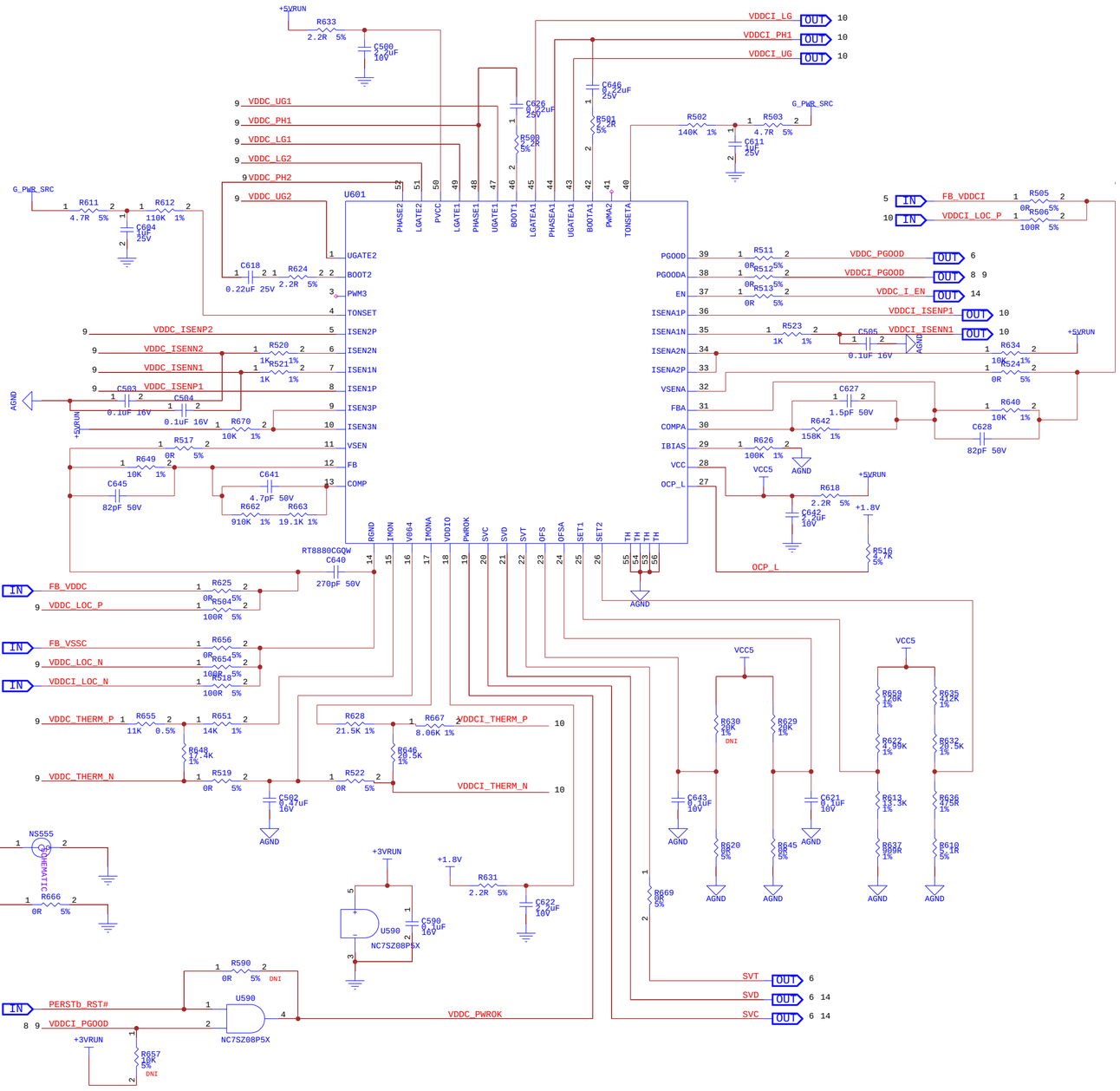
AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	
SHEET: BAFFIN TMDP_LVTMDP	
DATE: Mon Dec 14 13:53:01 2015	REV: 1.0
SHEET NUMBER: 7 OF 17	
DOCUMENT NUMBER: 105_C985xx_00B	
NOTES: NOTE	

TITLE:
BAFFIN GL Pro 4x32 G5 TYPE A



AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	
SHEET: MXM3.1 INTERFACE	
DATE: Mon Dec 14 13:52:57 2015	REV: 1.0
SHEET NUMBER: 8 OF 17	
DOCUMENT NUMBER: 105_C985xx_00B	
NOTES: NOTE	

TITLE: BAFFIN GL Pro 4x32 G5 TYPE A

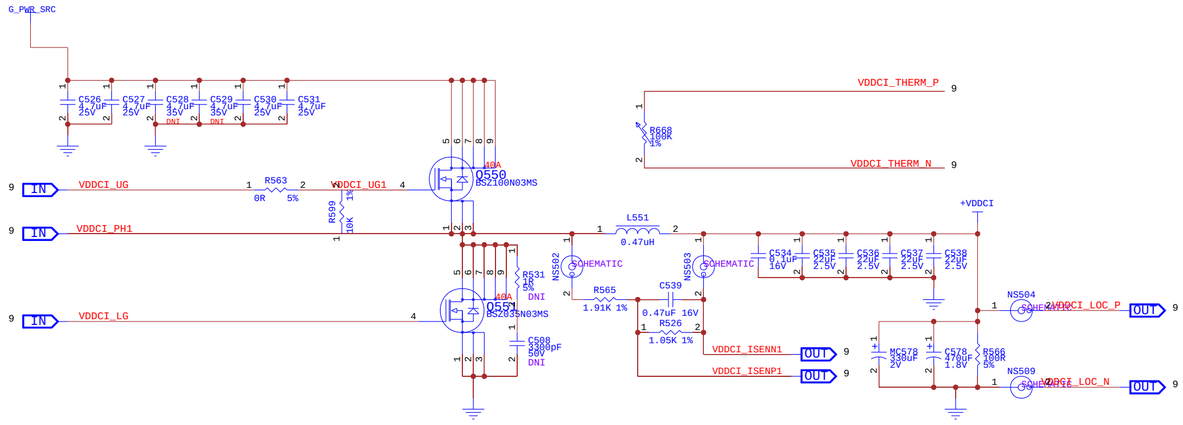


AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: VDDC	
DATE: Mon Dec 14 13:52:58 2015	REV: 1.0
SHEET NUMBER: 9 OF 17	
DOCUMENT NUMBER: 105_C985xx_00B	
NOTES: NOTE	

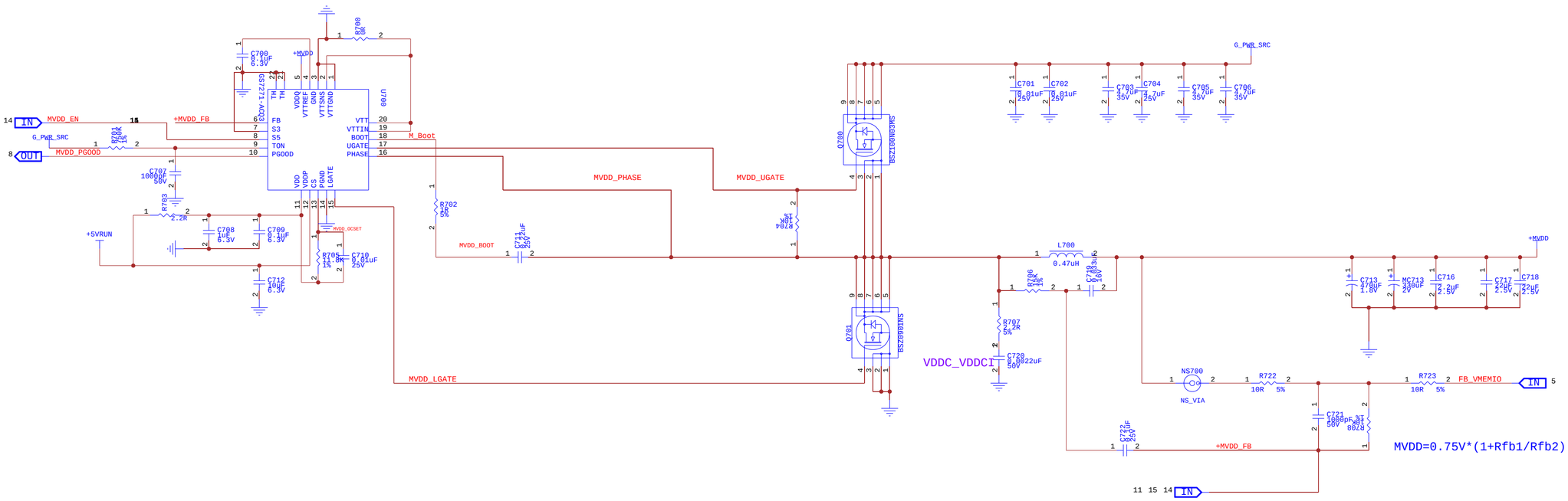
TITLE:

BAFFIN GL Pro 4x32 G5 TYPE A

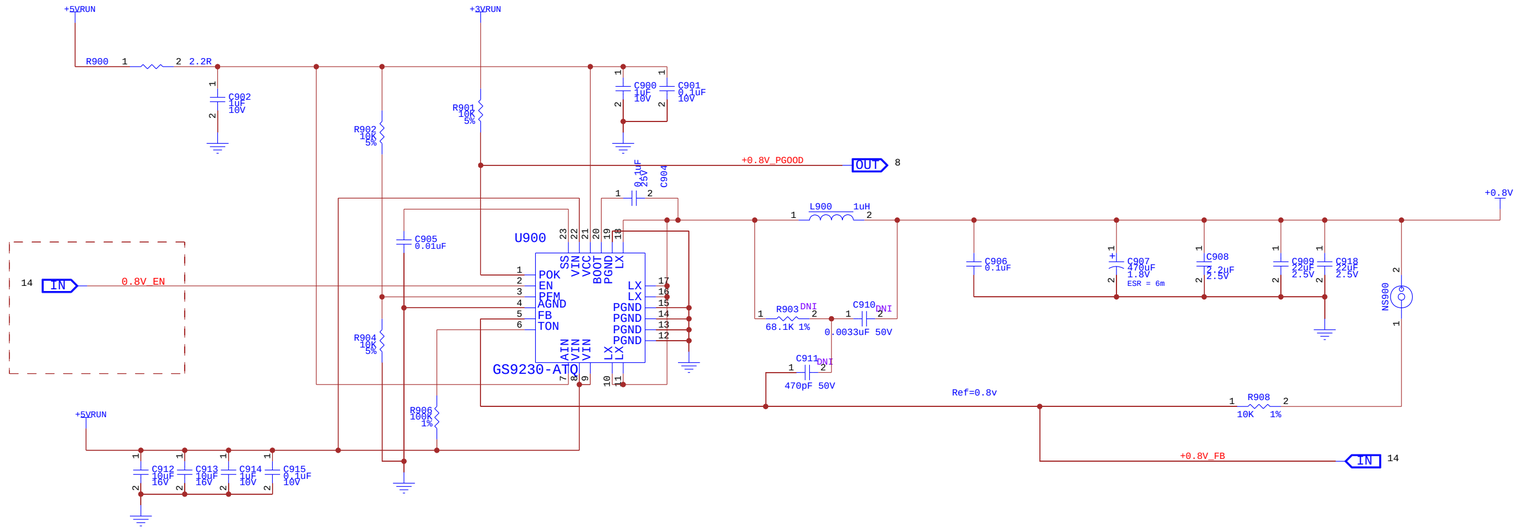


AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	
SHEET: VDDCI	
DATE: Mon Dec 14 13:52:58 2015	REV: 1.0
SHEET NUMBER: 10 OF 17	
DOCUMENT NUMBER: 105_C985xx_00B	
NOTES: NOTE	

TITLE:
BAFFIN GL Pro 4x32 G5 TYPE A

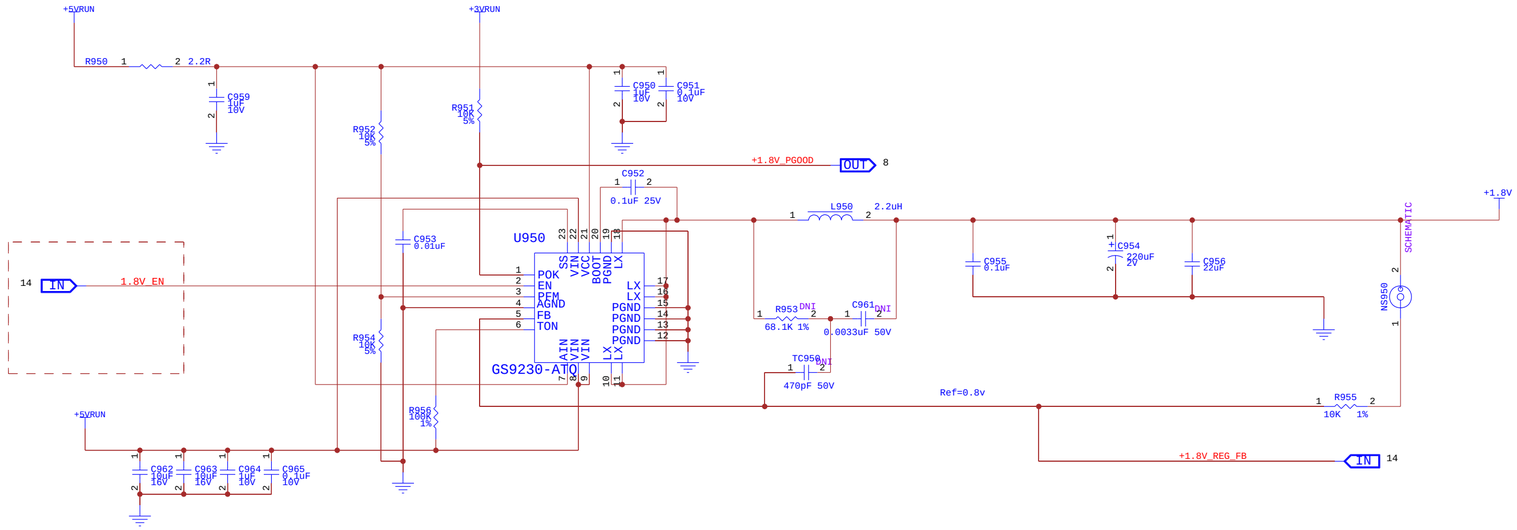


AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC. © 2015 Advanced Micro Devices This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representations or warranties of any kind regarding this schematic and design, including, not limited to, any implied warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information included herein.
SHEET: MVDD	
DATE: Mon Dec 14 13:52:58 2015 REV: 1.0	
SHEET NUMBER: 11 OF 17	TITLE: BAFFIN GL Pro 4x32 G5 TYPE A
DOCUMENT NUMBER: 105_C985xx_00B	
NOTES: NOTE	



AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	
SHEET: 0.8V REG	
DATE: Mon Dec 14 13:52:59 2015	REV: 1.0
SHEET NUMBER: 12 OF 17	
DOCUMENT NUMBER: 105_C985xx_00B	
NOTES: NOTE	

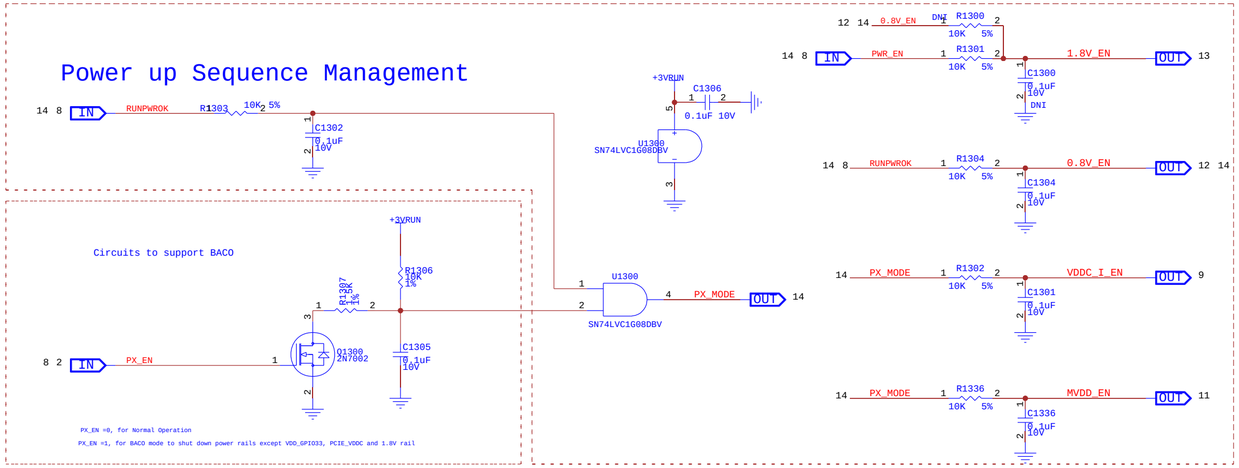
TITLE:
BAFFIN GL Pro 4x32 G5 TYPE A



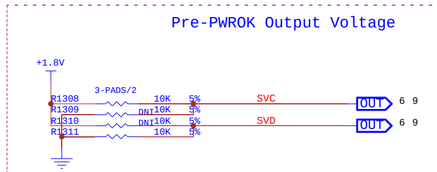
AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	
SHEET: 1.8V REG	
DATE: Mon Dec 14 13:52:59 2015	REV: 1.0
SHEET NUMBER: 13 OF 17	
DOCUMENT NUMBER: 105_C985xx_00B	
NOTES: NOTE	

TITLE:
BAFFIN GL Pro 4x32 G5 TYPE A

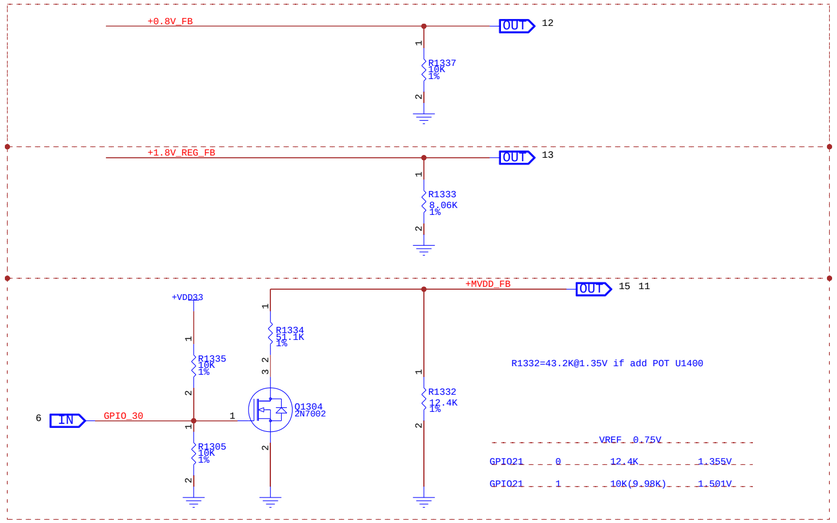
Power up Sequence Management



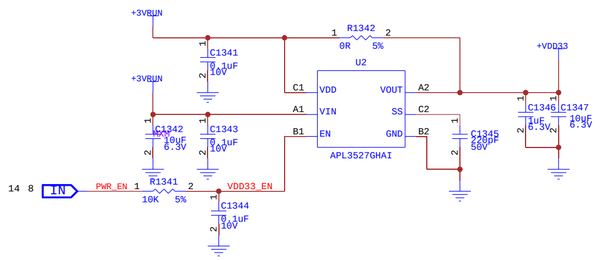
Pre-PWROK Output Voltage



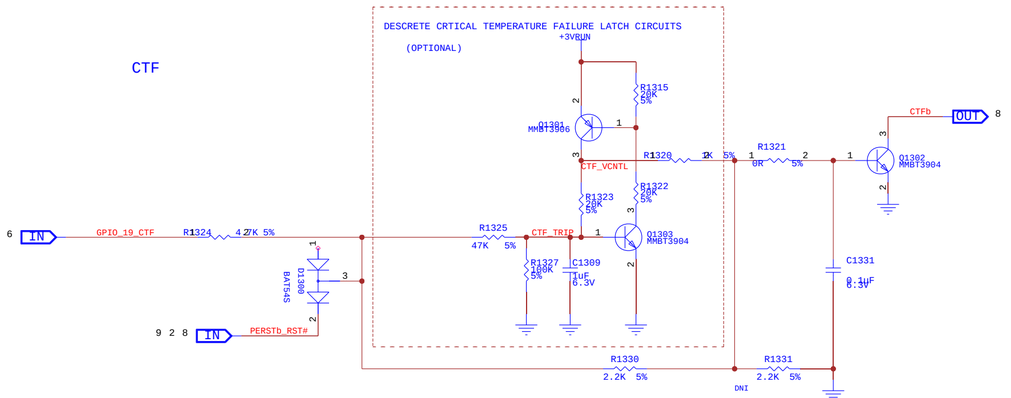
SVC	SVD	V
0	0	1.1
0	1	1.0
1	0	0.9
1	1	0.8



3.3v switch



CTF



AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: BACO & CTF & PWR Managerment

DATE: Mon Dec 14 13:52:59 2015 REV: 1.0

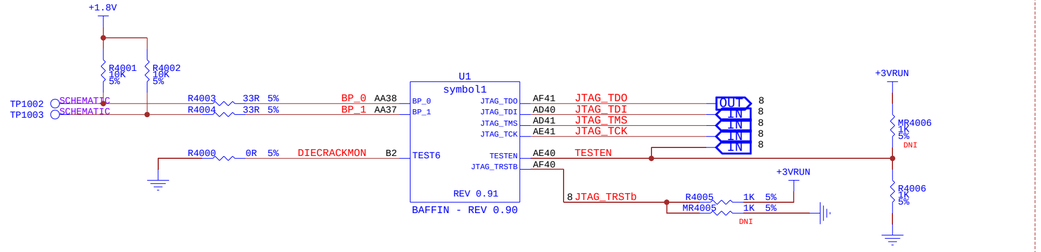
SHEET NUMBER: 14 OF 17

DOCUMENT NUMBER: 105_C985xx_00B

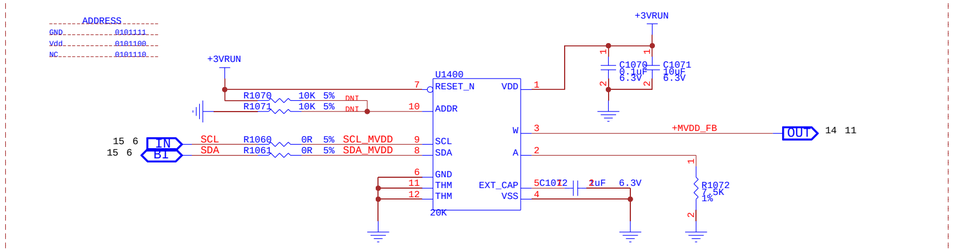
NOTES: NOTE

TITLE: BAFFIN GL Pro 4x32 G5 TYPE A

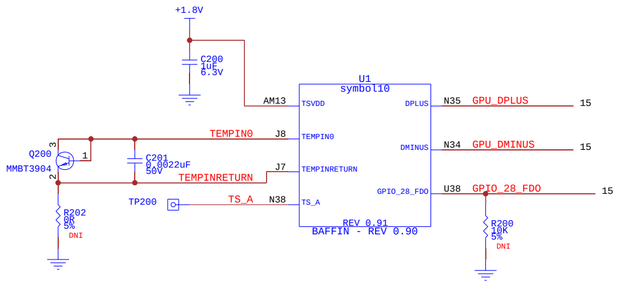
JTAG



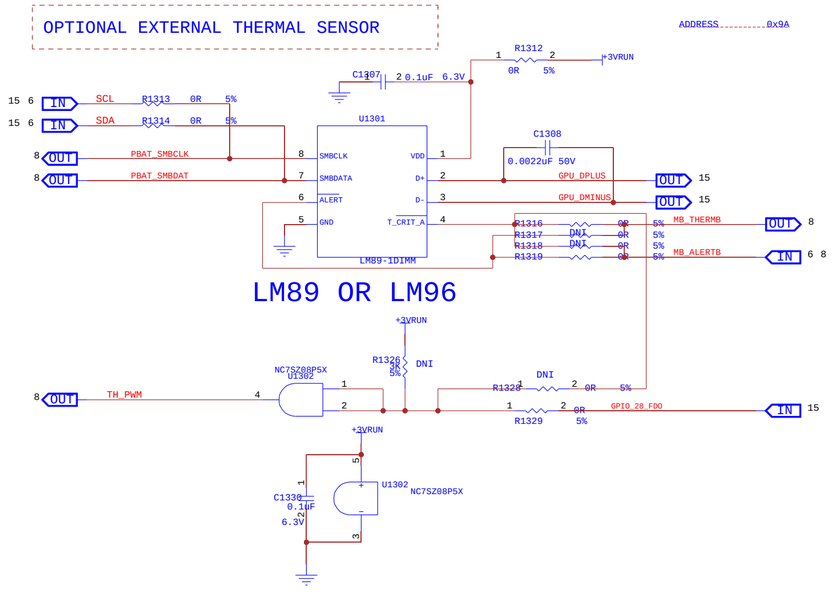
DIGITAL POT



THERMAL



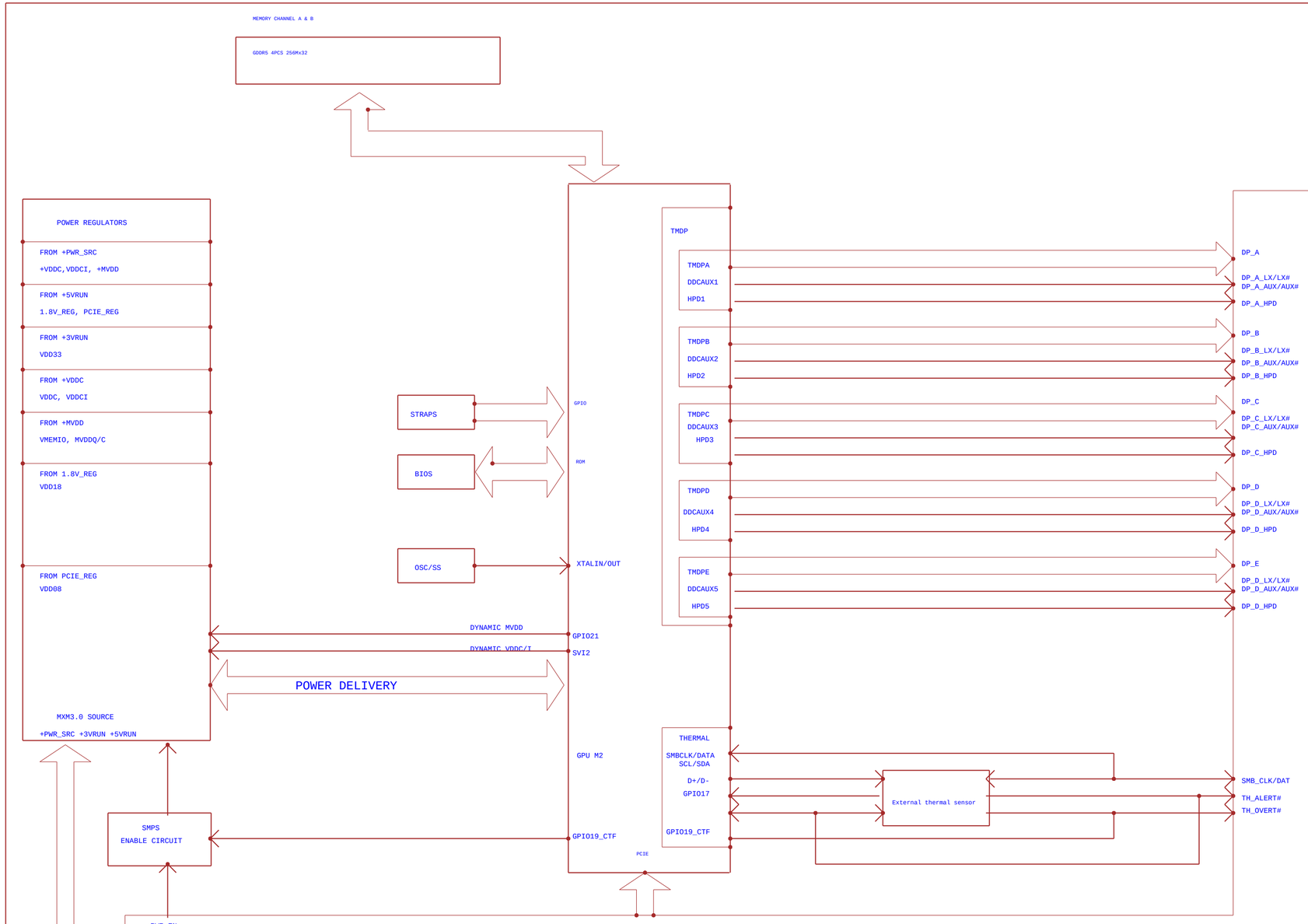
OPTIONAL EXTERNAL THERMAL SENSOR



AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	
SHEET: DEBUG & THERMAL	
DATE: Mon Dec 14 13:52:57 2015	REV: 1.0
SHEET NUMBER: 15 OF 17	
DOCUMENT NUMBER: 105_C985xx_00B	
NOTES: NOTE	

TITLE:

BAFFIN GL Pro 4x32 G5 TYPE A



MxM3.1 TYPE A
BAFFIN GDDR5

AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: BLOCK DIAGRAM

DATE: Mon Dec 14 13:36:59 2015 REV: 1.0

SHEET NUMBER: 16 OF 17

DOCUMENT NUMBER: 105_C985xx_00B

NOTES: NOTE

TITLE:
BAFFIN GL Pro 4x32 G5 TYPE A

AMD

TITLE: BAFFIN GL Pro 4x32 G5 TYPE A

DOCUMENT NUMBER: 105_C985xx_00B

DATE: Mon Dec 07 17:12:09 2015

SHEET NUMBER: 17 OF 17

REV: 1.0

REVISION HISTORY

ENGINEER: Peak

NOTES: NOTE

AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SCH Rev	PCB Rev	Date	REVISION DESCRIPTON
0	00A	08/23/2015	INITIAL DESIGN
1	00B	11/25/2015	1.ADD DDCVGADATA/CLK 2.MVDD SWITCH GPIO CHANGED TO GPIO30 3.ADD GPIO21 PULL UP/DOWN 4 ADD 3.3V SWITCH 5 REMAPPING DPA, DPB AND DPC, DPD TO SUPPORT DL_DVI 6 JTAG FOLLOW MXM3.1 SPEC