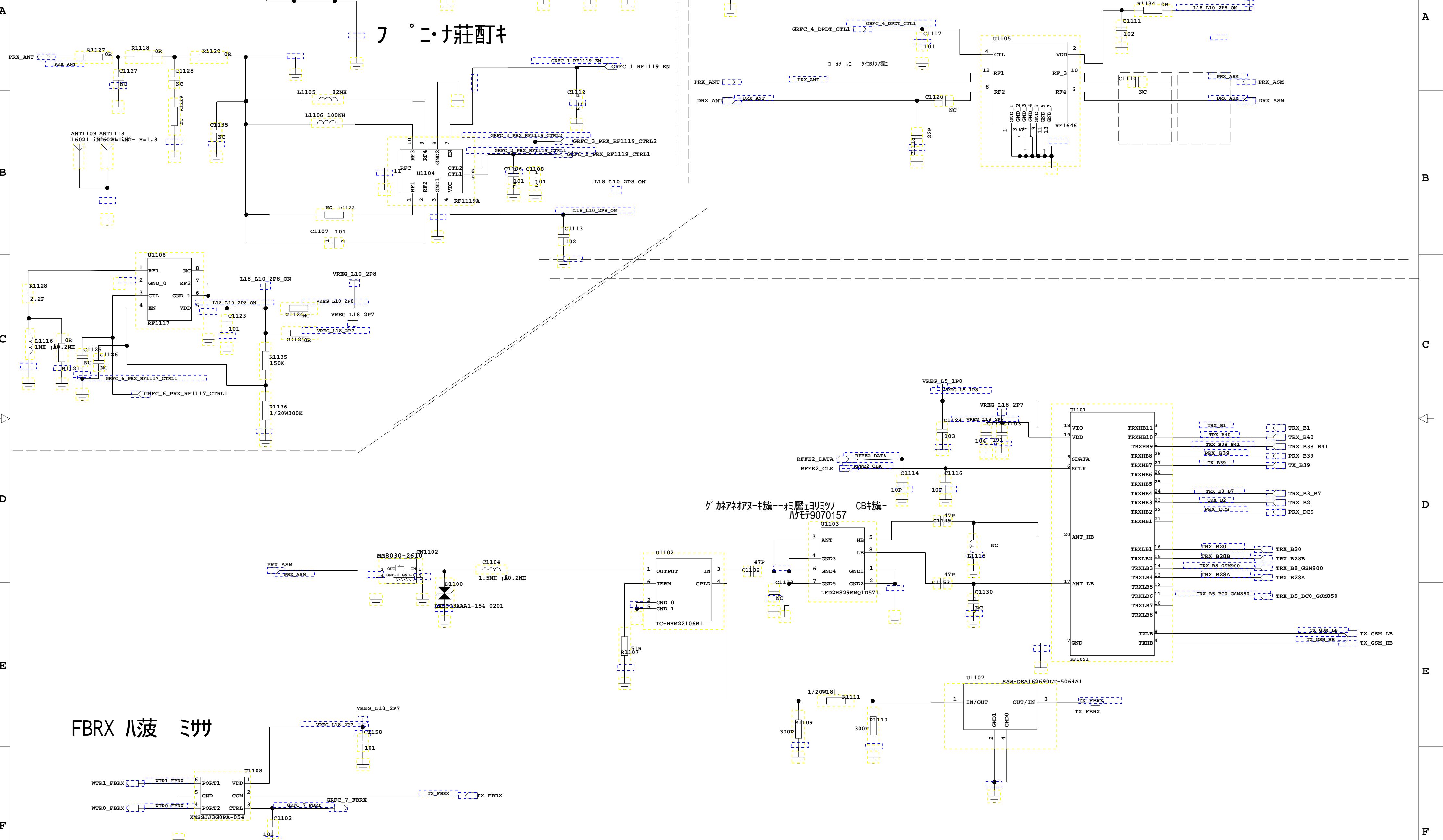
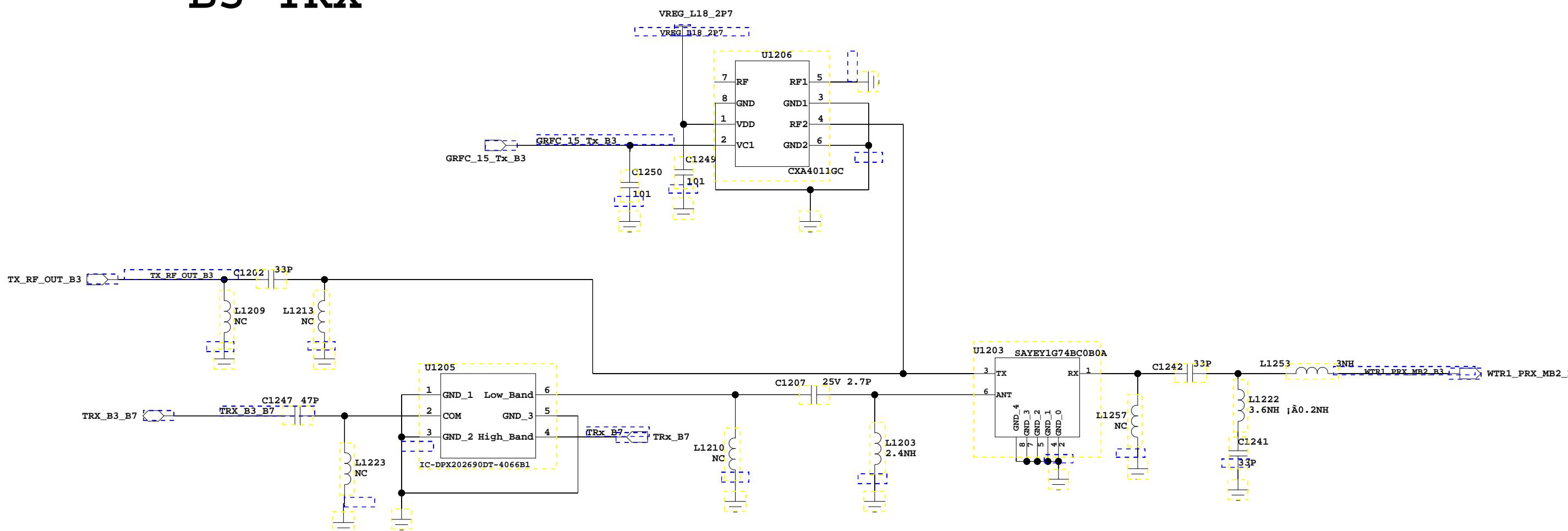




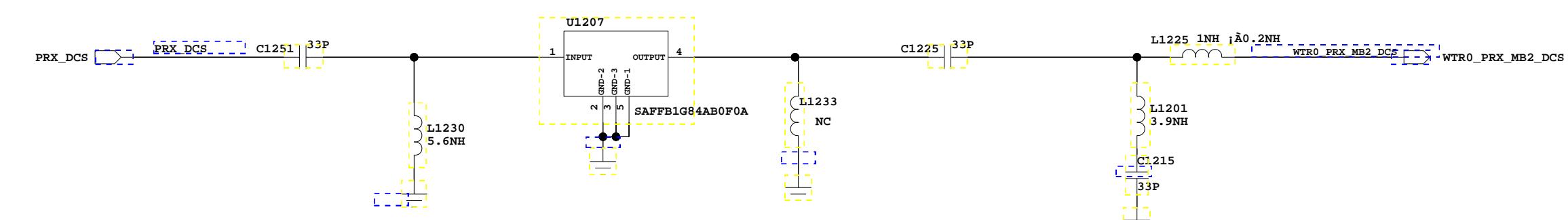
ノ 二・ナ莊酌ヰ



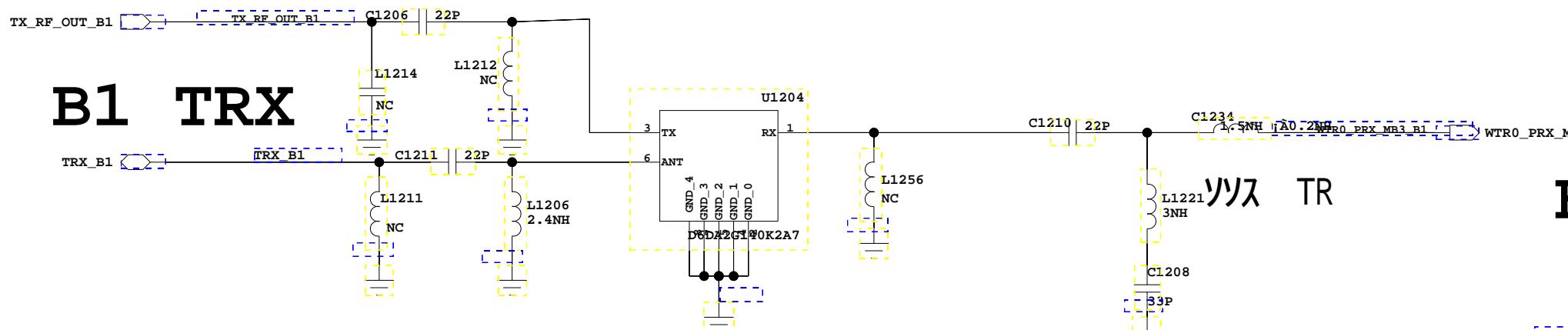
B3 TRX



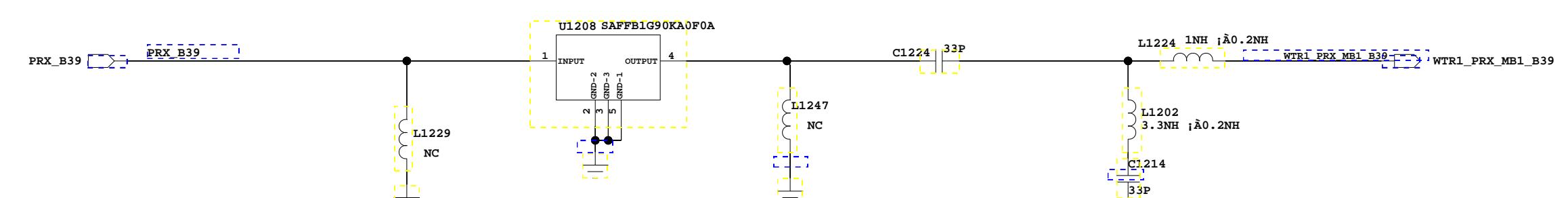
DCS RX SAW



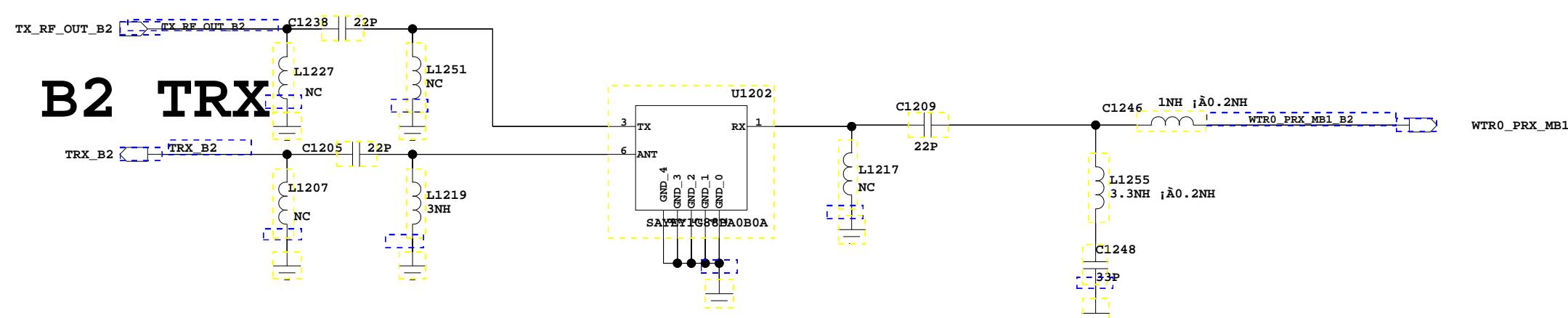
B1 TRX



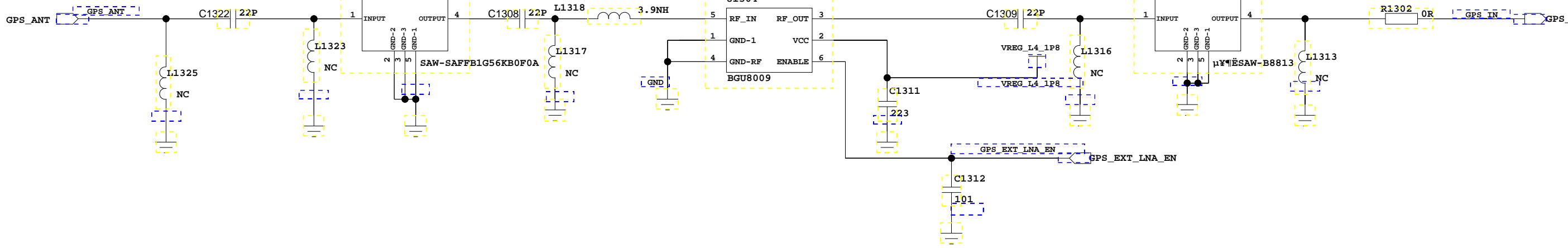
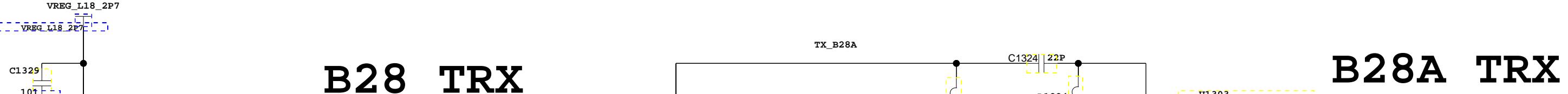
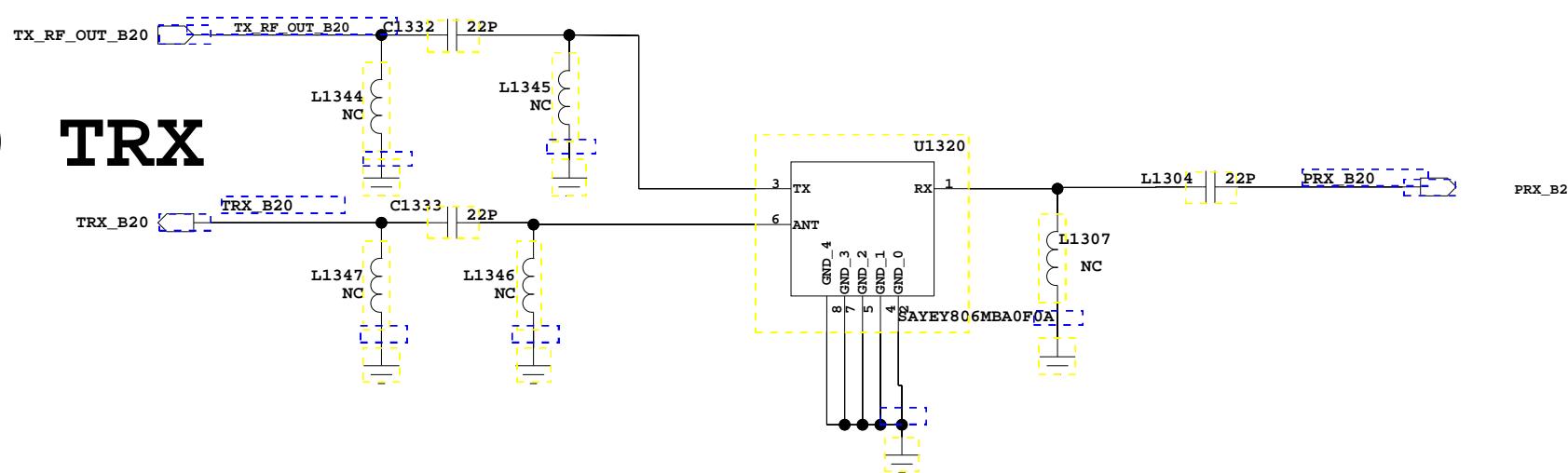
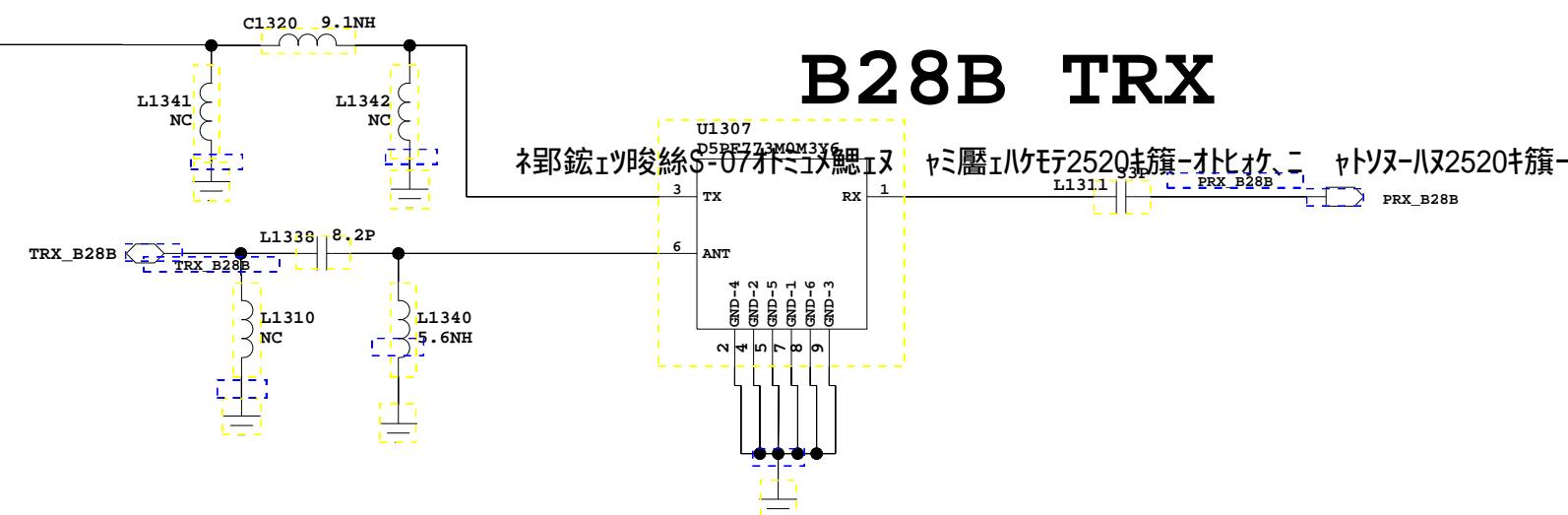
B39 RX SAW



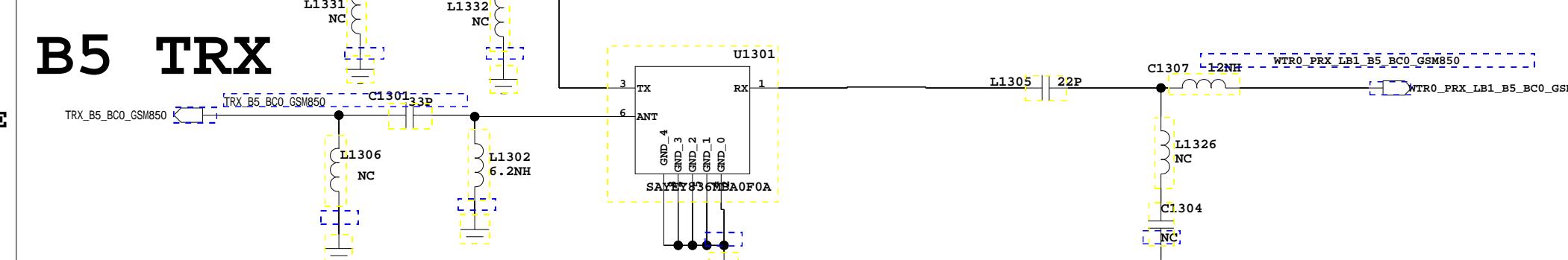
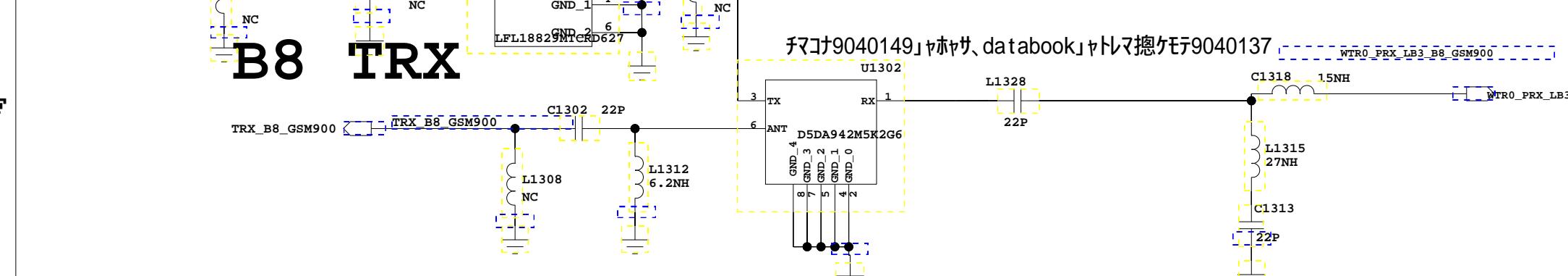
B2 TRX



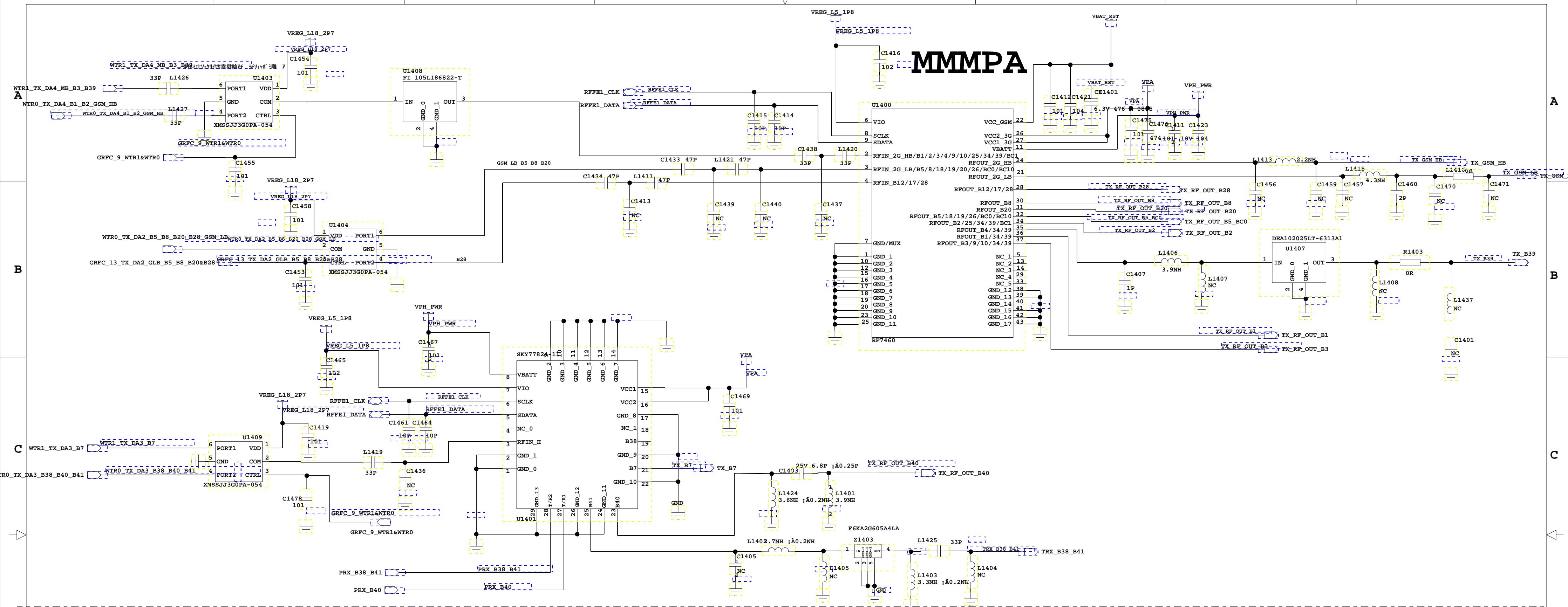
GPS

A**A****B****B****B28 TRX****B28A TRX****C****C****D****D****B28B TRX**

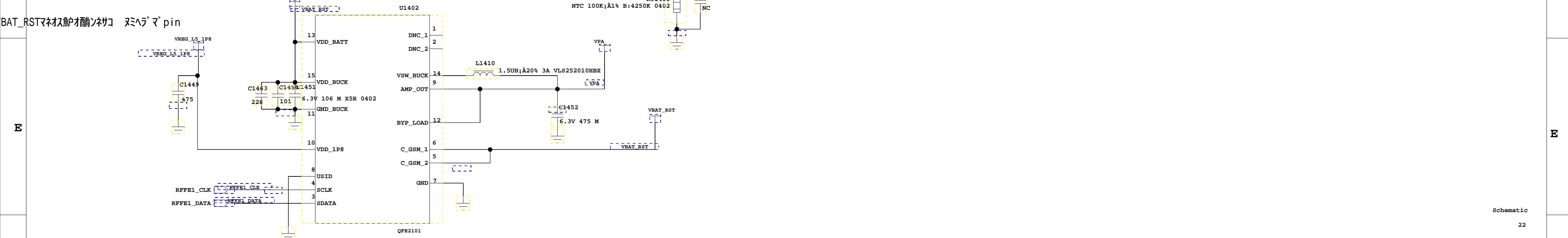
メモリB13に接続され、GPS用として機能する。

E**E****F****F**

MMMPA

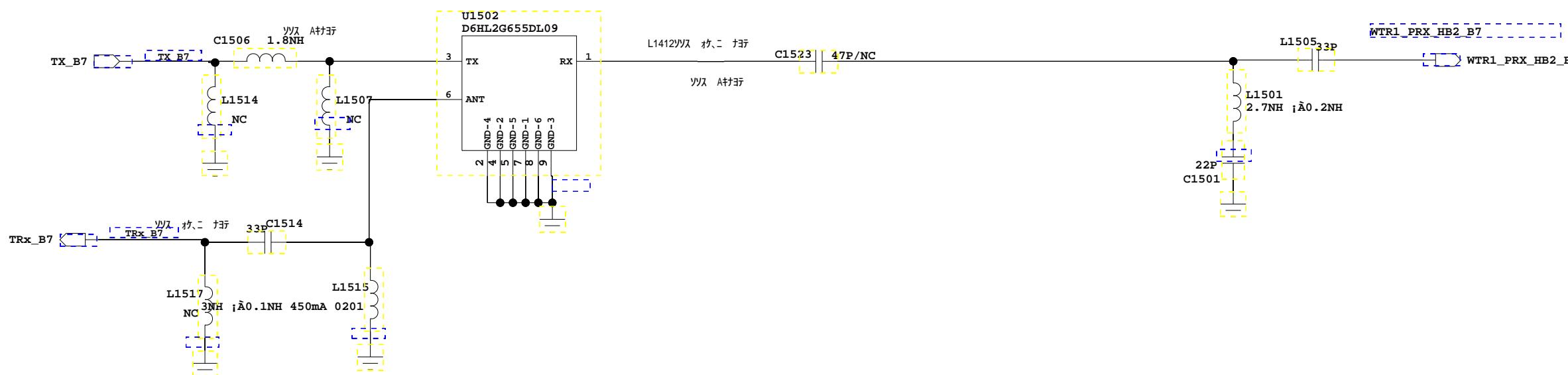


QFE2101

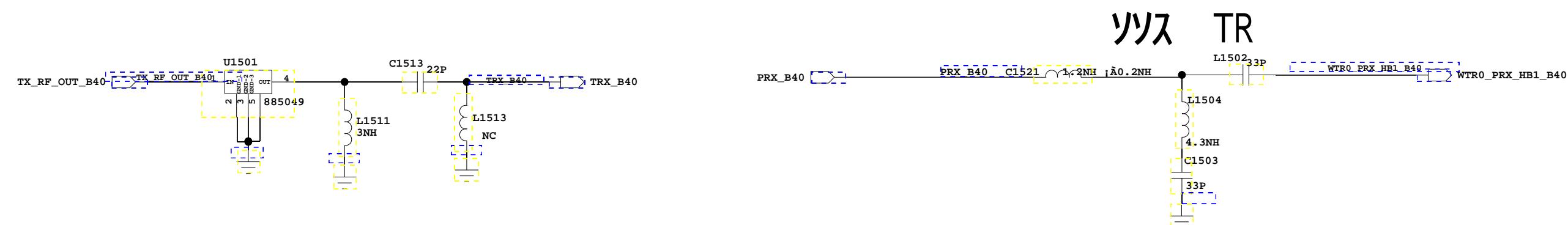


A

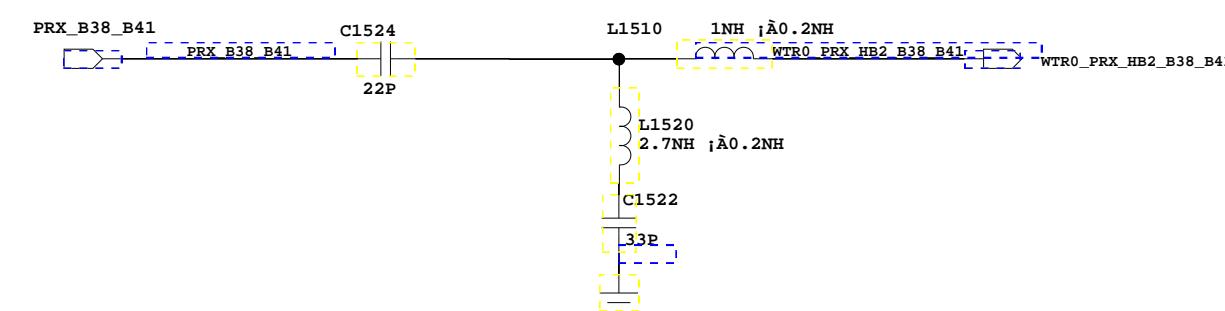
HB_DUP_PRX



B



C



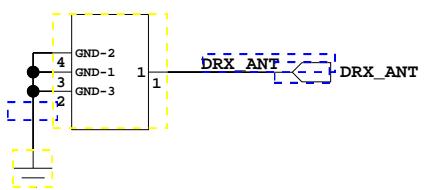
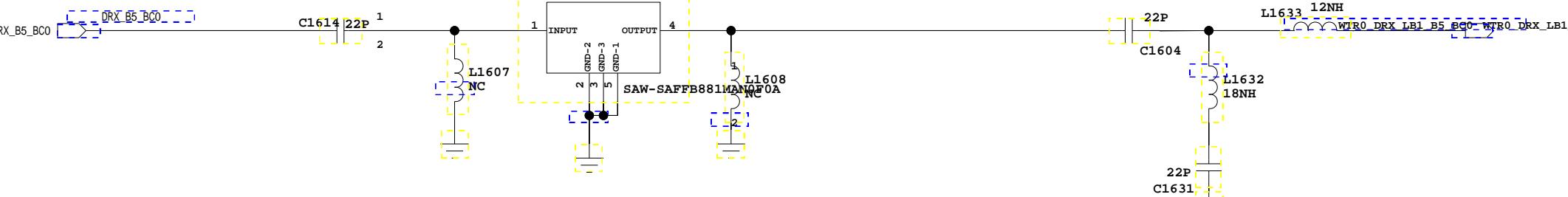
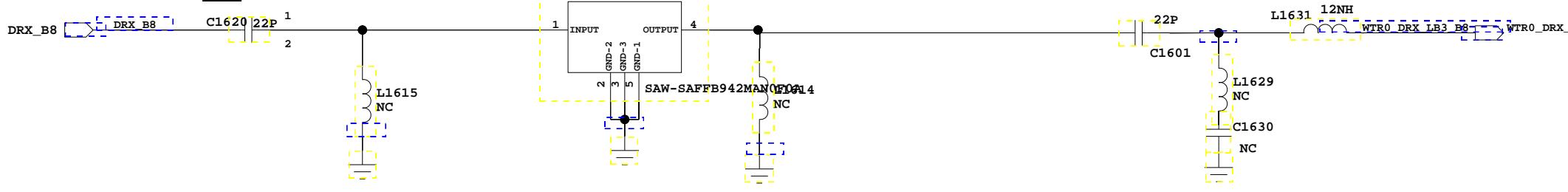
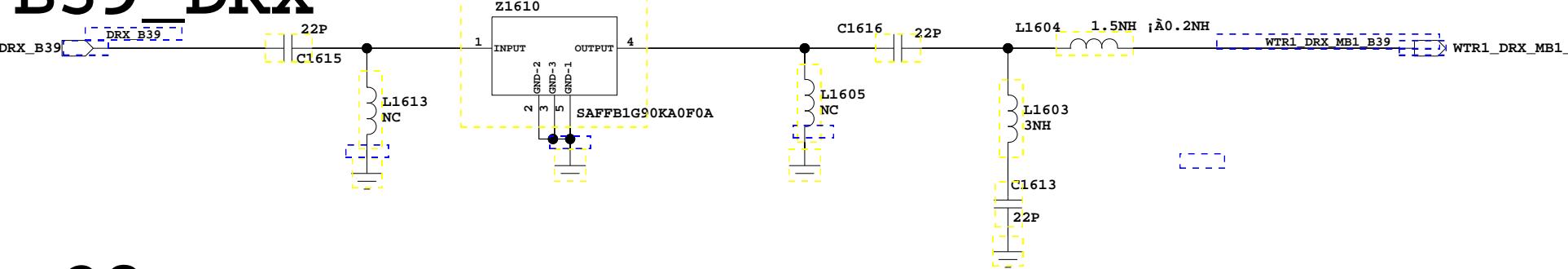
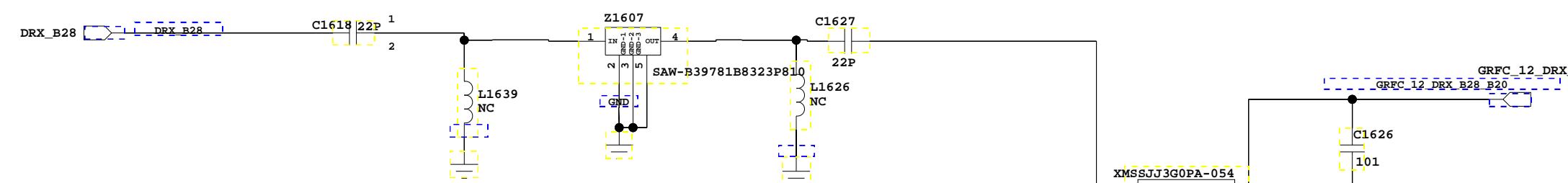
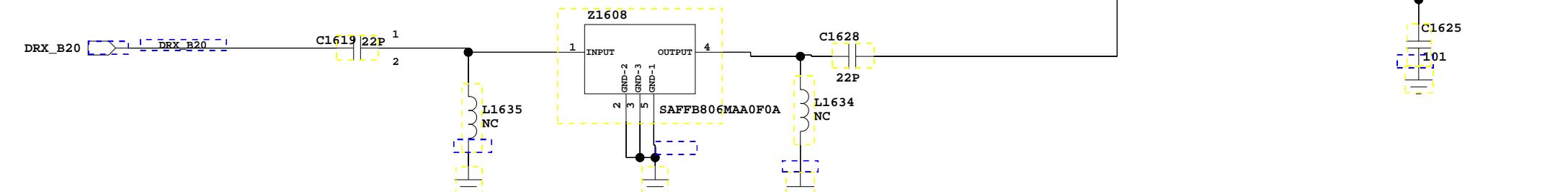
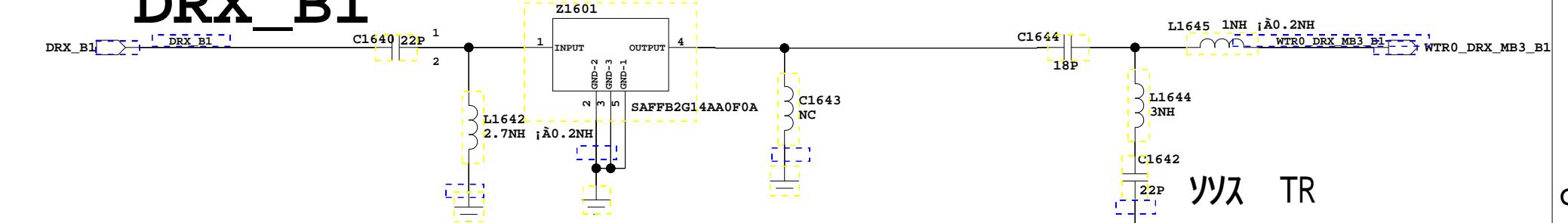
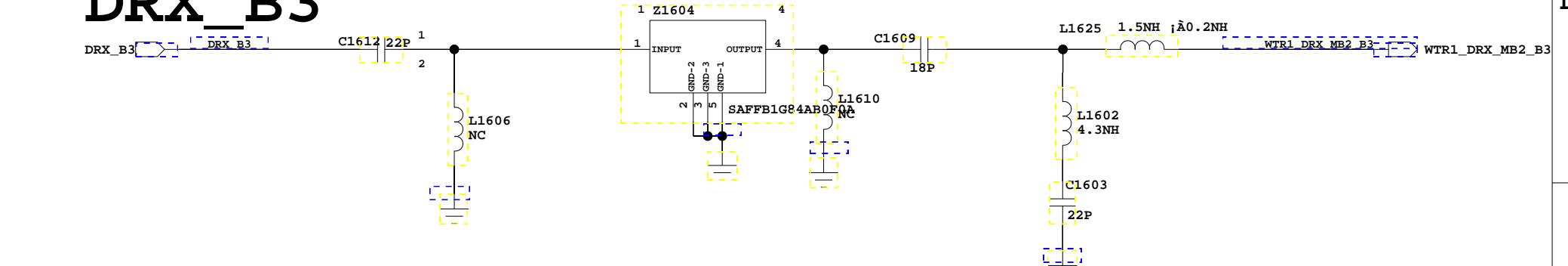
D

E

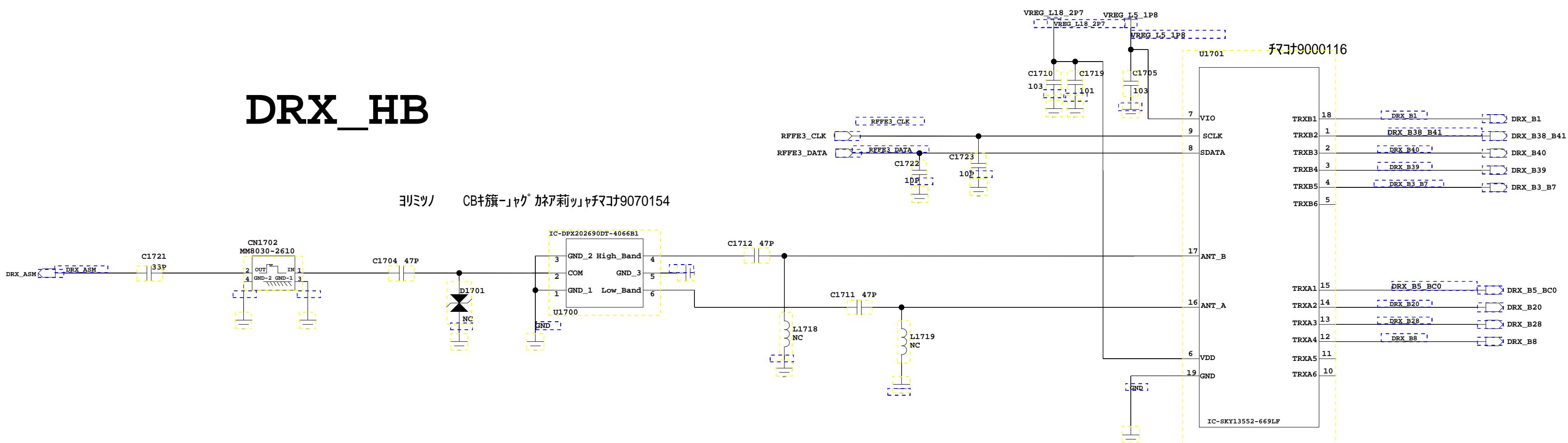
F

DRX_MLB

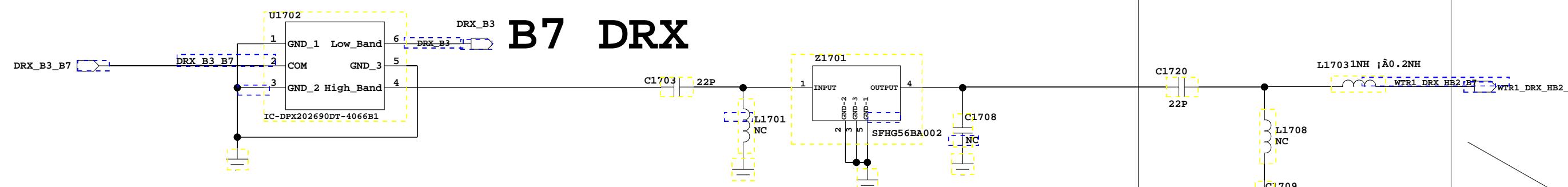
20566-001E-01 CN1602

**B5/BC0 DRX****DRX_B8****B39_DRX****B28_DRX****B20_DRX****DRX_B1****DRX_B3**

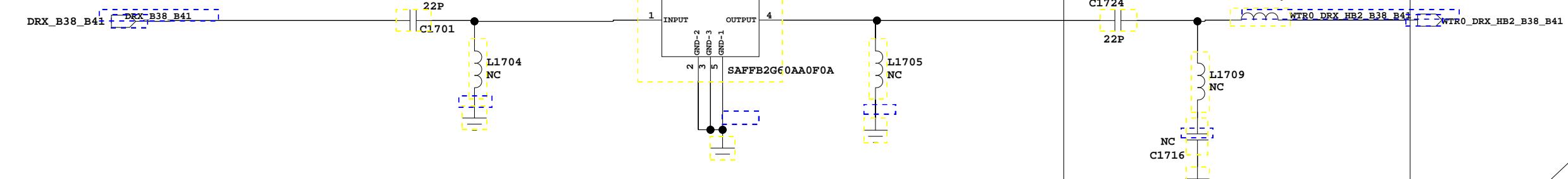
DRX_HB



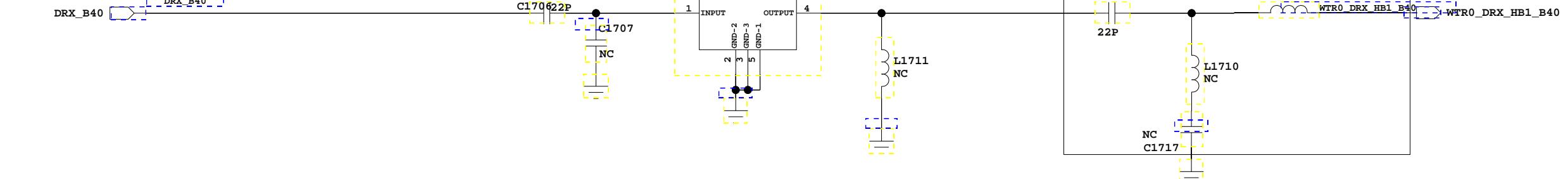
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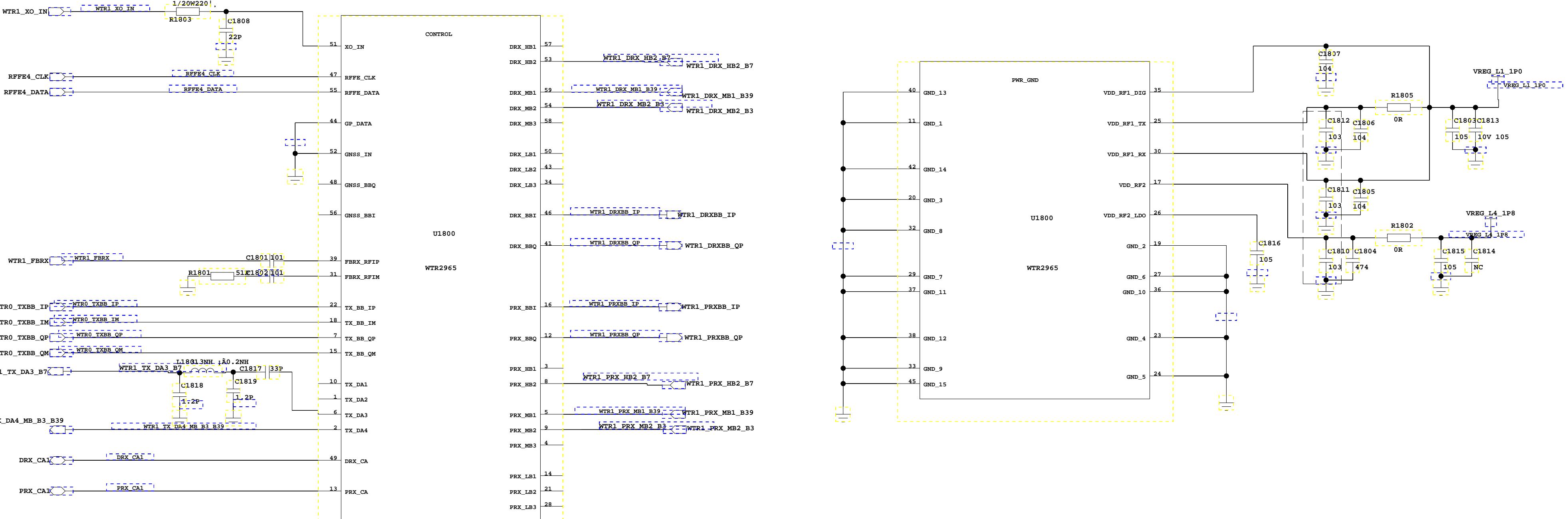
B41 DRX



B40 DRX

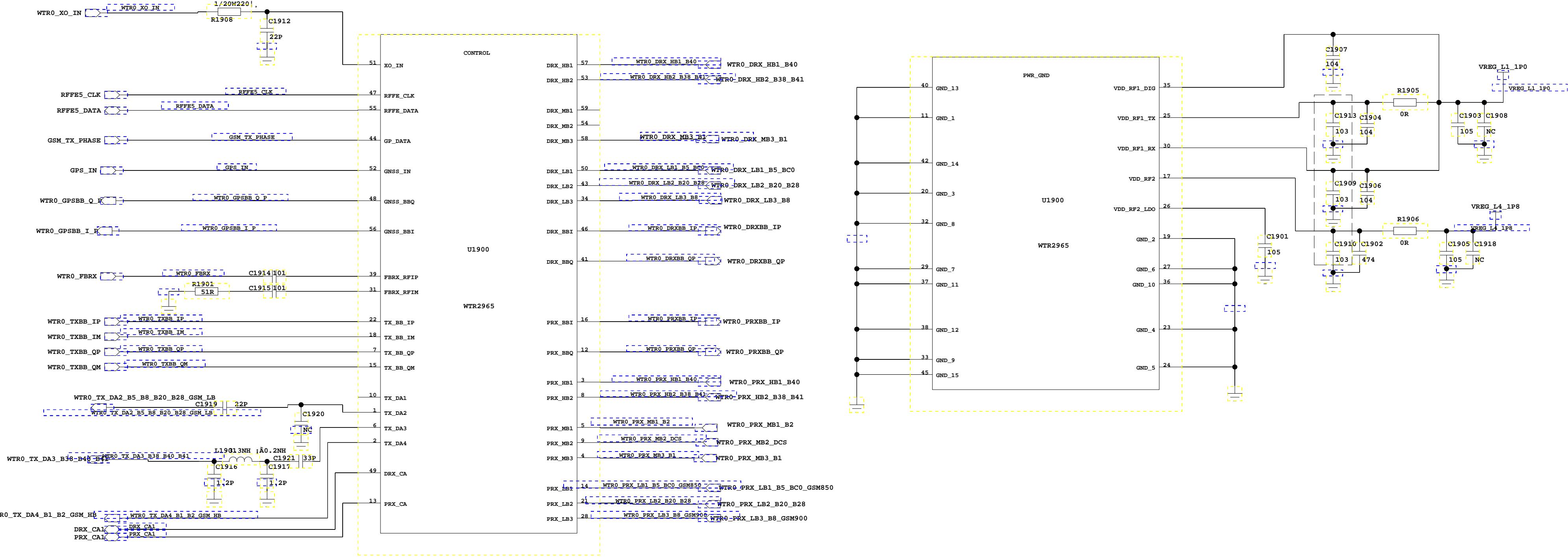


ソス TR



Design requirements for PRX_CA interconnections and DRX_CA interconnections: **B3+B7 CA, B3±Ø×SMB2£¬B7±Ø×SHB2**

- Total trace length between WTR0 and WTR1 pin < 15 mm
- Isolation between PRX_CA and DRX_CA pins > 50 dB
- Keep trace impedance 40 ohms with GND above and below these traces

A**A****A****B****B****C****C****D****D**

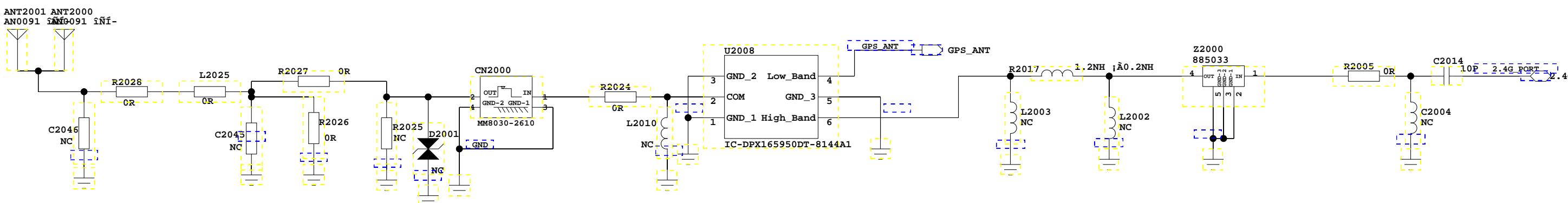
Design requirements for PRX_CA interconnections and DRX_CA interconnections:

- Total trace length between WTR0 and WTR1 pin < 15 mm
- Isolation between PRX_CA and DRX_CA pins > 50 dB
- Keep trace impedance 40 ohms with GND above and below these traces

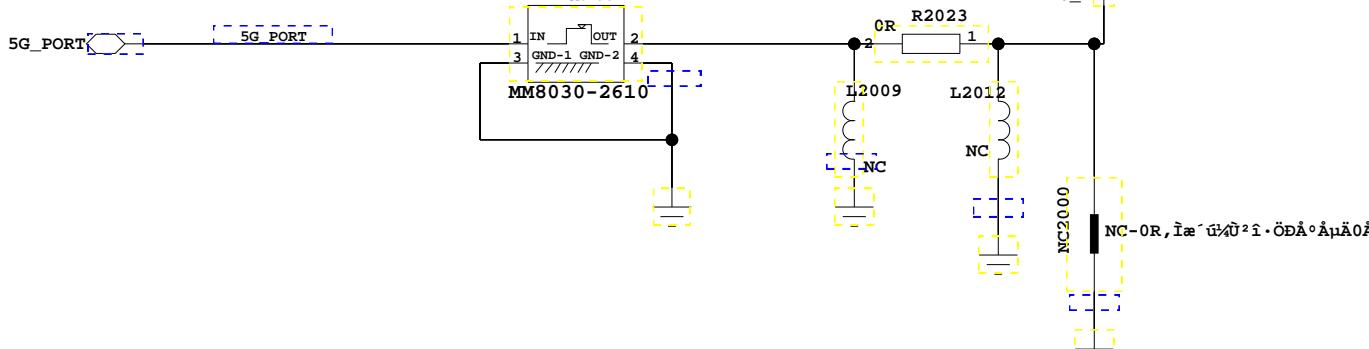
F**F**

2.4G WIFI+GPS フ

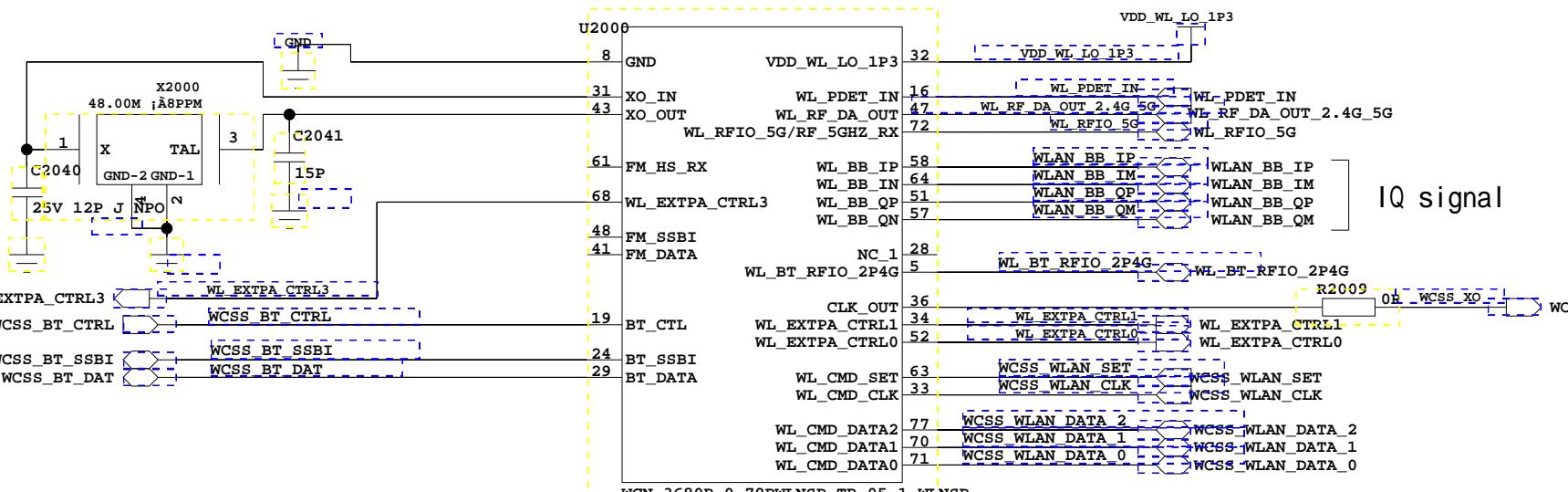
WIFI+BT_1



5G ノ

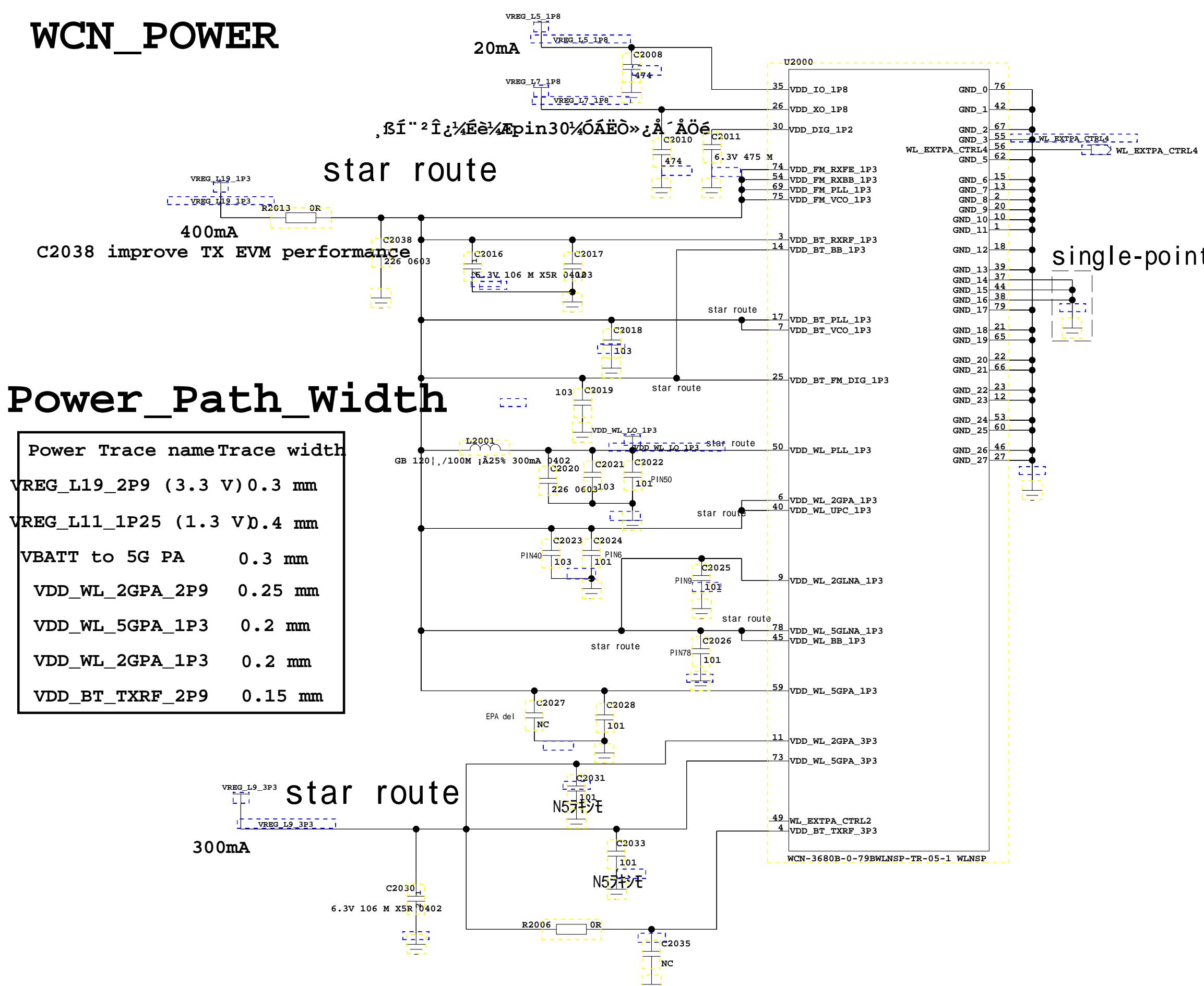


WIFI_HOST_PORT / TCXO



WCSS_X0 も 5020メサム ャモ °ヘイラ壽ツイホソニ需ニイサメ

WCN_POWER



Power Path Width

Power Trace name	Trace width
VREG_L19_2P9 (3.3 V)	0.3 mm
VREG_L11_1P25 (1.3 V)	0.4 mm
VBATT to 5G PA	0.3 mm
VDD_WL_2GPA_2P9	0.25 mm
VDD_WL_5GPA_1P3	0.2 mm
VDD_WL_2GPA_1P3	0.2 mm
VDD_BT_TXRF_2P9	0.15 mm

WIFI+BT_2

This diagram illustrates the internal circuitry of a 5G communication module. It includes:

- Power Management:** VPH_PWR and VPH_PWR lines connect to various components like U2101, U4202, and the SKY85702-11 module.
- Signal Processing:** A CPLD (U2101) is connected to a 5G_PORT and a 5G_CPLD. Other logic components include R2110, R2106, R2102, and D2100.
- RF Components:** The SAW2110 (LFL155G42TC7C282) is used for filtering. The SKY85702-11 module handles RF signals, with TX and RX paths. The PA_EN_SW_CTL section manages the power amplifier.
- Control and Interfacing:** WL_EXTPA_CTRL0, WL_EXTPA_CTRL1, and WL_RFIO_5G provide external control and interface connections.

R9p_ÔøÅô0Å•

For 2nd Gen FEM 5G

R9p_Ô¤Áô0Å•

R9pÖ»' ®ÁaÒ» , ö0

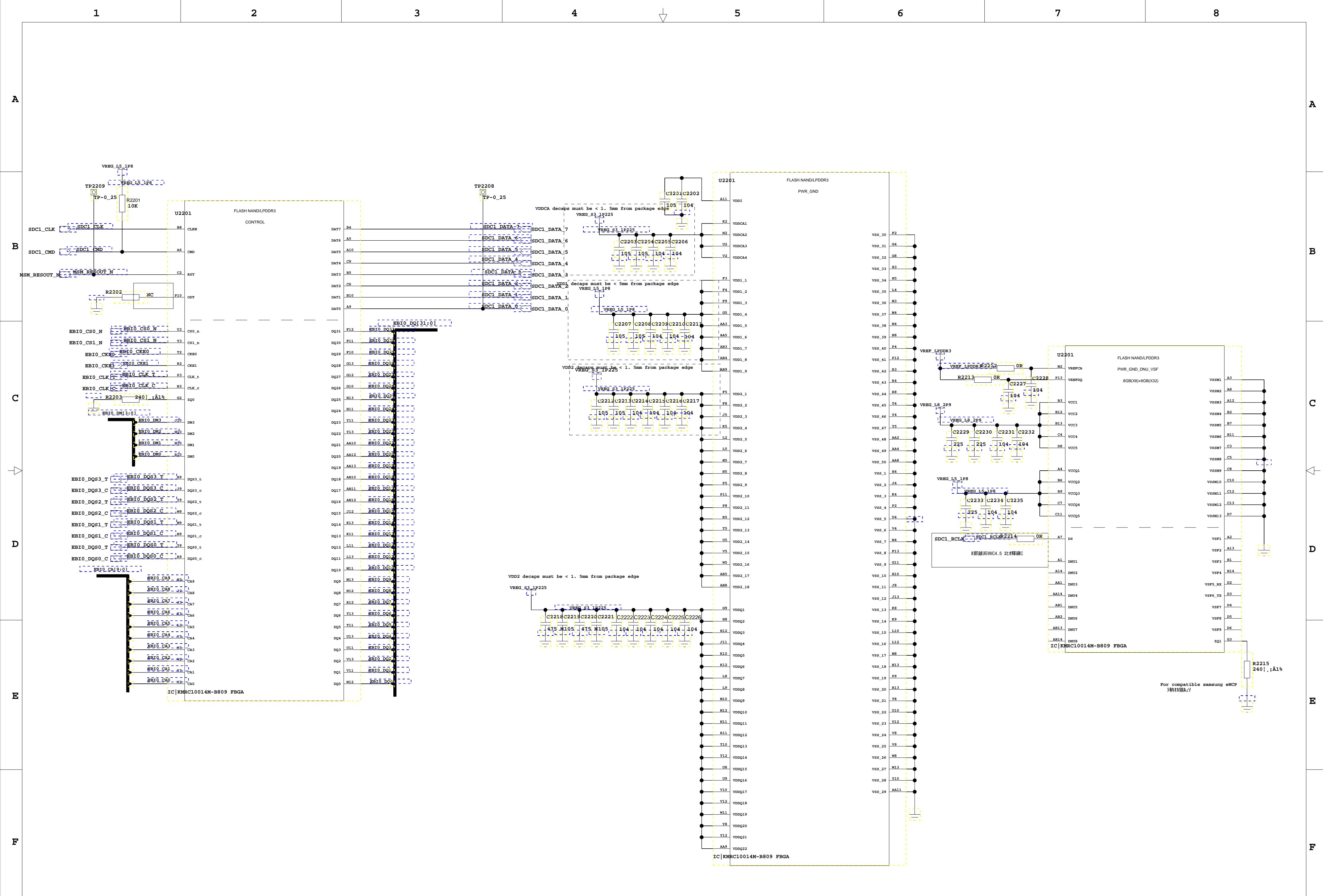
This diagram shows the internal circuitry of the SKY85312-11 IC. It includes the following components and connections:

- Power Supply:** VPH_PWR is connected to various nodes, including C2115, C2114, C2116, and L2102 (6.8NH).
- RF Section:** The U2103 package contains a LNAEN2 and PAEN2 section. The PAEN2 section has pins 1, 16, 12, 5, and 14. The LNAEN2 section has pins 2, 9, 10, 4, and 6. The U2103 package also has pins 8, 11, 13, 15, 17, and EPAD.
- Control and Interface:** The IC is connected to several digital and analog control buses:
 - WL_BT_RFIO_2P4G
 - WL_BT_RFIO_2P4G_C2118
 - WL_BT_RFIO_2P4G_C2118[10P]
 - WL_EXTPA_CTRL4
 - WL_EXTPA_CTRL3
 - WL_RF_DA_OUT_2.4G_5G
 - WL_RF_DA_OUT
 - 2.4G_PORT
 - 2.4G_PORT
- Other Components:** R2109, R2105 (2.2K), R2106, and various capacitors (C2119, C2120, C2115, C2114, C2116) are used for biasing and filtering.

L⁴ R9p_Ô¤Áô0Å•

R9p_Ô¤Áô0Å•

5G For 3rd Gen FEM 2.

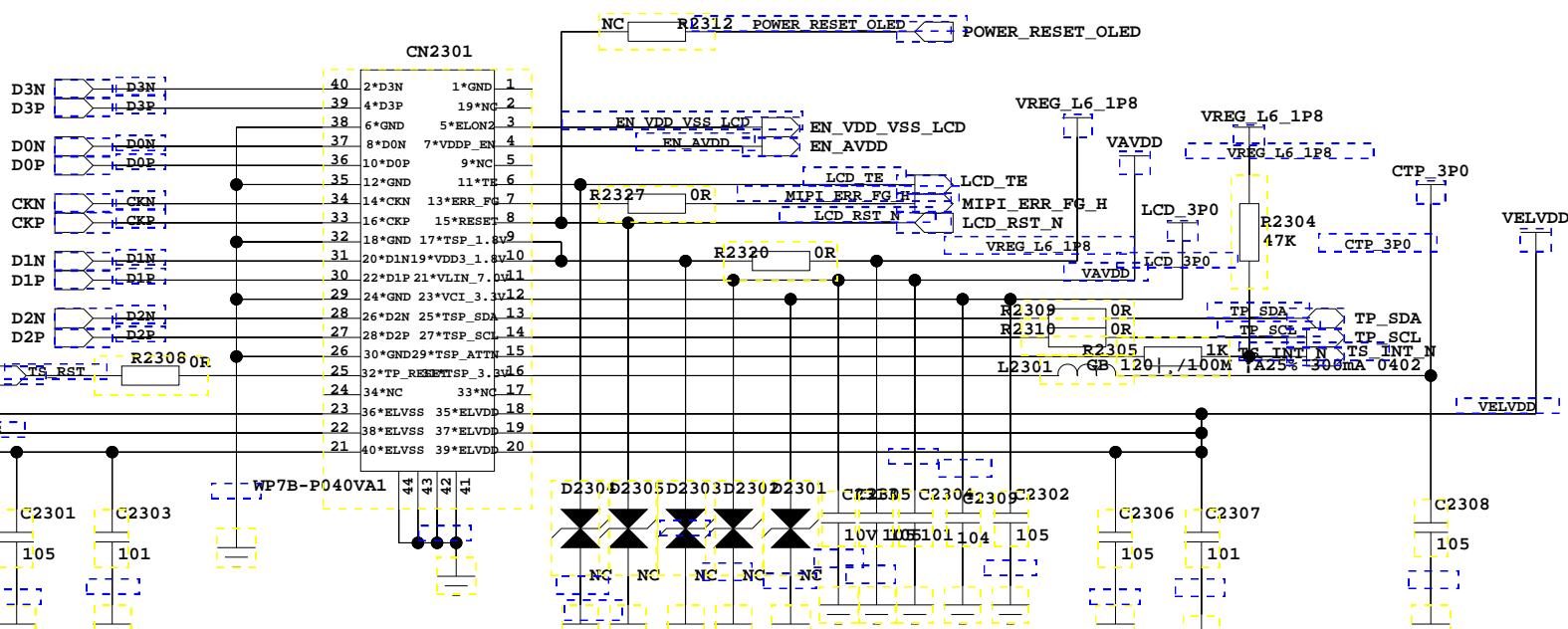


LCM Connector

マツハセニチBTBイ袁キイチヤマナハセニチウ 9730086

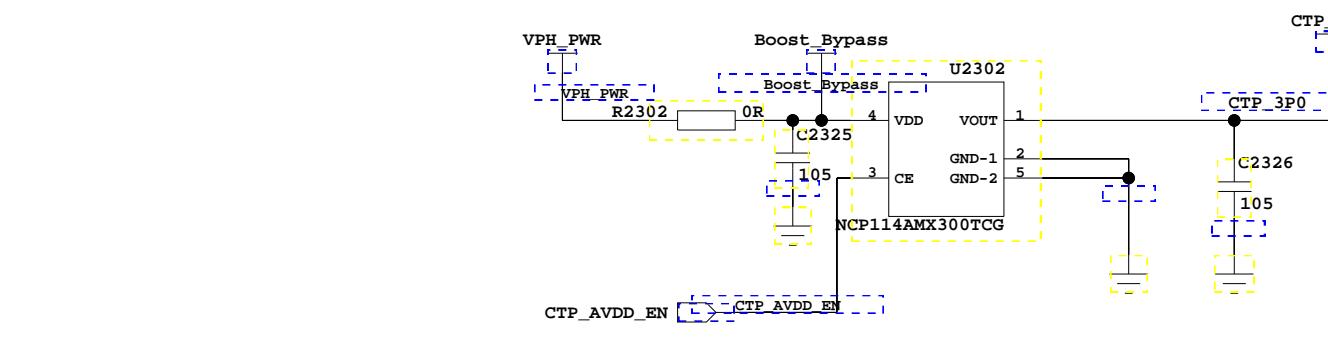
DSI CMF

A

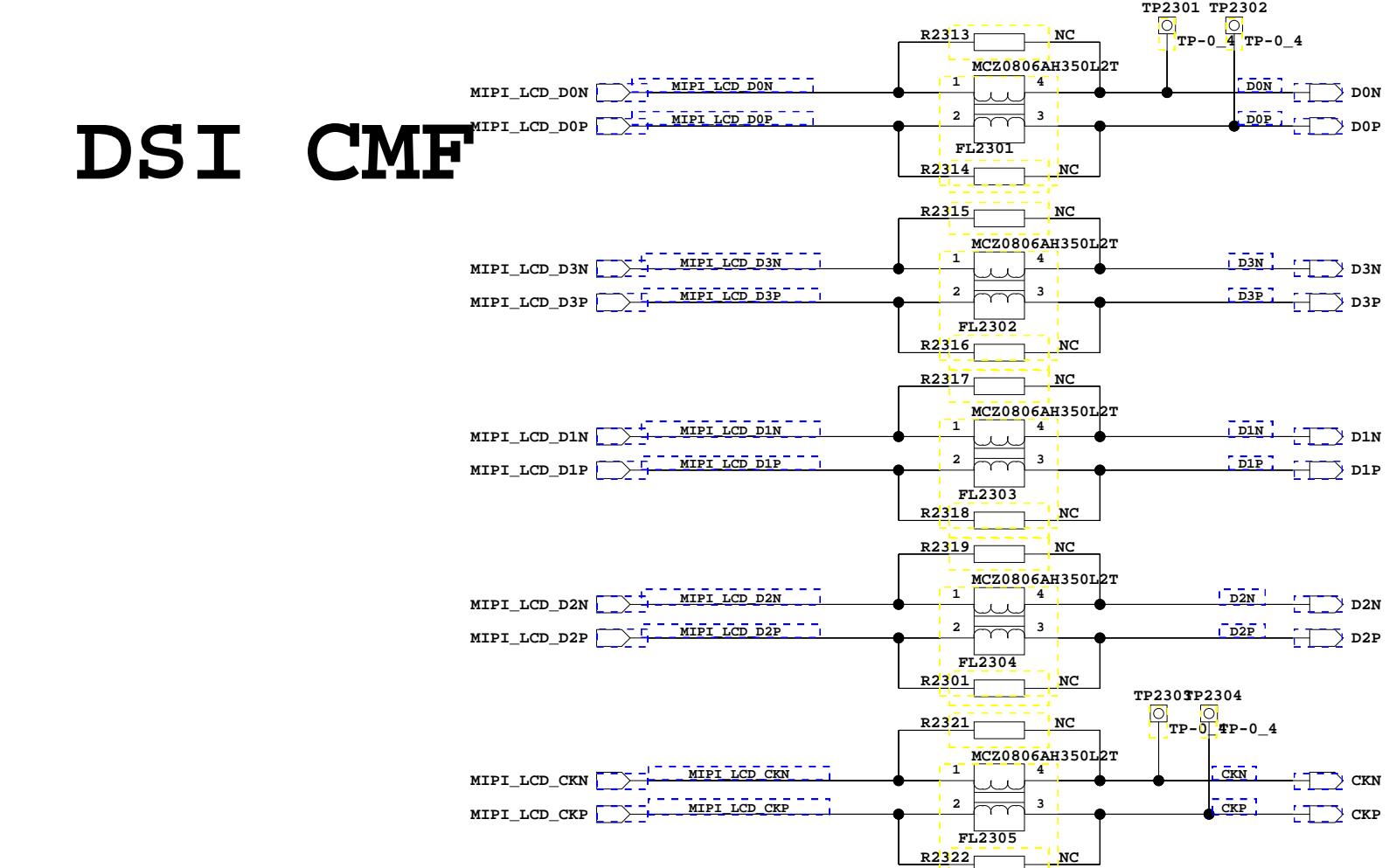
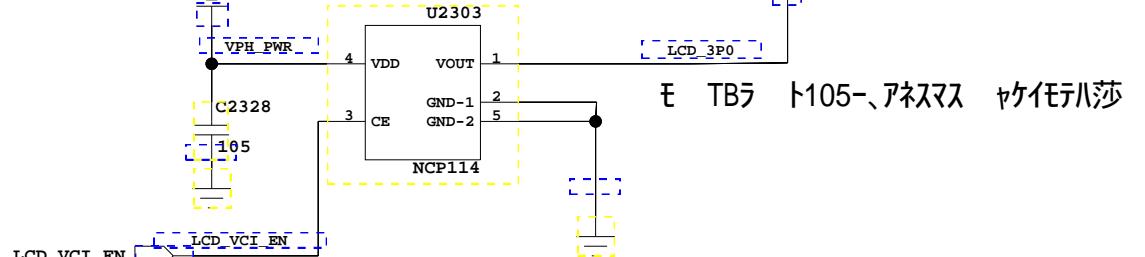


B

CTP AVDD 3.0V

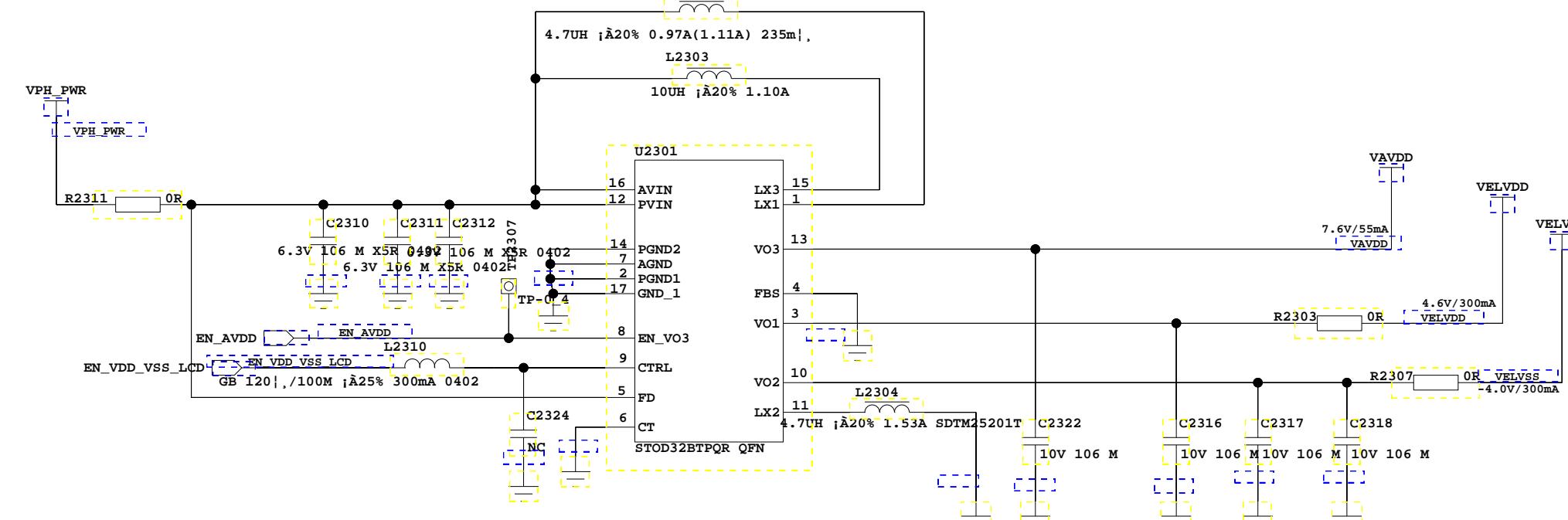


LCD AVDD(3.0V)



OLED Driver

C

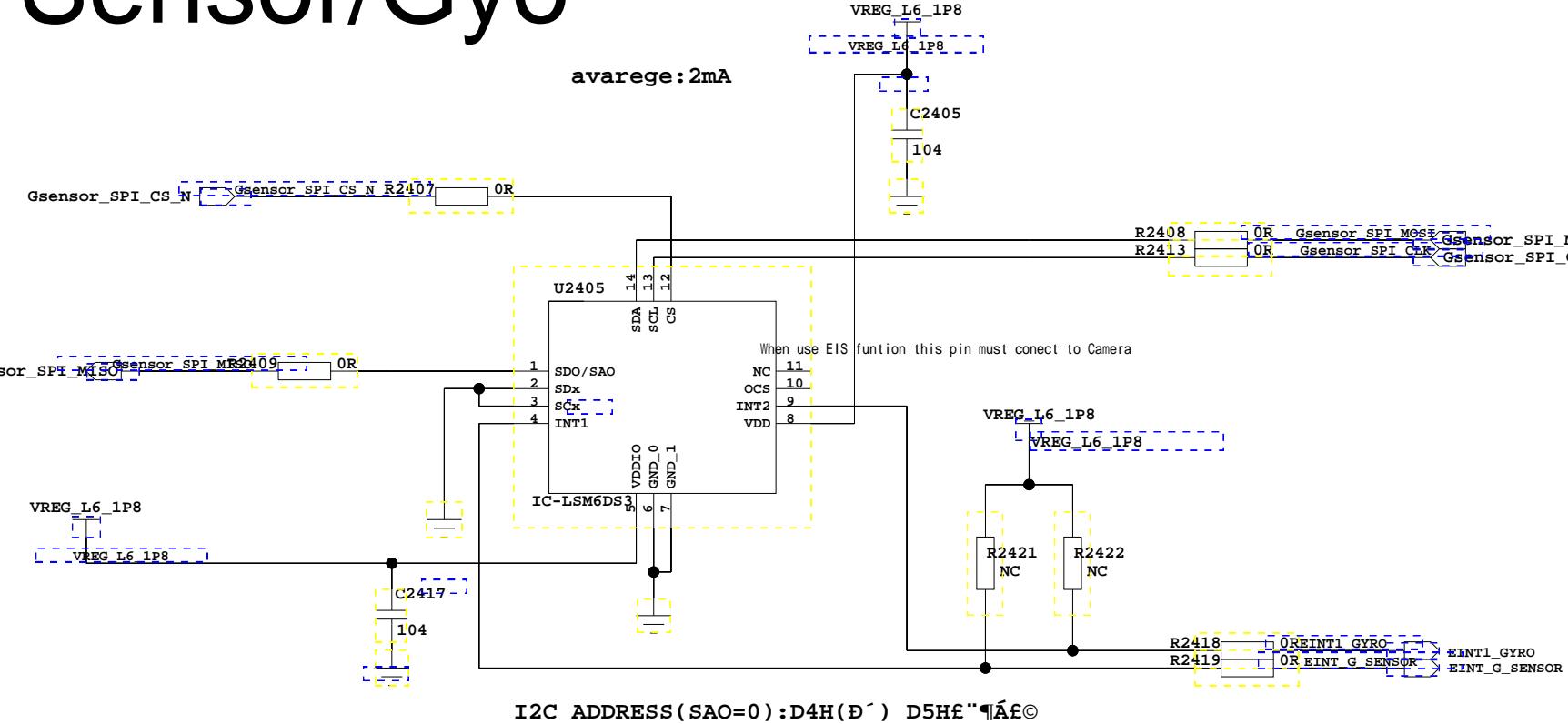


D

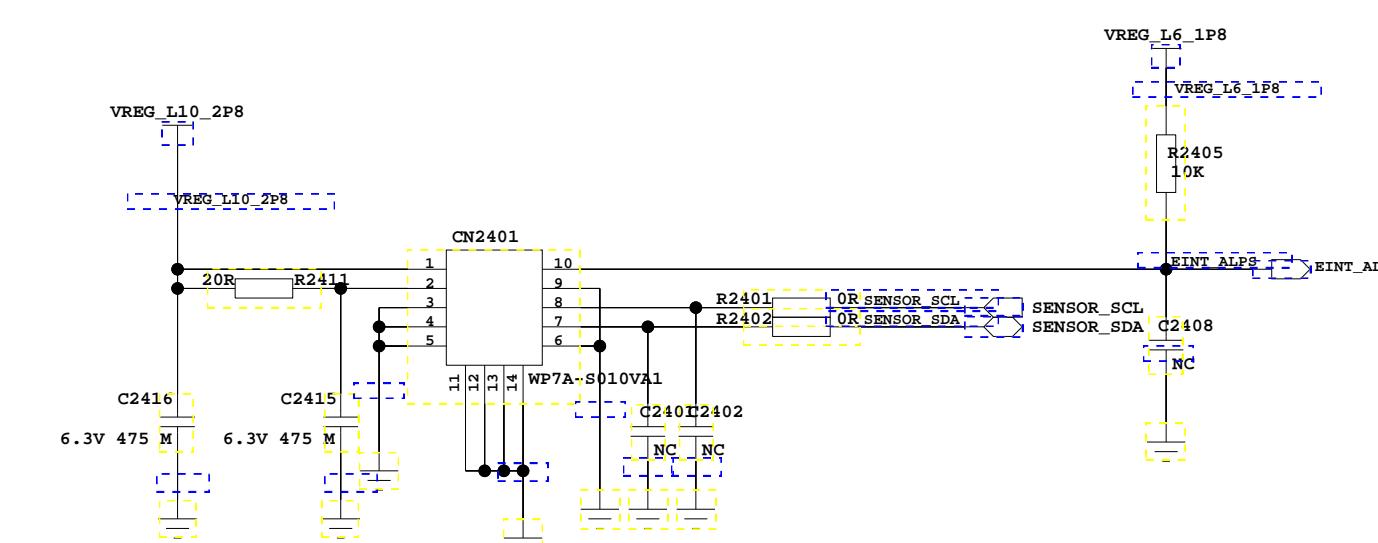
E

F

G-Sensor/Gyo

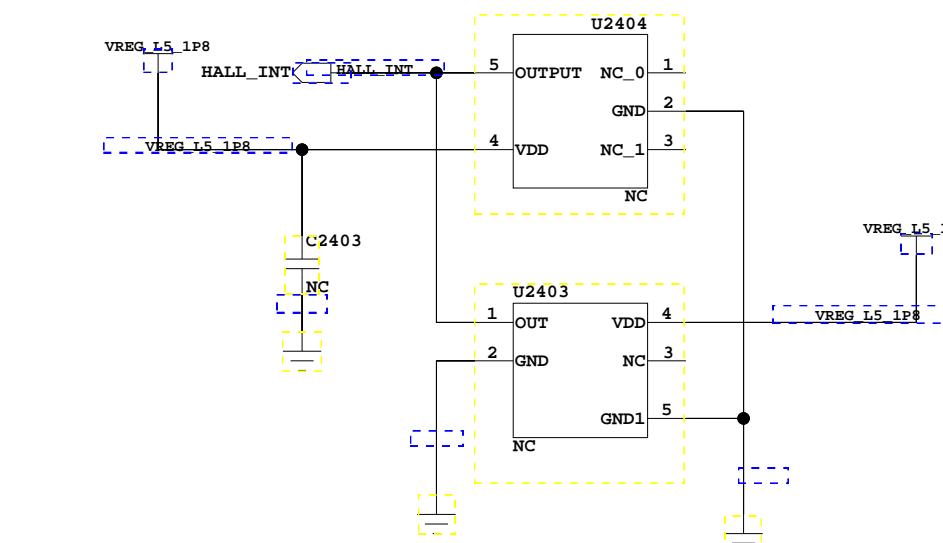
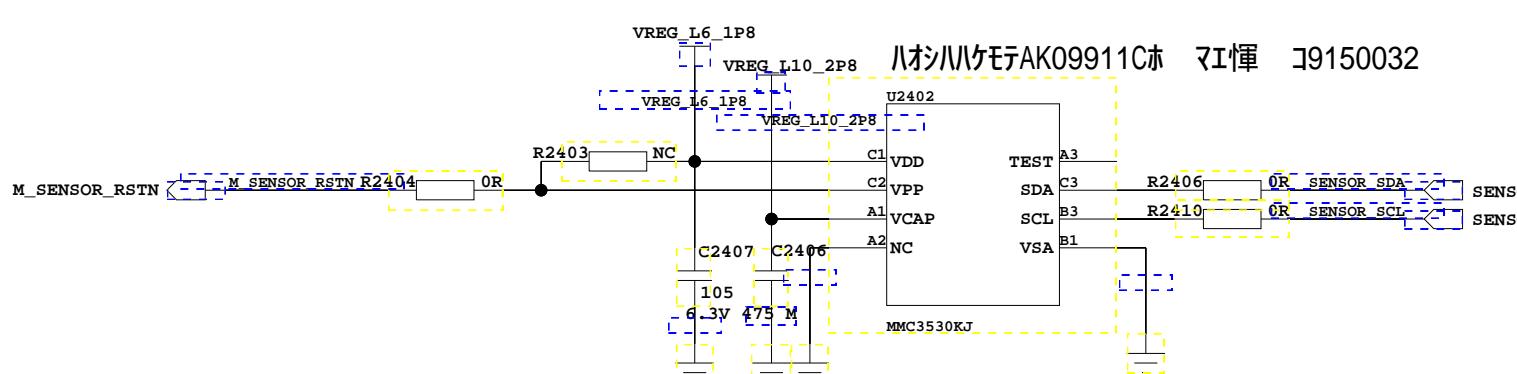


A.L.S.+P.S sensor module BTB



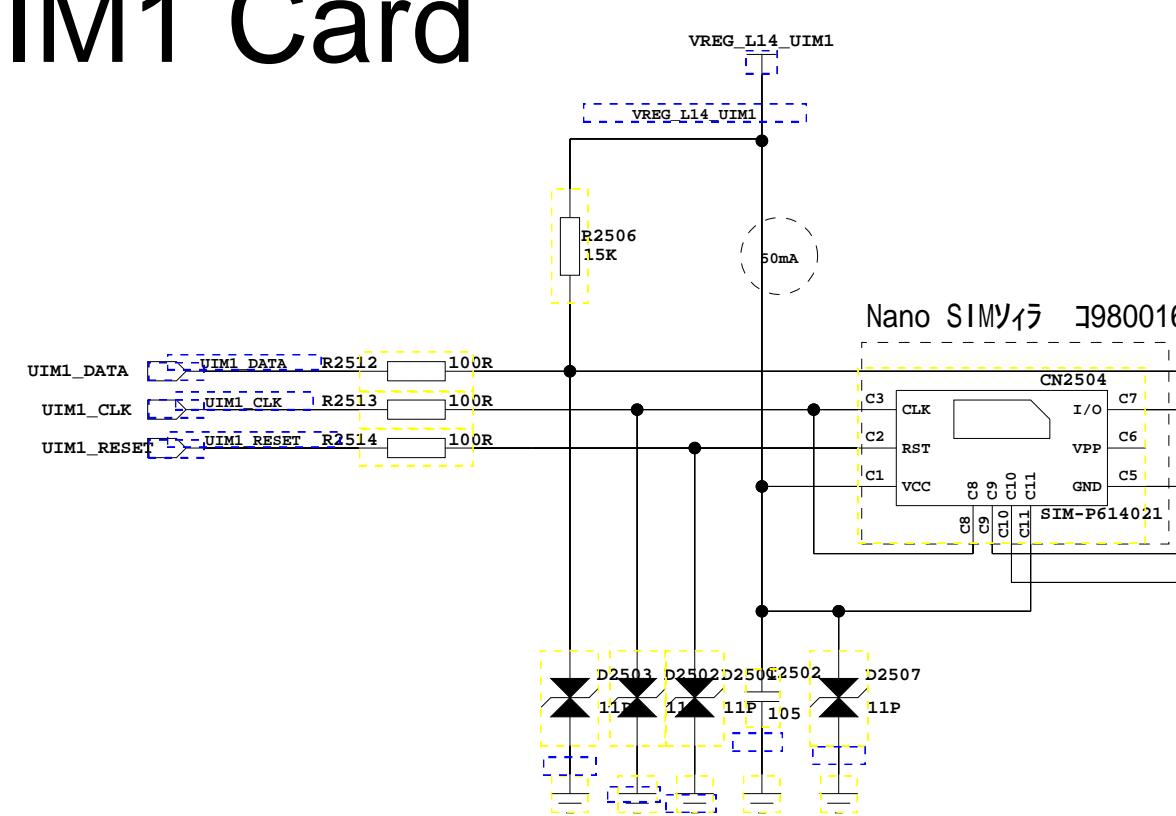
TÔÓ! J°

M Sensor

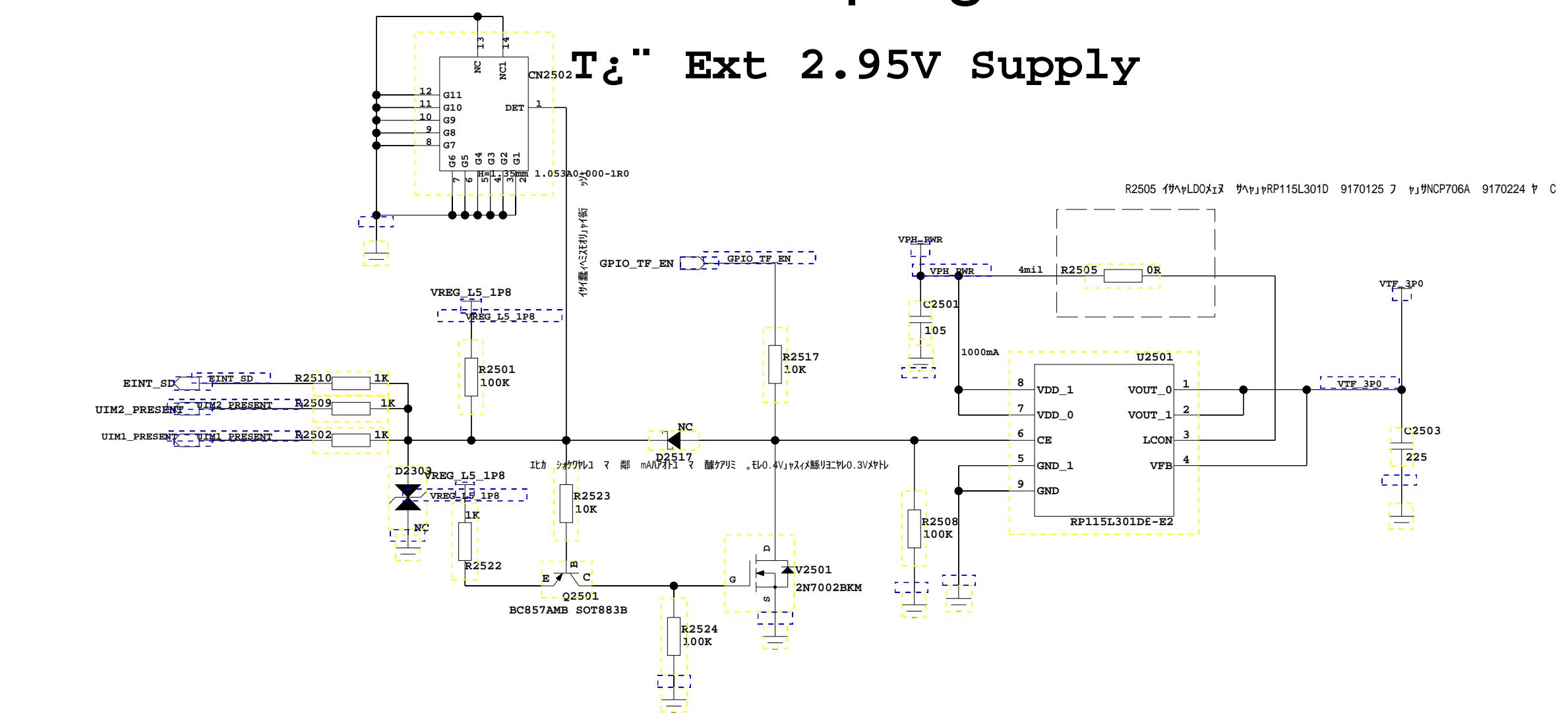


ÓÉÓÚ·ÀË®ξ¹¼ÂÇ£¬½«CN2502µÄÁ½, ÖNC¶"Î»ξ×½ÓµØ¹|ÀÍ, Hot plug

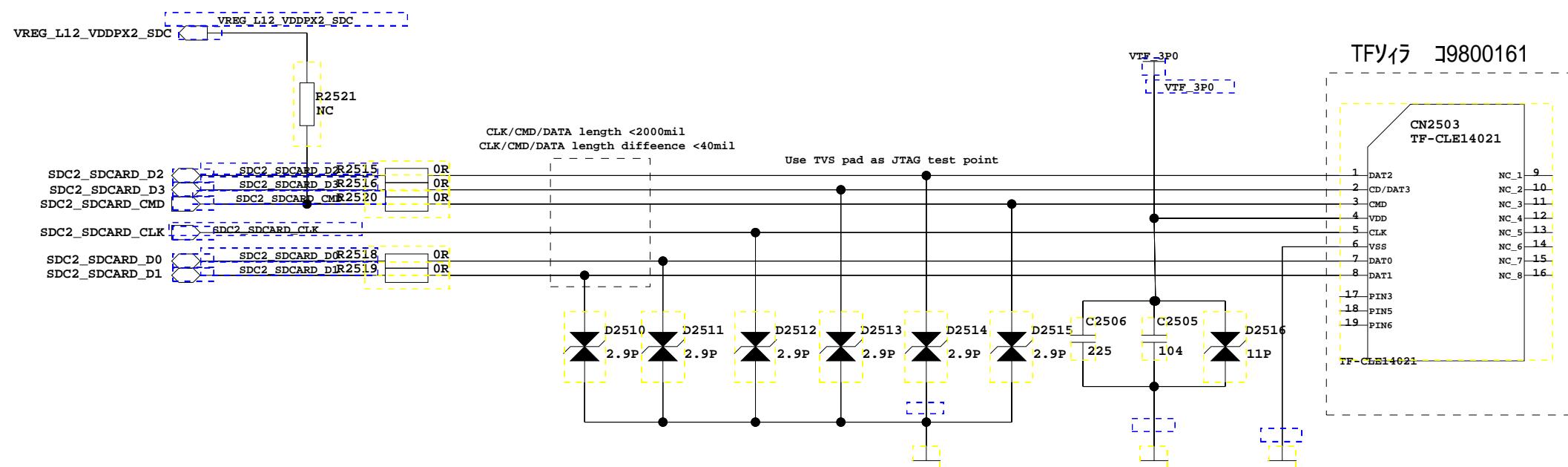
SIM1 Card



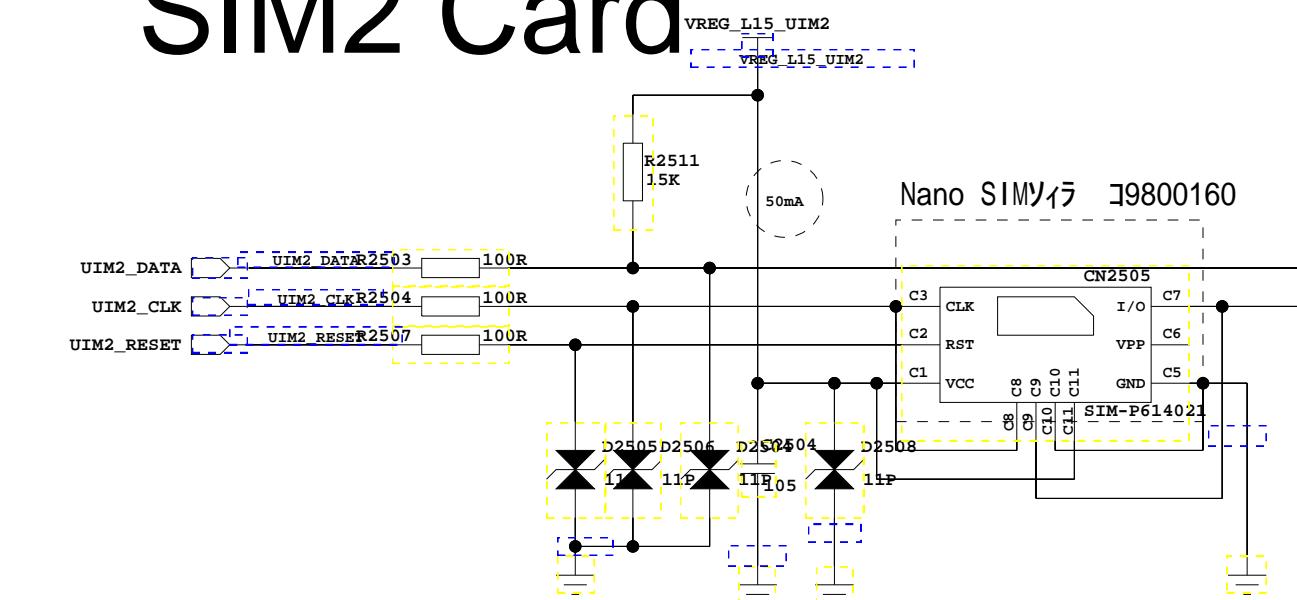
Tξ" Ext 2.95V Supply



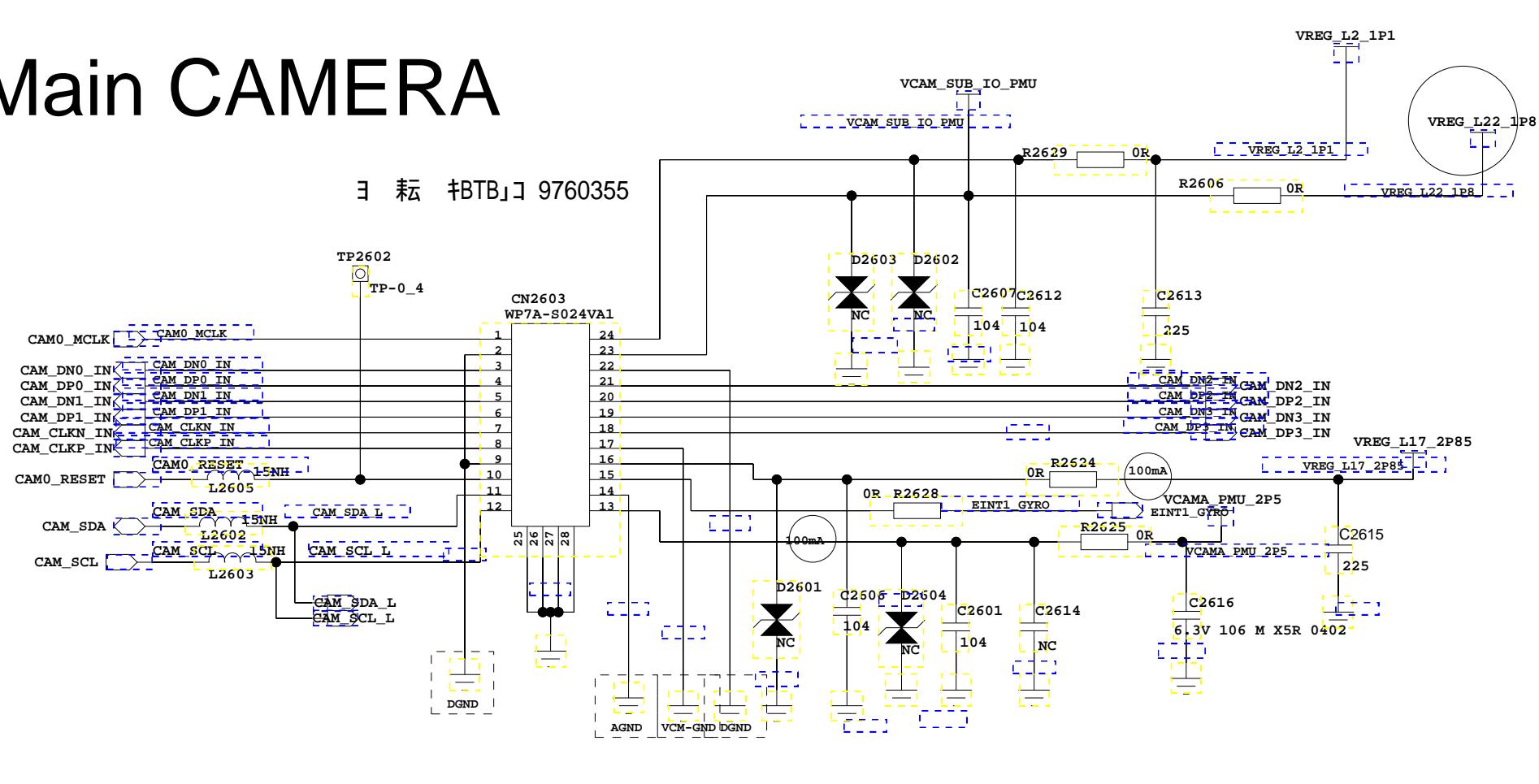
TF_CARD



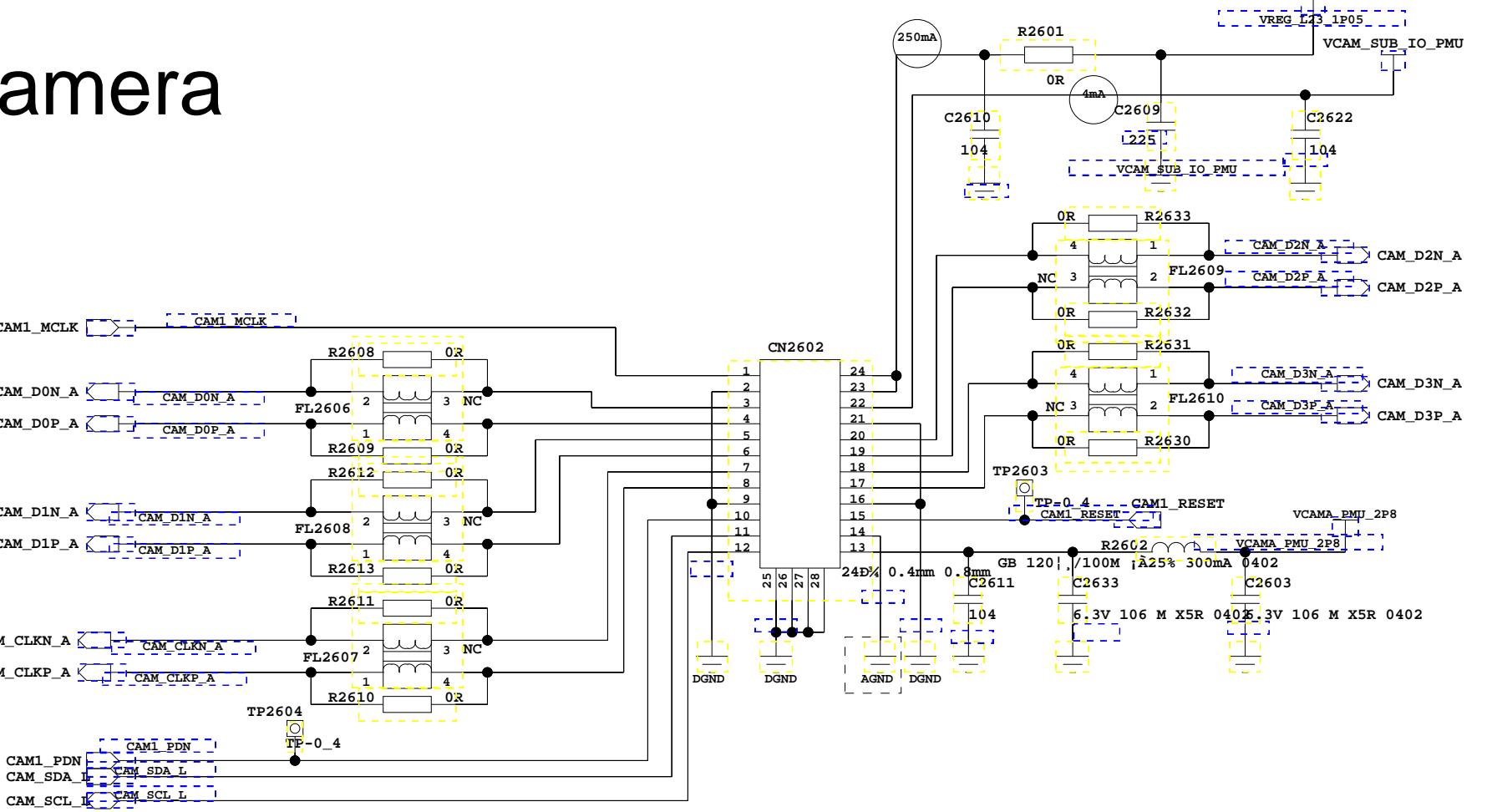
SIM2 Card



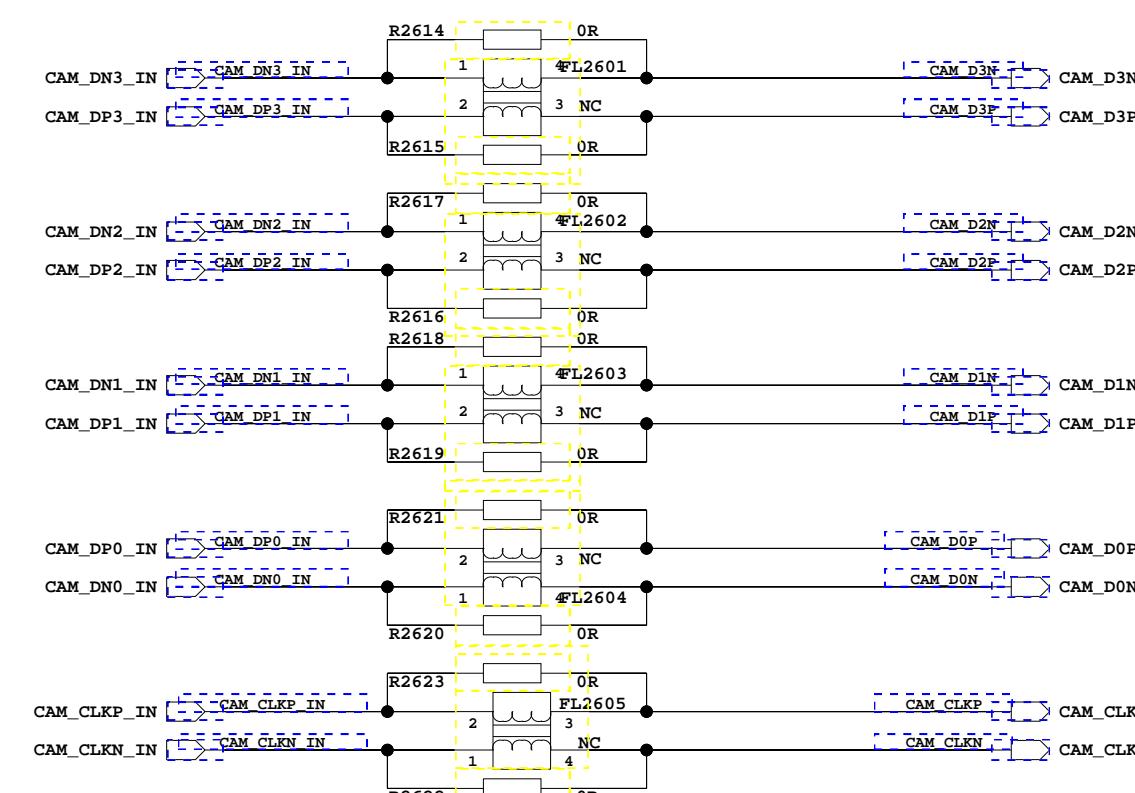
Main CAMERA



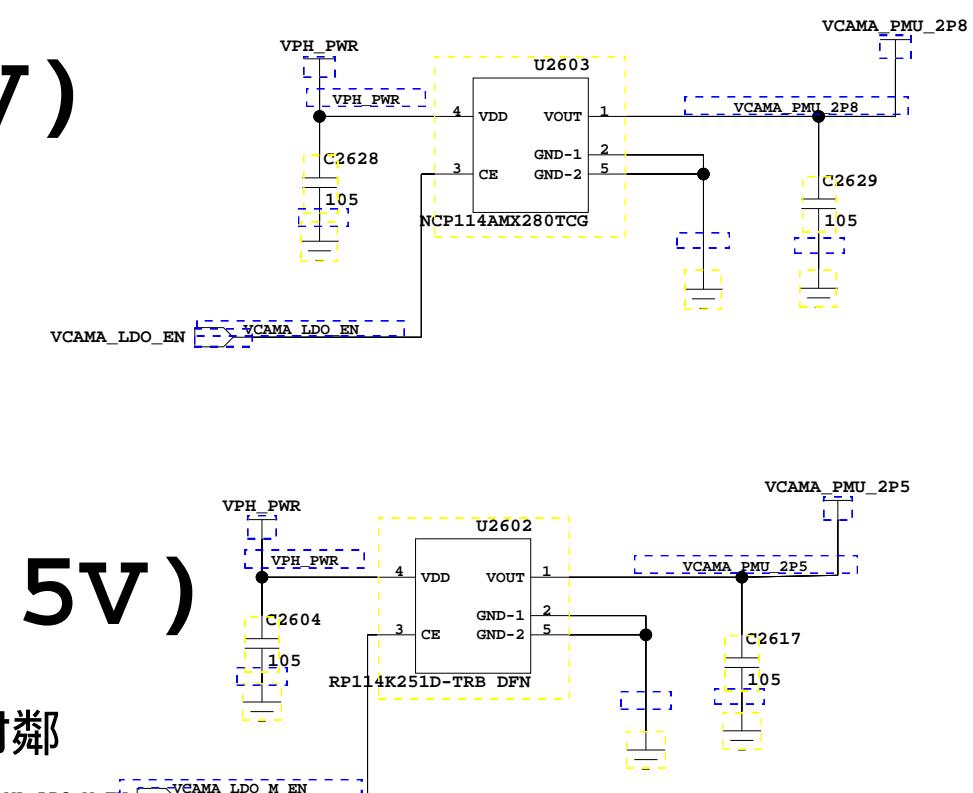
SUB_Camera



CSI CMF

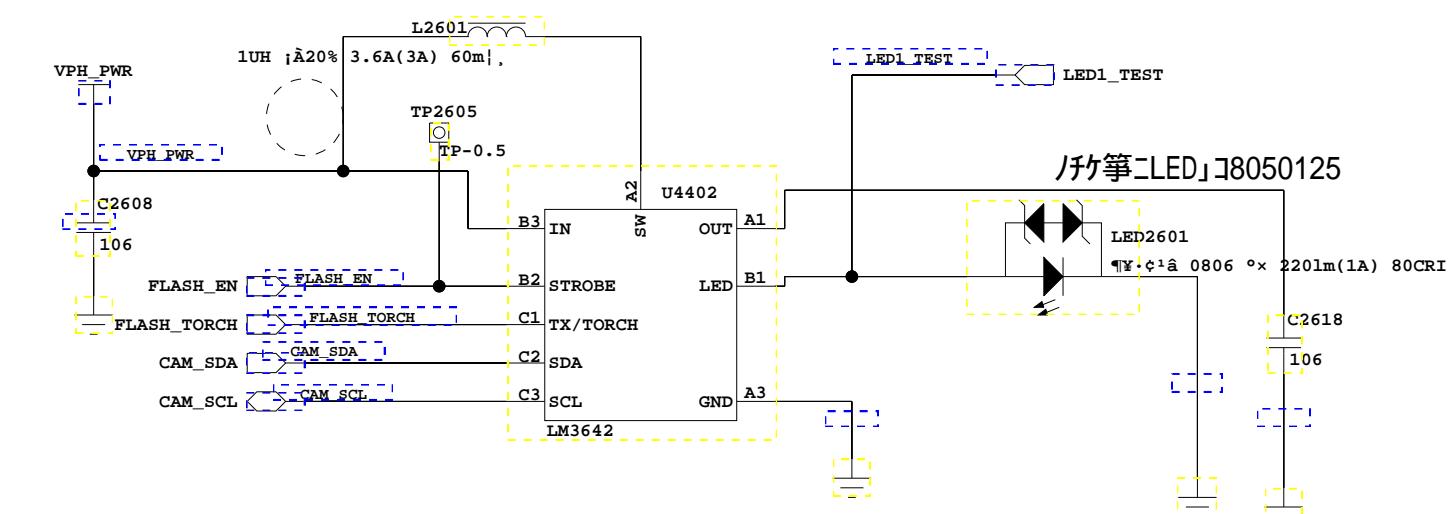


Sub Camera AVDD(2.8V)

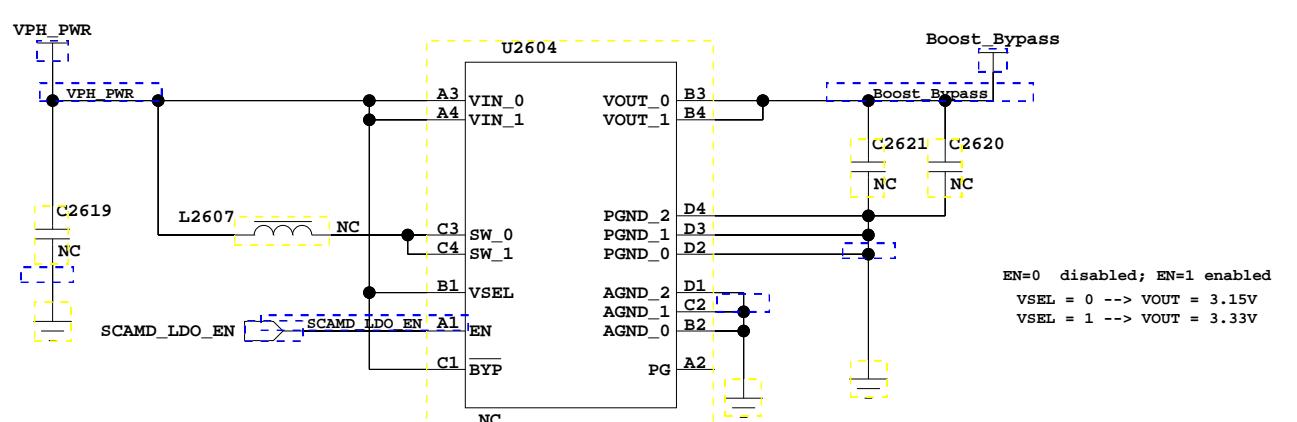


Main Camera AVDD(2.5V)

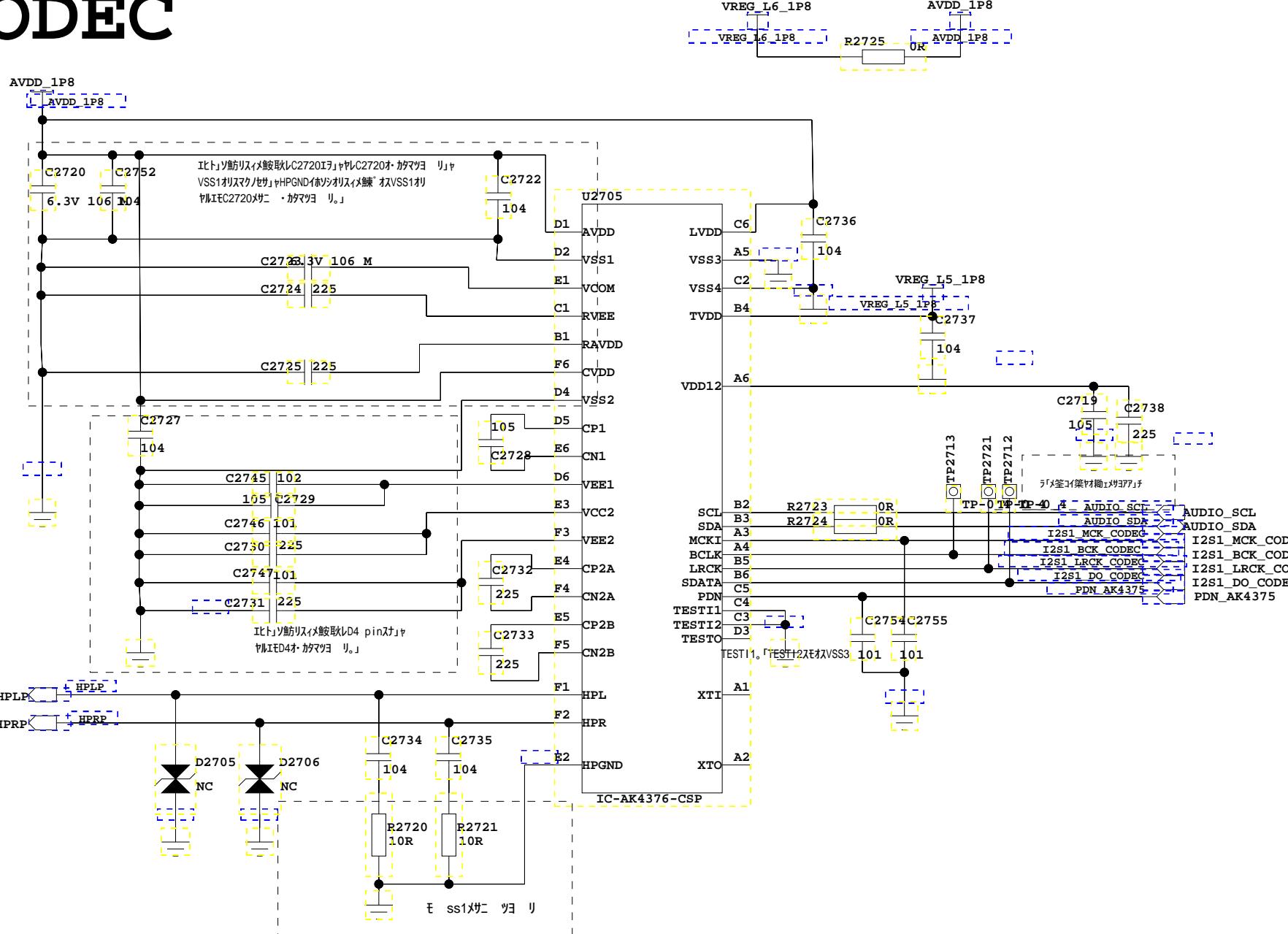
Flash LED Driver



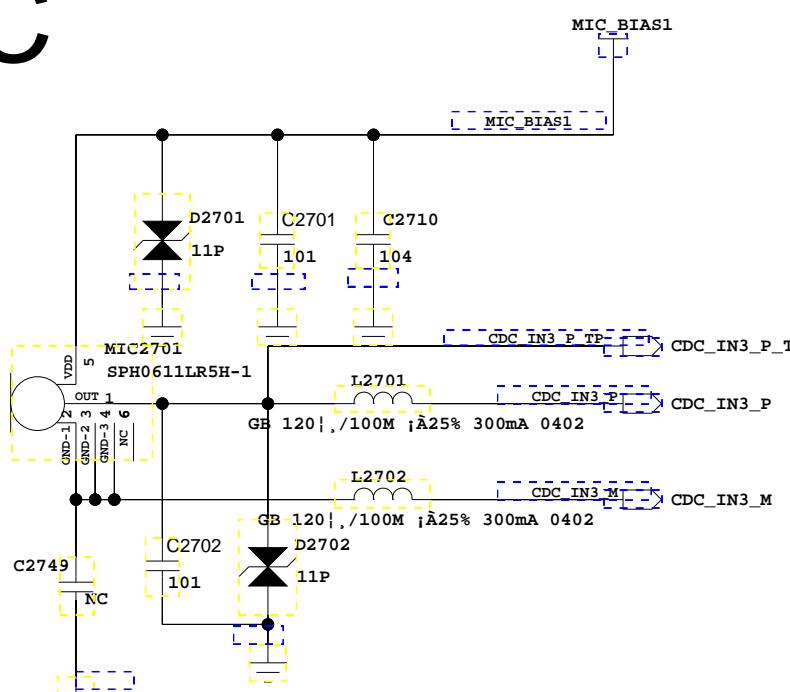
Boost Bypass



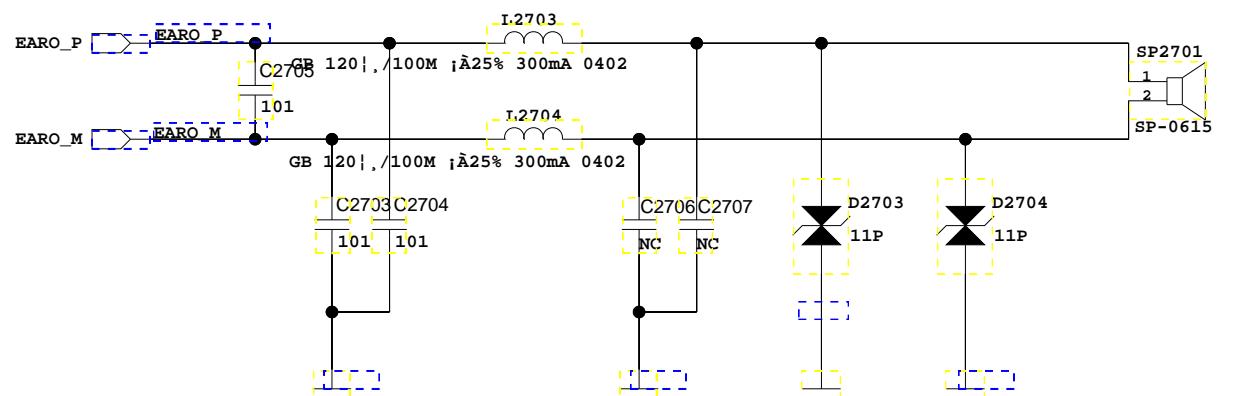
CODEC



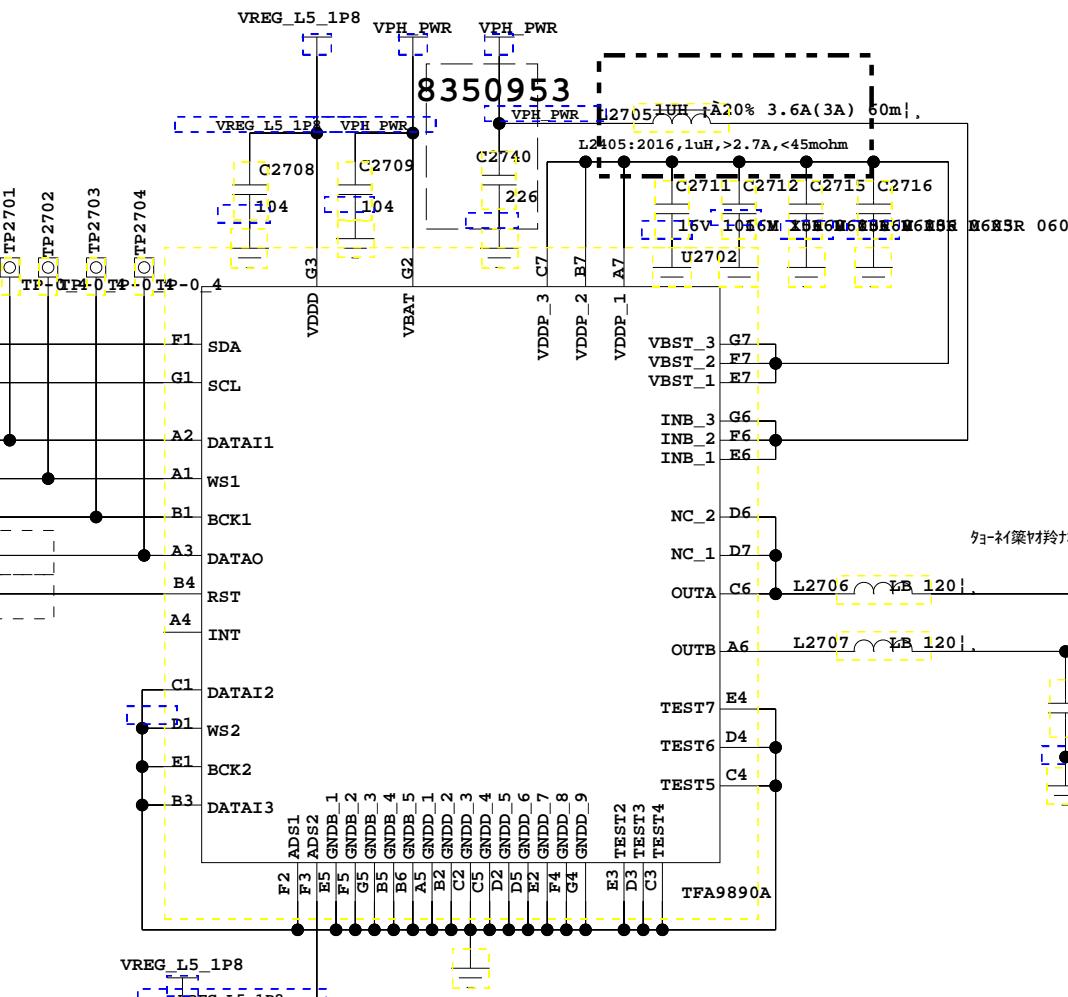
Ant-Noise MIC



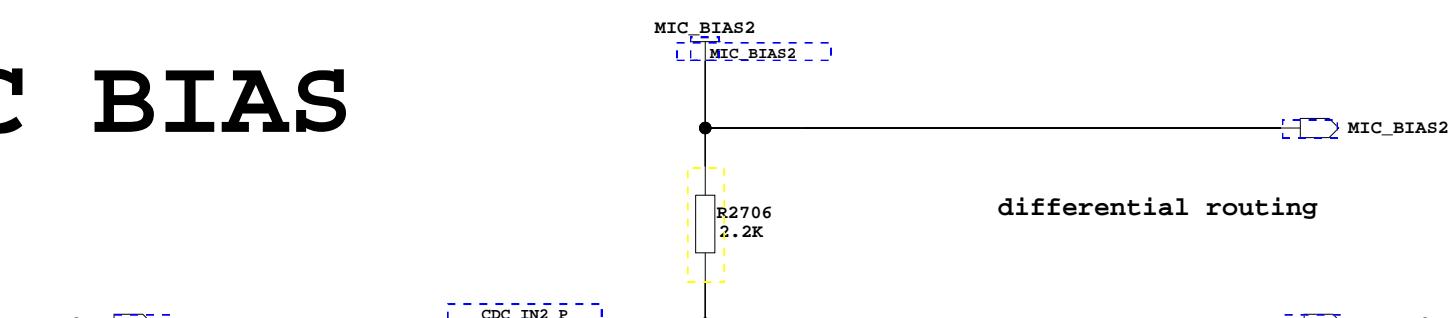
Receiver



SMART PA

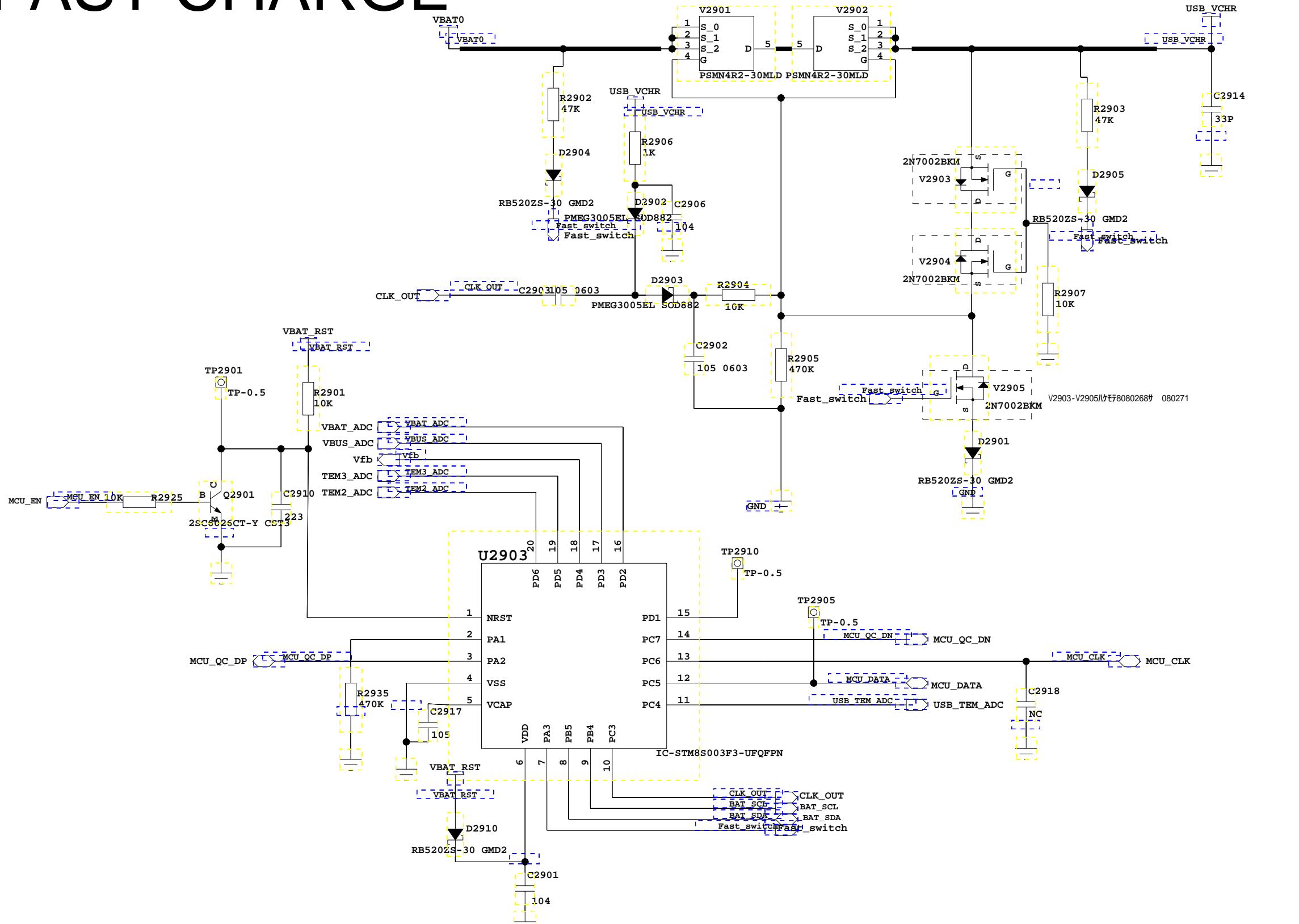


Speaker

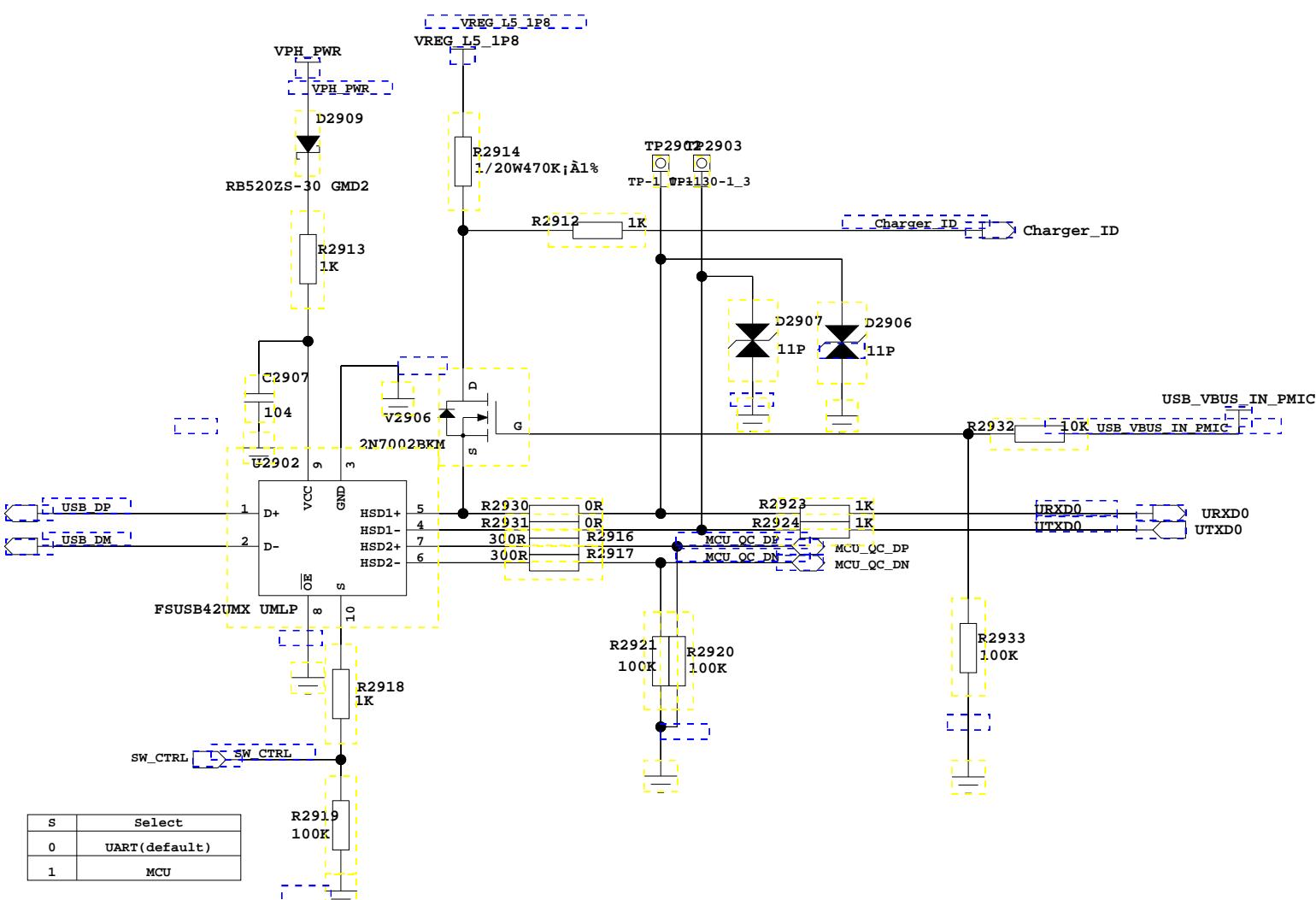


HP MIC BIAS

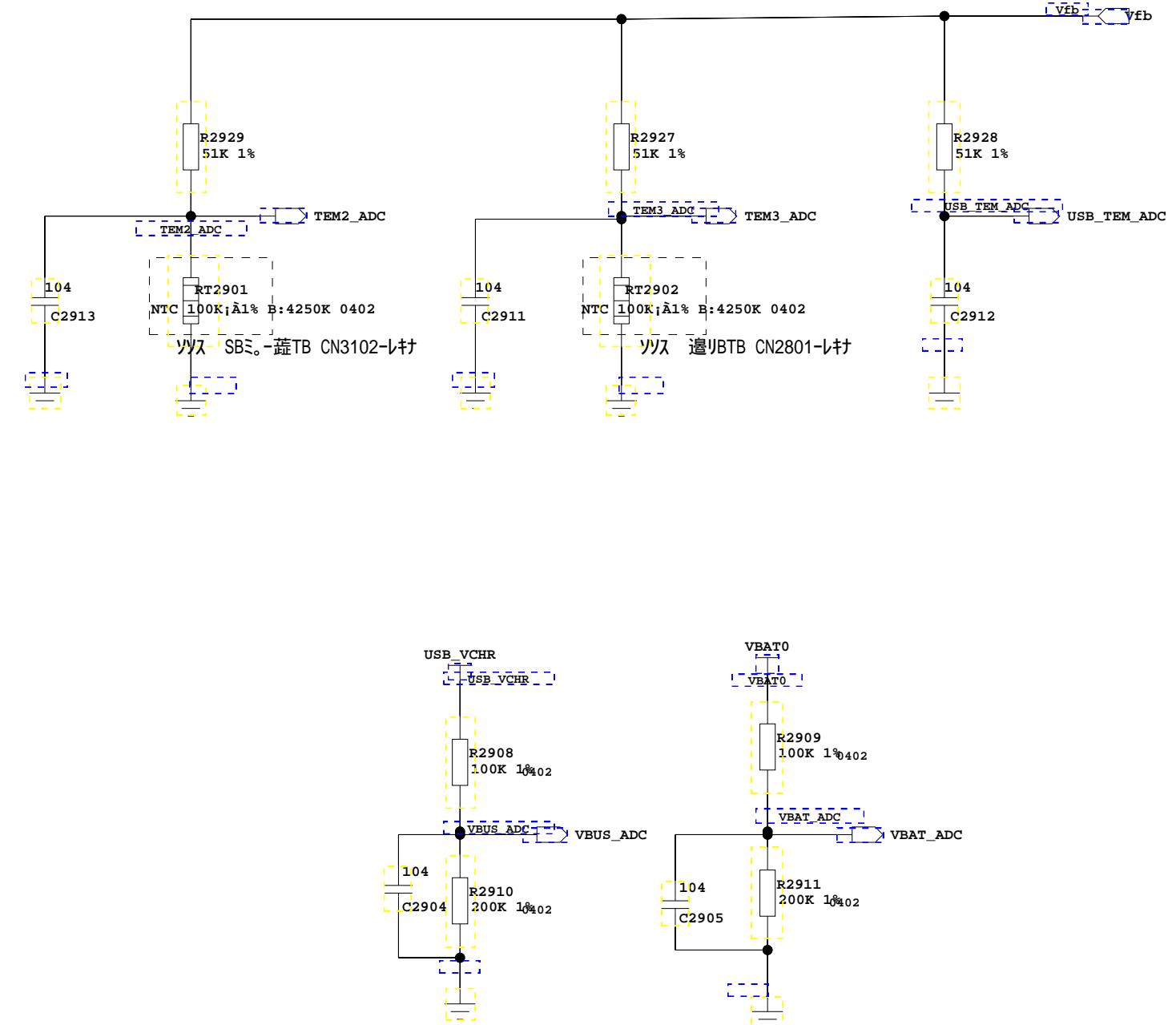
FAST CHARGE



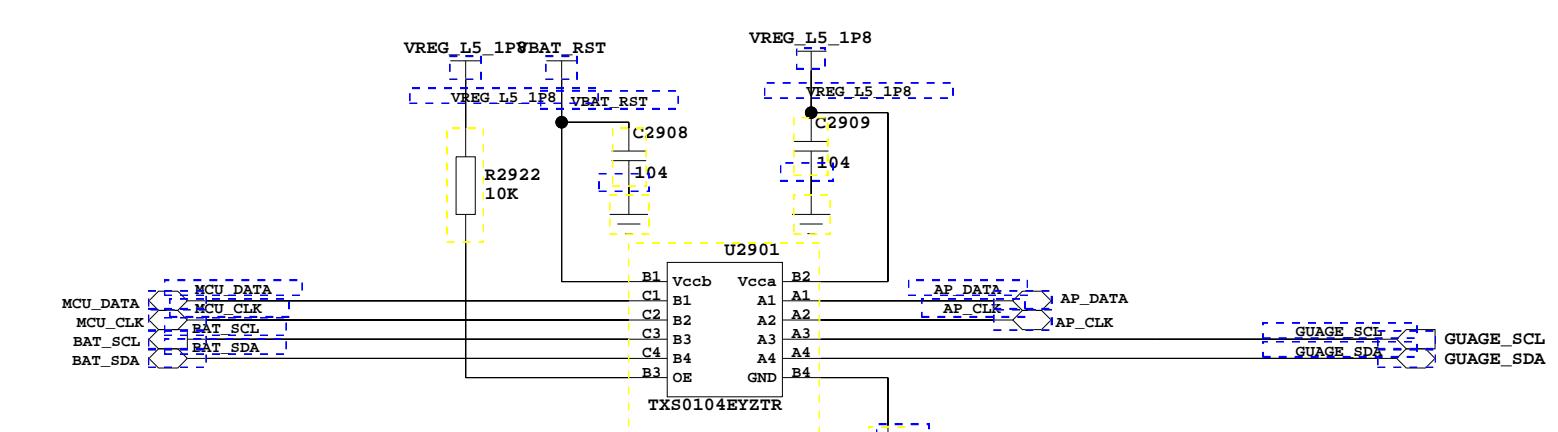
USBソエケリ



ADC, x ß ï ß x c ò â ° ü µ ø

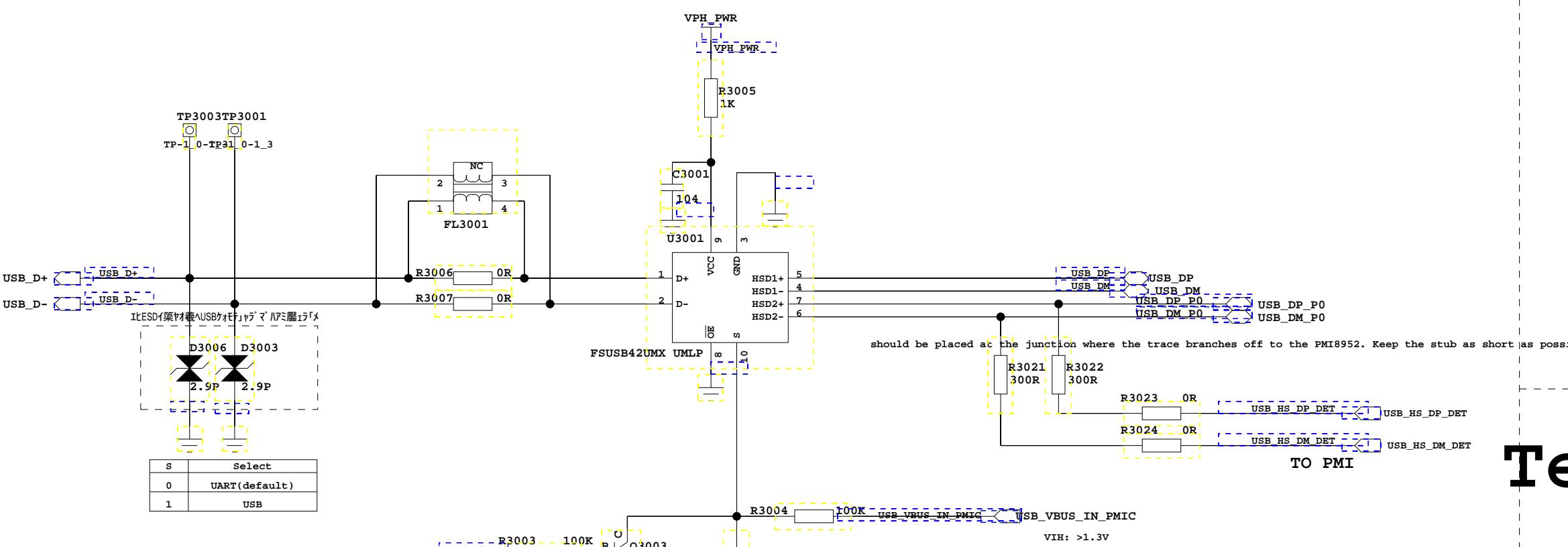


才酔スラエササ IC

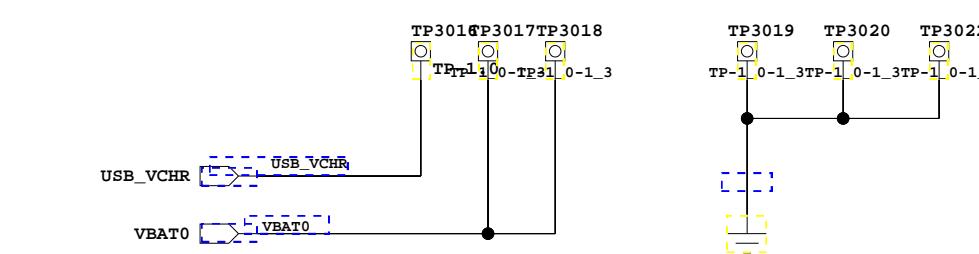


USB Switch

A



TEST POINT



KEY PAD

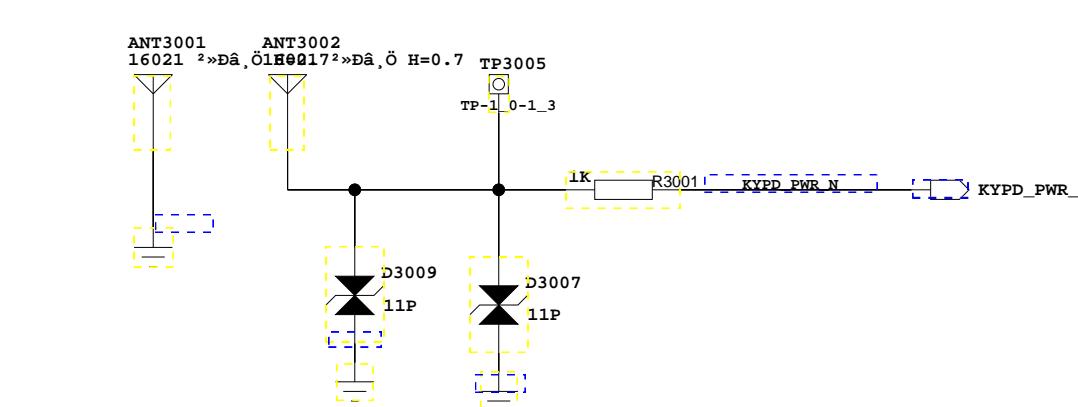
¶Ó!V°å

POWERKEY

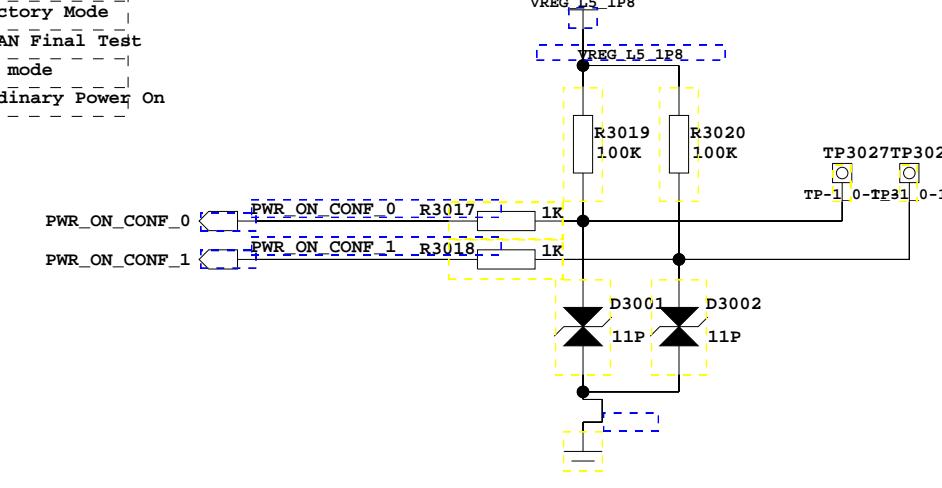
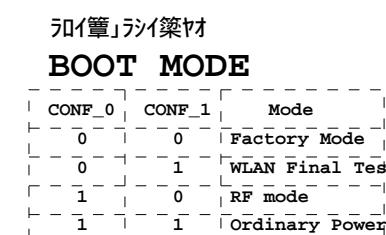
¶Ó!P°å

E

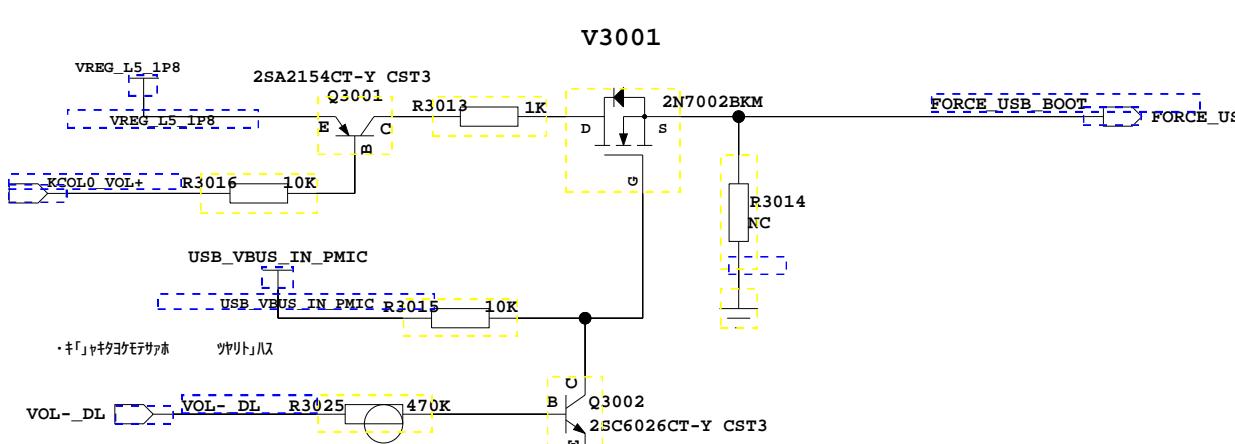
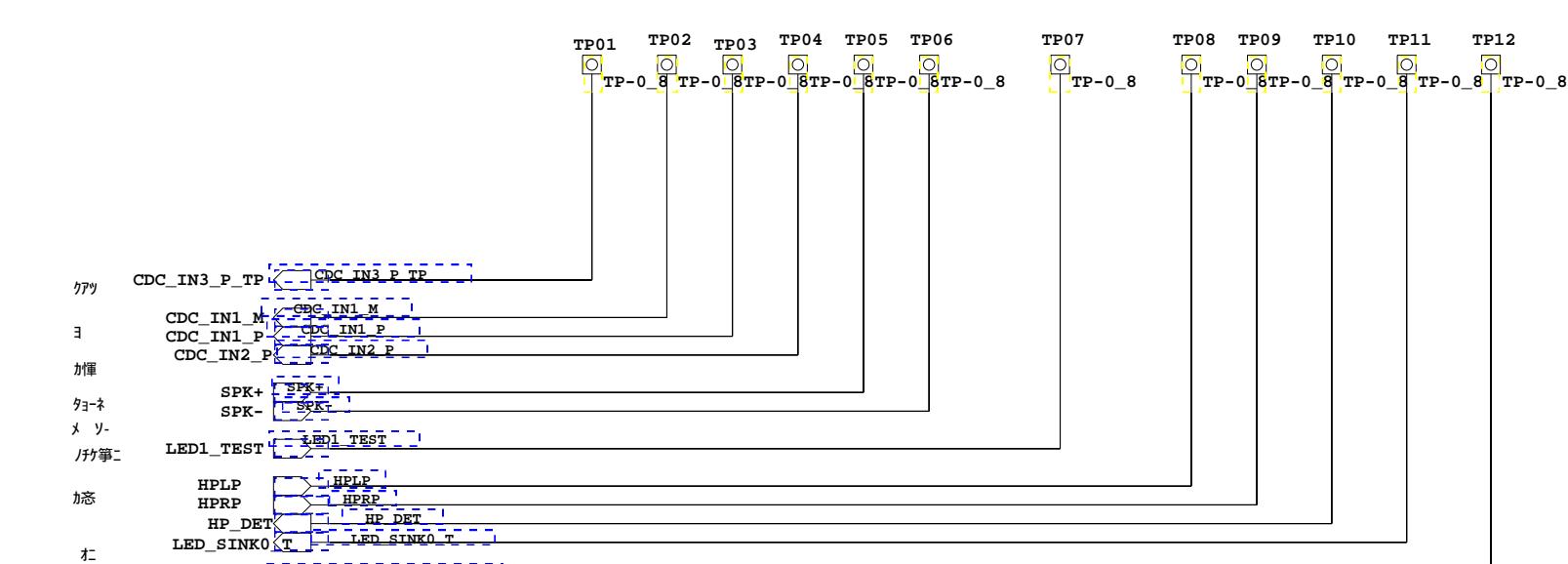
F



Test Confige



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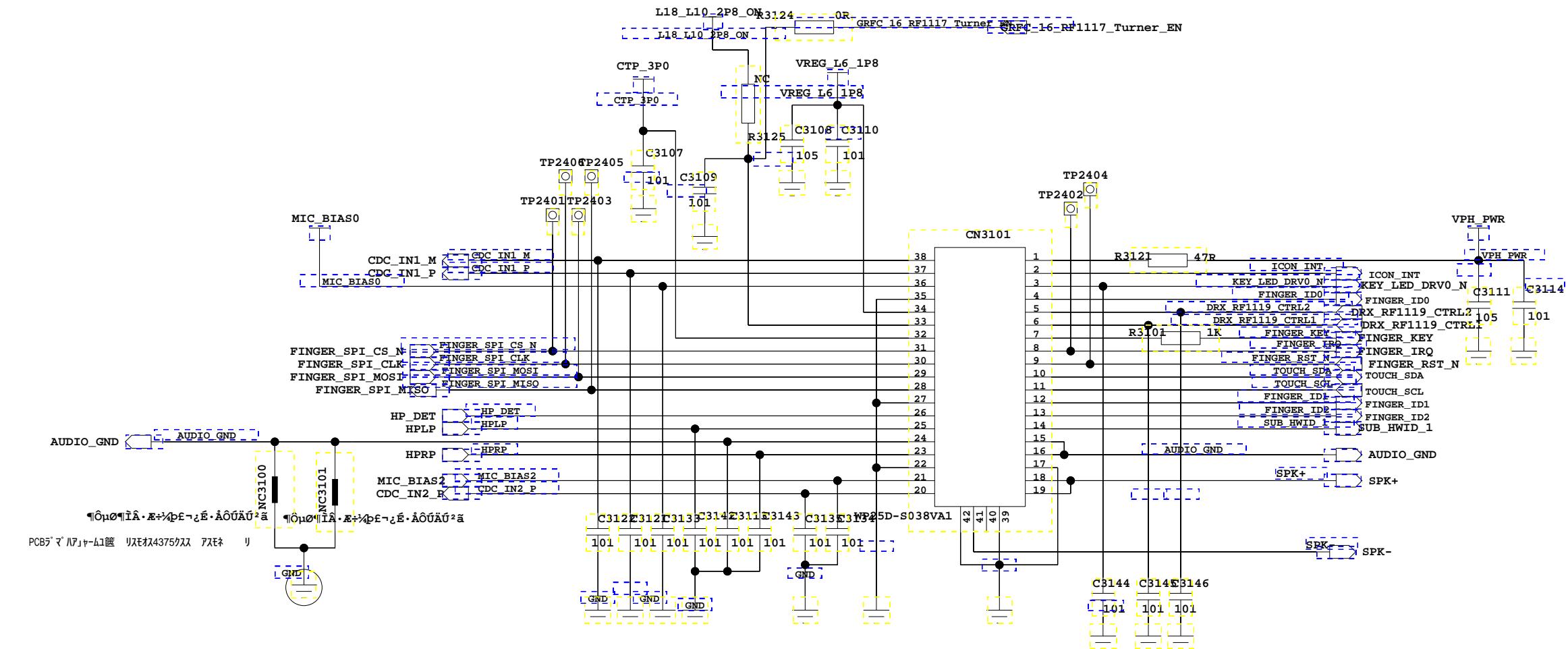
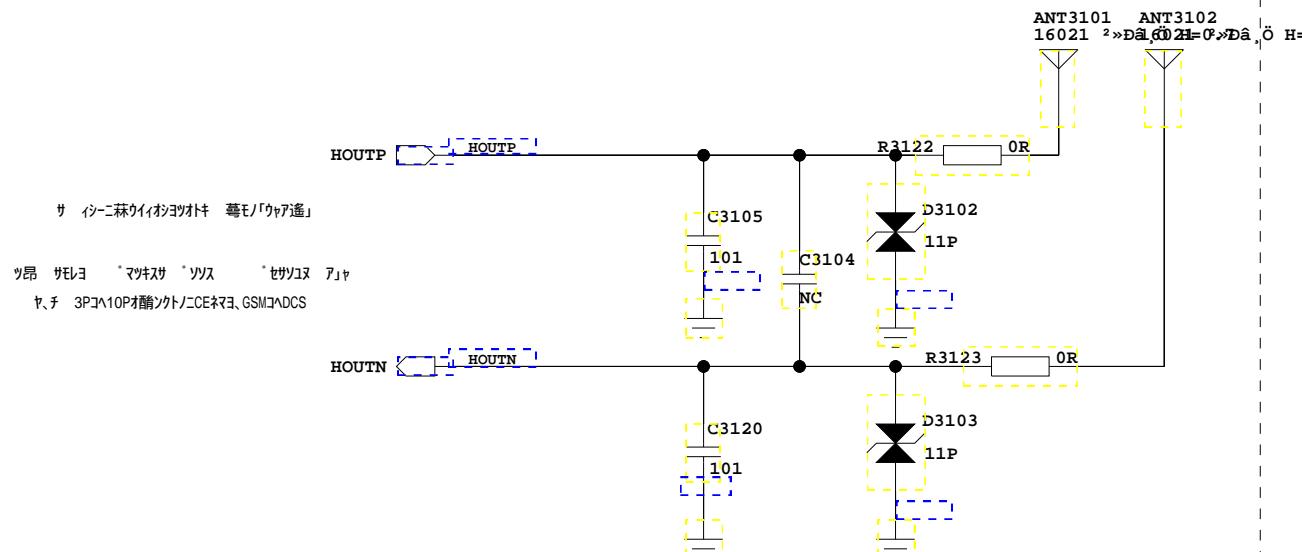


Motor Drive

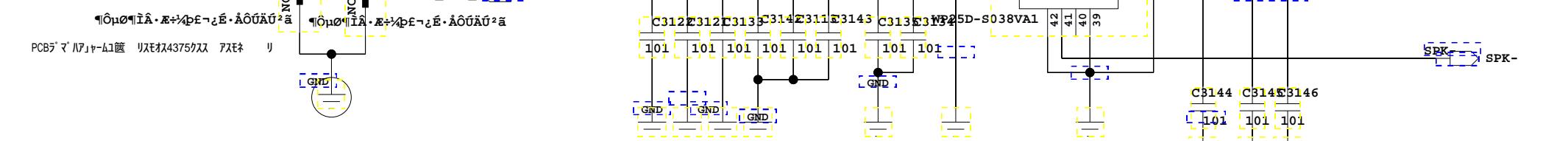
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A

サ イー二蔵ウイシヨリホト
モモ/カマ追
マタリ リソス
マチ 3P&10Pオシント/CCEモ3,GSMモDCS



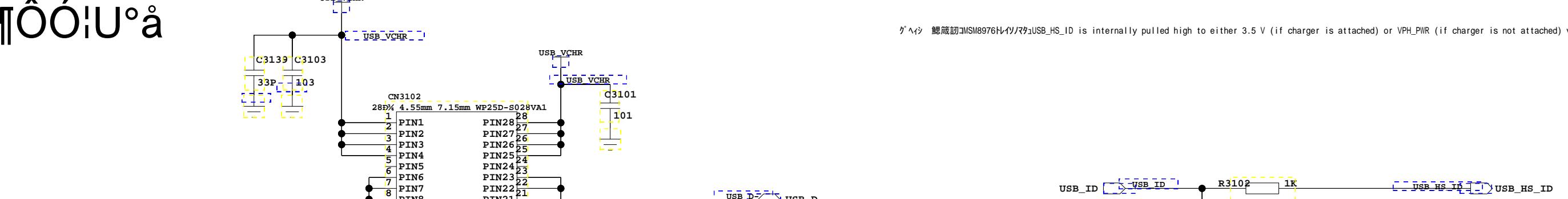
B



C

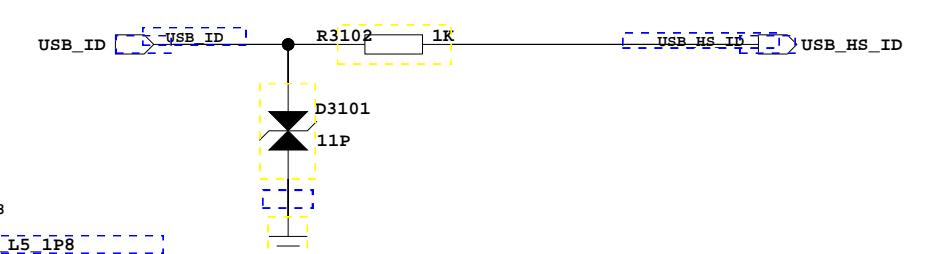
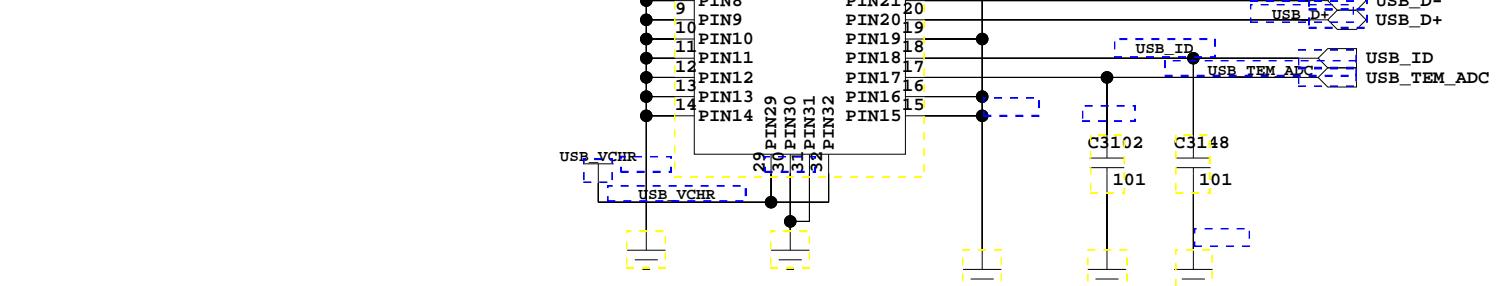
Fast Charger BTB ¶Ó!U°å

RGB LED DRIVER



フヘイ 電源切替用MOSFETはVPH_PWRは内部で高電位に保たれ、USB_HS_IDは充電器が接続された場合3.5V、充電器が接続されていない場合はVPH_PWRによって設定される。

D

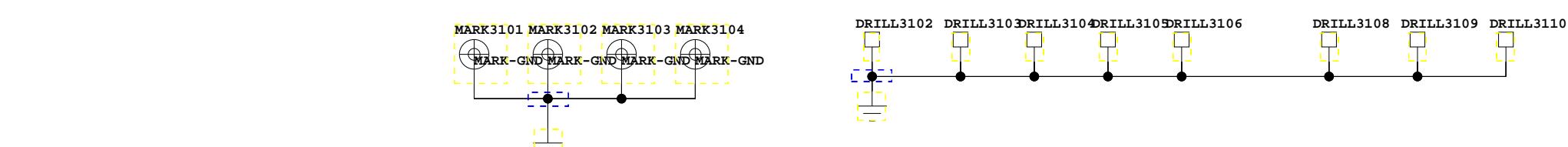


E

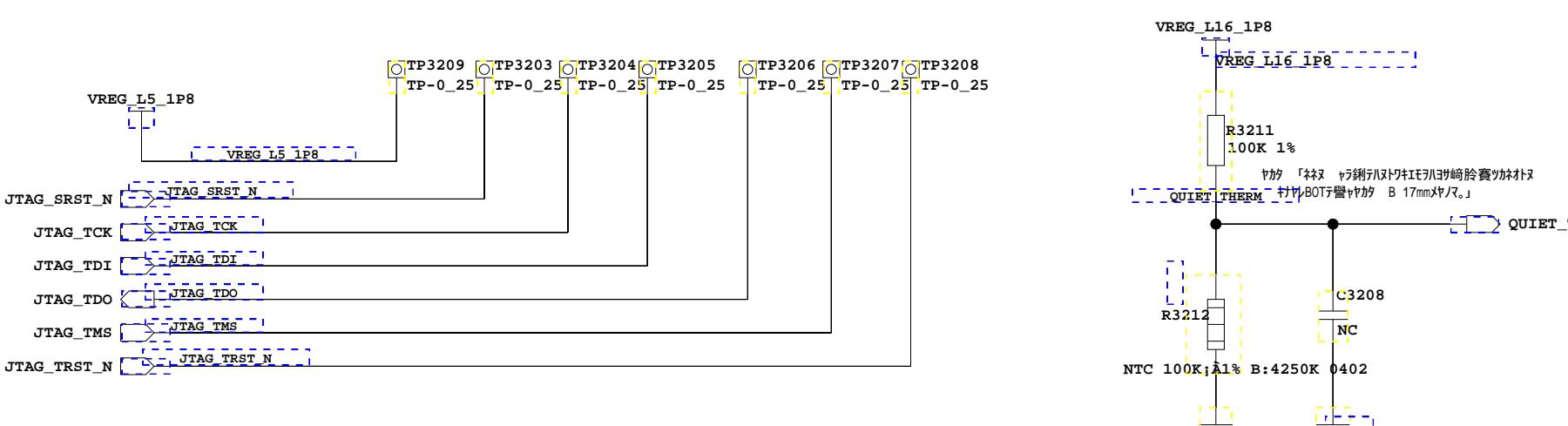
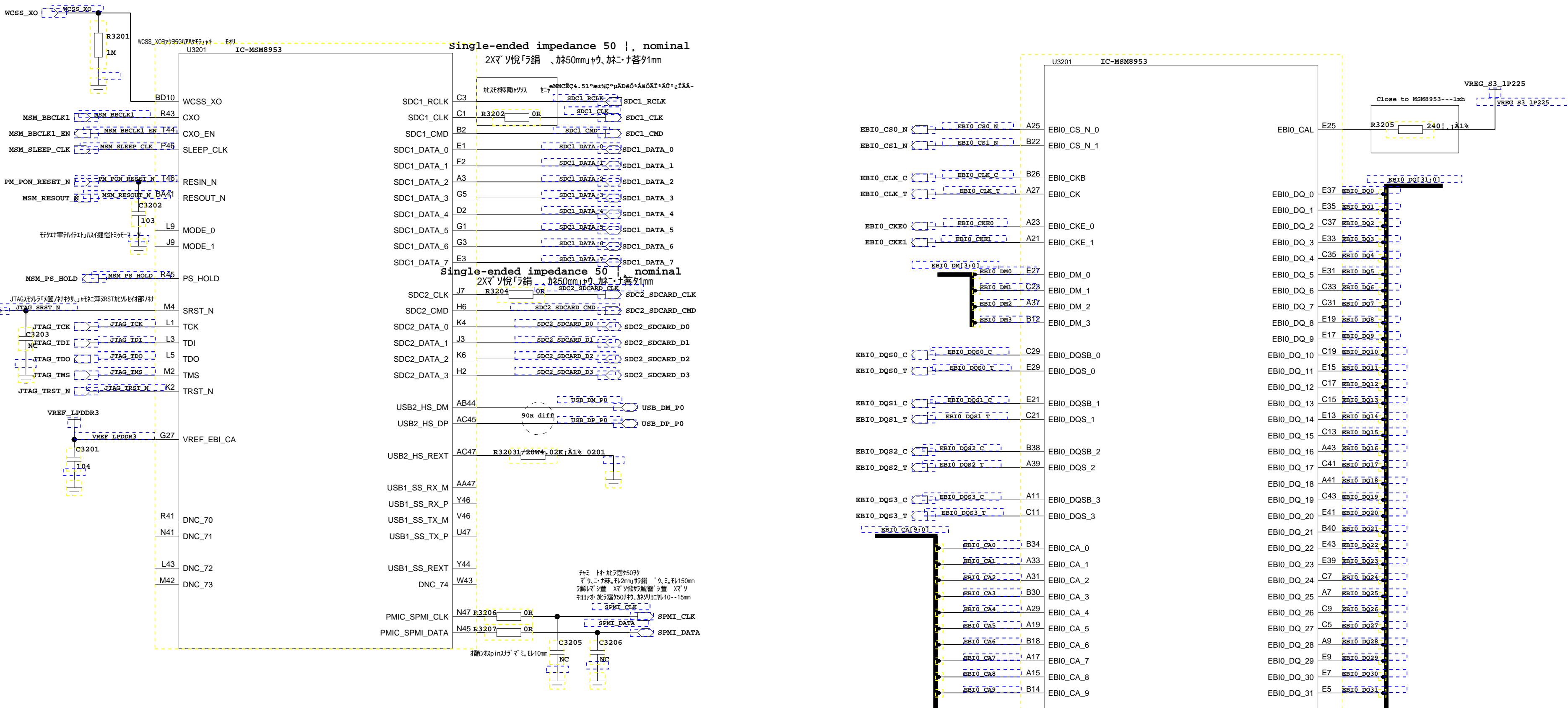


F

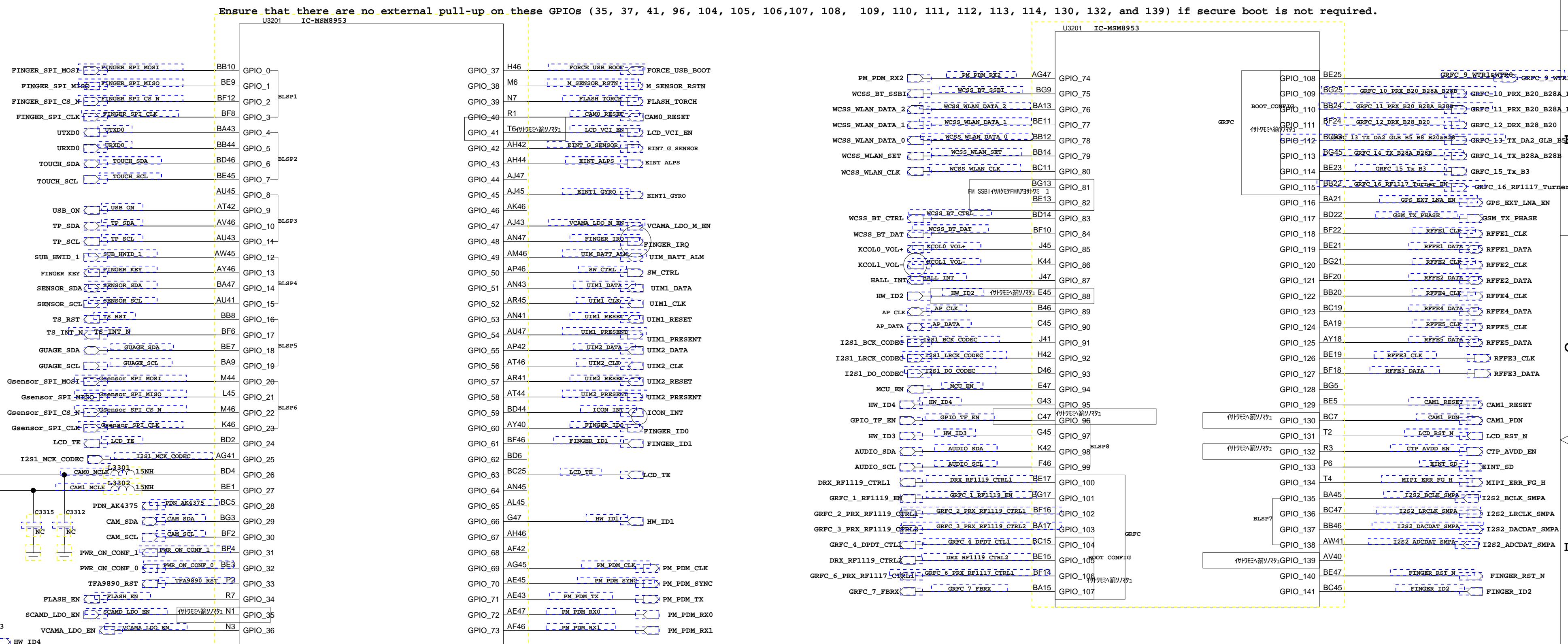
MARK POINT



メムヨリミツコ釣 3.09.17.16



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メムク ツ0309.17.16

CAM_I2C_SDA0	I2C/唯々 IIC2	がれりヨ	ミエリヨ
MAIN CAMERA	0X35	0X34	
Sub Camera	0X21	0X20	
Flash Driver	0xC7	0xC6	

	I2C/唯々	がれりヨ	ミエリヨ
/メタリヨ・コサ	0x60		
初期リヨン	0xA8		

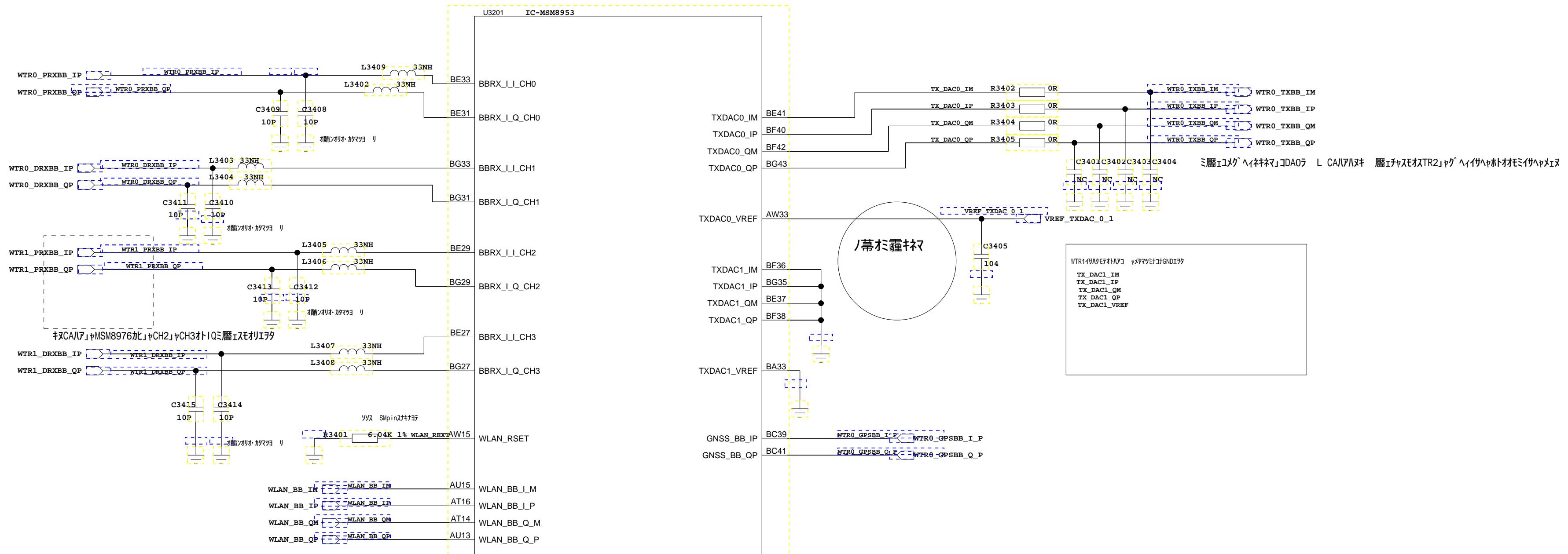
TP_I2C_SDA	I2C/唯々(I2C0 MT6755)	がれりヨ	ミエリヨ
TP	0x41		
RGB LED	0x61		

	I2C/唯々	がれりヨ	ミエリヨ
G-sensor/Gyo	0x05	0x04	
ALS+PS	0XA7		
M-sensor	0x61		
RGB sensor	0x73		

	I2C/唯々	がれりヨ	ミエリヨ
Smart PA	0x60		
Codec	0x21		

	I2C/唯々	がれりヨ	ミエリヨ
TOUCH IC	0x41		

A



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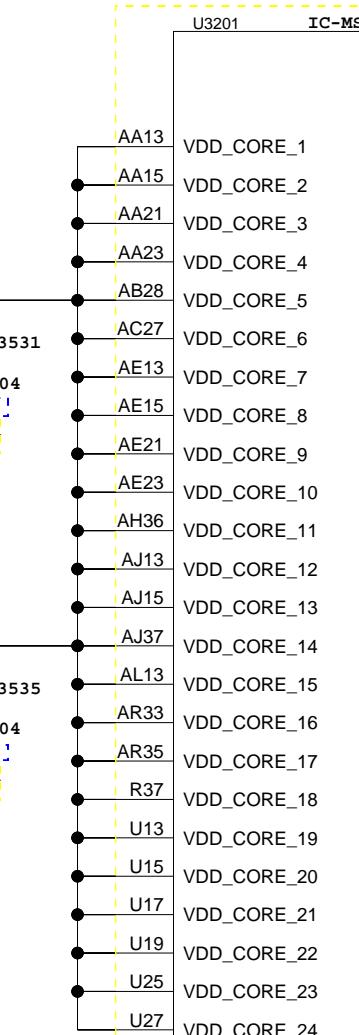
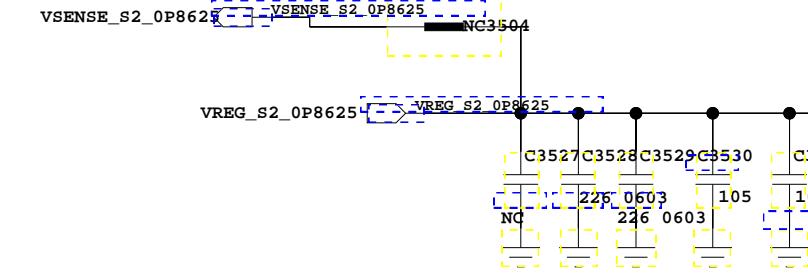
15

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ムク ヴ03.10.10.22

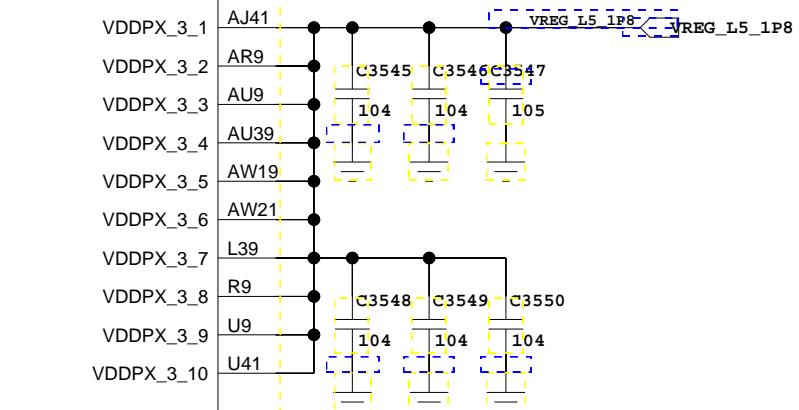
A

Power domain	VDD_CORE
DC to 10 Hz resistance(mΩ)	see spec
10 Hz to 25 MHz(mΩ)	see spec
positive port	see pin mark
negative port	see pin mark



Power domain	VDD_P1
DC to 10 Hz resistance(mΩ)	see spec
10 Hz to 25 MHz(mΩ)	see spec
positive port	see pin mark
negative port	see pin mark

VDDPX_2 L11 VREG_L11_VDDPX2_SDC VREG_L12_VDDPX2_SDC

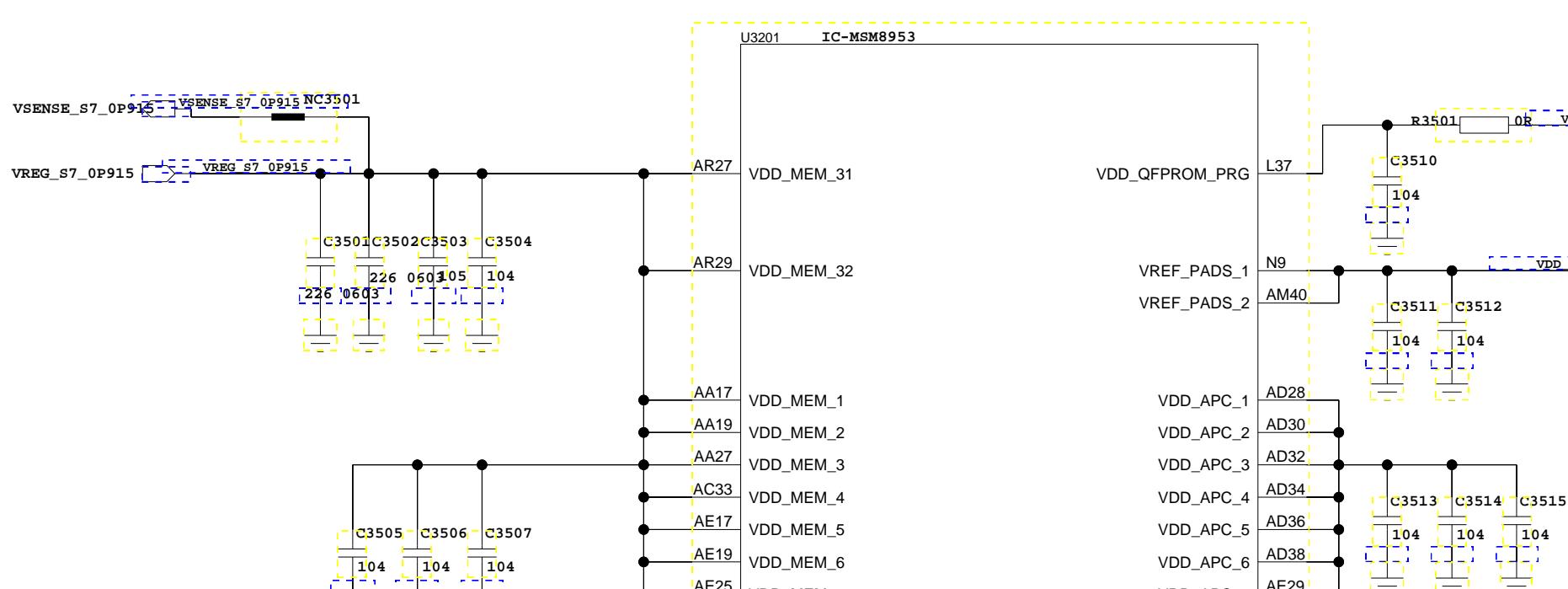


VDDPX_5 AL41 VREG_L14_UIM1 VREG_L14_UIM2

VDDPX_6 AK40 VREG_L15_UIM2 VREG_L15_UIM1

VDDPX_7 L13 VREG_L5_1P8 VREG_L5_1P8

VDDPX_CK J25 VREG_S3_1P225 VREG_S3_1P225

B

VDD_QFPROM_PRG N9 VREF_PADS_1 VREF_PADS_2

AM40

C3511-C3512

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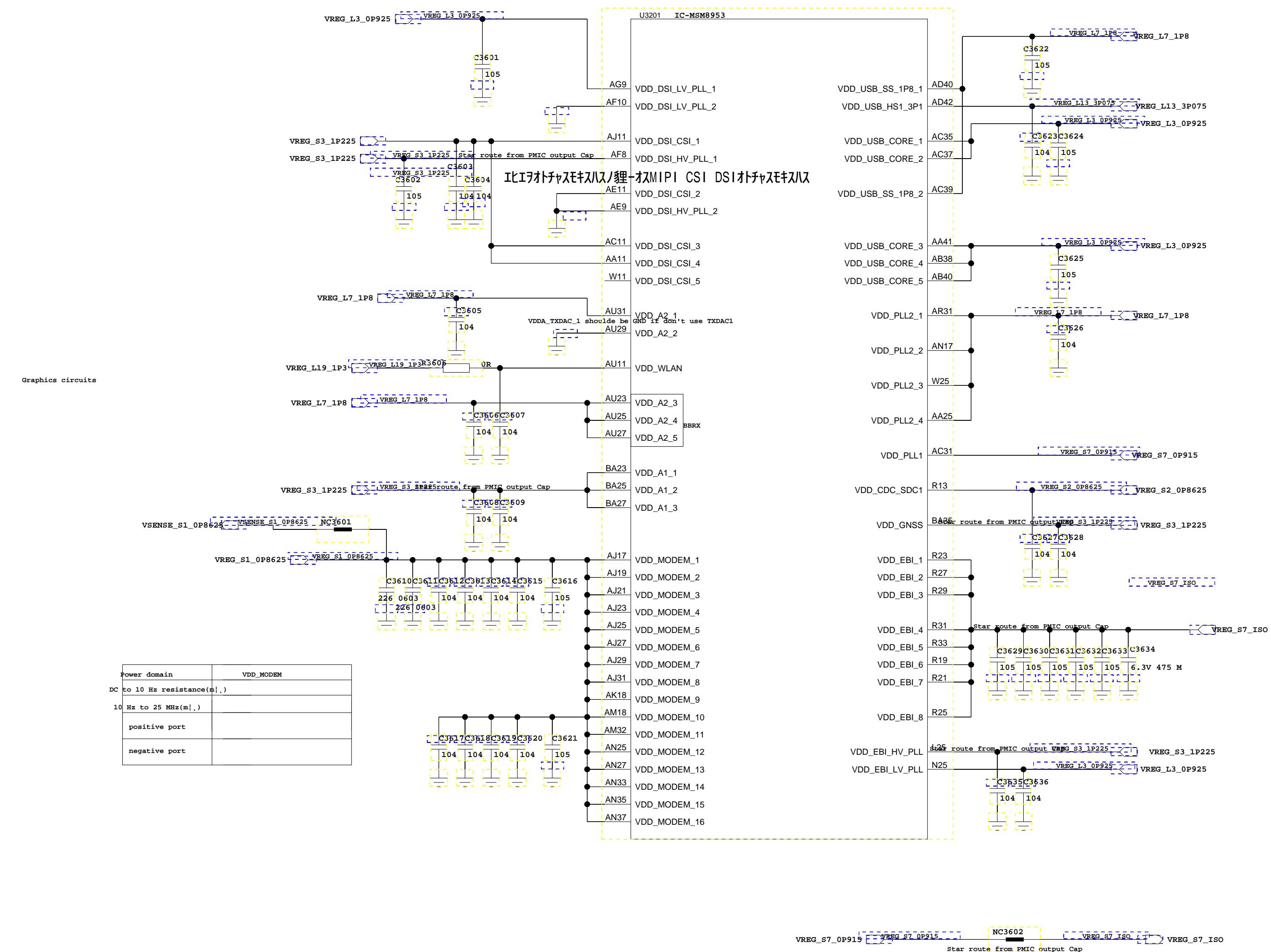
104 104

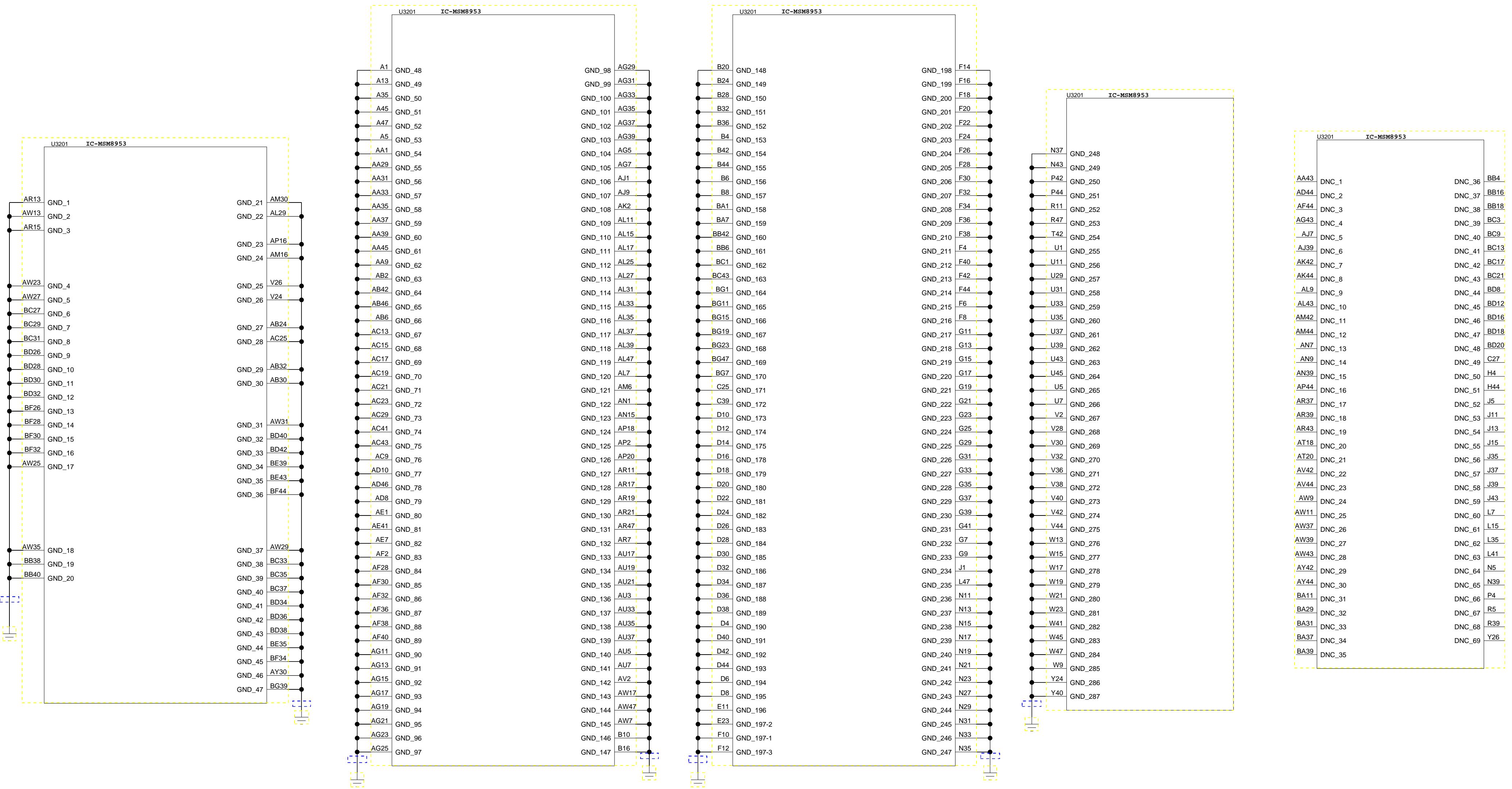
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104 104

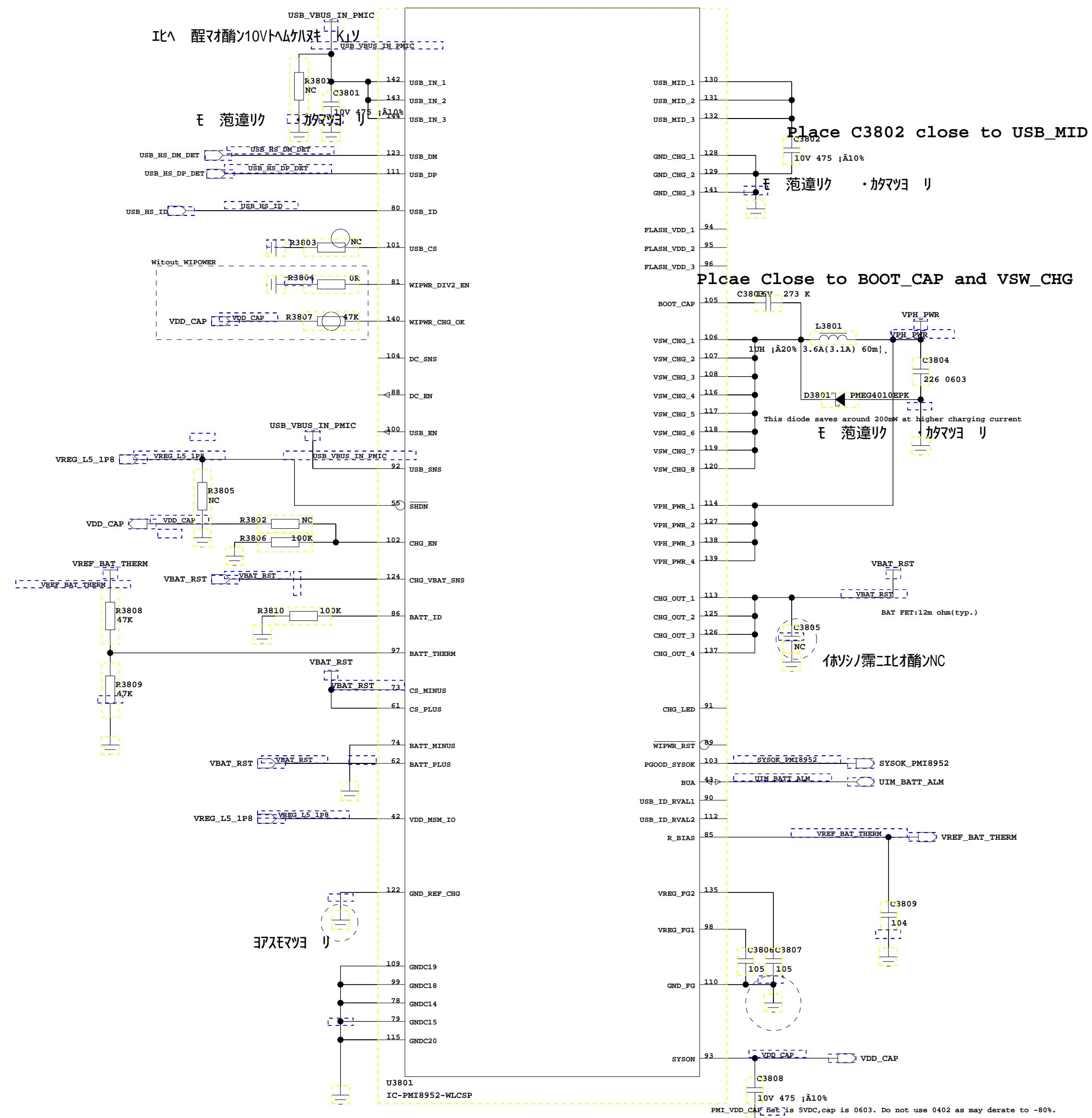
104 104

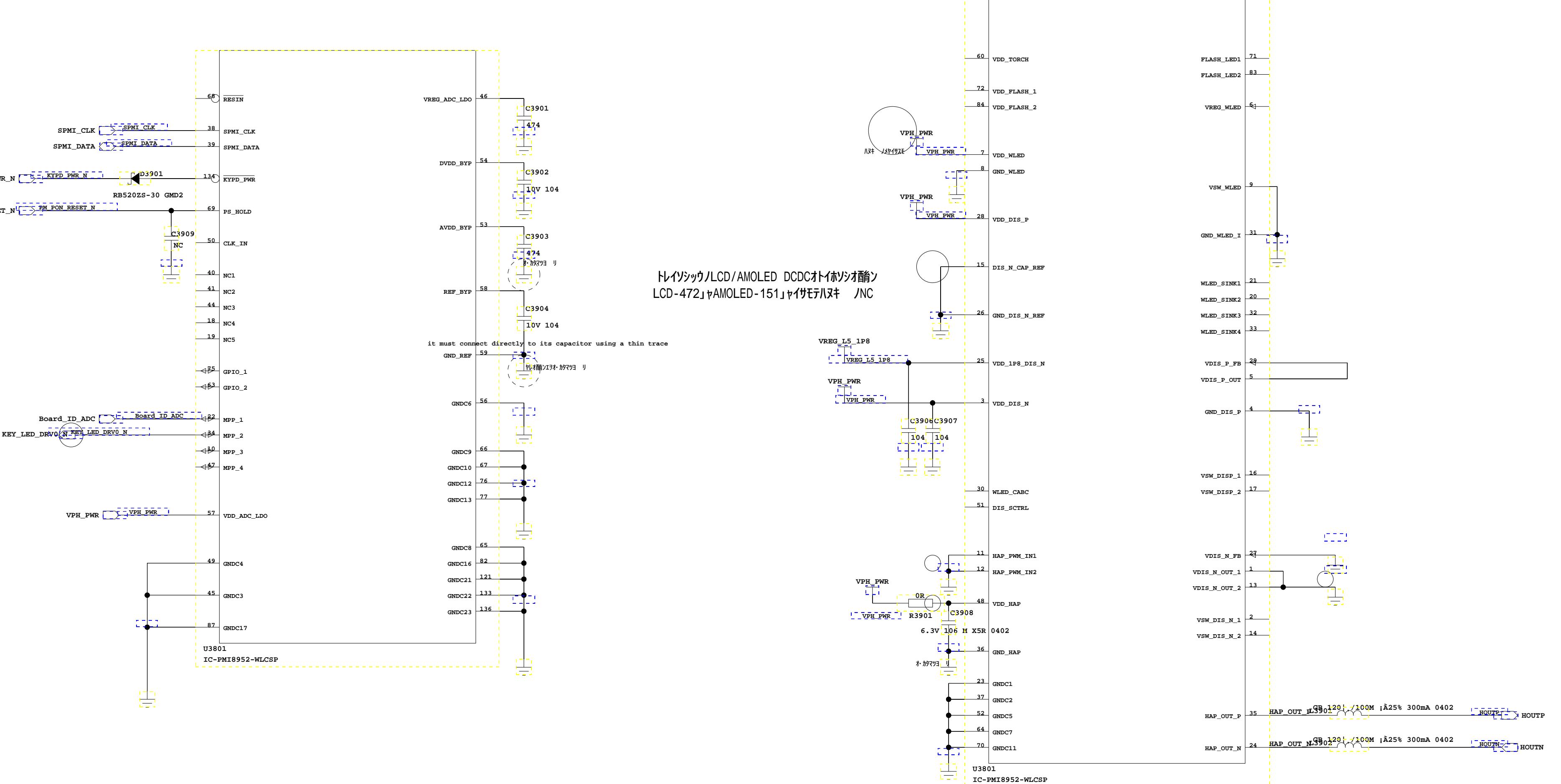
104 104

A**MSM8976-H**



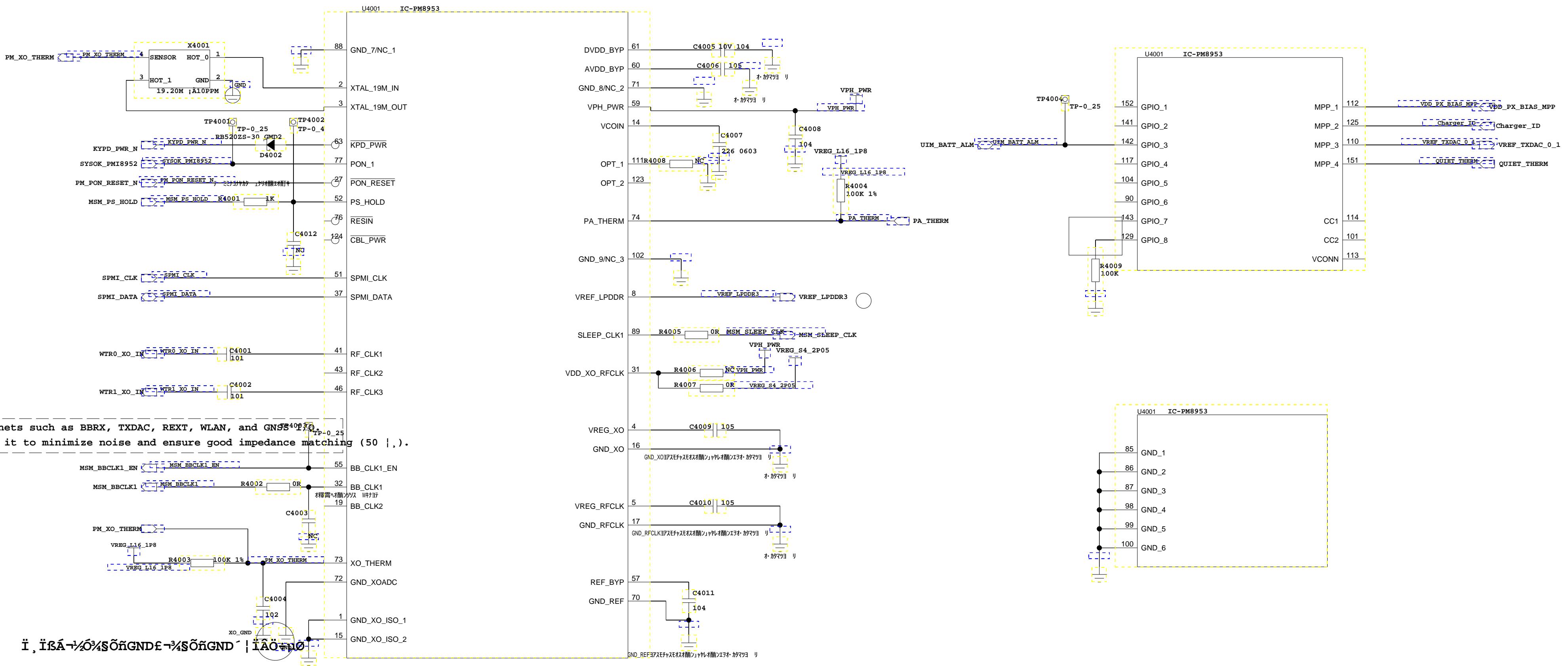
PMI8952-A



PMI8952-B**PMI8952-C**

PM8953

A



B

A

C

B

D

C

E

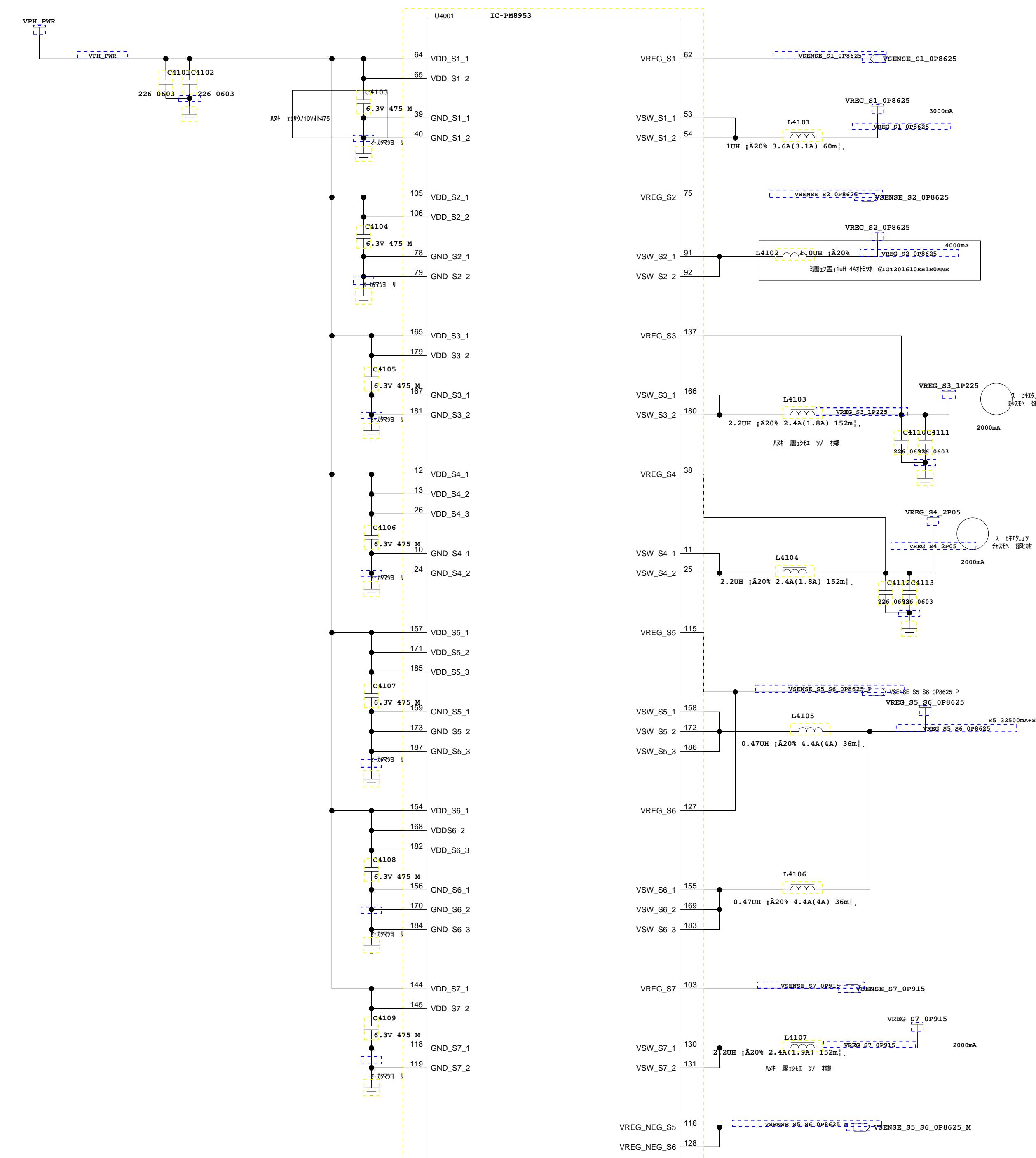
D

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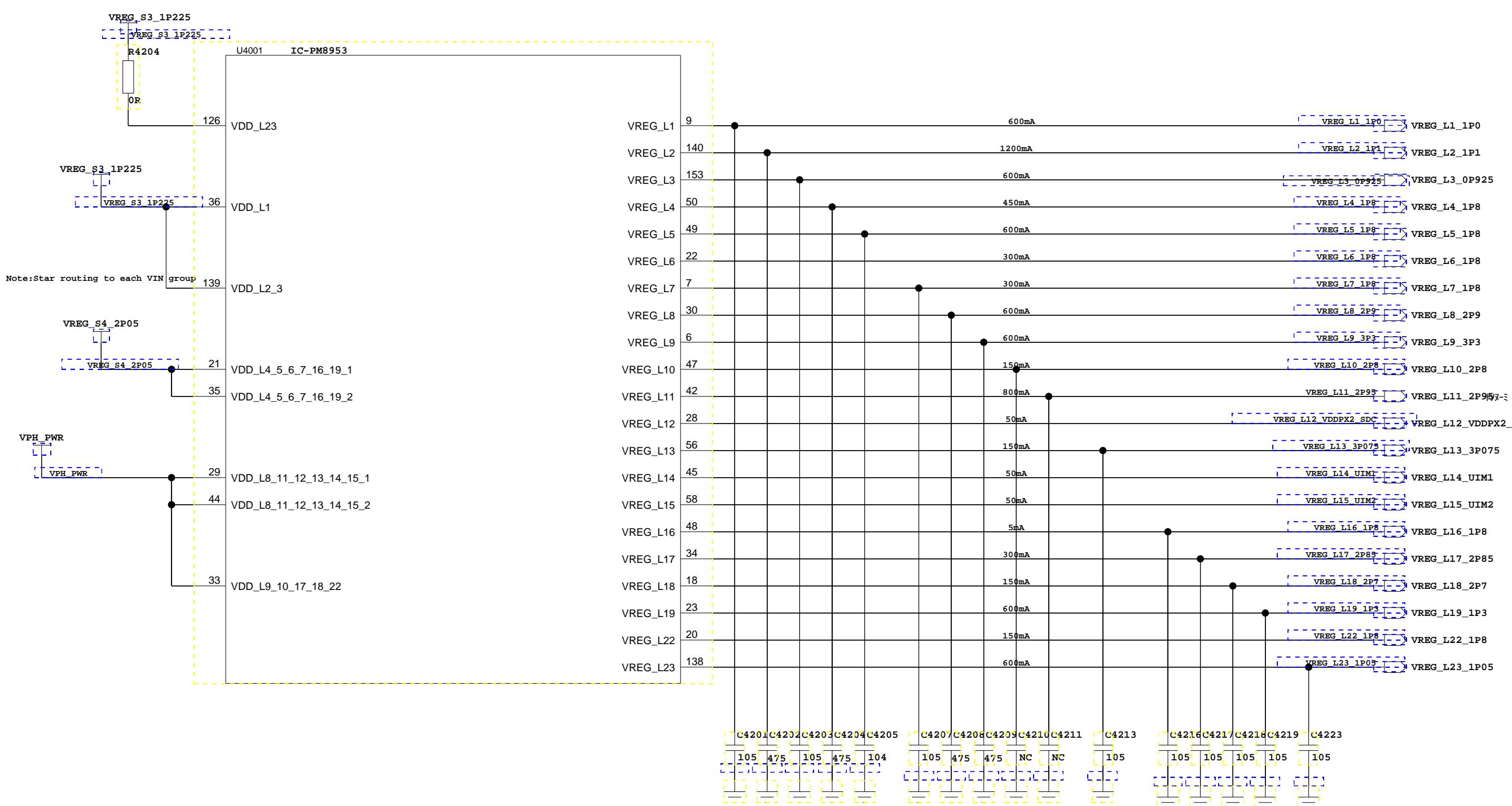
PM8953

A



A**A****B****B****C****C****D****D****E****E****F****F**

PM8953-LDO



A**A****B****B****C****C****D****D****E****E****F****F**