

P2132-A01

NVG GK208 DDR3 64BIT

DVI-I/DVI-D + HDMI + VGA

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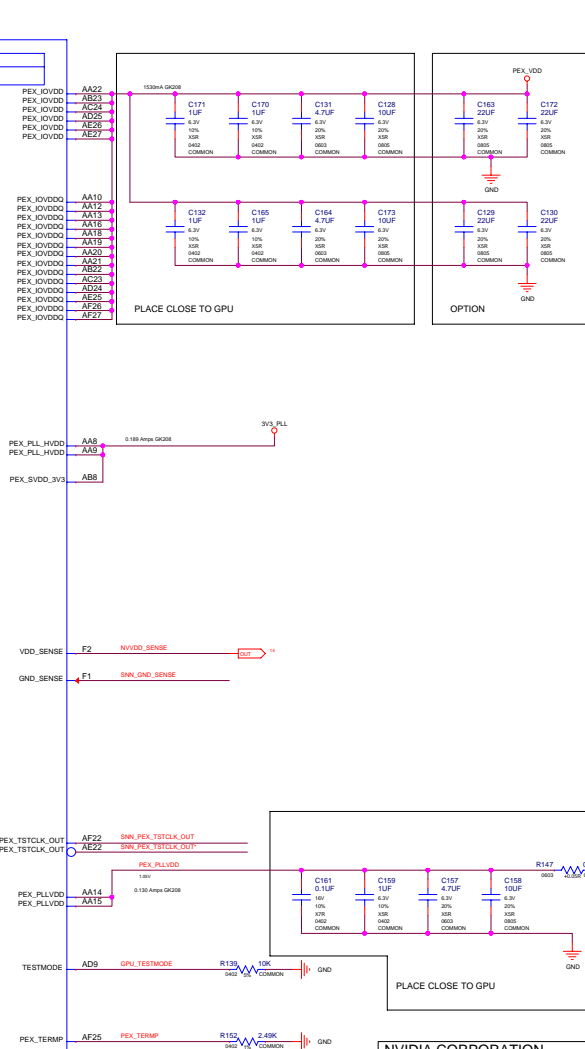
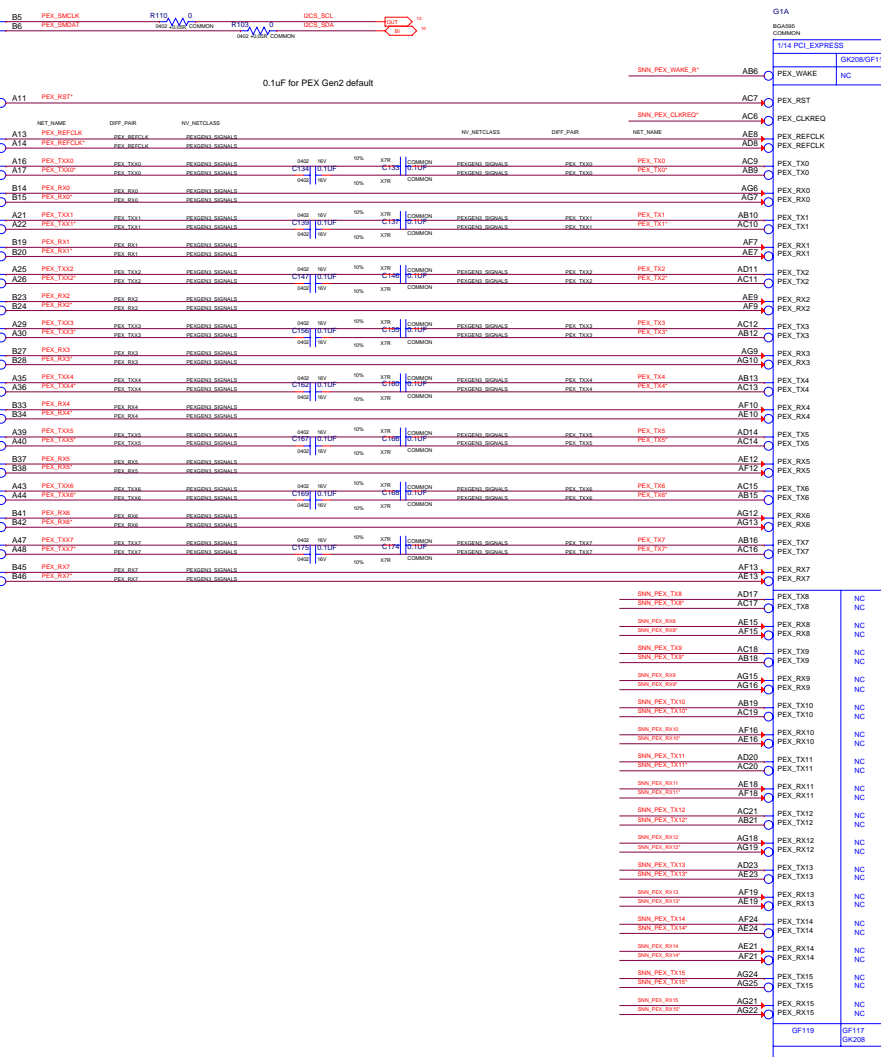
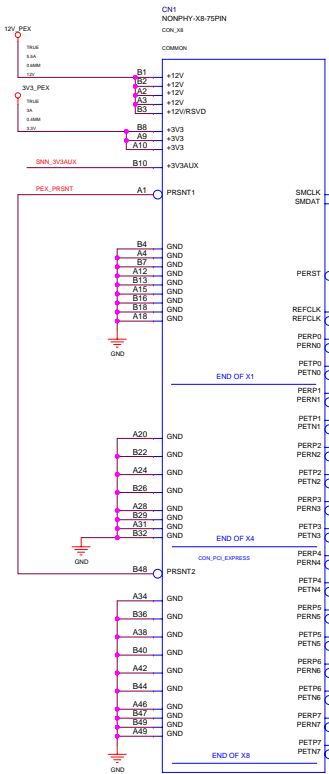
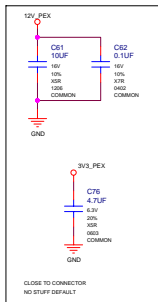
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PCI EXPRESS



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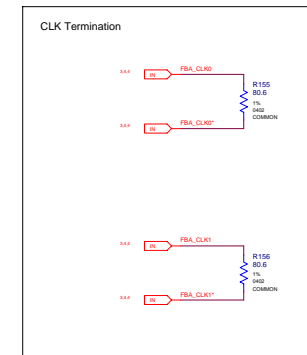
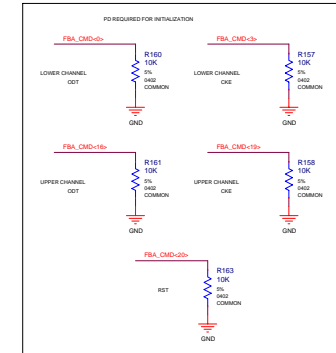
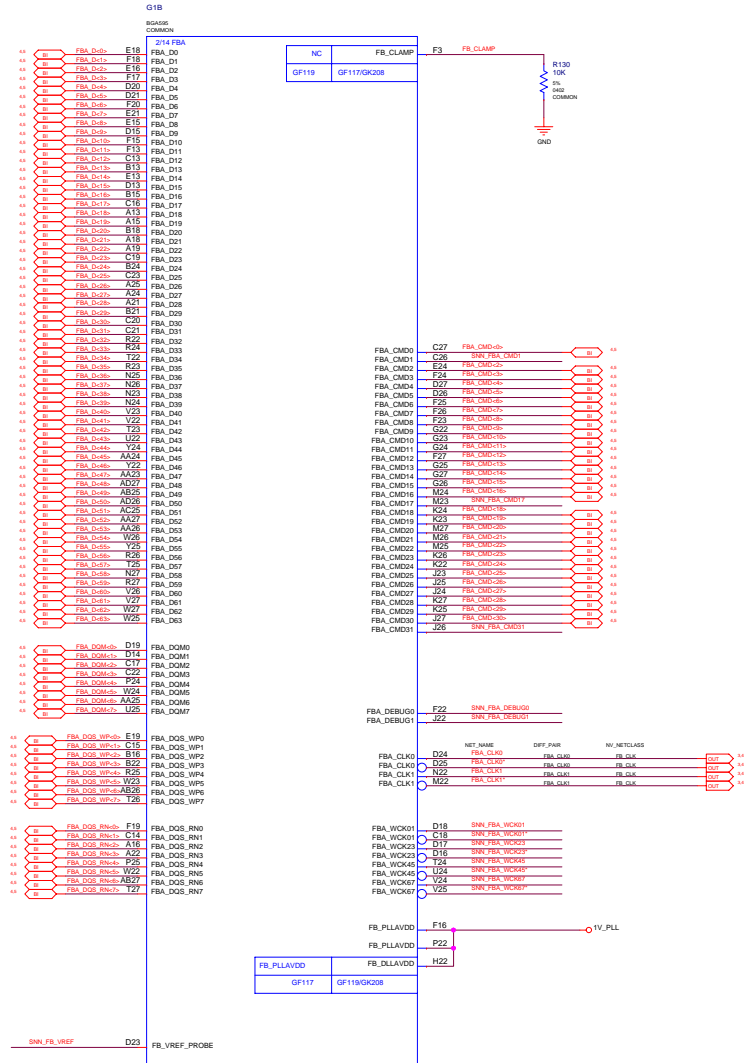
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GPU FRAME BUFFER: PARTITION A



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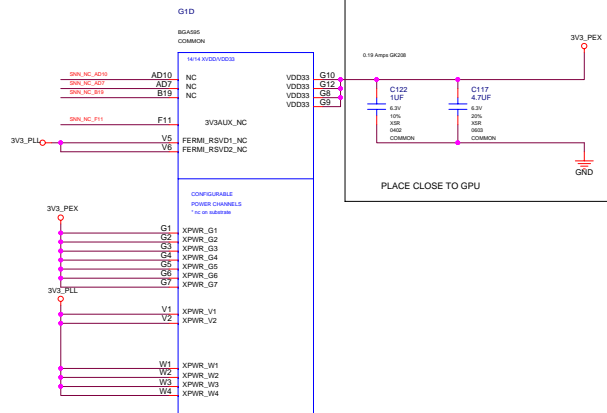
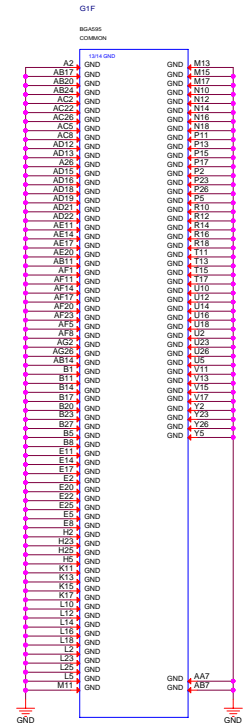
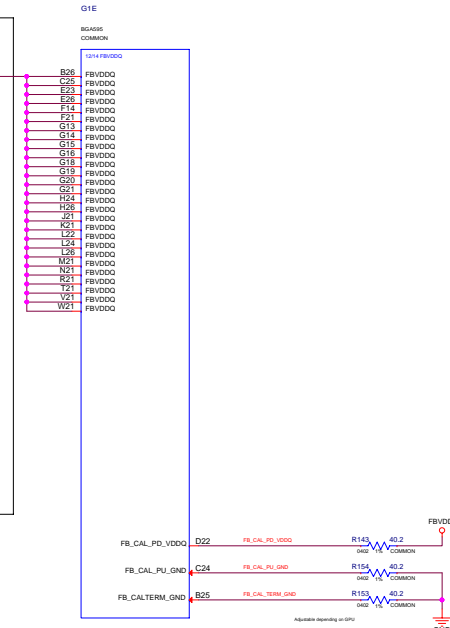
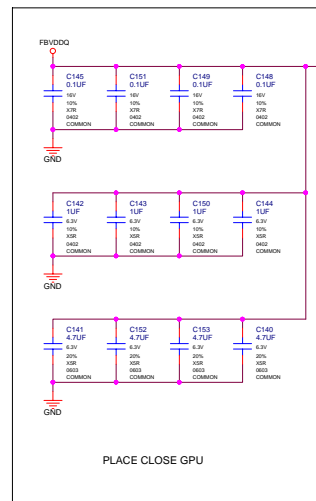
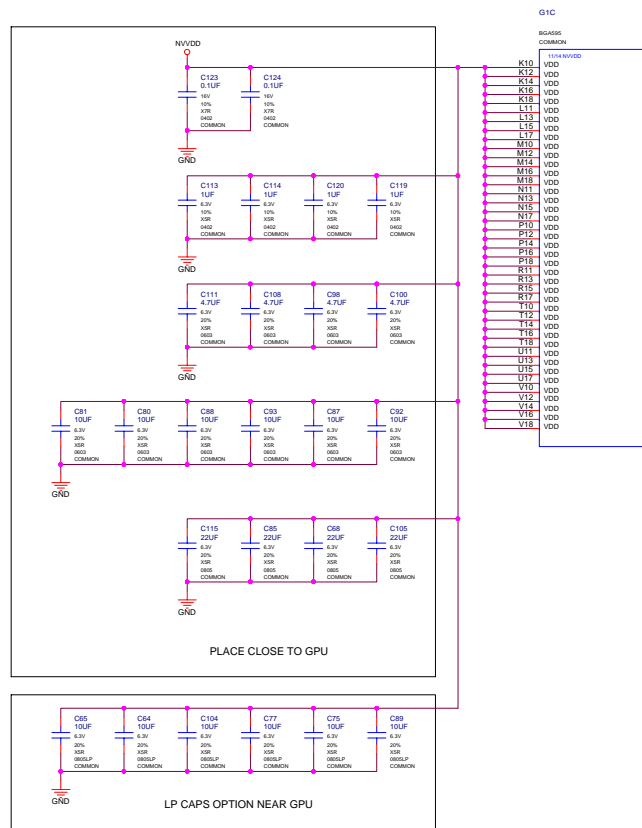
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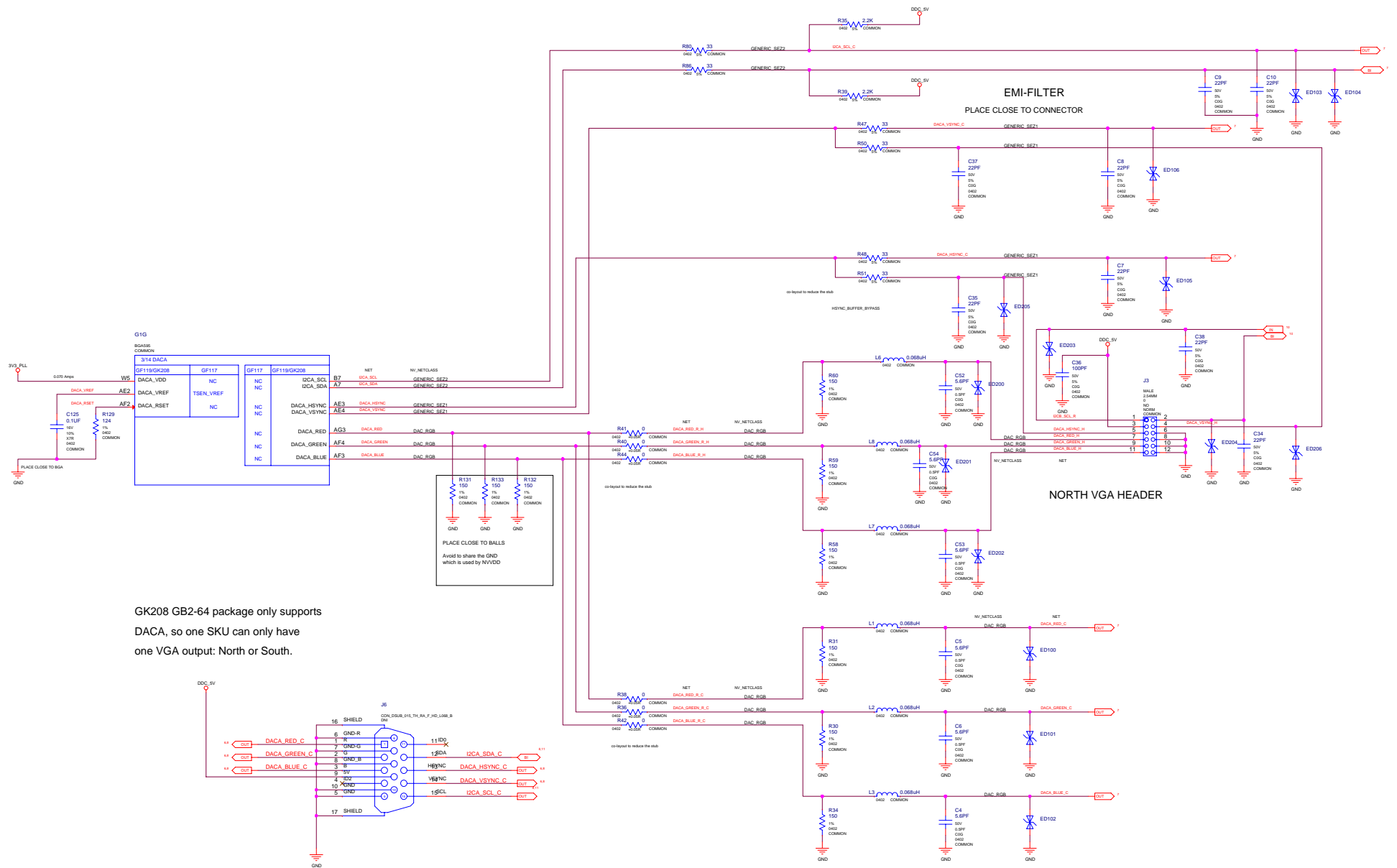
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DECOUPLING FOR CORE POWER_TOP



DACA VGA



GK208 GB2-64 package only supports
DACA, so one SKU can only have
one VGA output: North or South.

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PAGE DETAIL	DACA VGA

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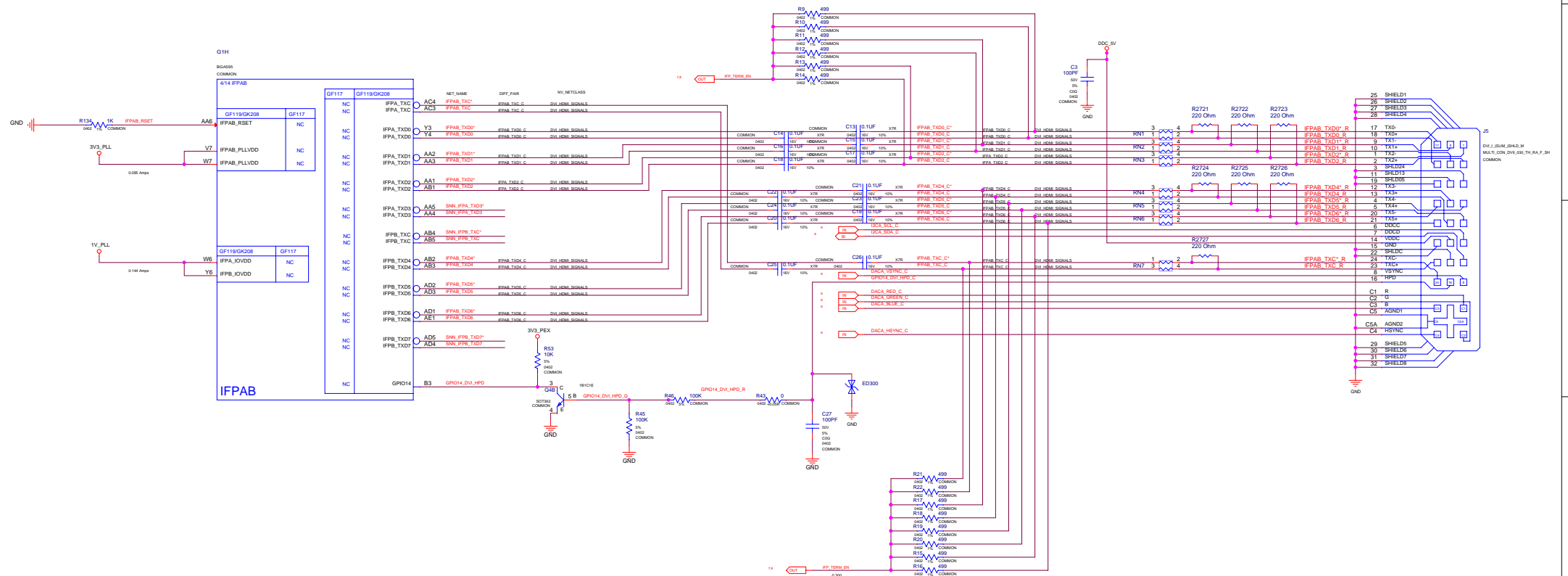
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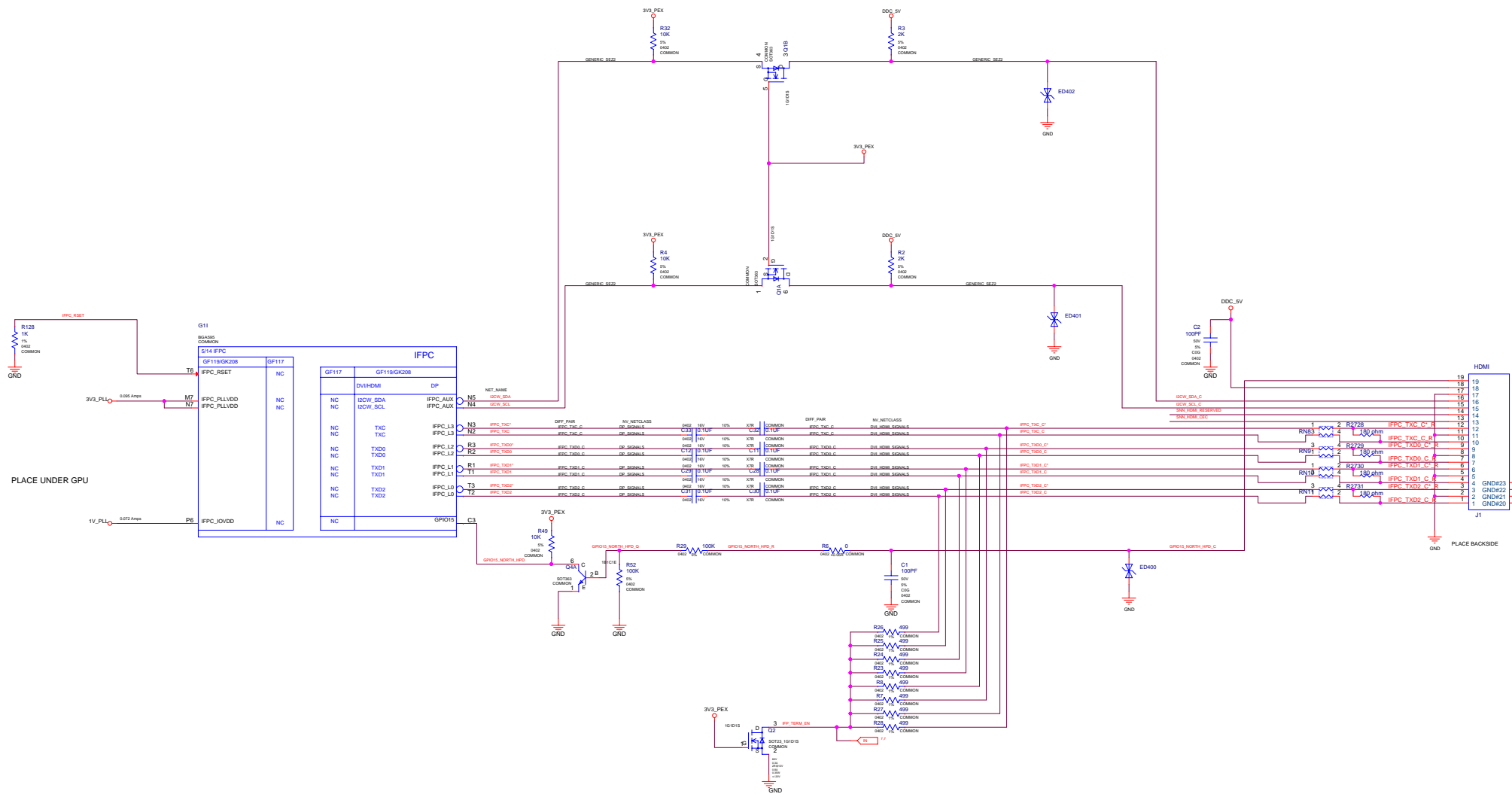
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IFPAB TMDS



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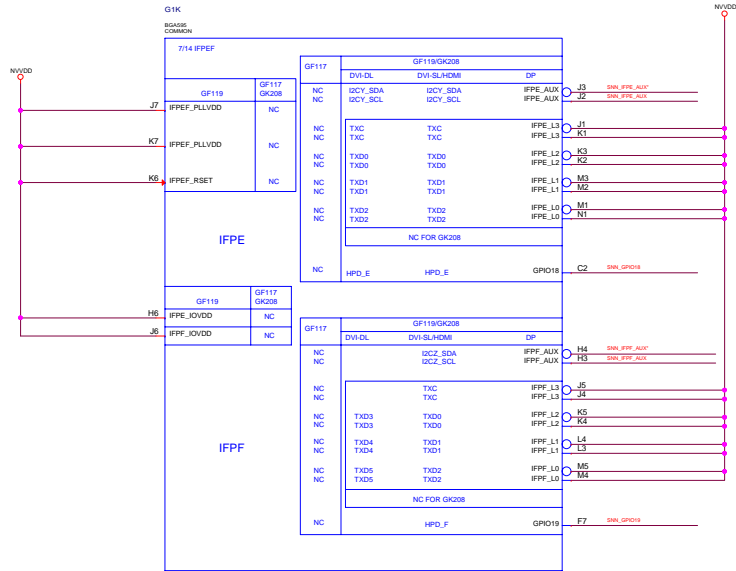
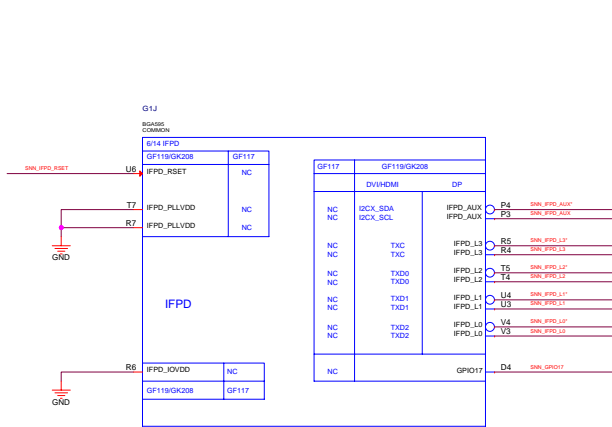
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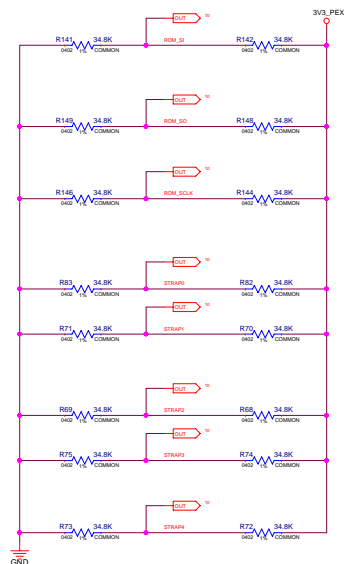
UNUSED IFPD, IFPE & IFPF



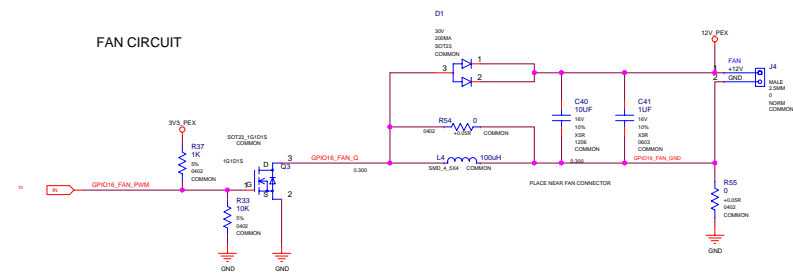
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0000	5.1K to GND	1000	5.1K to VCC
0001	10K to GND	1001	10K to VCC
0010	15K to GND	1010	15K to VCC
0011	20K to GND	1011	20K to VCC
0100	24.9K to GND	1100	24.9K to VCC
0101	30.1K to GND	1101	30.1K to VCC
0110	34.9K to GND	1110	34.9K to VCC
0111	45.3K to GND	1111	45.3K to VCC

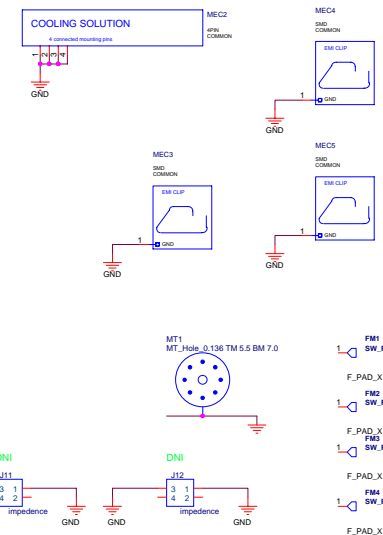
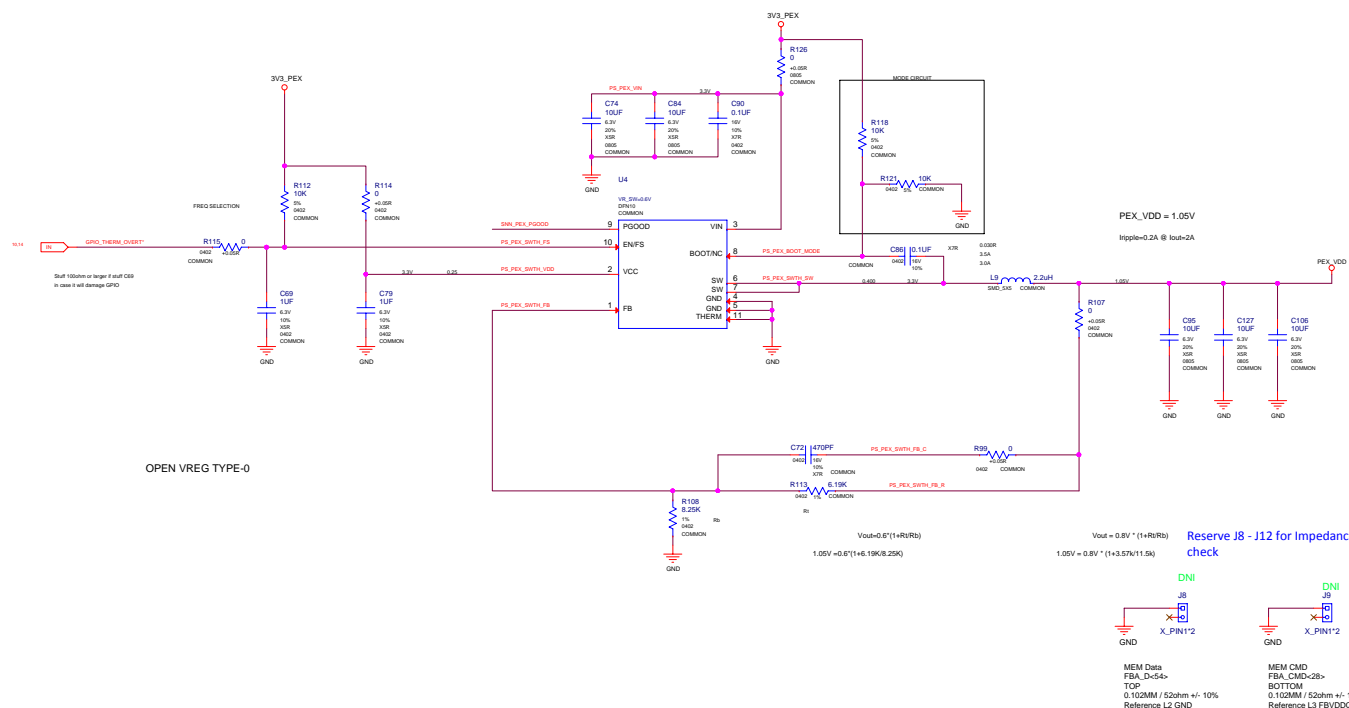
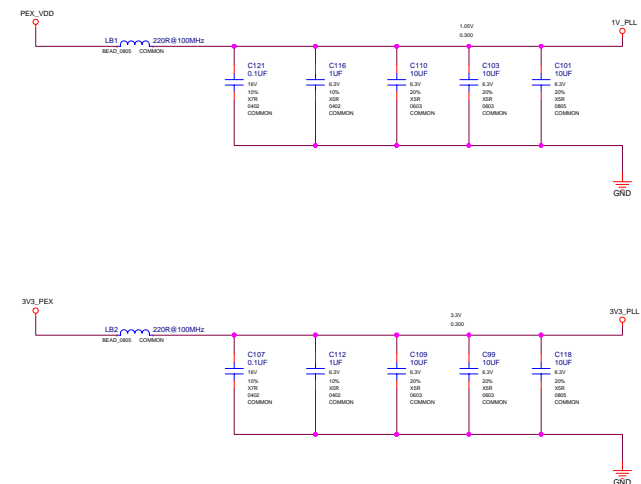
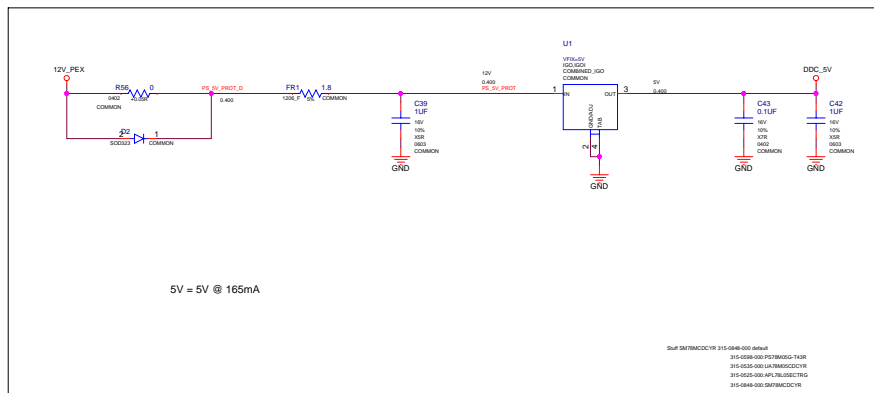
Bit Signal	Values
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Bit Signal		Values				
03	RAMCFG[2]	0000	Reserved			ROM_SI
		0001	250MnH DCR3 X16 666n Mhz			
		0010	250MnH DCR3 X16 666n Hmhz			
01	RAMCFG[1]	0011	250MnH DCR3 X16 666n Samsung			
		0100	120MnH DCR3 X16 666n Mhz			
00	RAMCFG[0]	0101	120MnH DCR3 X16 666n Hmhz			
		0110	120MnH DCR3 X16 666n Hmhz			
		0111	120MnH DCR3 X16 666n Samsung			
03	FR[0]	01	03000			ROM_SO
		10	20000			
03	FR[0]					
01	SMB_ALT_ADDR	0	DISABLED	1	ENABLED	
01	VGA_DEVICE	0	DISABLED	1	ENABLED	
03	PCI_DEVICE_EXT	SWU0	0	SWU1	TBD	ROM_SCL1
	SUB_VENDOR	0	NO_BIOS	1	BIOS	
01	PCI_DEVICE_EXT_BITS	SWU0	0	SWU1	TBD	
00	PEX_FLX_EN_TERM100	0	DISABLED	1	ENABLED	
01	USERREQ	0000	NOT			Strap0
01	USERREQ					
01	USERREQ					
01	USERREQ					
03	SGSC_PCFG0_LUT_ADDR[0]	0000	OSGTPOT_DEFAULT	1000	OSGTPOT_HTHMS	Strap1
		0001	MOBILE_DEFAULT	1001	MOBILE_HTHMS_LAMP	
		0010	MOBILE_HTHMS_LAMP	1010	MOBILE_HTHMS_LAMP	
01	SGSC_PCFG0_LUT_ADDR[0]	0011	MOBILE_HTHMS_LAMP	1011	MOBILE_HTHMS_LAMP	
		0100	MOBILE_HTHMS_LAMP	1100	MOBILE_HTHMS_LAMP	
		0101	MOBILE_HTHMS_LAMP	1101	MOBILE_HTHMS_LAMP	
		0110	MOBILE_HTHMS_LAMP	1110	MOBILE_HTHMS_LAMP	
01	SGSC_PCFG0_LUT_ADDR[0]	0111	MOBILE_HTHMS_LAMP	1111	MOBILE_HTHMS_LAMP	
03	PCI_DEVICE	SWU0	0000			Strap2
01	PCI_DEVICE0					
01	PCI_DEVICE1					
01	PCI_DEVICE3					
03	SORL_EXPOSED	0	DISABLED	1	ENABLED	Strap3
01	SORL1_EXPOSED	0	DISABLED	1	ENABLED	
01	SORL1_EXPOSED	0	DISABLED	1	ENABLED	
01	SORL1_EXPOSED	0	DISABLED	1	ENABLED	
03	POE_SPEED_CHANGE_GEN0	0	DISABLED	1	ENABLED	Strap4
01	POE_MAX_SPEED	0	GEN0	1	GEN3	
01	DP_PLL_VDD_3V3	0	USERV0	1	USERV3	



POWER SUPPLY I: PEXVDD, 5V



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PAGE DETAIL	POWER SUPPLY: PEXVDD, 5V

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NVVDD Power Sequencing



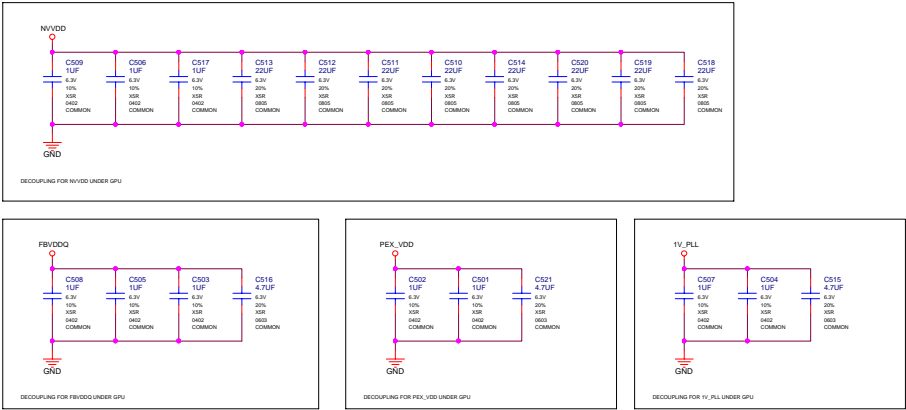
Bootup Voltage set to 0.9000V

PWM	Freq	Vref(V)
APW6722:	300kHz	0.6V
EM5303AGE:	300kHz	0.6V
RT8120AGS:	300kHz	0.6V
AP3586AMTG:	300kHz	0.6V

R2:1	R2:2	R2:3	R2:4	R2:5
60.4k	29.4k	14.3k	7.15k	3.57k

values for Rb1, Rb2
Rb3, Rb4 and Rb5
will be calculated base
on the needed voltage

DECOUPLING FOR CORE POWER_BOTTOM



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