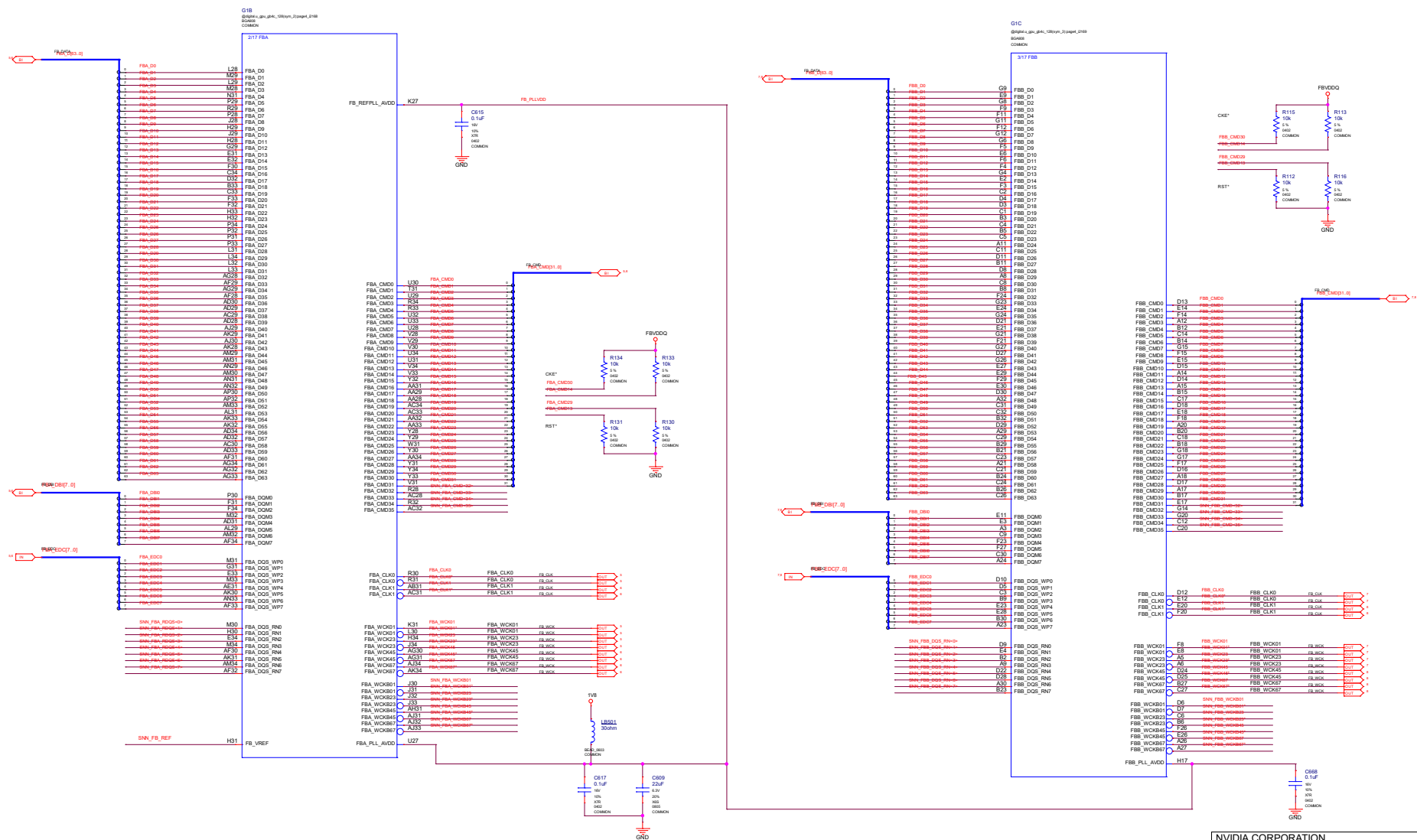
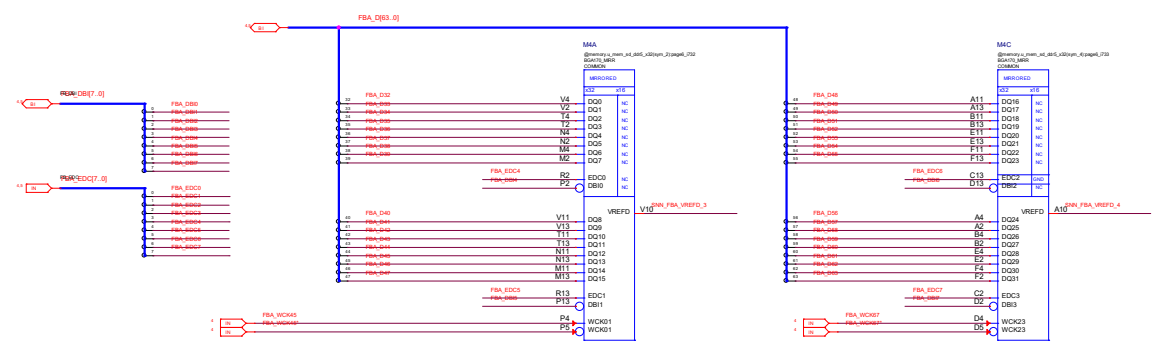
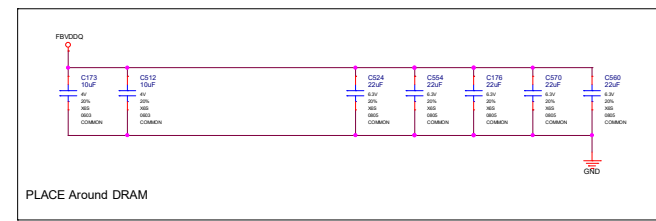
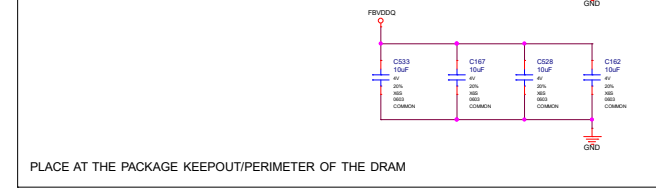
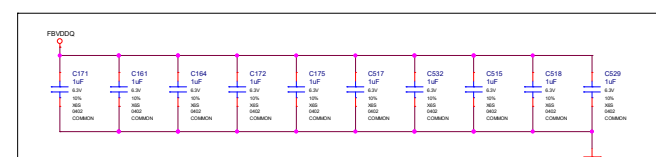
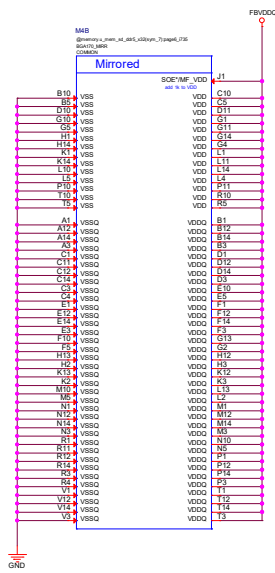



# MEMORY: GPU Partition A/B





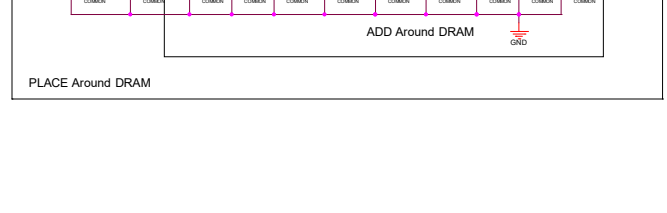
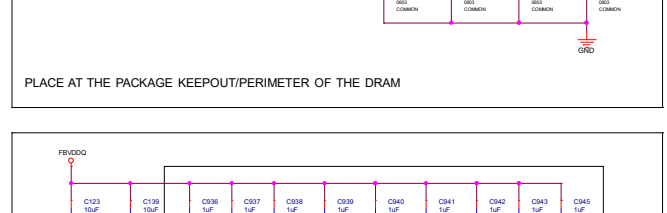
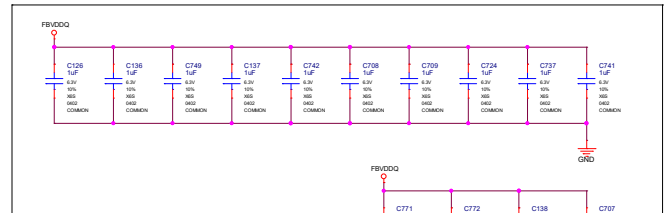
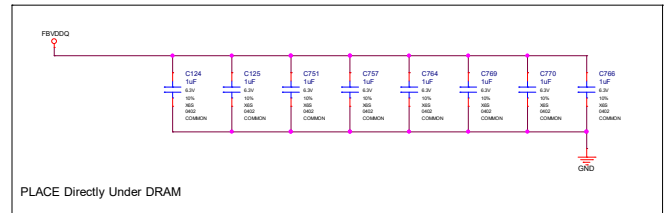
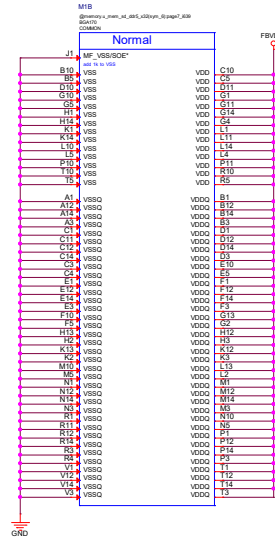
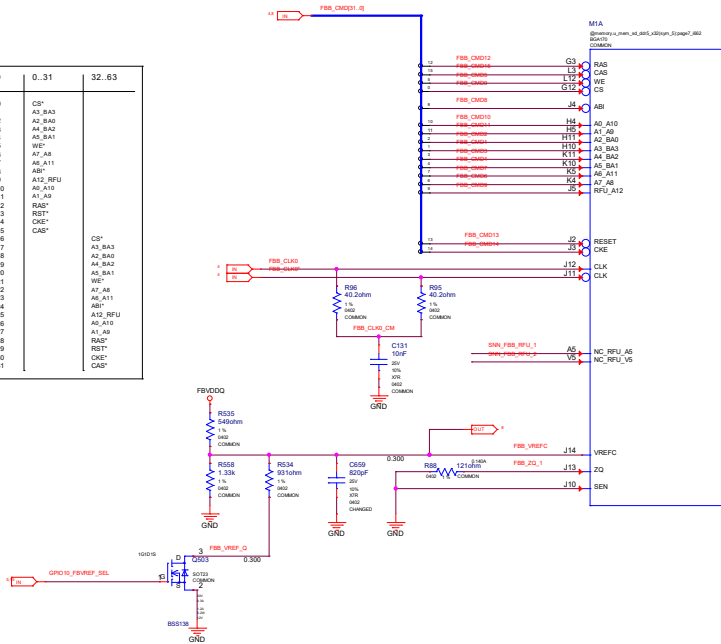
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[illegible]

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NV_PN	600-1G210-BASE-SCH		
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# MEMORY: FBB Partition 31..0

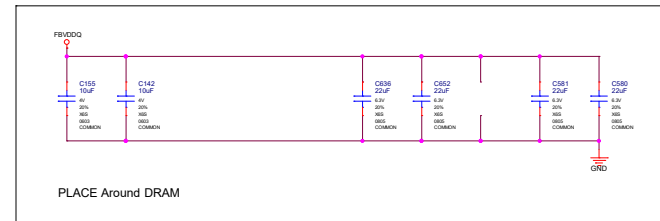
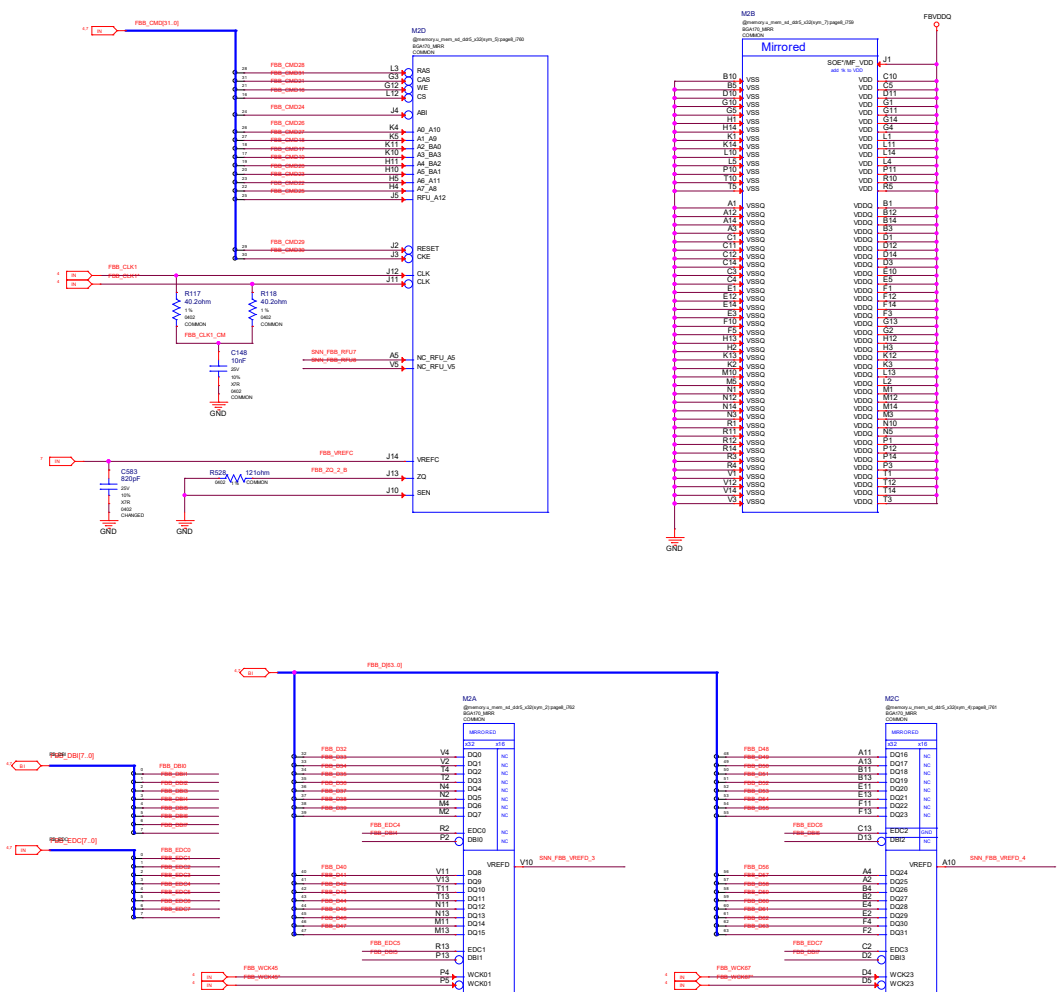
CMD	0..31	32..63
CMD0	CS*	
CMD1	A2_BA2	
CMD2	A2_BA0	
CMD3	A4_BA2	
CMD4	A4_BA1	
CMD5	WE*	
CMD6	A7_A8	
CMD7	AB*	
CMD8	A6_A11	
CMD9	A12_RFU	
CMD10	A0_A10	
CMD11	A1_A9	
CMD12	BA0*	
CMD13	RBT*	
CMD14	CKE*	
CMD15	CAS*	
CMD16		CS*
CMD17		A2_BA3
CMD18		A2_BA2
CMD19		A4_BA2
CMD20		A4_BA1
CMD21		WE*
CMD22		A7_A8
CMD23		AB*
CMD24		A6_A11
CMD25		A12_RFU
CMD26		A0_A10
CMD27		A1_A9
CMD28		BA0*
CMD29		RBT*
CMD30		CKE*
CMD31		CAS*



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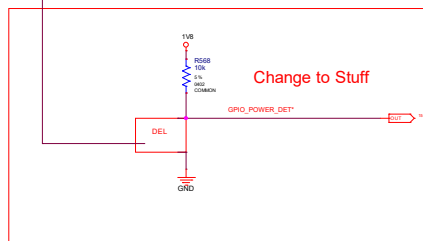
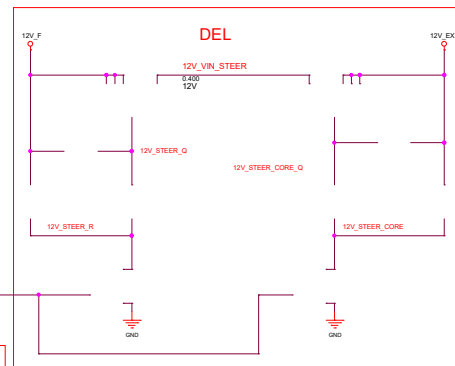
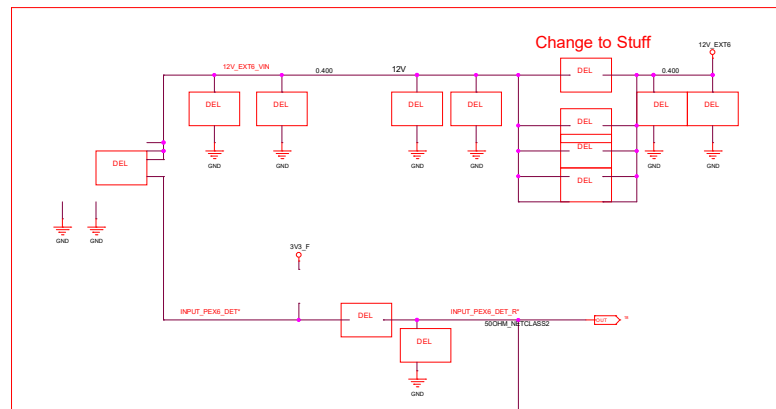
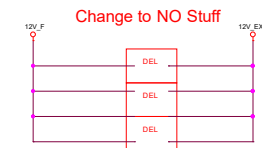
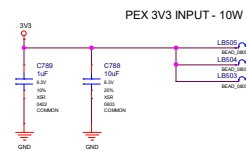
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CMD	0.31	32.63
CMD0	CS*	
CMD1	AS, BA3	
CMD2	AS, BA2	
CMD3	AS, BA2	
CMD4	AS, BA1	
CMD5	WE*	
CMD6	AF, AB	
CMD7	AE, A11	
CMD8	AB*	
CMD9	A12, RFU1	
CMD10	AS, A10	
CMD11	A1, AB	
CMD12	RA5*	
CMD13	RS1*	
CMD14	ONE*	
CMD15	CMD*	
CMD16	CS*	
CMD17	AF, BA3	
CMD18	AS, BA2	
CMD19	AS, BA2	
CMD20	AS, BA1	
CMD21	WE*	
CMD22	AF, AB	
CMD23	AE, AB1	
CMD24	AB*	
CMD25	A12, RFU1	
CMD26	AS, A10	
CMD27	A1, AB	
CMD28	RA5*	
CMD29	RS1*	
CMD30	ONE*	
CMD31	CMD*	



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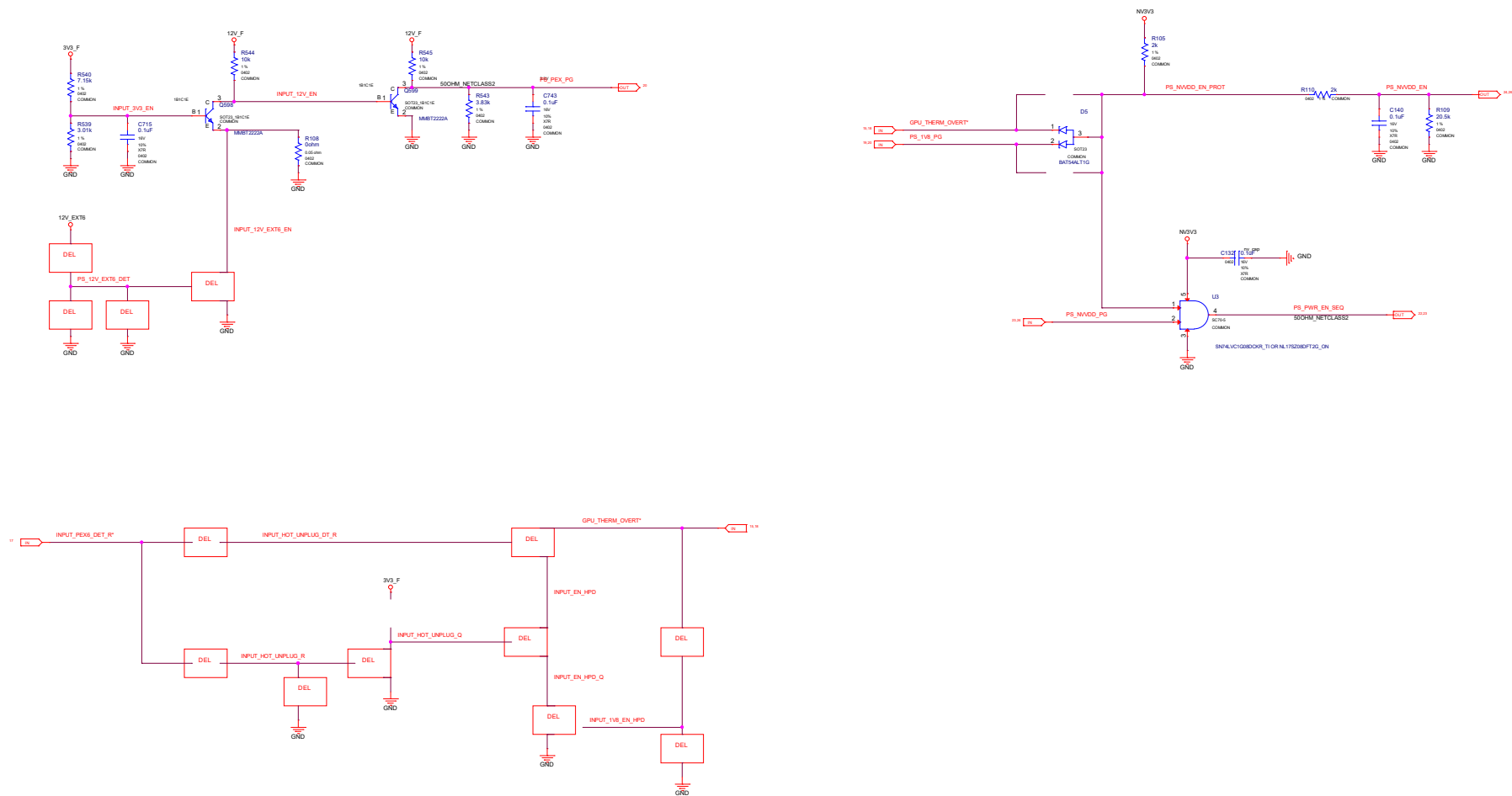
## Power Inputs and Filtering



ASSEMBLY		ASSEMBLY DESCRIPTION		SANTA CLARA, CA 95050, USA
PAGE DETAIL		Power Input and Filtering		
MAY DISCLOSE ALL STANDARDS.				
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		TDS REV	P0210-B00	PAGE 17 OF 27
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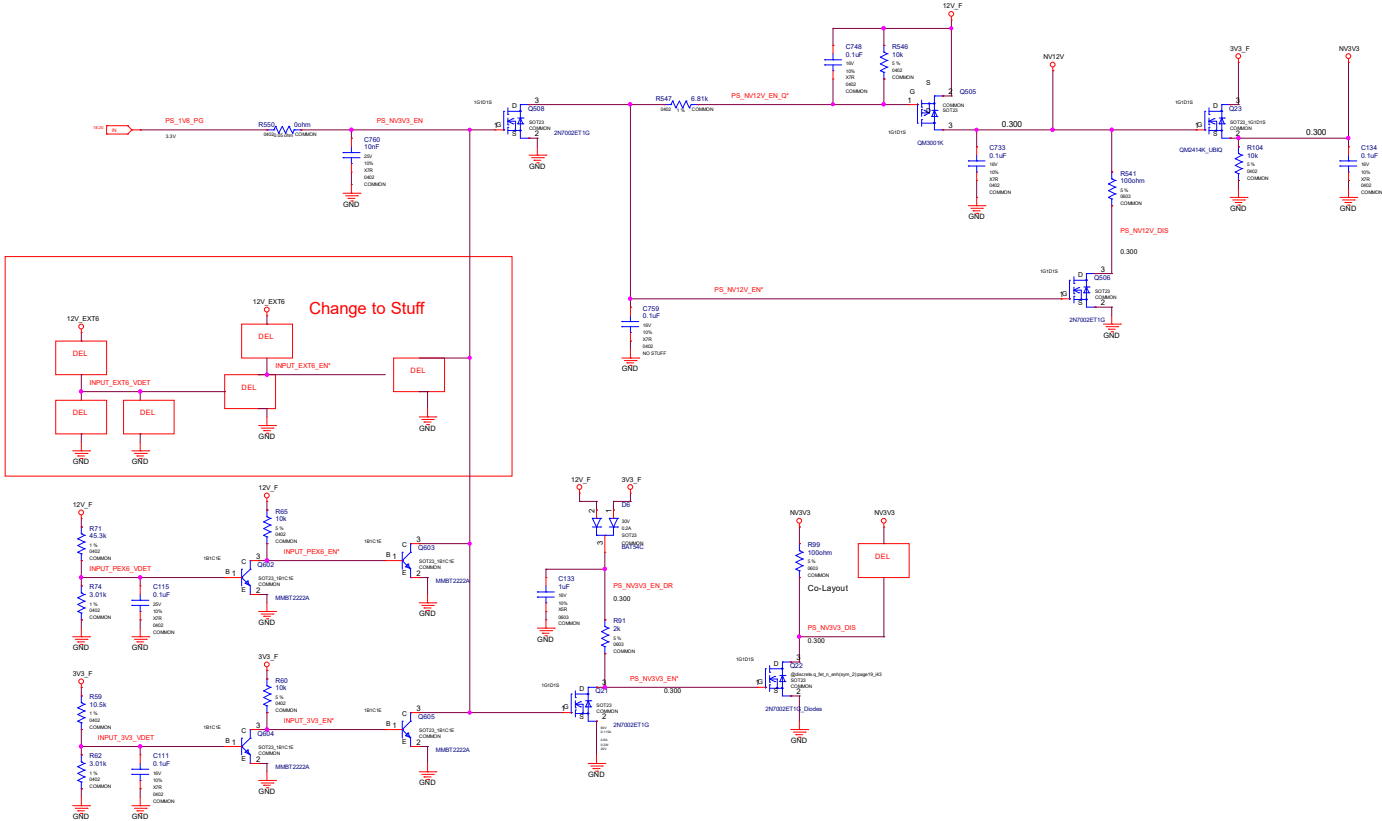
## Power sequence



ASSEMBLY	ASSEMBLY DESCRIPTION	SANTA CLARA, CA 95050, USA	
PAGE DETAIL	Power Sequence	<div> <div> NV_PN 600-1G210-BASE-SCH </div> </div>	
MAY DISCLOSE ALL STANDARDS		<div> <div> PCB REV PQ210-B00 </div> <div> BOM REV A </div> </div>	<div> <div> PAGE 18 OF 27 </div> <div> DATE 18 DEC 2015 </div> </div>

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PS: NV3V3 & NV12V



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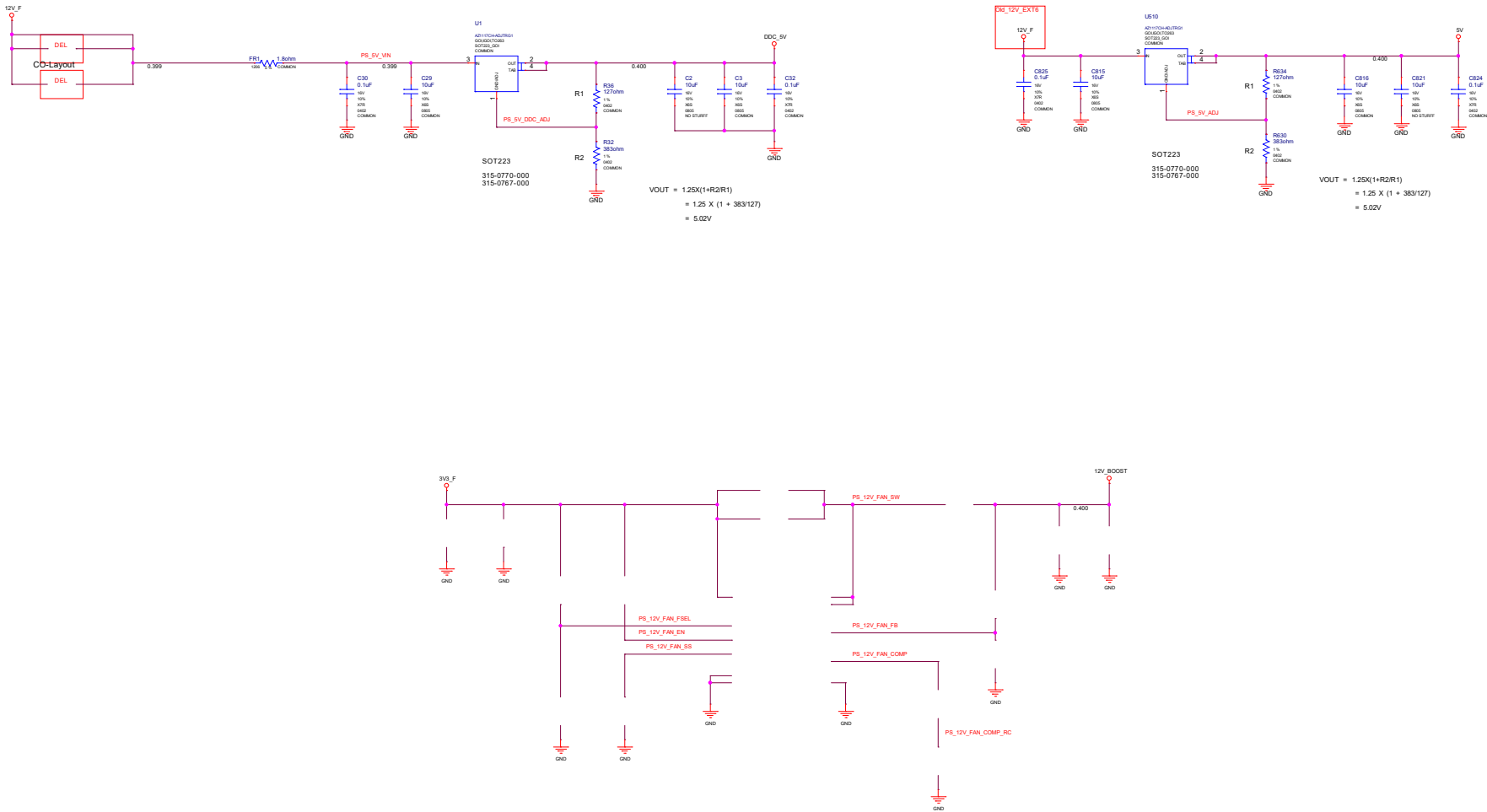
ASSEMBLY	"ASSEMBLY DESCRIPTION"
PS: NV3V3 & NV12V	

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PSII:5V & 12V\_FAN

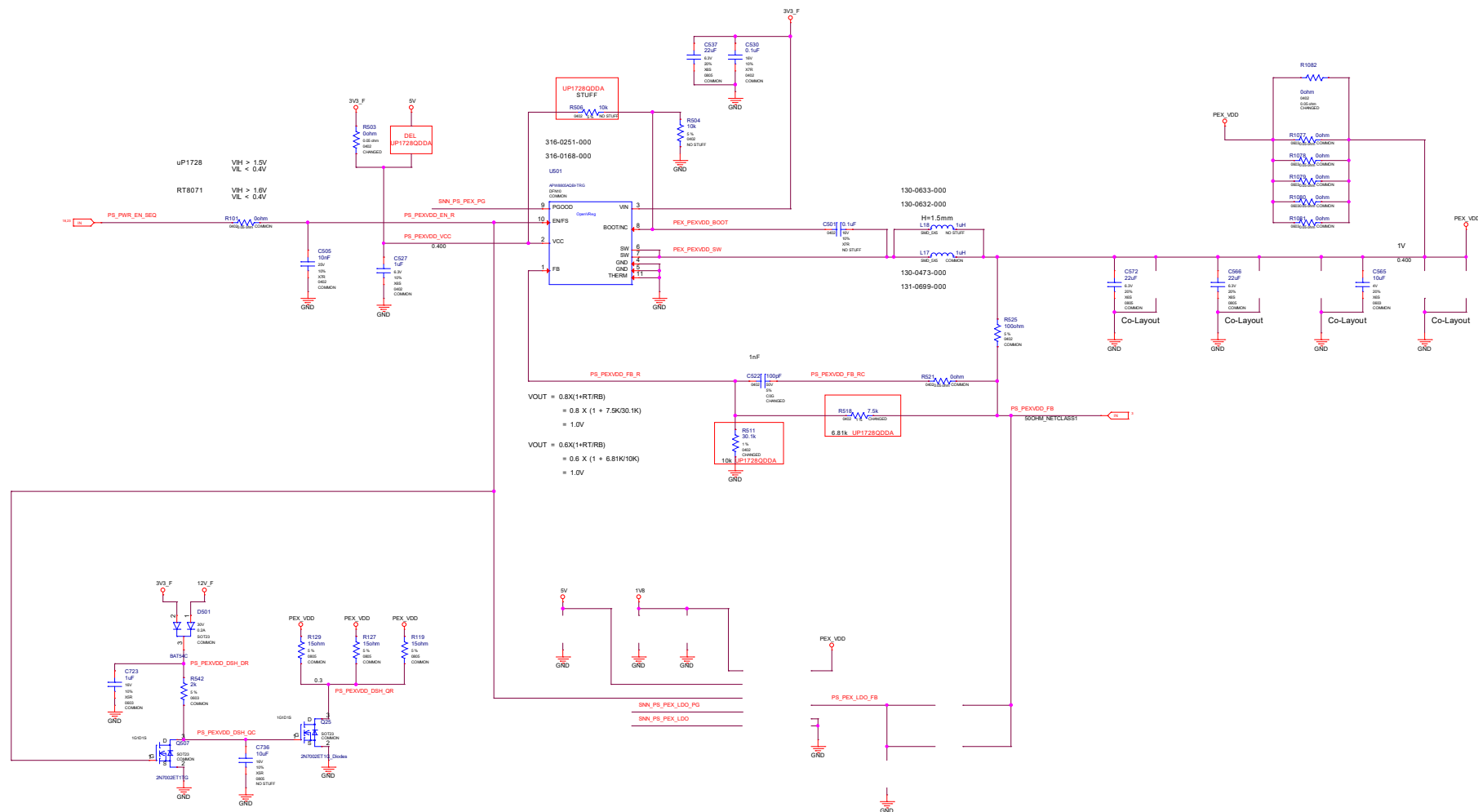


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ASSEMBLY		ASSEMBLY DESCRIPTION	
PAGE DETAIL		PSII: 5V & 12V_FAN	

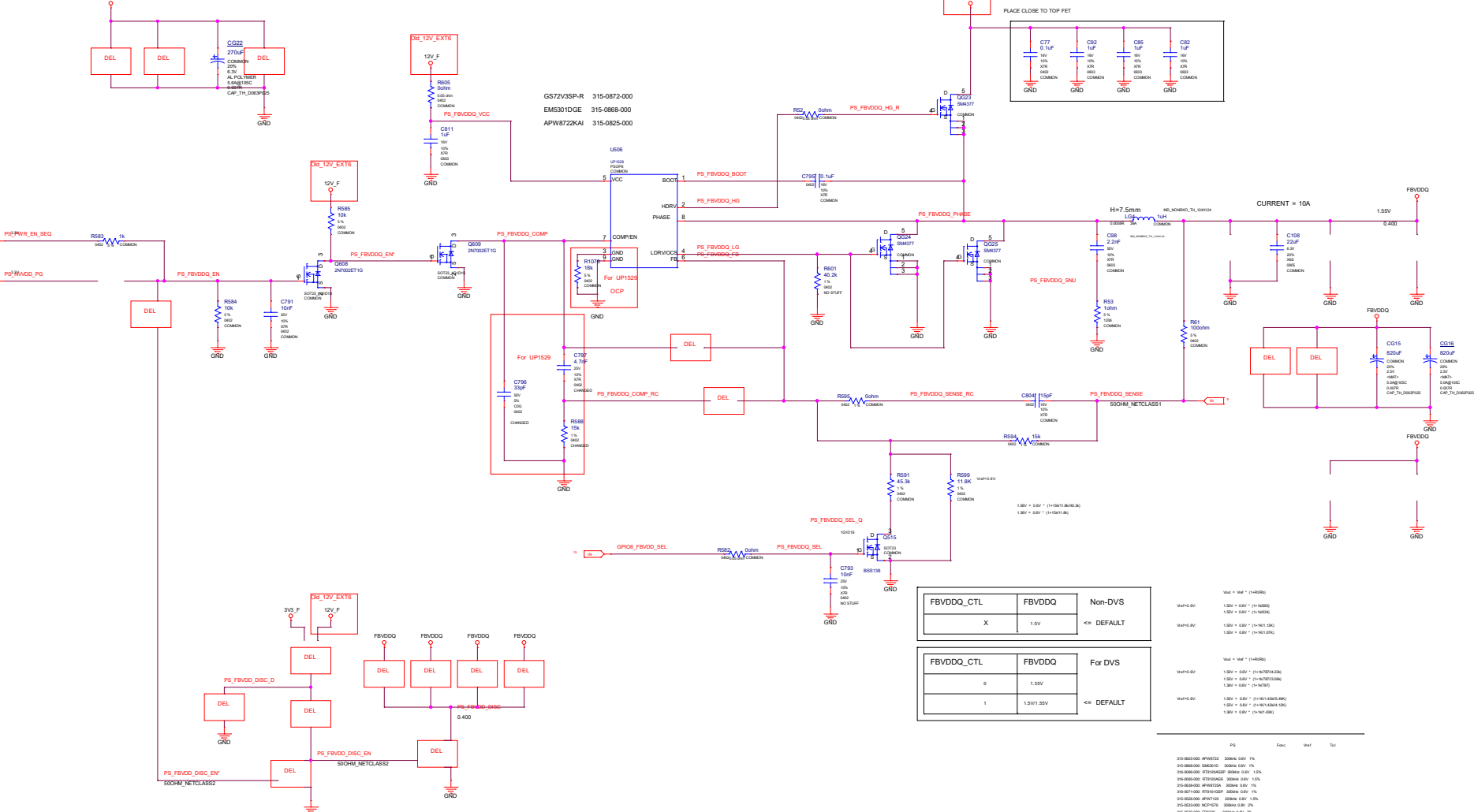
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NV_PN	600-1G210-BASE-SCH	PCB REV	PC210-B00
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## PS III: PEXVDD



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ASSEMBLY	ASSEMBLY DESCRIPTION	SANTA CLARA, CA 95050, USA
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FBVDDQ_CTL	FBVDDQ	Non-DVS
X	1.5V	<= DEFAULT

FBVDDQ_CTL	FBVDDQ	For DVS
0	1.35V	
1	1.5V/1.35V	<= DEFAULT

[illegible]

	PS	Facc	Vel	Tot
315-0025-00	APW8732	300kg	0.0V	1%
315-0068-00	SMC610	300kg	0.0V	1%
315-0094-00	RT810SAGP	300kg	0.0V	1.5%
315-0095-00	RT810SAGS	300kg	0.0V	1.5%
315-0038-00	APW8725A	300kg	0.0V	1%
315-0071-00	RT810GSP	300kg	0.0V	1%
315-0026-00	APW87G0	300kg	0.0V	1.5%
315-0033-00	NCP1579	300kg	0.0V	2%

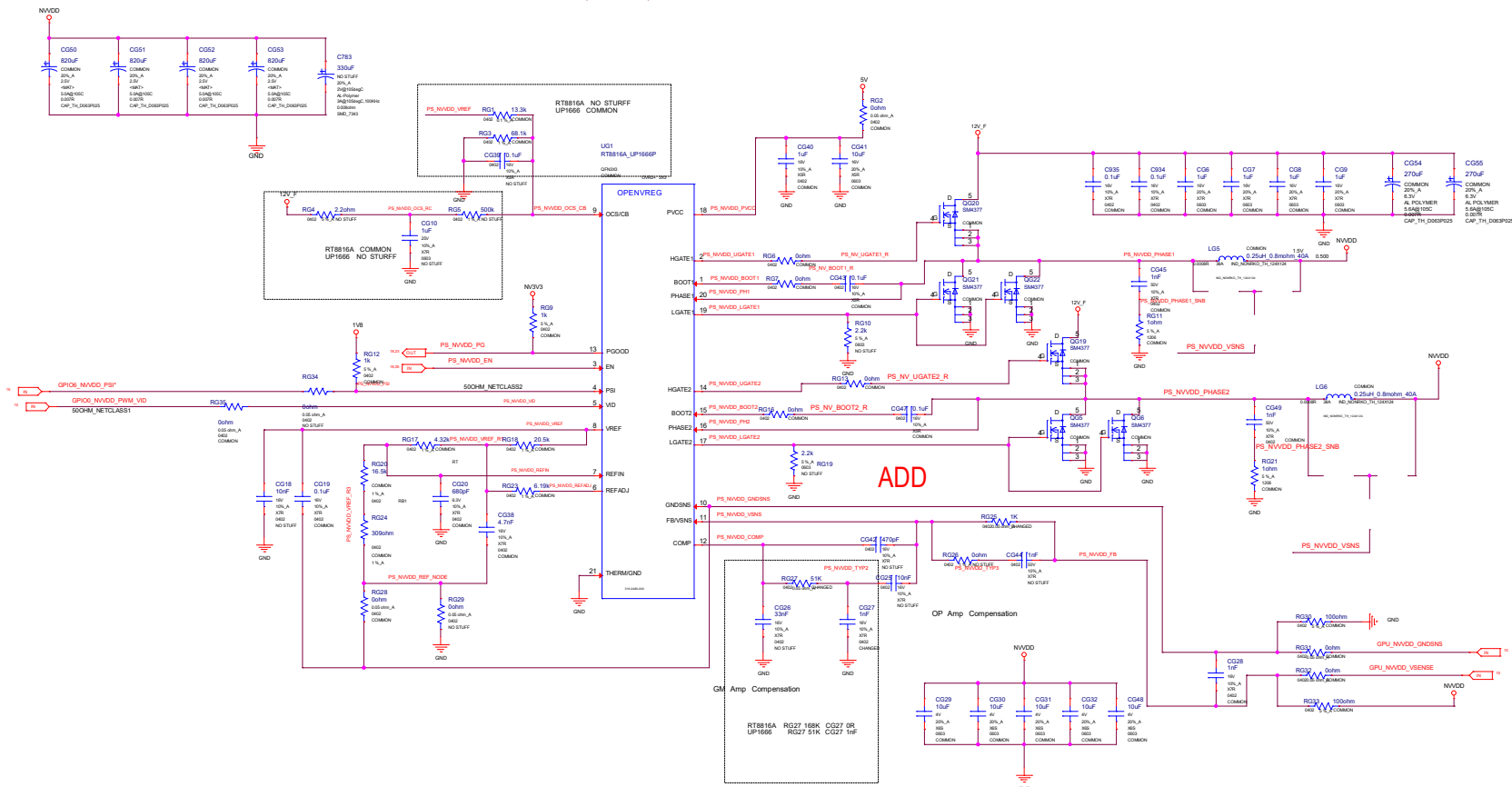


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PS V: NVVDD OVR2+1



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ASSEMBLY	ASSEMBLY DESCRIPTION
PAGE DETAIL	PS V: NVVDD OVR2+1

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PS V: NVVDD Phase 1,2


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ASSEMBLY	ASSEMBLY DESCRIPTION
PCB DETAIL	PS V: NVVDD Phase 1,2

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PS V: NVVDD Phase 3

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ASSEMBLY	<ASSEMBLY DESCRIPTION>
PCB DETAIL	PS V: NVVDD Phase 3

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