

HISTORY :
1. MS-8808 VER:1A 8MB/32MB SDRAM
2."0" MEANS "RESERVE"
3."1" MEANS "OPTIONAL"

DEFAULT nVIDIA RIVA TNT2 Model 64

OPTIONAL nVIDIA Vanta

VER:1.0 V.S VER:1A

1. R63,R64,R65 Change Placement near to D3,D4,D5

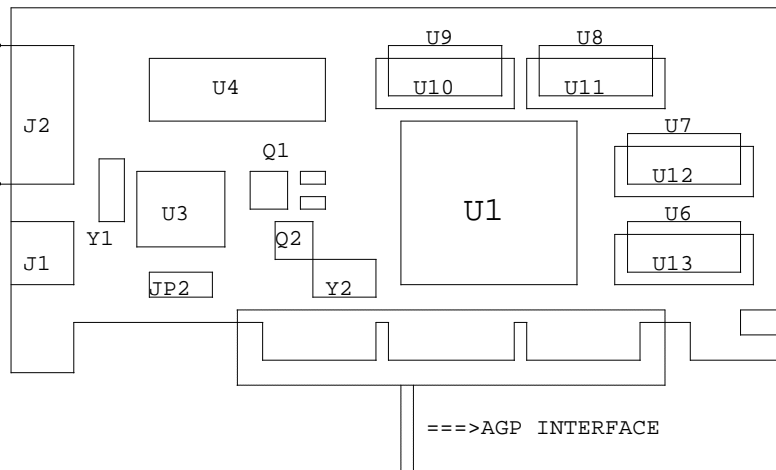
1. FB5,FB6 Uninstall.

MS-8808

N1996

FCC

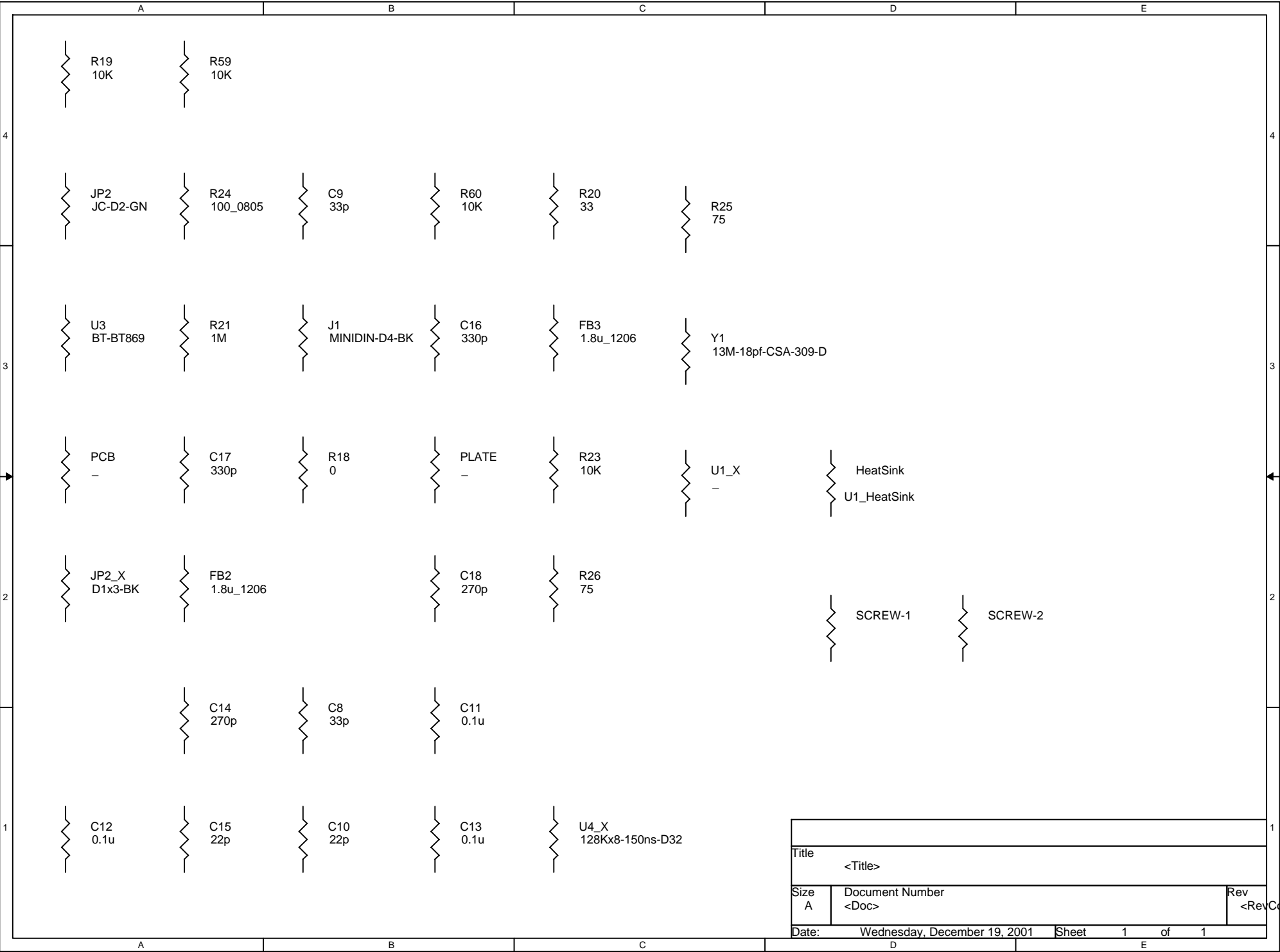
CE

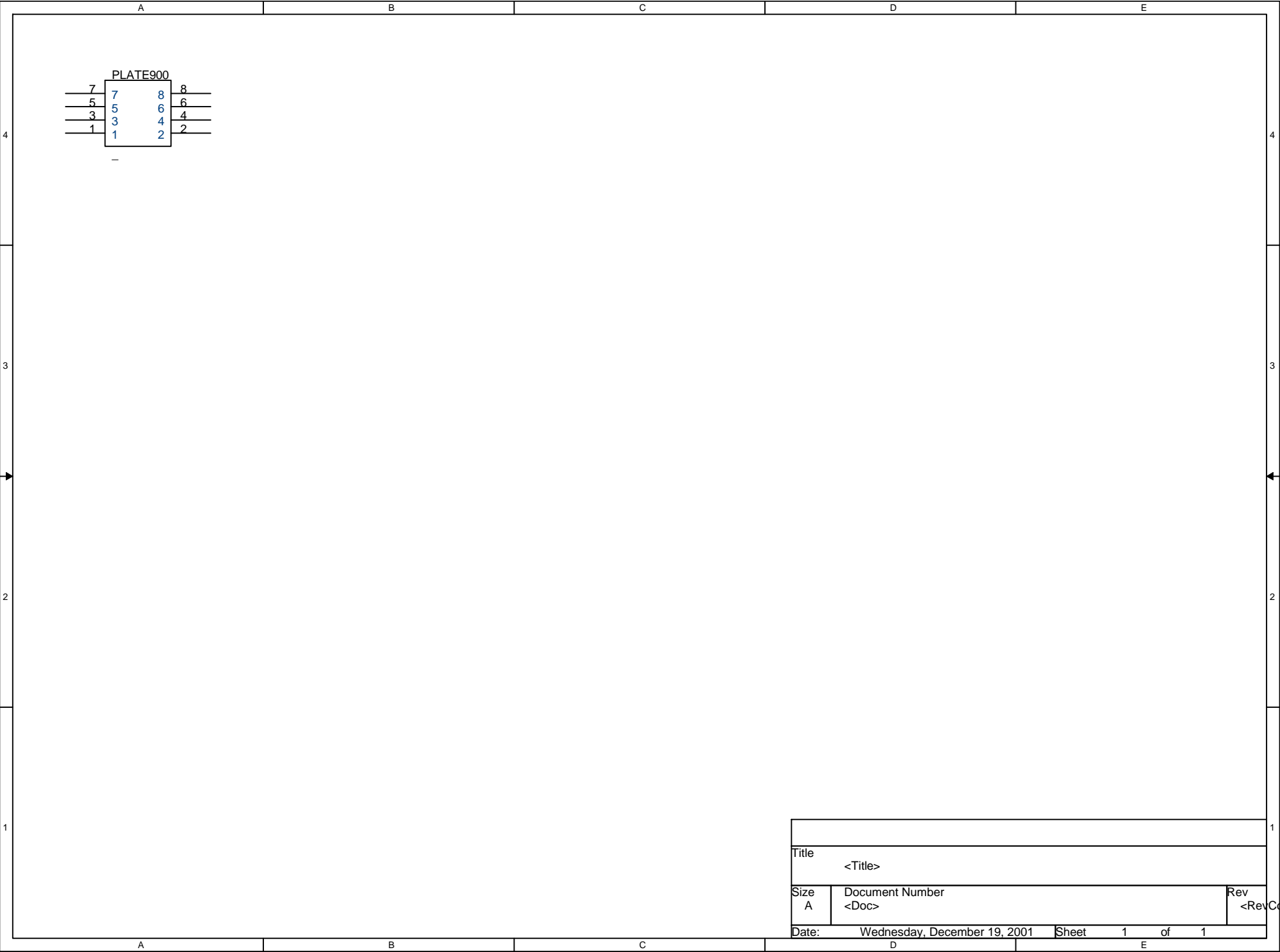


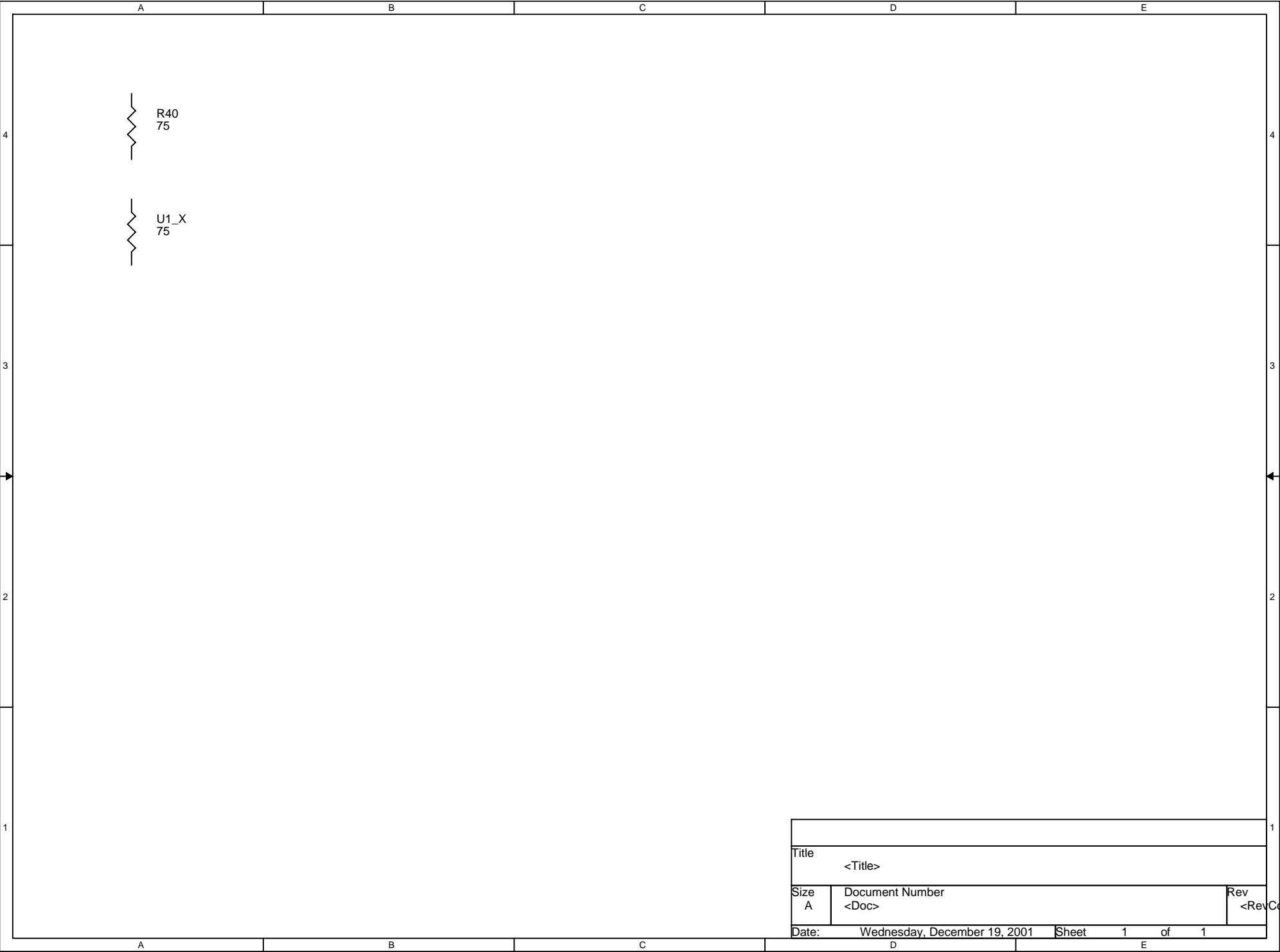
U1:nVIDIA VANTA/TNT2 TM64 AGP GRAPHICS PROCESSOR
U3:BROOKTREE BT868/869 TV-OUT (OPTIONAL)
U4:VGA BIOS PLCC
U6,U7,U8,U9:SDRAM 1M*16 8MB
U10,U11,U12,U13:SDRAM 4M*16 32MB
Y1:CRYSTAL 13.50MHZ TV-OUT (OPTIONAL)
Y2:CRYSTAL 14.31818MHZ
J1:S-VIDEO CONNECTOR TV-OUT (OPTIONAL)
J2:VGA CONNECTOR
JP2:TV-OUT NTSC/PAL JUMPER (OPTIONAL)
Q1:MOSFET - N
Q2:TL431 SMD

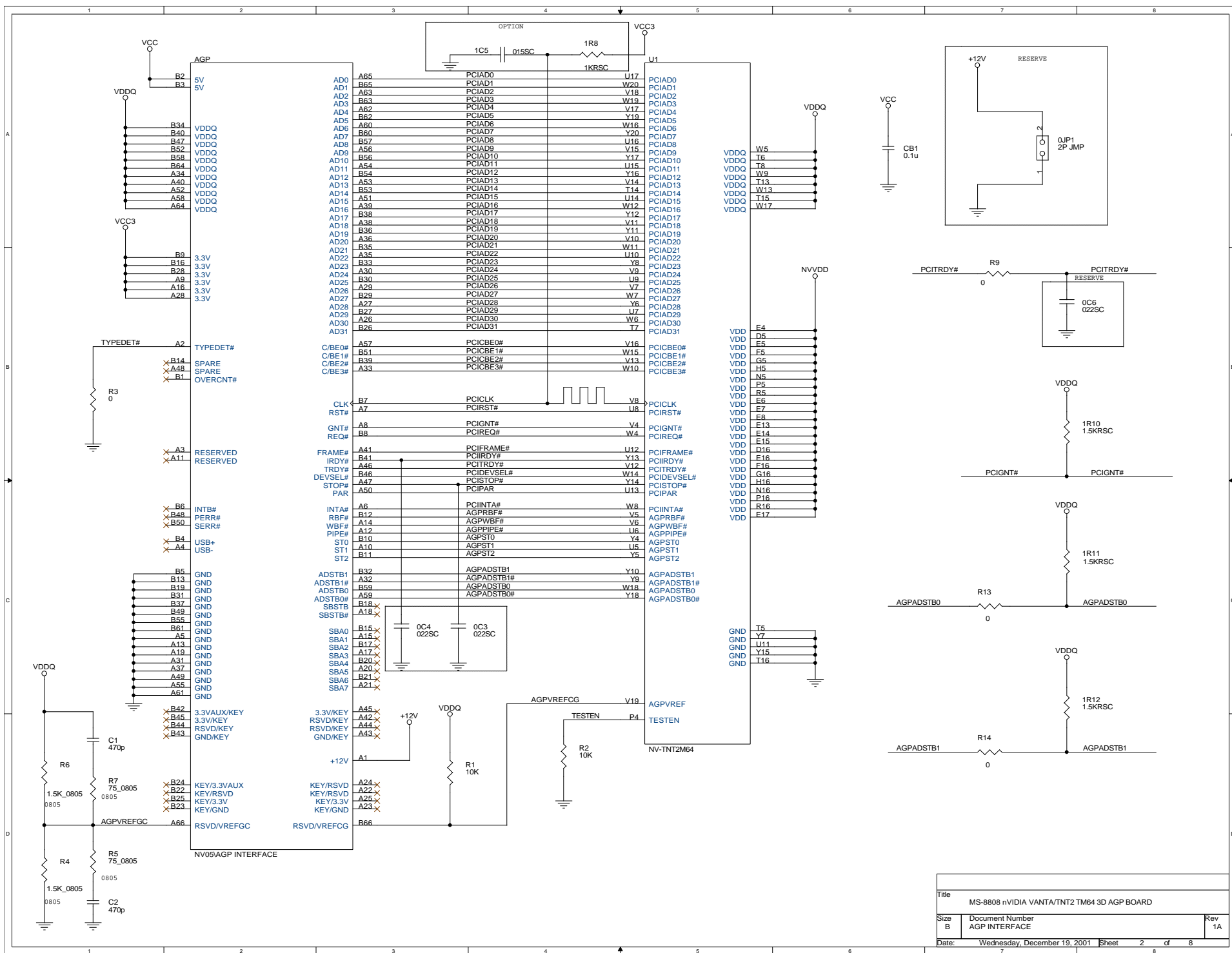
LINK
P2.SCH
P3.SCH
P4.SCH
P5.SCH
P6.SCH
P7.SCH
P8.SCH

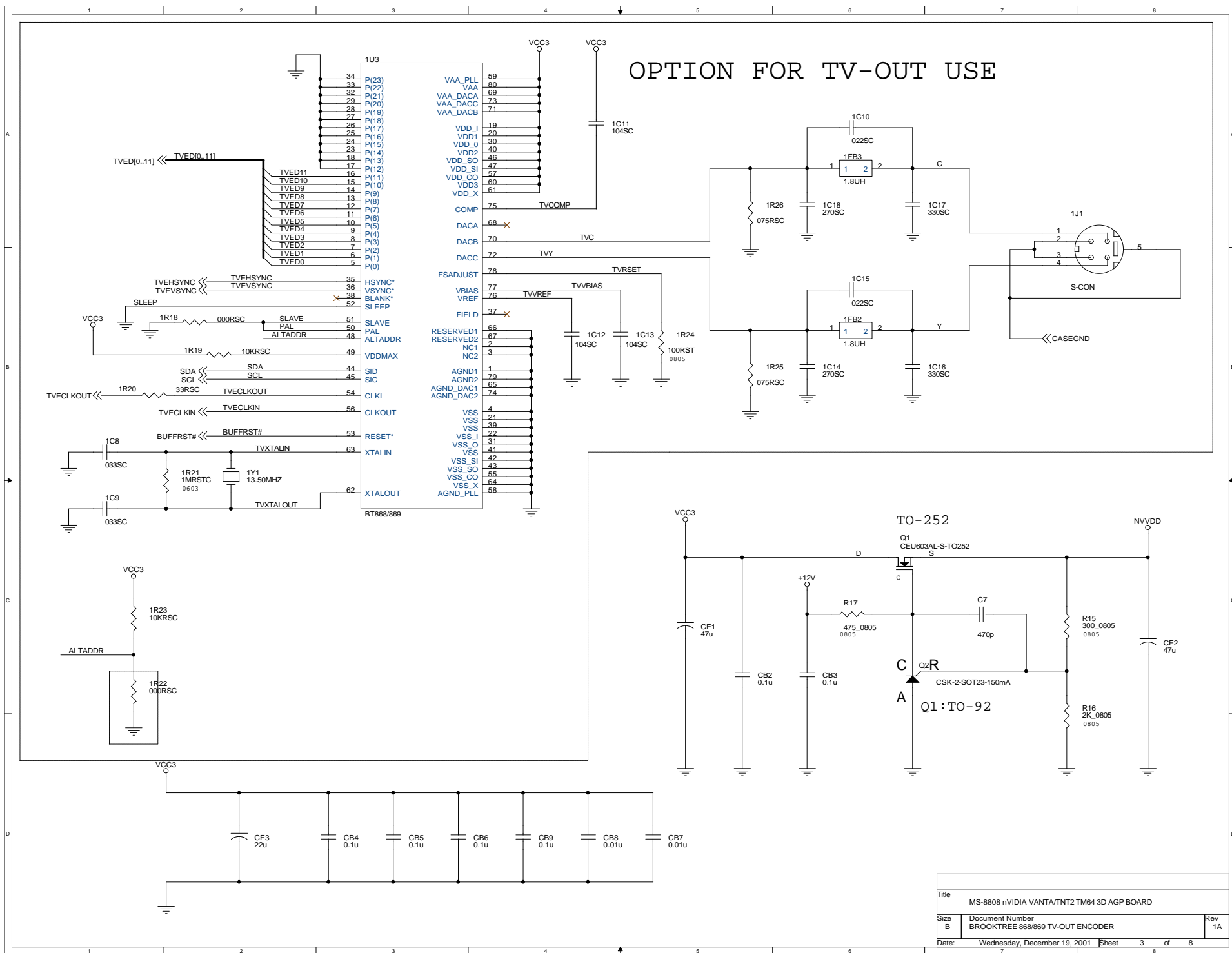
Title		
MS-8808 nVIDIA VANTA/TNT2 TM64 3D AGP BOARD		
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B	HISTORY LIST 8MB/32MB SDRAM	1A
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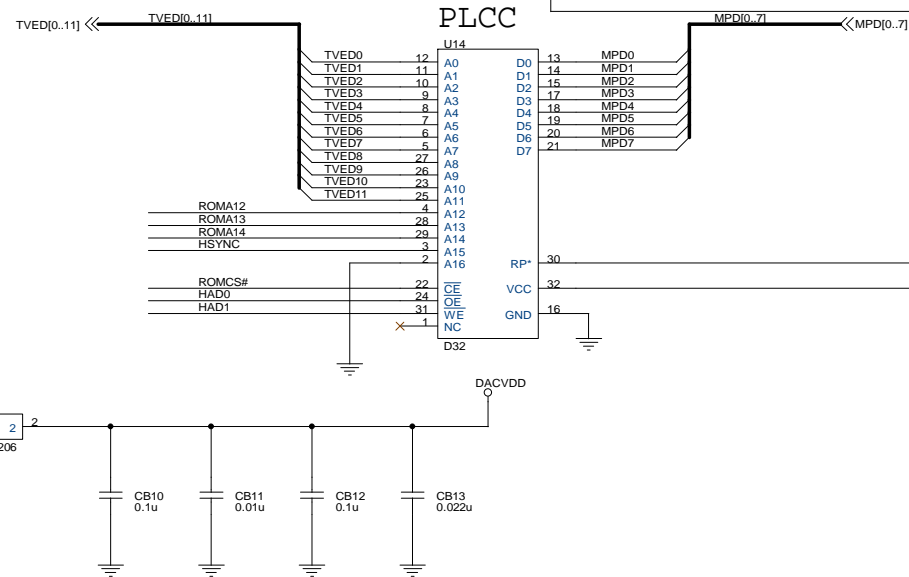
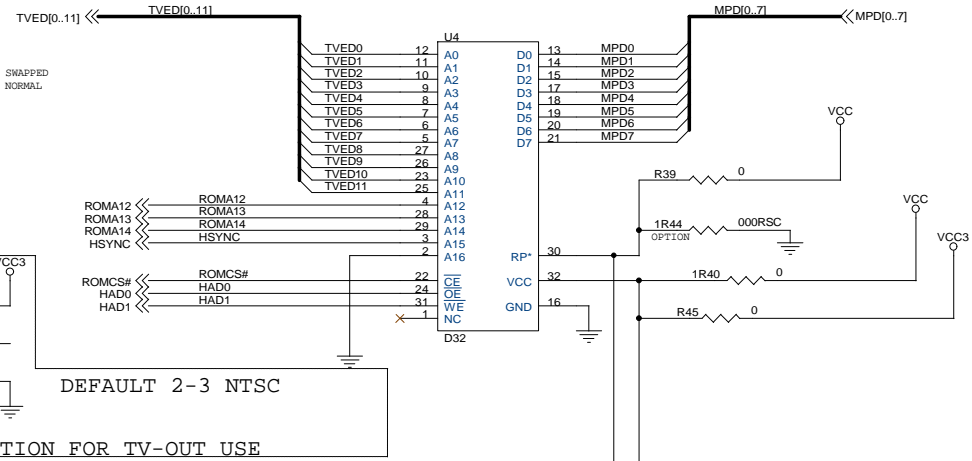
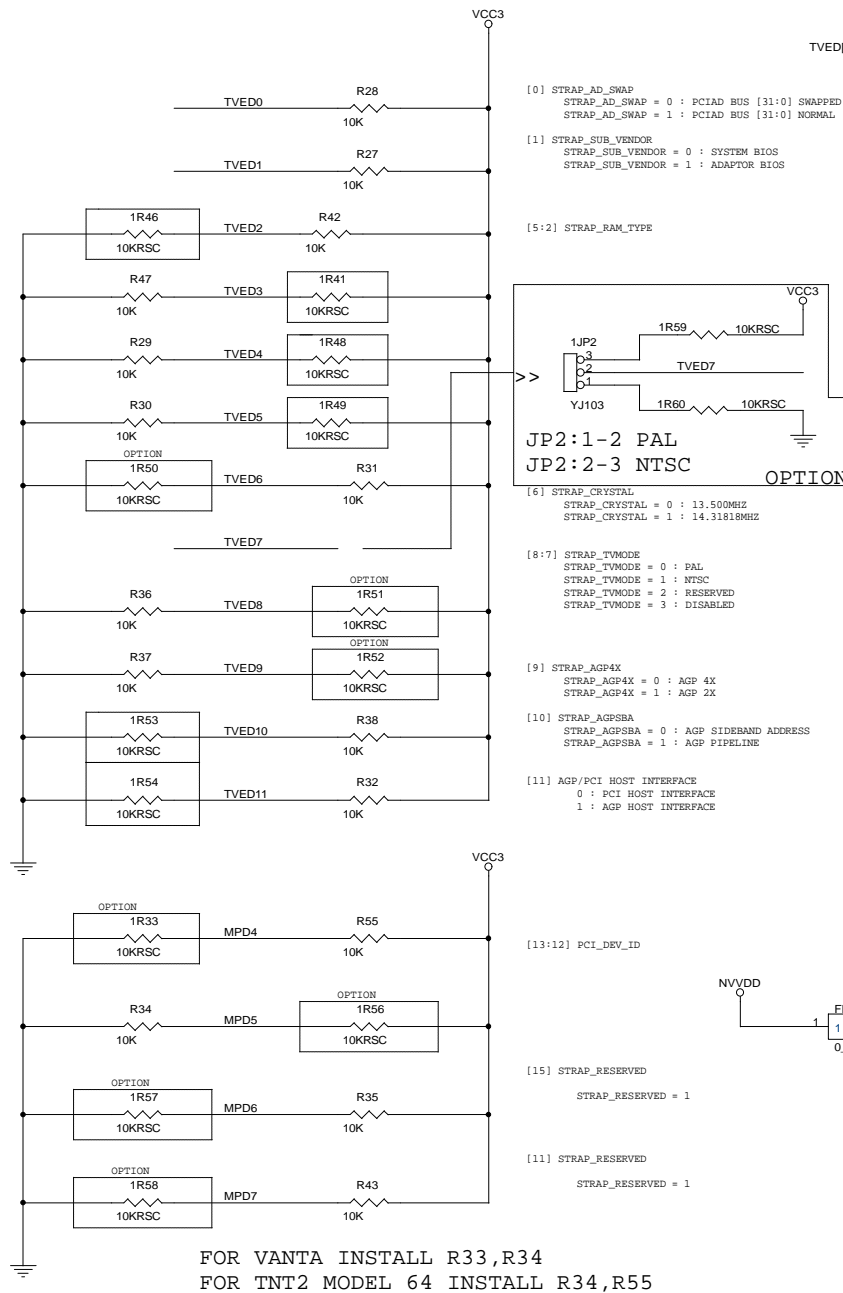




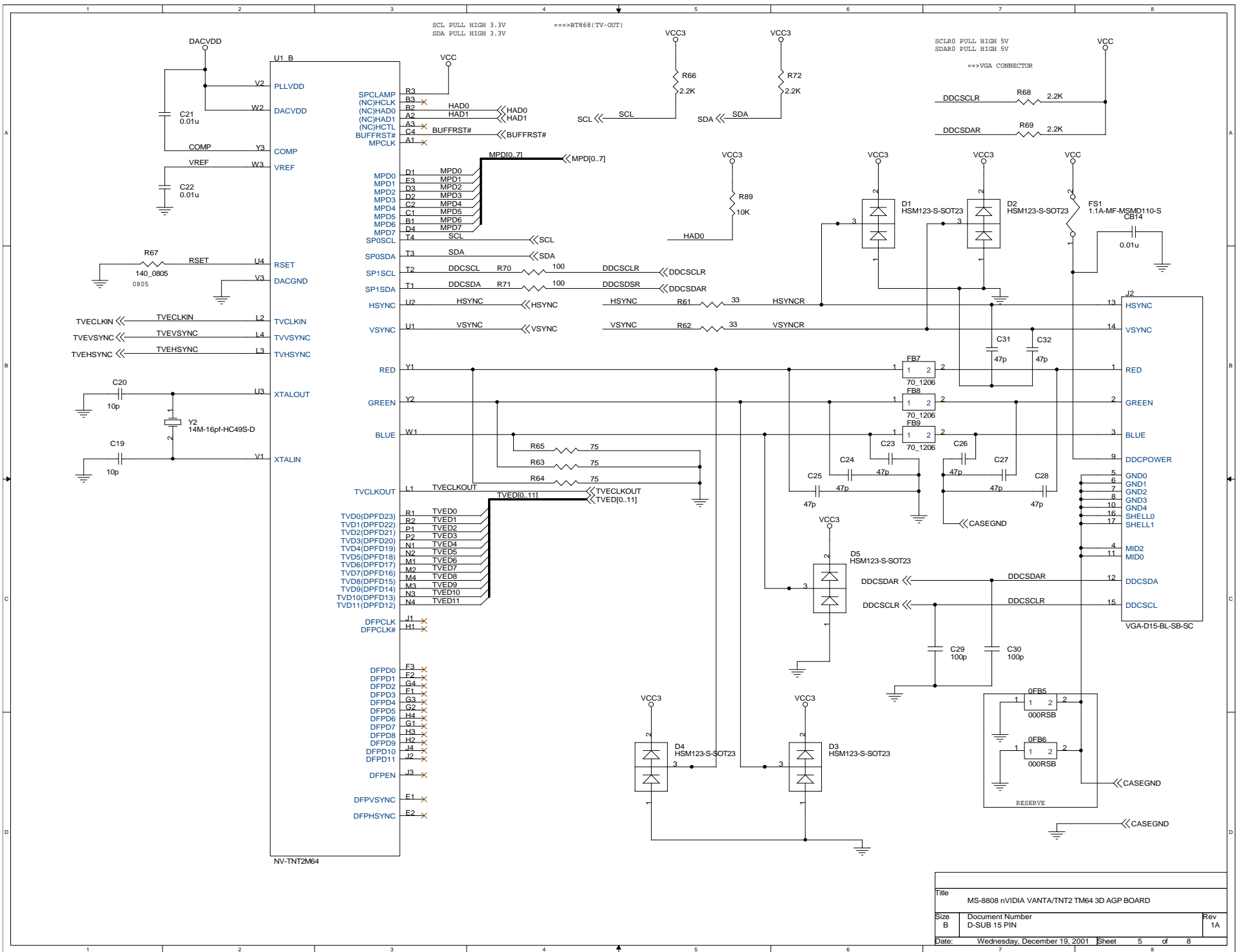


POWER STRAPING

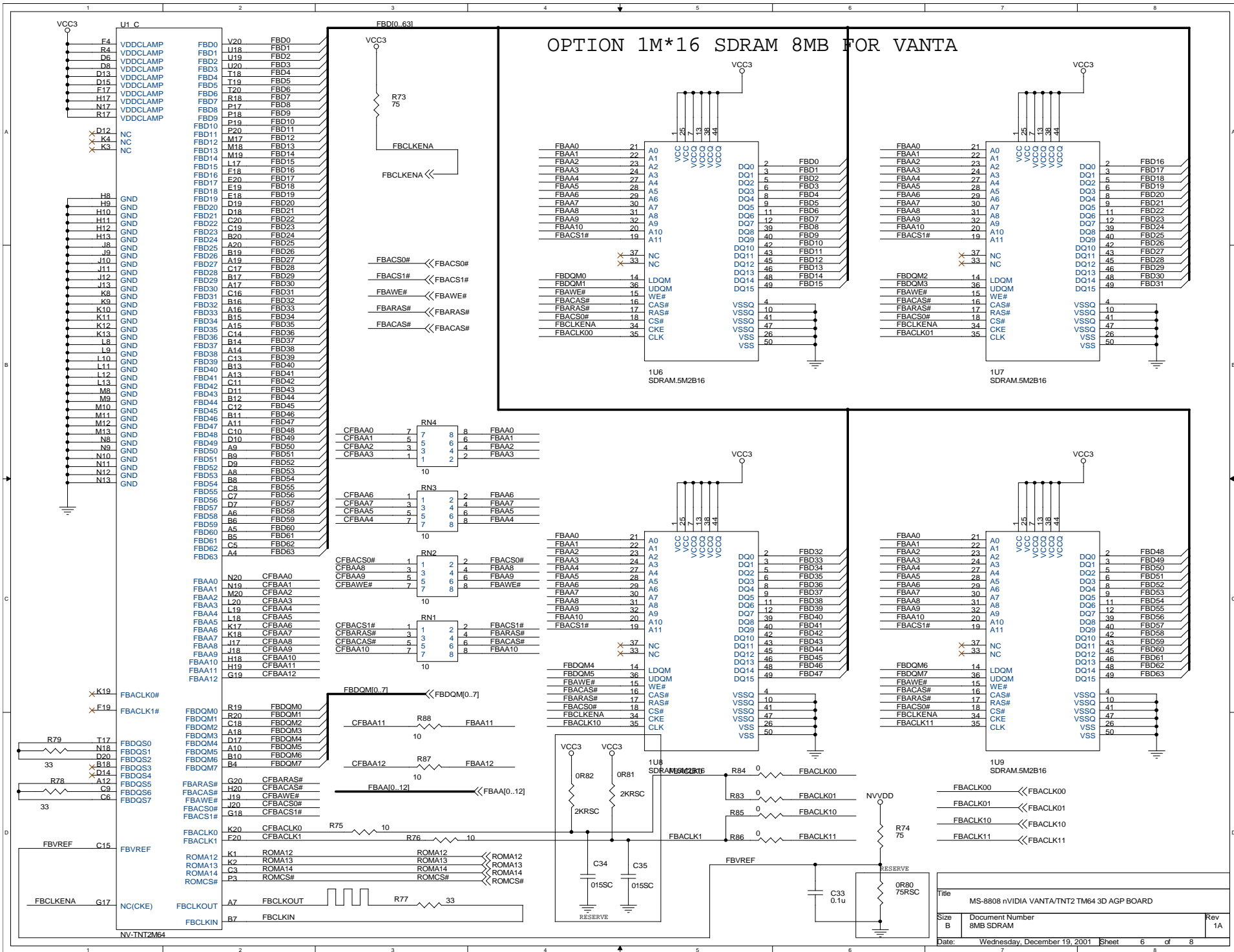
BIOS



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OPTION 1M*16 SDRAM 8MB FOR VANTA



FBAA[0..12] << FBAA[0..12]

FBD[0..63] << FBD[0..63]

FBDQM[0..7] << FBDQM[0..7]

FBACS0# << FBACS0#

FBACS1# << FBACS1#

FBARAS# << FBARAS#

FBACAS# << FBACAS#

FBAWE# << FBAWE#

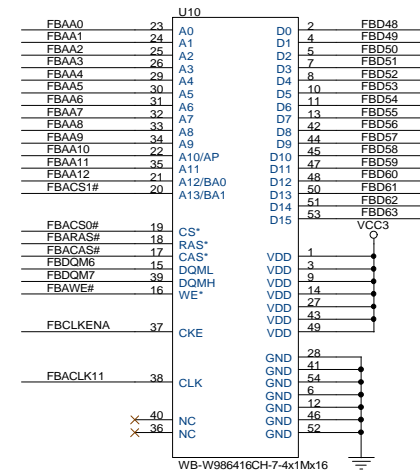
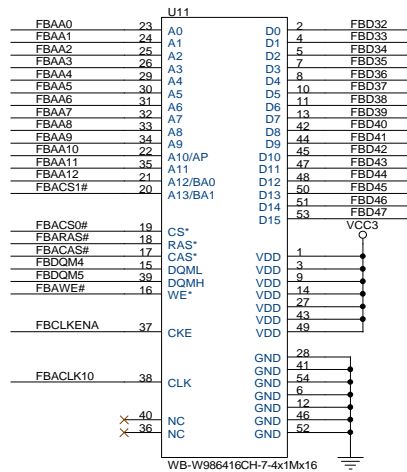
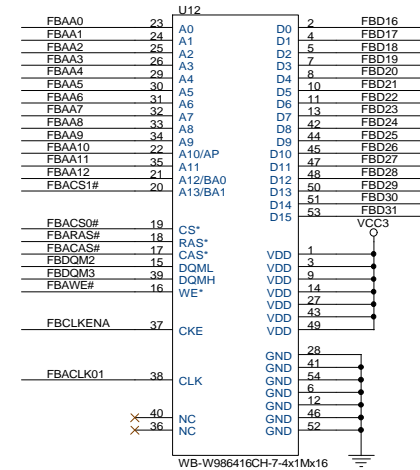
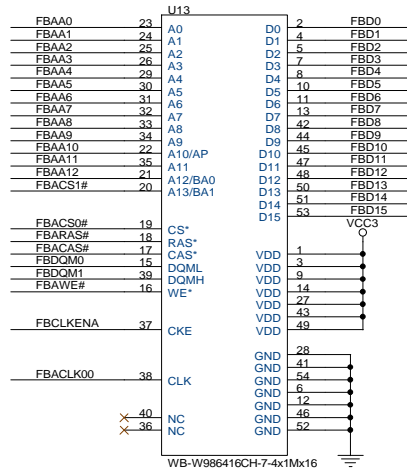
FBCLKENA << FBCLKENA

FBACLK00 << FBACLK00

FBACLK01 << FBACLK01

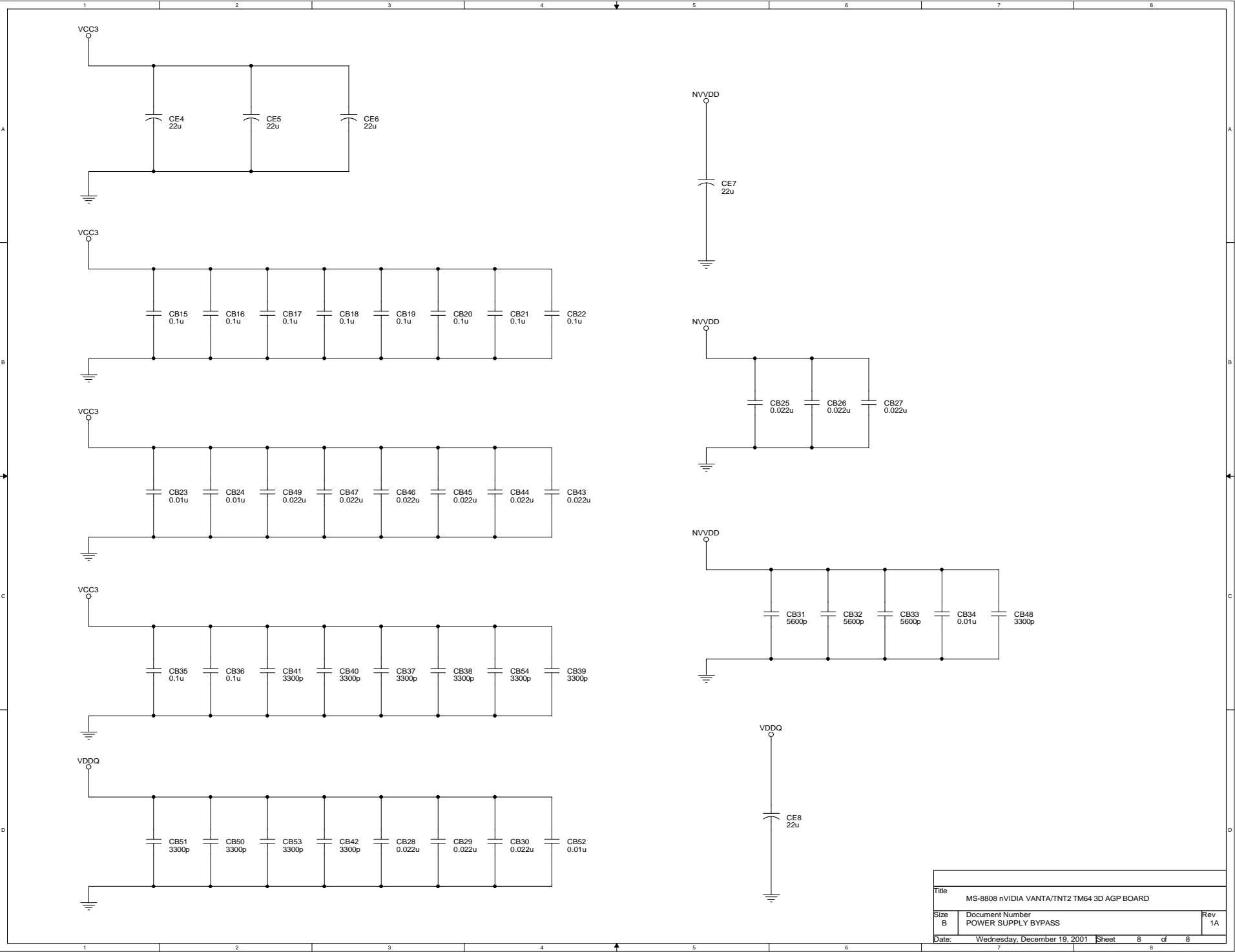
FBACLK10 << FBACLK10

FBACLK11 << FBACLK11



4M*16 SDRAM 32MB

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