P1071: GF108, 1024MB x16 DDR3, DVI+HDMI+VGA

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Page 7: Memory Decoupling
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Page 13: XTAL, ROM, JTAG, Misc
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Page 16: Power Supply II: FBVDDQ
Page 17: Power Supply III: NVVDD

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BRD	VARIANT	NVPN	ASSEMBLY
B	BASE	600-11071-BASE-SCH	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU0000	600-11071-0000-000	GF108-320-A1 600/1200/900MHz 1024MB/128bit DDR3, DVI-DL+HDMI+VGA
2	SKU0001	600-11071-0001-000	GF108-300-A1 625/1250/900MHz 1024MB/128bit DDR3, DVI-DL+HDMI+VGA
3	SKU0002	600-11071-0002-000	GF108-200-A1 650/1300/900MHz 1024MB/128bit DDR3, DVI-DL+HDMI+VGA
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5	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Overview

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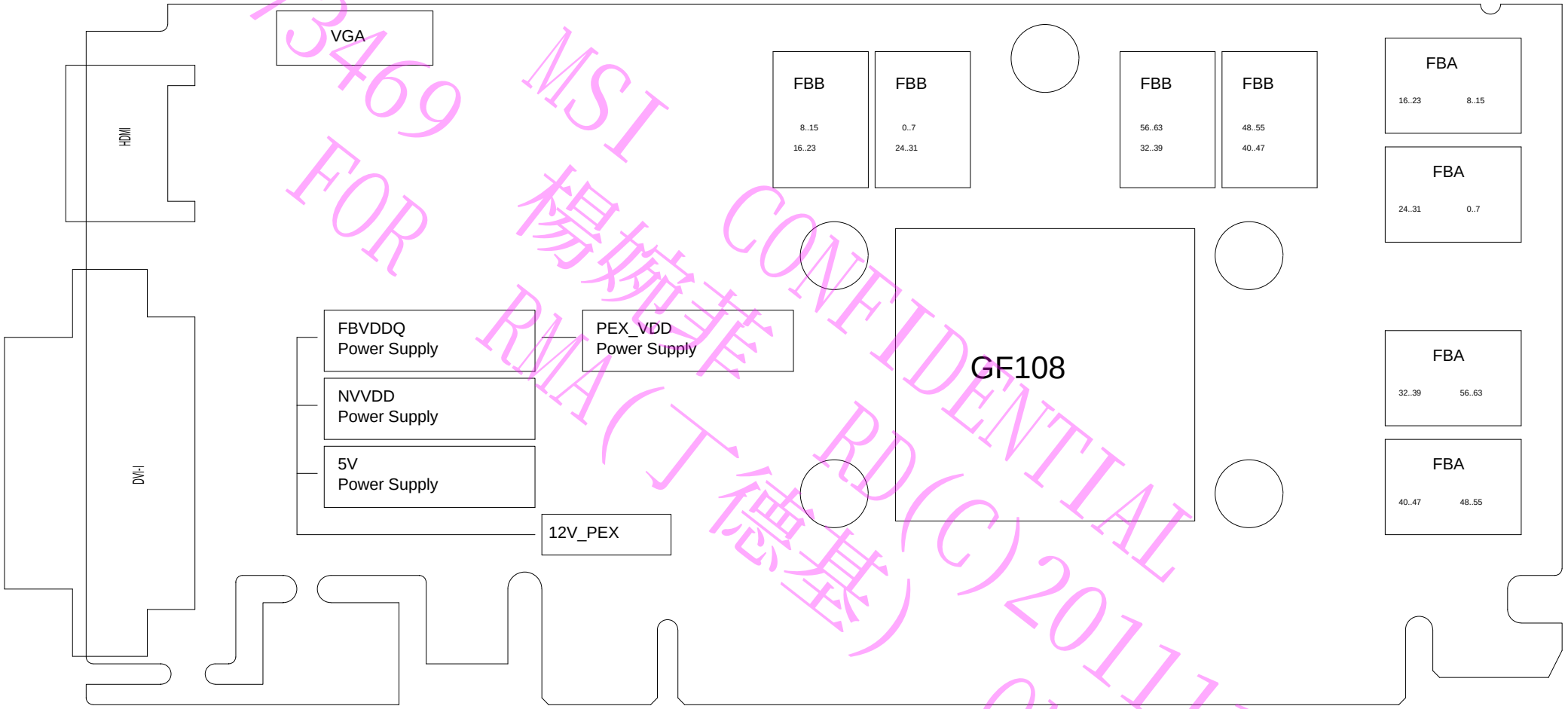
Block Diagram



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PAGE DETAIL	Board Block Diagram

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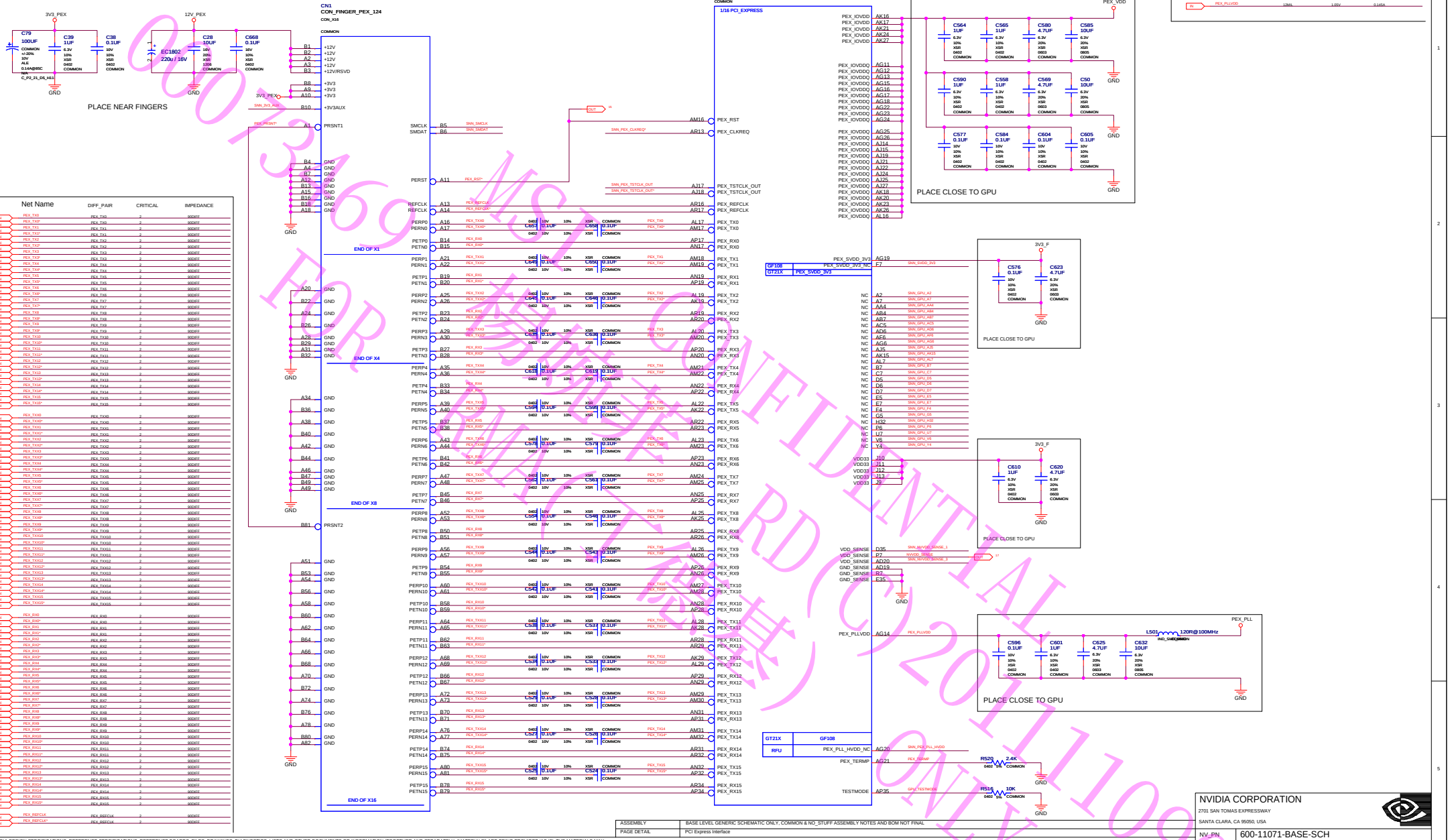
PCI Express Interface



Net Name	DIFF_PAIR	CRITICAL	IMPEDANCE
PEX_TX0	PEX_TX0	2	90OHM
PEX_TX0*	PEX_TX0	2	90OHM
PEX_TX1	PEX_TX1	2	90OHM
PEX_TX1*	PEX_TX1	2	90OHM
PEX_TX2	PEX_TX2	2	90OHM
PEX_TX2*	PEX_TX2	2	90OHM
PEX_TX3	PEX_TX3	2	90OHM
PEX_TX3*	PEX_TX3	2	90OHM
PEX_TX4	PEX_TX4	2	90OHM
PEX_TX4*	PEX_TX4	2	90OHM
PEX_TX5	PEX_TX5	2	90OHM
PEX_TX5*	PEX_TX5	2	90OHM
PEX_TX6	PEX_TX6	2	90OHM
PEX_TX6*	PEX_TX6	2	90OHM
PEX_TX7	PEX_TX7	2	90OHM
PEX_TX7*	PEX_TX7	2	90OHM
PEX_TX8	PEX_TX8	2	90OHM
PEX_TX8*	PEX_TX8	2	90OHM
PEX_TX9	PEX_TX9	2	90OHM
PEX_TX9*	PEX_TX9	2	90OHM
PEX_TX10	PEX_TX10	2	90OHM
PEX_TX10*	PEX_TX10	2	90OHM
PEX_TX11	PEX_TX11	2	90OHM
PEX_TX11*	PEX_TX11	2	90OHM
PEX_TX12	PEX_TX12	2	90OHM
PEX_TX12*	PEX_TX12	2	90OHM
PEX_TX13	PEX_TX13	2	90OHM
PEX_TX13*	PEX_TX13	2	90OHM
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PEX_TX01	PEX_TX01	2	90OHM
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PEX_TX02	PEX_TX02	2	90OHM
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PEX_RX2*	PEX_RX2	2	90OHM
PEX_RX3	PEX_RX3	2	90OHM
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PEX_RX4	PEX_RX4	2	90OHM
PEX_RX4*	PEX_RX4	2	90OHM
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PEX_RX5*	PEX_RX5	2	90OHM
PEX_RX6	PEX_RX6	2	90OHM
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PEX_RX7	PEX_RX7	2	90OHM
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PEX_RX15	PEX_RX15	2	90OHM
PEX_RX15*	PEX_RX15	2	90OHM
PEX_REFCLK	PEX_REFCLK	2	90OHM
PEX_REFCLK*	PEX_REFCLK	2	90OHM

Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
PEX_PRSNT*	12MIL		
PEX_TERMP	12MIL		
GPU_TESTMODE	12MIL		
PEX_PLLVDD	12MIL	1.05V	0.145A

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PAGE DETAIL	PCI Express Interface

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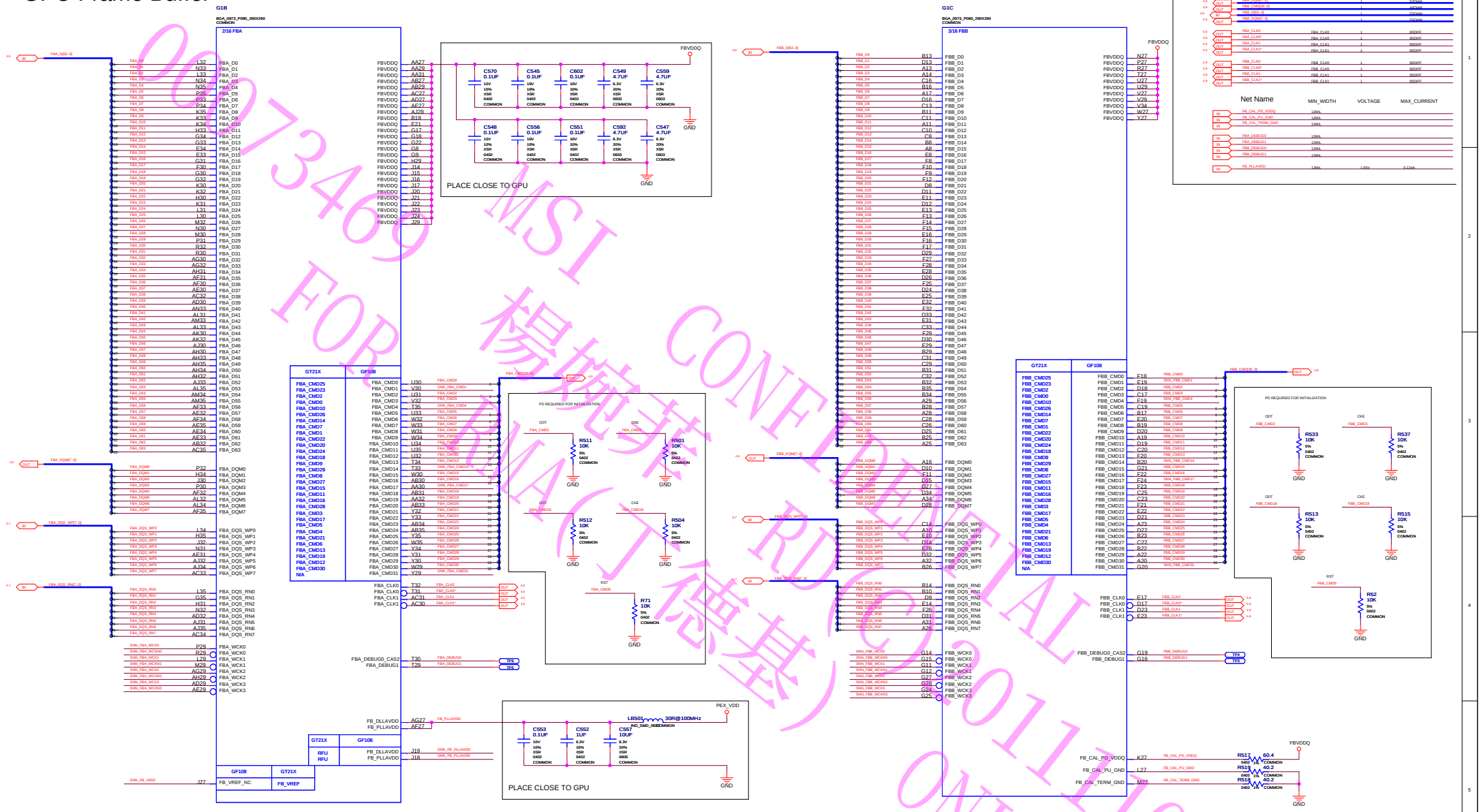
GPU Frame Buffer



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PAGE DETAIL	GPU Frame Buffer

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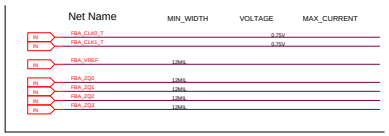
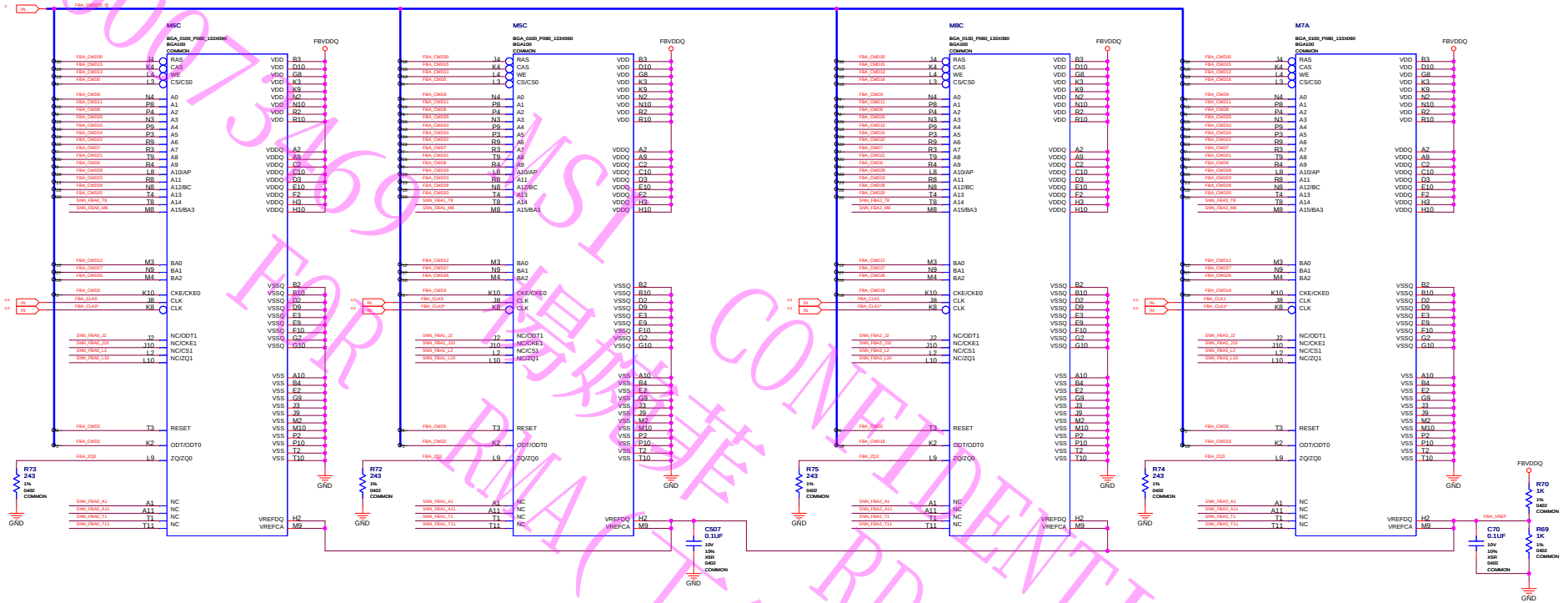
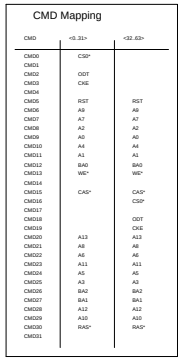
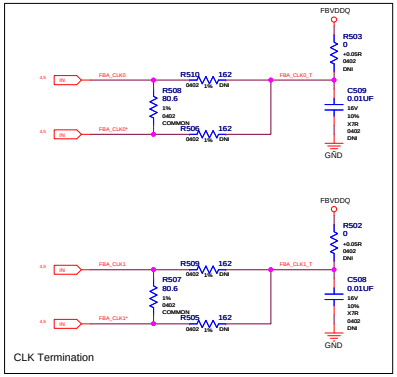
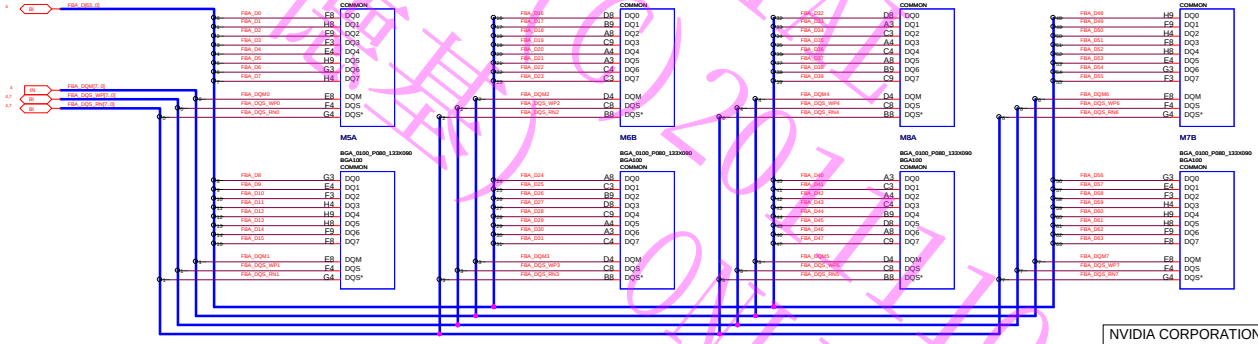
FBA Partition











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PAGE DETAIL	FBA Partition

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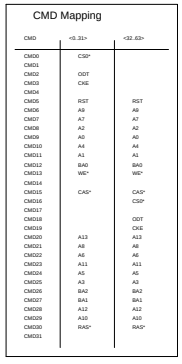
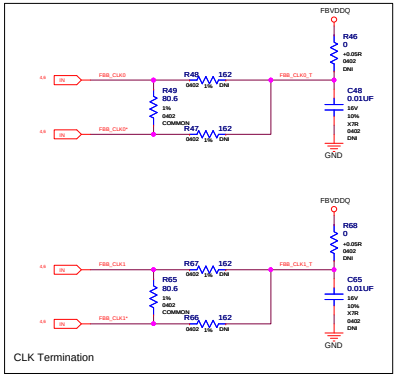
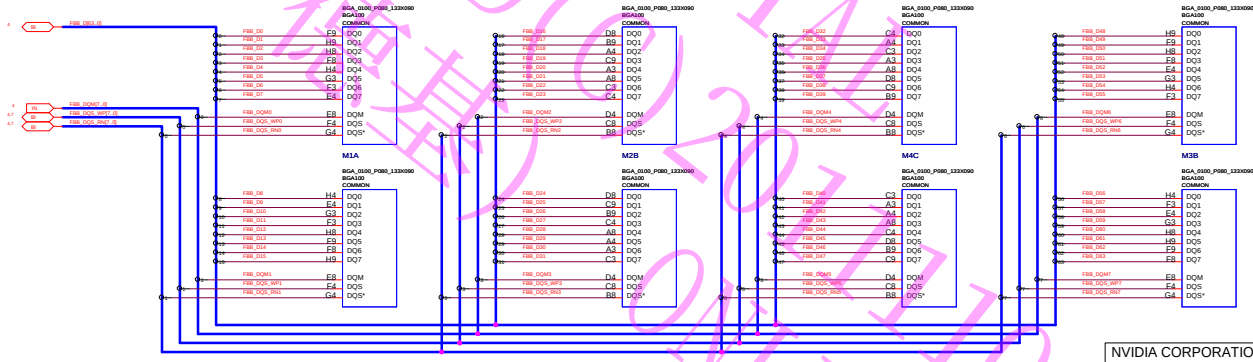
FBB Partition







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PAGE DETAIL	FBB Partition

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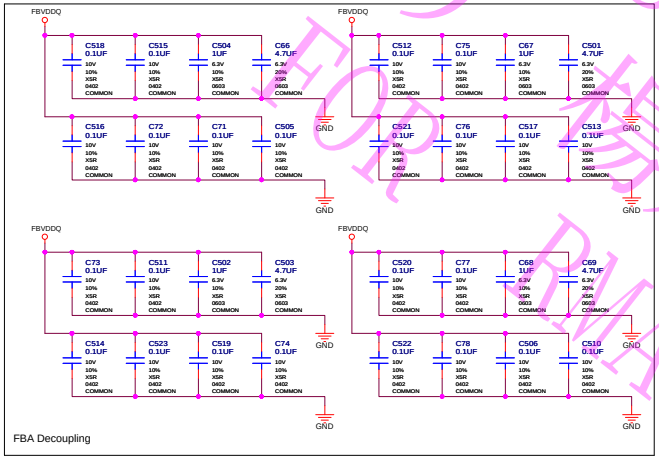
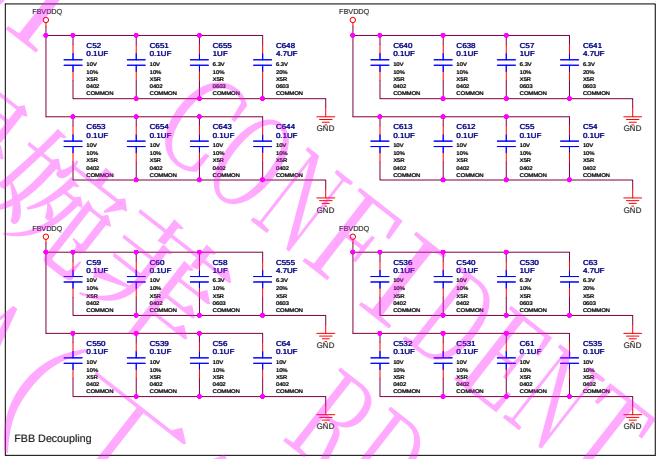
Memory Decoupling





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PAGE DETAIL	Memory Decoupling

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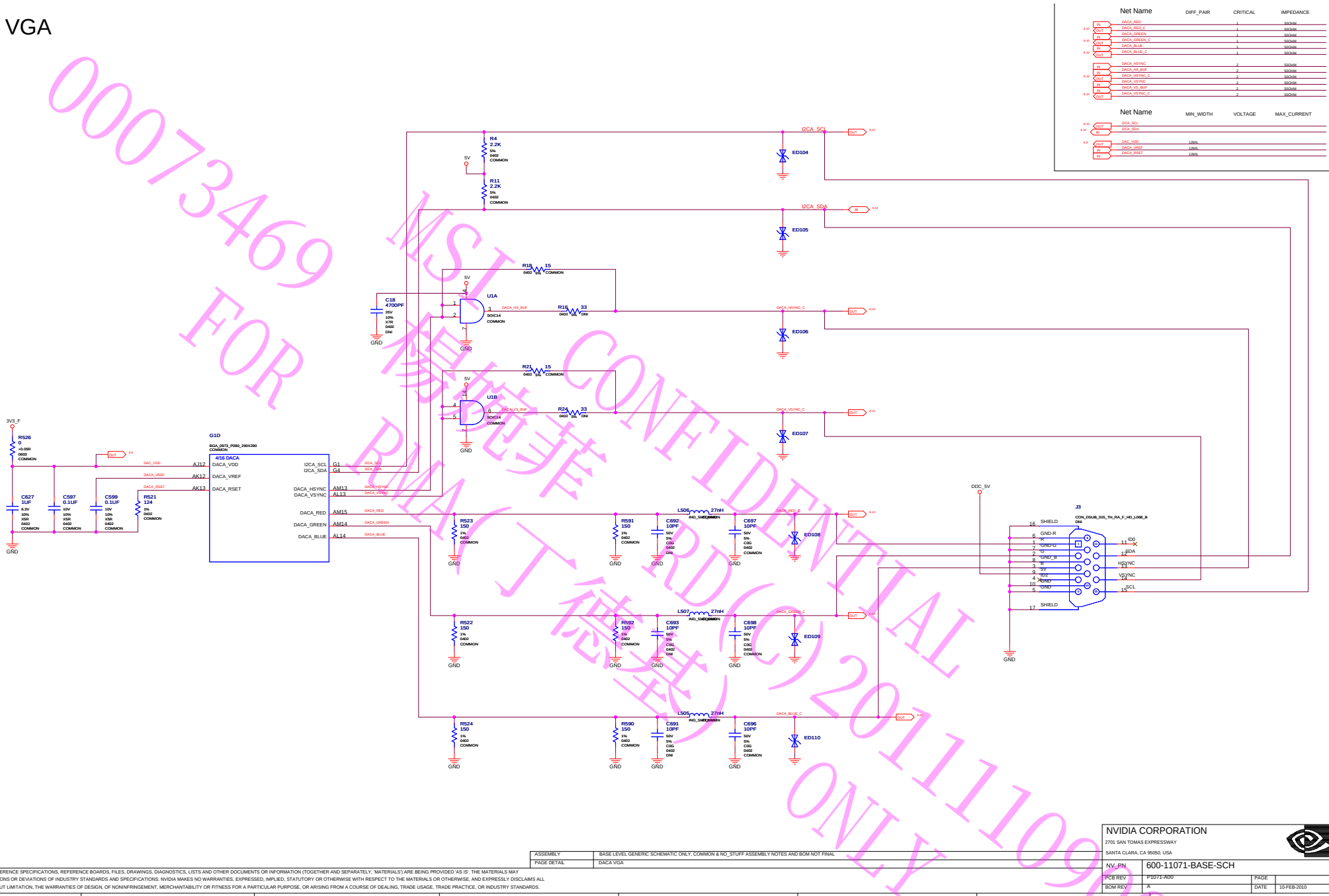
DACA VGA



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PAGE DETAIL	DACA VGA

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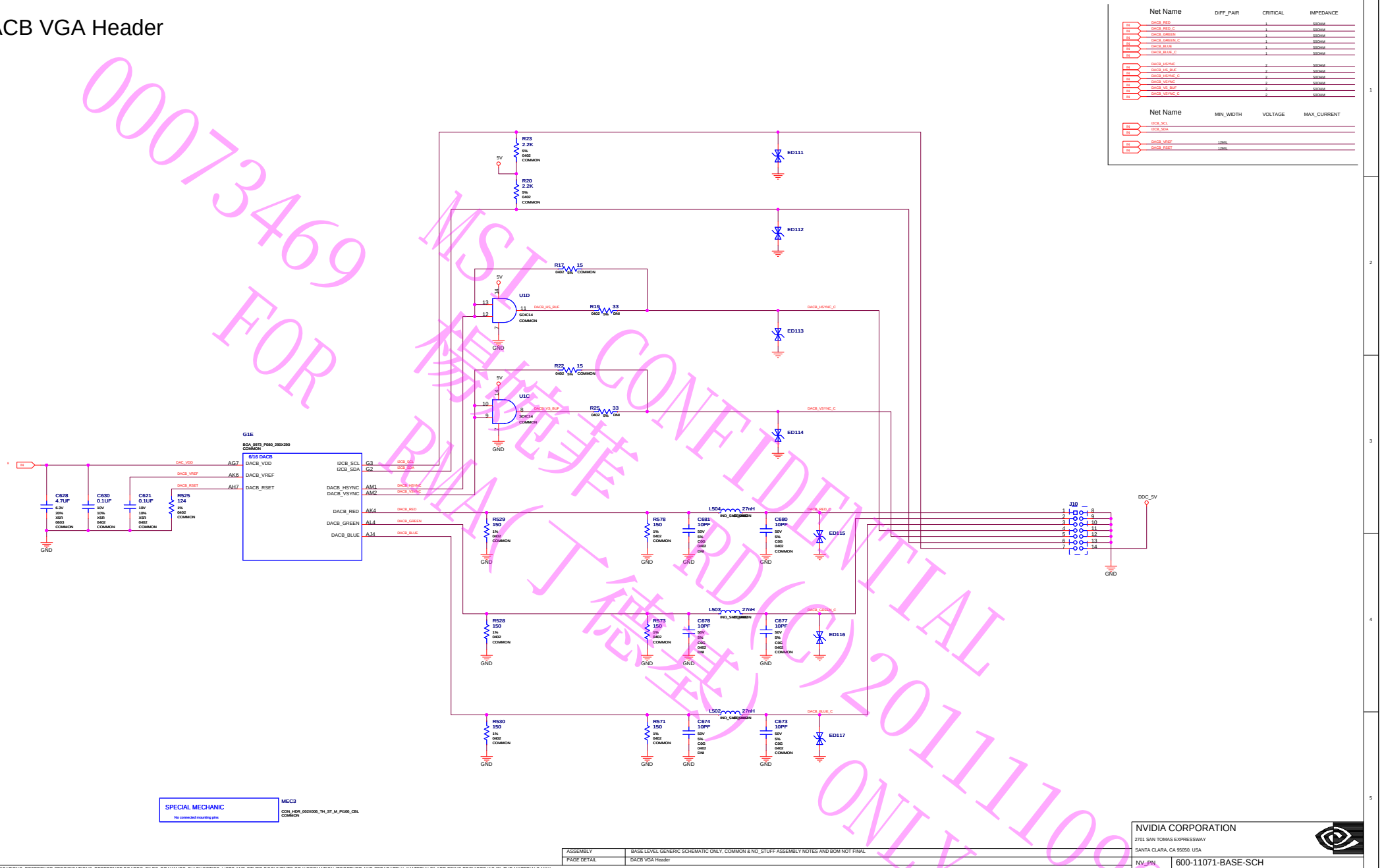
DACB VGA Header



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PAGE DETAIL	DACB VGA Header

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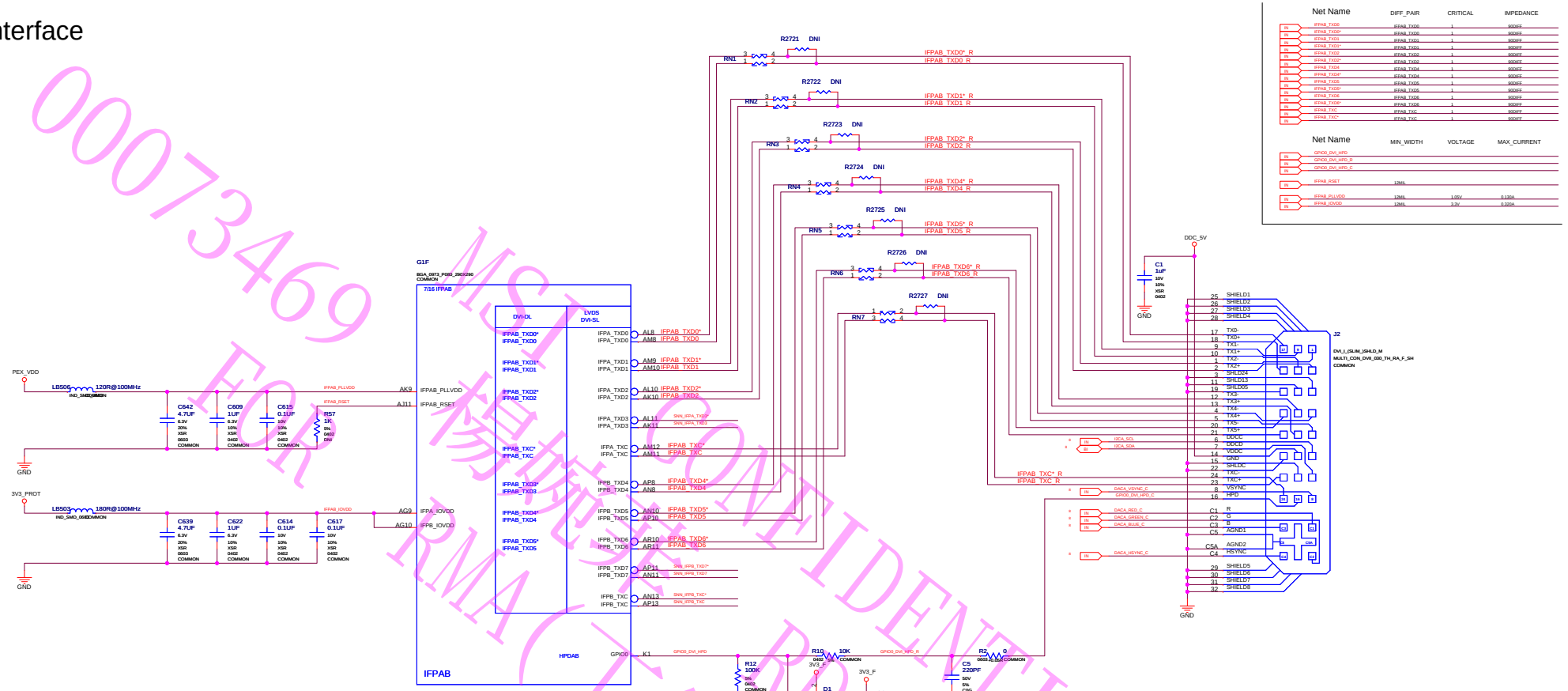
TMDS Interface



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PAGE DETAIL	TMDS Interface

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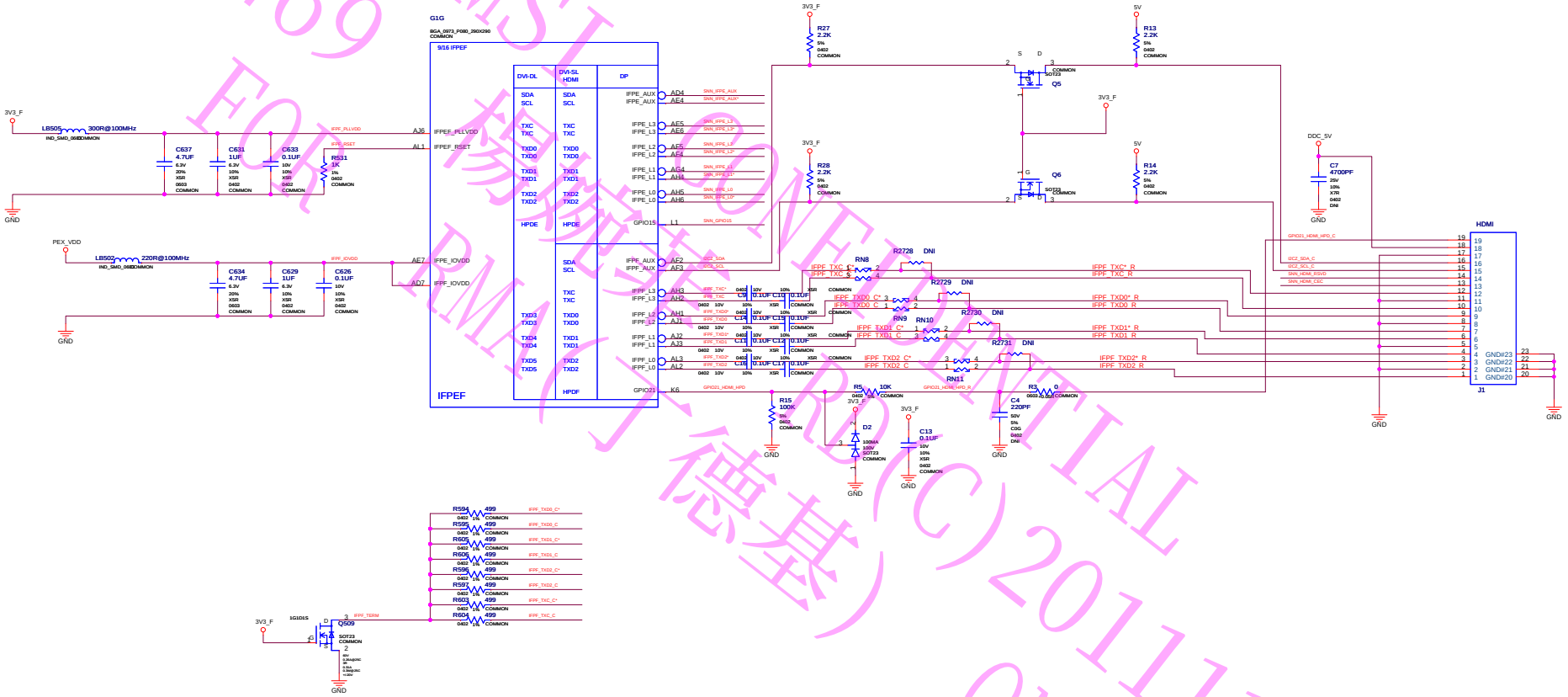
HDMI Interface



Net Name	DIFF_PAIR	CRITICAL	IMPEDANCE
IFPE_TXD0	IFPE_TXD0	1	95OHM
IFPE_TXD0*	IFPE_TXD0	1	95OHM
IFPE_TXD1	IFPE_TXD1	1	95OHM
IFPE_TXD1*	IFPE_TXD1	1	95OHM
IFPE_TXD2	IFPE_TXD2	1	95OHM
IFPE_TXD2*	IFPE_TXD2	1	95OHM
IFPE_TXC	IFPE_TXC	1	95OHM
IFPE_TXC*	IFPE_TXC	1	95OHM
IFPE_TXD0_C	IFPE_TXD0_C	1	95OHM
IFPE_TXD0_C*	IFPE_TXD0_C	1	95OHM
IFPE_TXD1_C	IFPE_TXD1_C	1	95OHM
IFPE_TXD1_C*	IFPE_TXD1_C	1	95OHM
IFPE_TXD2_C	IFPE_TXD2_C	1	95OHM
IFPE_TXD2_C*	IFPE_TXD2_C	1	95OHM
IFPE_TXC_C	IFPE_TXC_C	1	95OHM
IFPE_TXC_C*	IFPE_TXC_C	1	95OHM

Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
I2C2_SDA			
I2C2_SCL			
I2C2_SDA_C			
I2C2_SCL_C			
GPIO21_HDMI_HPD			
GPIO21_HDMI_HPD_R			
GPIO21_HDMI_HPD_C			
IFPE_TERM	12MIL	0V	
IFPE_RSET	12MIL		
IFPE_PLLVDD	12MIL	3.3V	0.085A
IFPE_IOVDD	12MIL	1.05V	0.105A

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PAGE DETAIL	HDMI Interface

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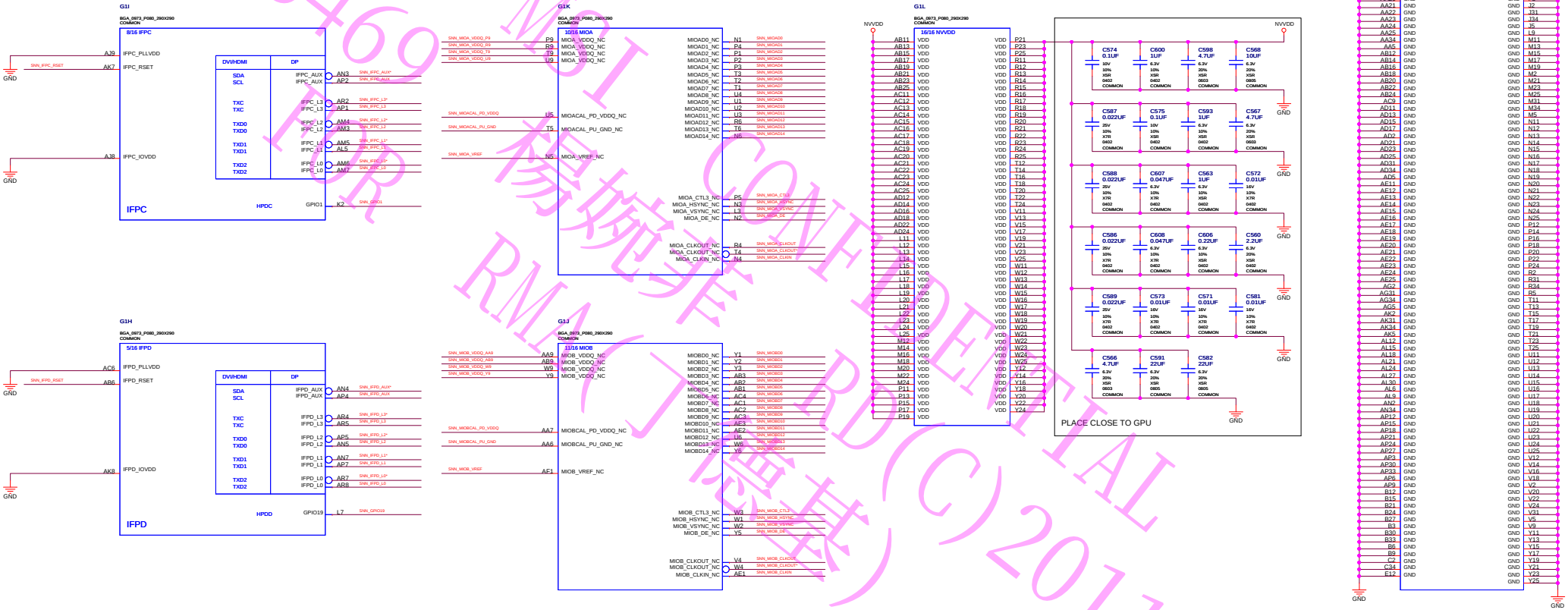
Unused Interfaces, NVVDD Decoupling



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PAGE DETAIL	Unused Interfaces, NVVDD Decoupling

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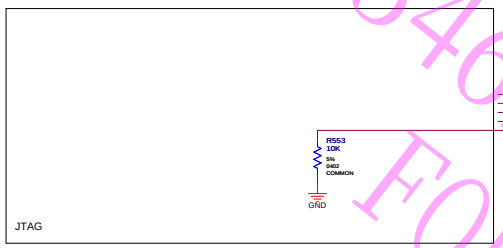
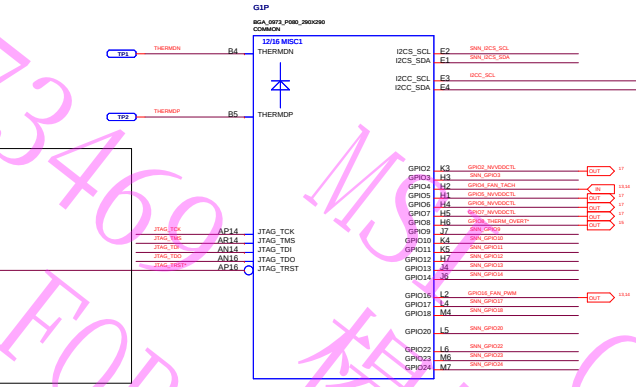
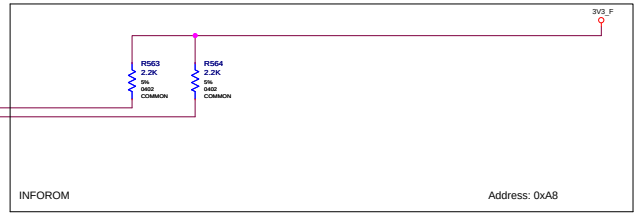
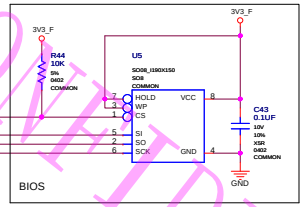
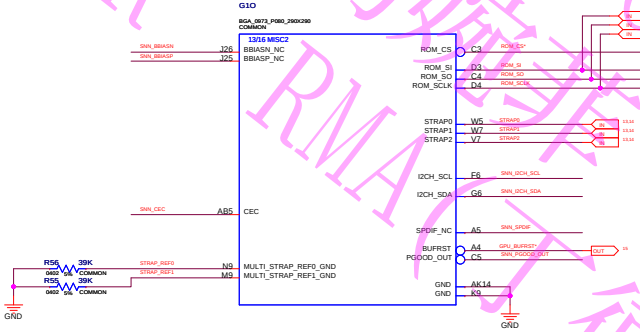
XTAL, ROM, JTAG, Misc

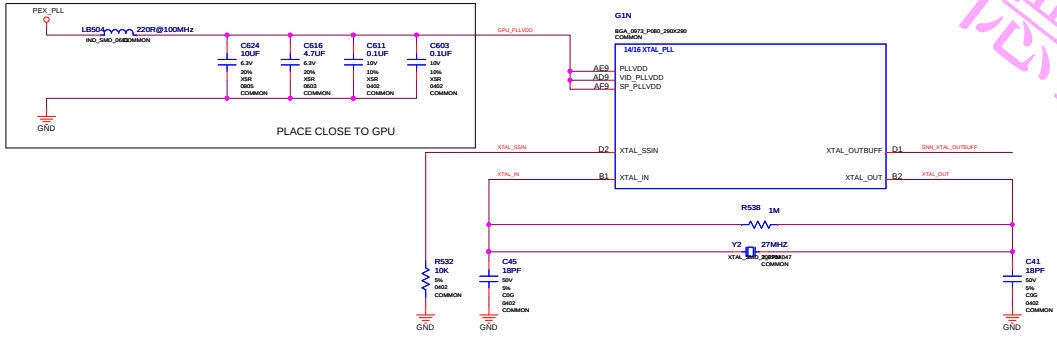


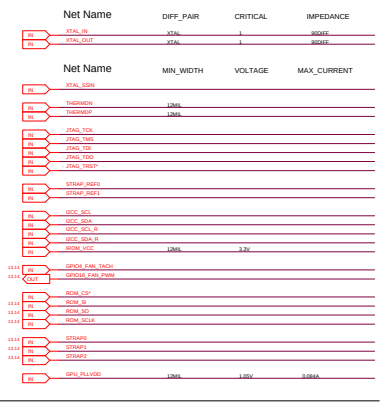












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PAGE DETAIL	XTAL, ROM, JTAG, Misc

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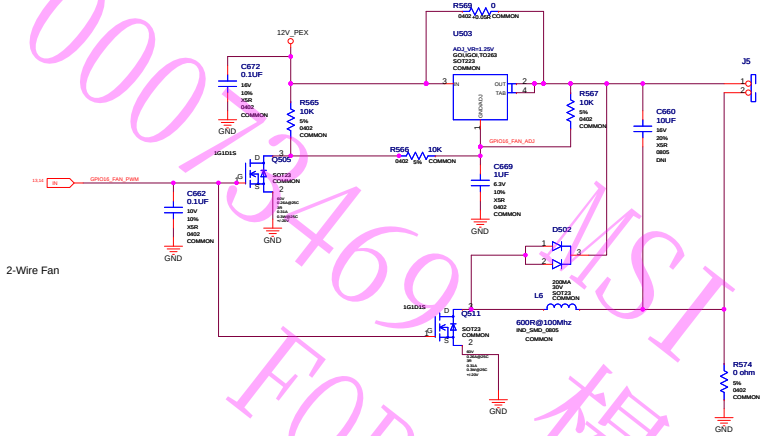
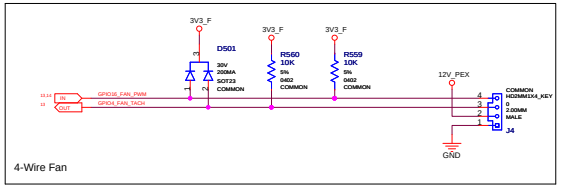
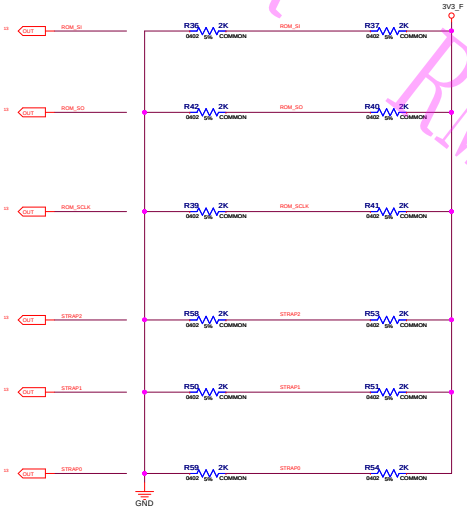
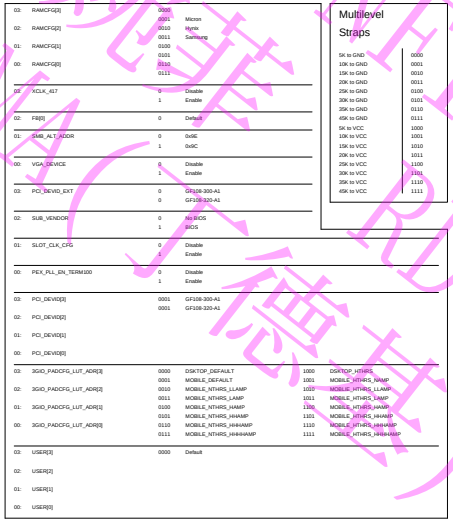
Straps, Thermal, Mechanical

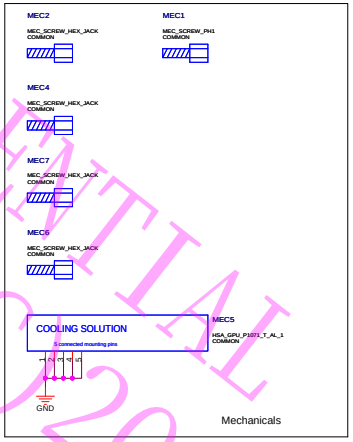


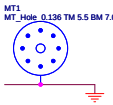


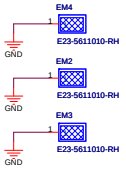


GF108 Straps









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PAGE DETAIL	Straps, Thermal, Mechanical

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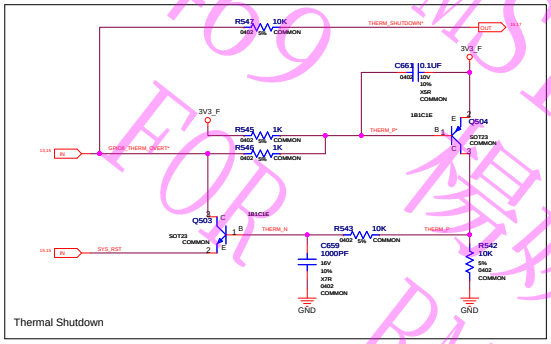
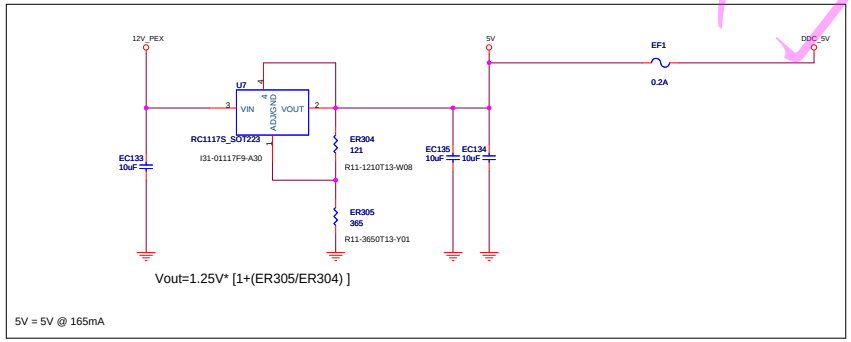
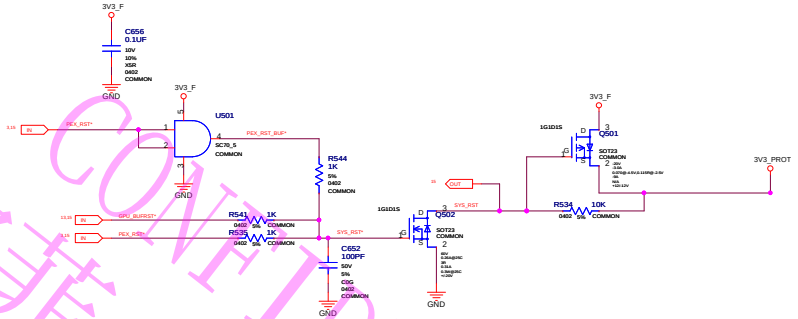
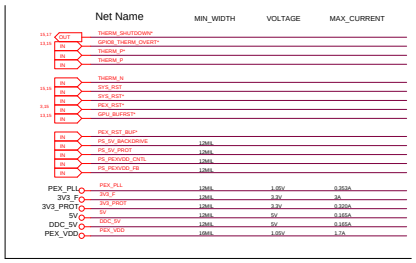
Power Supply I: Misc Power, Thermal Shutdown





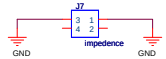




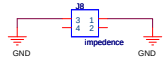
Top
Single Ended_Memory CMD
6.6929 mil
40 ohm +/- 10%

Int3
Single Ended_RGB
4.76377 mil
50 ohm +/- 10%

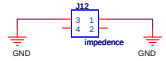
Bottom
Single Ended_Memory Data
3.937 mil
53 ohm +/- 10%



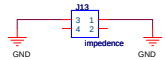
Top
Diffenential_PCIE BUS
3.94 / 4.72 mil
90 ohm +/- 10%



Int3
Diffenential_DVI
3.94 / 5.98 mil
90 ohm +/- 10%



Int3
Diffenential_Memory Clock
4.33 / 4.13 mil
80 ohm +/- 10%



Bottom
Diffenential_PCIE BUS
3.94 / 4.72 mil
90 ohm +/- 10%

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Power Supply I: Misc Power, Thermal Shutdown

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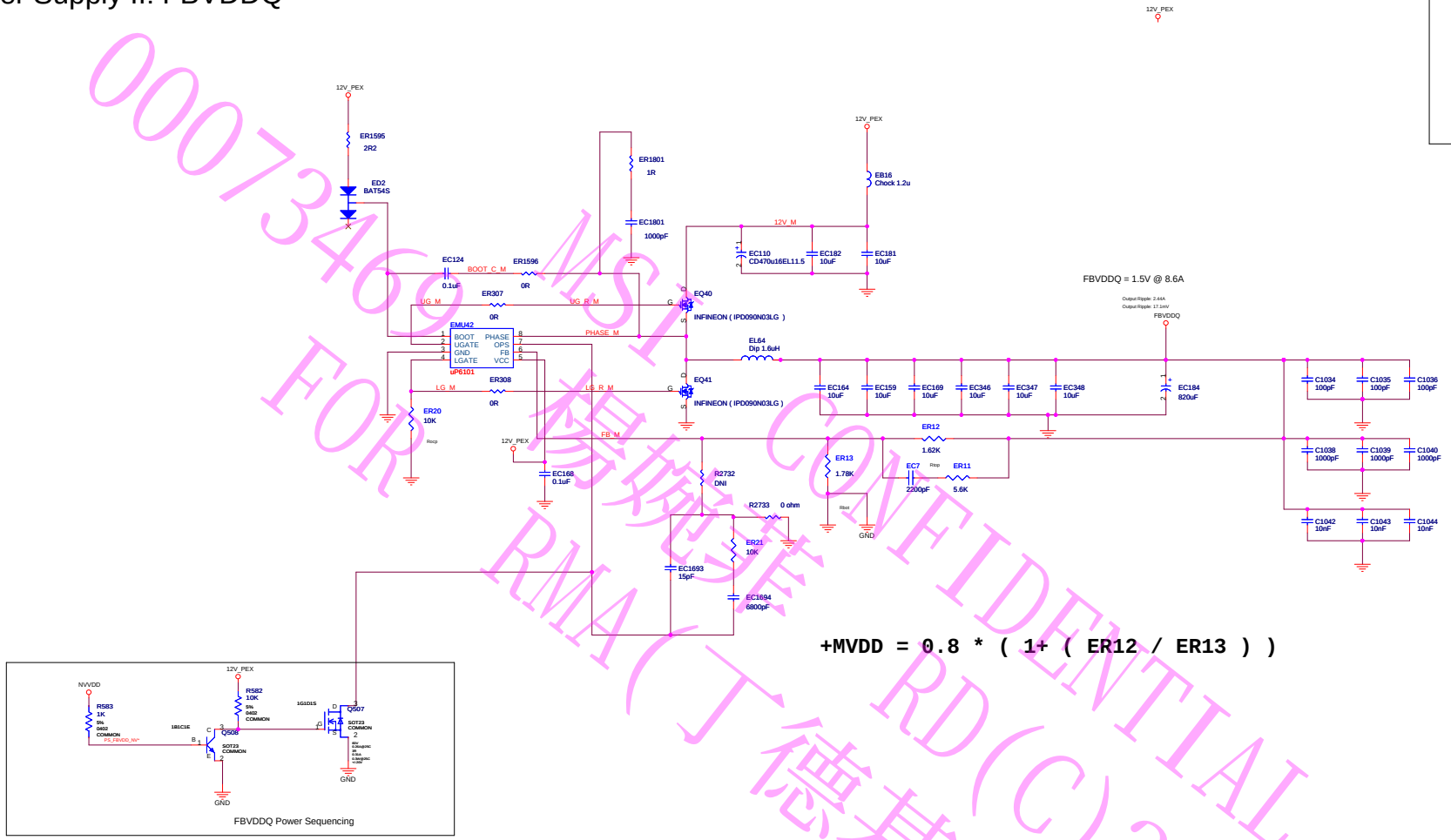
Power Supply II: FBVDDQ



FBVDDQ = 1.5V @ 8.6A

+MVDD = 0.8 * (1+ (ER12 / ER13))

FBVDDQ Power Sequencing

Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
PS_FBVDD_VCC	12MIL		
PS_FBVDD_EN	12MIL		
PS_FBVDD_ENF			
PS_FBVDD_BOOT	12MIL		
PS_FBVDD_BOOT_RC	12MIL		
PS_FBVDD_FB	12MIL		
PS_FBVDD_FB_RC	12MIL		
PS_FBVDD_CF_RC	12MIL		
PS_FBVDD_UG	16MIL		
PS_FBVDD_UG_R	16MIL		
PS_FBVDD_LG	16MIL		
PS_FBVDD_PHASE	16MIL		
PS_FBVDD_SNUB	16MIL		
FBVDDQ	16MIL	1.50V	8.6A

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PAGE DETAIL	Power Supply II: FBVDDQ

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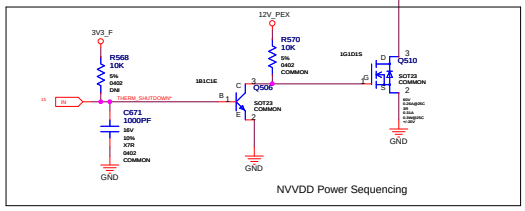
Power Supply III: NVVDD



NVVDD Power Sequencing

GPIO[2:7:5]	NVVDD
0000	0.870V
0001	0.885V
0010	0.900V
0011	0.915V
0100	0.930V
0101	0.945V
0110	0.960V
0111	0.975V
1000	0.990V
1001	1.005V
1010	1.020V
1011	1.035V
1100	1.050V
1101	1.065V
1110	1.080V
1111	1.095V

Vref=0.6V; Rin=1.15k; Rb=2.55k; Rb2=47.5k; Rb3=23.7k; Rb4=11.5k; Rb5=5.76k
Vref=0.8V; Rin=931; Rb=10.7k; Rb2=88.7k; Rb3=24.3k; Rb4=12.4k; Rb5=6.19k

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PAGE DETAIL	Power Supply III: NVVDD

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NV_PN	600-11071-BASE-SCH		
PCB REV	P1071-A00	PAGE	
BOM REV	A	DATE	10-FEB-2010

Title: Basenet Report
Design: p1071_a00
Date: Feb 10 12:52:26 2010

Base nets and synonyms for
p1071_a00_lib:P1071_A00(@p1071_a00_lib.p
1071_a00(sch_1))
Base Signal Location([Zone][dir])

3V3_F 15.1G
3V3_PEX 17.2H
3V3_PROT 15.1G
5V 15.1G
12V_PEX 17.2H
DACA_BLUE 8.1G< 8.4C
DACA_BLUE_C 8.1G> 8.5F> 10.3F<
DACA_GREEN 8.1G< 8.3C
DACA_GREEN_C 8.1G> 8.4F> 10.3F<
DACA_HSYNC 8.1G< 8.3C
DACA_HSYNC_C 8.1G> 8.2F> 10.3F<
DACA_HS_BUF 8.1G< 8.2D
DACA_RED 8.1G< 8.3C
DACA_RED_C 8.1G> 8.3F> 10.3F<
DACA_RSET 8.1G< 8.3B
DACA_VREF 8.1G< 8.3B
DACA_VSYNC 8.1G< 8.3C
DACA_VSYNC_C 8.1G> 8.3F> 10.3F<
DACA_VS_BUF 8.1G< 8.3D
DACB_BLUE 9.1G< 9.4C
DACB_BLUE_C 9.1G< 9.5E
DACB_GREEN 9.1G< 9.3C
DACB_GREEN_C 9.1G< 9.4E
DACB_HSYNC 9.1G< 9.3C
DACB_HSYNC_C 9.1G> 9.2E
DACB_HS_BUF 9.1G< 9.3D
DACB_RED 9.1G< 9.3C
DACB_RED_C 9.1G< 9.3E
DACB_RSET 9.1G< 9.3B
DACB_VREF 9.1G< 9.3B
DACB_VSYNC 9.1G< 9.3C
DACB_VSYNC_C 9.1G> 9.3E
DACB_VS_BUF 9.1G< 9.3B
DAC_VDD 8.1G> 8.3B> 9.3A<
DDC_5V 15.1G
FBA_CLK0 4.1G> 4.4C> 5.2A<
5.2C< 5.4B<
FBA_CLK0* 4.1G> 4.4C> 5.2A<
5.2C< 5.4B<
FBA_CLK0_T 5.1G< 5.4B
FBA_CLK1 4.1G> 4.4C> 5.2E<
5.2F< 5.5B<
FBA_CLK1* 4.1G> 4.4C> 5.2E<
5.2F< 5.5B<
FBA_CLK1_T 5.1G< 5.5B
FBA_CMD<0> 4.3C 5.1A 5.1C
FBA_CMD<10..0> 4.1G> 4.3D> 5.1A<
FBA_CMD<2> 4.3C 4.3C 5.3A 5.3C
FBA_CMD<3> 4.3C 4.3D 5.2A 5.2C
FBA_CMD<5> 4.3C 4.4D 5.3A 5.3C
5.3E 5.3F
FBA_CMD<6> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<7> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<8> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<9> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<10> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<11> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<12> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<13> 4.3C 5.1A 5.1C 5.1E
5.1F
FBA_CMD<15> 4.3C 5.1A 5.1C 5.1E
5.1F
FBA_CMD<16> 4.3C 5.1E 5.1F
FBA_CMD<18> 4.3C 4.3C 5.3E 5.3F
FBA_CMD<19> 4.3C 4.3D 5.2E 5.2F
FBA_CMD<20> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<21> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<22> 4.4C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<23> 4.4C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<24> 4.4C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<25> 4.4C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<26> 4.4C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<27> 4.4C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<28> 4.4C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<29> 4.4C 5.2A 5.2C 5.2E
5.2F

FBA_CMD<30> 4.4C 5.1A 5.1C 5.1E
5.1F
FBA_D<0> 4.1A 5.4D
FBA_D<63..0> 4.1A<> 4.1G<> 5.4C<>
FBA_D<1> 4.1A 5.4D
FBA_D<2> 4.1A 5.4D
FBA_D<3> 4.1A 5.4D
FBA_D<4> 4.1A 5.4D
FBA_D<5> 4.1A 5.4D
FBA_D<6> 4.1A 5.4D
FBA_D<7> 4.1A 5.4D
FBA_D<8> 4.1A 5.5D
FBA_D<9> 4.1A 5.5D
FBA_D<10> 4.1A 5.5D
FBA_D<11> 4.1A 5.5D
FBA_D<12> 4.1A 5.5D
FBA_D<13> 4.1A 5.5D
FBA_D<14> 4.2A 5.5D
FBA_D<15> 4.2A 5.5D
FBA_D<16> 4.2A 5.4E
FBA_D<17> 4.2A 5.4E
FBA_D<18> 4.2A 5.4E
FBA_D<19> 4.2A 5.4E
FBA_D<20> 4.2A 5.4E
FBA_D<21> 4.2A 5.4E
FBA_D<22> 4.2A 5.4E
FBA_D<23> 4.2A 5.4E
FBA_D<24> 4.2A 5.5E
FBA_D<25> 4.2A 5.5E
FBA_D<26> 4.2A 5.5E
FBA_D<27> 4.2A 5.5E
FBA_D<28> 4.2A 5.5E
FBA_D<29> 4.2A 5.5E
FBA_D<30> 4.2A 5.5E
FBA_D<31> 4.2A 5.5E
FBA_D<32> 4.2A 5.4F
FBA_D<33> 4.2A 5.4F
FBA_D<34> 4.2A 5.4F
FBA_D<35> 4.2A 5.4F
FBA_D<36> 4.2A 5.4F
FBA_D<37> 4.2A 5.4F
FBA_D<38> 4.2A 5.4F
FBA_D<39> 4.2A 5.4F
FBA_D<40> 4.2A 5.4F
FBA_D<41> 4.2A 5.4F
FBA_D<42> 4.2A 5.5F
FBA_D<43> 4.2A 5.5F
FBA_D<44> 4.2A 5.5F
FBA_D<45> 4.3A 5.5F
FBA_D<46> 4.3A 5.5F
FBA_D<47> 4.3A 5.5F
FBA_D<48> 4.3A 5.4G
FBA_D<49> 4.3A 5.4G
FBA_D<50> 4.3A 5.4G
FBA_D<51> 4.3A 5.4G
FBA_D<52> 4.3A 5.4G
FBA_D<53> 4.3A 5.4G
FBA_D<54> 4.3A 5.4G
FBA_D<55> 4.3A 5.4G
FBA_D<56> 4.3A 5.5G
FBA_D<57> 4.3A 5.5G
FBA_D<58> 4.3A 5.5G
FBA_D<59> 4.3A 5.5G
FBA_D<60> 4.3A 5.5G
FBA_D<61> 4.3A 5.5G
FBA_D<62> 4.3A 5.5G
FBA_D<63> 4.3A 5.5G
FBA_DEBUG0 4.1G< 4.4C
FBA_DEBUG1 4.2G< 4.4C
FBA_DQM<0> 4.3A 5.4D
FBA_DQM<1..0> 4.1G< 4.3D> 5.4C<
FBA_DQM<1> 4.3A 5.5D
FBA_DQM<2> 4.3A 5.4E
FBA_DQM<3> 4.3A 5.5E
FBA_DQM<4> 4.3A 5.4F
FBA_DQM<5> 4.3A 5.5F
FBA_DQM<6> 4.3A 5.4G
FBA_DQM<7> 4.3A 5.5G
FBA_DQS_RN<0> 4.4A 5.4D 7.1G
FBA_DQS_RN<7..0> 4.4A<> 5.4C<> 7.1G<>
FBA_DQS_RN<1> 4.4A 5.5D 7.1G
FBA_DQS_RN<2> 4.4A 5.4E 7.1G
FBA_DQS_RN<3> 4.4A 5.5E 7.1G
FBA_DQS_RN<4> 4.4A 5.4F 7.1G
FBA_DQS_RN<5> 4.4A 5.5F 7.1G
FBA_DQS_RN<6> 4.4A 5.4G 7.1G
FBA_DQS_RN<7> 4.4A 5.5G 7.1G
FBA_DQS_WP<0> 4.4A 5.4D 7.1G
FBA_DQS_WP<7..0> 4.4A<> 5.4C<> 7.1G<>
FBA_DQS_WP<1> 4.4A 5.5D 7.1G
FBA_DQS_WP<2> 4.4A 5.4E 7.1G
FBA_DQS_WP<3> 4.4A 5.5E 7.1G
FBA_DQS_WP<4> 4.4A 5.4F 7.1G
FBA_DQS_WP<5> 4.4A 5.5F 7.1G
FBA_DQS_WP<6> 4.4A 5.4G 7.1G
FBA_DQS_WP<7> 4.4A 5.5G 7.1G
FBA_VREF 5.1G< 5.3H
FBA_ZQ0 5.1G< 5.3A
FBA_ZQ1 5.1G< 5.3C
FBA_ZQ2 5.1G< 5.3E
FBA_ZQ3 5.1G< 5.3F

FBB_CLK0 4.1G> 4.4G> 6.2A<
6.2C< 6.4B<
FBB_CLK0* 4.1G> 4.4G> 6.2A<
6.2C< 6.4B<
FBB_CLK0_T 6.1G< 6.4B
FBB_CLK1 4.1G> 4.4G> 6.2E<
6.2F< 6.5B<
FBB_CLK1* 4.1G> 4.4G> 6.2E<
6.2F< 6.5B<
FBB_CLK1_T 6.1G< 6.5B
FBB_CMD<0> 4.3G 6.1A 6.1C
FBB_CMD<30..0> 4.1G> 4.3H> 6.1A<
FBB_CMD<2> 4.3G 4.3G 6.3A 6.3C
FBB_CMD<3> 4.3G 4.3H 6.2A 6.2C
FBB_CMD<5> 4.3G 4.4H 6.3A 6.3C
6.3E 6.3F
FBB_CMD<6> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<7> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<8> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<9> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<10> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<11> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<12> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<13> 4.3G 6.1A 6.1C 6.1E
6.1F
FBB_CMD<15> 4.3G 6.1A 6.1C 6.1E
6.1F
FBB_CMD<16> 4.3G 6.1E 6.1F
FBB_CMD<18> 4.3G 4.3G 6.3E 6.3F
FBB_CMD<19> 4.3G 4.3H 6.2E 6.2F
FBB_CMD<20> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<21> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<22> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<23> 4.4G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<24> 4.4G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<25> 4.4G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<26> 4.4G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<27> 4.4G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<28> 4.4G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<29> 4.4G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<30> 4.4G 6.1A 6.1C 6.1E
6.1F
FBB_D<0> 4.1E 6.4D
FBB_D<63..0> 4.1E<> 4.1G<> 6.4C<>
FBB_D<1> 4.1E 6.4D
FBB_D<2> 4.1E 6.4D
FBB_D<3> 4.1E 6.4D
FBB_D<4> 4.1E 6.4D
FBB_D<5> 4.1E 6.4D
FBB_D<6> 4.1E 6.4D
FBB_D<7> 4.1E 6.4D
FBB_D<8> 4.1E 6.5D
FBB_D<9> 4.1E 6.5D
FBB_D<10> 4.1E 6.5D
FBB_D<11> 4.1E 6.5D
FBB_D<12> 4.1E 6.5D
FBB_D<13> 4.1E 6.5D
FBB_D<14> 4.1E 6.5D
FBB_D<15> 4.2E 6.5D
FBB_D<16> 4.2E 6.4E
FBB_D<17> 4.2E 6.4E
FBB_D<18> 4.2E 6.4E
FBB_D<19> 4.2E 6.4E
FBB_D<20> 4.2E 6.4E
FBB_D<21> 4.2E 6.4E
FBB_D<22> 4.2E 6.4E
FBB_D<23> 4.2E 6.4E
FBB_D<24> 4.2E 6.5E
FBB_D<25> 4.2E 6.5E
FBB_D<26> 4.2E 6.5E
FBB_D<27> 4.2E 6.5E
FBB_D<28> 4.2E 6.5E
FBB_D<29> 4.2E 6.5E
FBB_D<30> 4.2E 6.5E
FBB_D<31> 4.2E 6.5E
FBB_D<32> 4.2E 6.4F
FBB_D<33> 4.2E 6.4F
FBB_D<34> 4.2E 6.4F
FBB_D<35> 4.2E 6.4F
FBB_D<36> 4.2E 6.4F
FBB_D<37> 4.2E 6.4F
FBB_D<38> 4.2E 6.4F
FBB_D<39> 4.2E 6.4F
FBB_D<40> 4.2E 6.5F
FBB_D<41> 4.2E 6.5F
FBB_D<42> 4.2E 6.5F
FBB_D<43> 4.2E 6.5F
FBB_D<44> 4.2E 6.5F
FBB_D<45> 4.3E 6.5F
FBB_D<46> 4.3E 6.5F
FBB_D<47> 4.3E 6.5F
FBB_D<48> 4.3E 6.4G
FBB_D<49> 4.3E 6.4G
FBB_D<50> 4.3E 6.4G
FBB_D<51> 4.3E 6.4G
FBB_D<52> 4.3E 6.4G
FBB_D<53> 4.3E 6.4G
FBB_D<54> 4.3E 6.4G
FBB_D<55> 4.3E 6.4G
FBB_D<56> 4.3E 6.5G
FBB_D<57> 4.3E 6.5G
FBB_D<58> 4.3E 6.5G
FBB_D<59> 4.3E 6.5G
FBB_D<60> 4.3E 6.5G
FBB_D<61> 4.3E 6.5G
FBB_D<62> 4.3E 6.5G
FBB_D<63> 4.3E 6.5G
FBB_DEBUG0 4.2G< 4.4G
FBB_DEBUG1 4.2G< 4.4G
FBB_DQM<0> 4.3E 6.4D
FBB_DQM<7..0> 4.1G> 4.3E> 6.4C<
FBB_DQM<1> 4.3E 6.5D
FBB_DQM<2> 4.3E 6.4E
FBB_DQM<3> 4.3E 6.5E
FBB_DQM<4> 4.3E 6.4F
FBB_DQM<5> 4.3E 6.5F
FBB_DQM<6> 4.3E 6.4G
FBB_DQM<7> 4.3E 6.5G
FBB_DQS_RN<0> 4.4E 6.4D 7.1G
FBB_DQS_RN<7..0> 4.4E<> 6.4C<> 7.1G<>
FBB_DQS_RN<1> 4.4E 6.5D 7.1G
FBB_DQS_RN<2> 4.4E 6.4E 7.1G
FBB_DQS_RN<3> 4.4E 6.5E 7.1G
FBB_DQS_RN<4> 4.4E 6.4F 7.2G
FBB_DQS_RN<5> 4.4E 6.5F 7.2G
FBB_DQS_RN<6> 4.4E 6.4G 7.2G
FBB_DQS_RN<7> 4.4E 6.5G 7.2G
FBB_DQS_WP<0> 4.4E 6.4D 7.1G
FBB_DQS_WP<7..0> 4.4E<> 6.4C<> 7.1G<>
FBB_DQS_WP<1> 4.4E 6.5D 7.1G
FBB_DQS_WP<2> 4.4E 6.4E 7.1G
FBB_DQS_WP<3> 4.4E 6.5E 7.1G
FBB_DQS_WP<4> 4.4E 6.4F 7.2G
FBB_DQS_WP<5> 4.4E 6.5F 7.2G
FBB_DQS_WP<6> 4.4E 6.4G 7.2G
FBB_DQS_WP<7> 4.4E 6.5G 7.2G
FBB_VREF0 6.1G< 6.3D
FBB_VREF1 6.1G< 6.3H
FBB_ZQ0 6.1G< 6.3A
FBB_ZQ1 6.1G< 6.3C
FBB_ZQ2 6.1G< 6.3E
FBB_ZQ3 6.1G< 6.3F
FBVDDQ 16.1G
FB_CAL_PD_VDDQ 4.1G< 4.5G
FB_CAL_PU_GND 4.1G< 4.5G
FB_CAL_TERM_GND 4.1G< 4.5G
FB_PLLAVDD 4.2G< 4.5C
GPIO0_DVI_HPD 10.1G> 10.4D
GPIO0_DVI_HPD_C 10.1G< 10.3F
GPIO0_DVI_HPD_R 10.1G< 10.4E
GPIO2_NVVDDCTL 13.2D> 17.1G< 17.5G<
GPIO2_NVVDDCTL_R 17.2G< 17.5F
GPIO4_FAN_TACH 13.2D< 13.2G< 14.2E>
GPIO5_NVVDDCTL 13.2D> 17.1G< 17.4D<
GPIO5_NVVDDCTL_R 17.1G< 17.4E
GPIO6_NVVDDCTL 13.2D> 17.1G< 17.4G<
GPIO6_NVVDDCTL_R 17.1G< 17.4F
GPIO7_NVVDDCTL 13.2D> 17.1G< 17.5D<
GPIO7_NVVDDCTL_R 17.1G< 17.5E
GPIO8_THERM_OVERT* 13.2D> 15.1G< 15.2B<
GPIO16_FAN_ADJ 14.1G< 14.2C
GPIO16_FAN_C 14.1G< 14.2D
GPIO16_FAN_PWM 13.2D> 13.2G> 14.2B<
14.2E<
GPIO16_FAN_Q 14.1G< 14.2C
GPIO21_HDMI_HPD 11.2G< 11.4D
GPIO21_HDMI_HPD_C 11.2G< 11.3G
GPIO21_HDMI_HPD_R 11.2G< 11.4E
GPU_BUFRST* 13.4D> 15.1G< 15.3E<
GPU_PLLVDD 13.2G< 13.4B
GPU_TESTMODE 3.1G< 3.5F
I2CA_SCL 8.1F> 8.1G> 10.3F<
I2CA_SDA 8.1G<> 8.2F<> 10.3F<>
I2CB_SCL 9.1G< 9.3C
I2CB_SDA 9.1G< 9.3C
I2CC_SCL 13.1D 13.1G<
I2CC_SCL_R 13.1F 13.1G<
I2CC_SDA 13.1D 13.1G<
I2CC_SDA_R 13.1F 13.1G<
I2CZ_SCL 11.1G< 11.3D
I2CZ_SCL_C 11.1G< 11.3G
I2CZ_SDA 11.1G< 11.3D
I2CZ_SDA_C 11.1G< 11.3G
IFPAB_IOVDD 10.2G< 10.3C
IFPAB_PLLVDD 10.1G< 10.2C

IFPAB_RSET 10.1G< 10.2C
IFPAB_TXC 10.1G< 10.3D
IFPAB_TXC* 10.1G< 10.3D
IFPAB_TXD0 10.1G< 10.2D
IFPAB_TXD0* 10.1G< 10.2D
IFPAB_TXD1 10.1G< 10.2D
IFPAB_TXD1* 10.1G< 10.2D
IFPAB_TXD2 10.1G< 10.2D
IFPAB_TXD2* 10.1G< 10.2D
IFPAB_TXD4 10.1G< 10.3D
IFPAB_TXD4* 10.1G< 10.3D
IFPAB_TXD5 10.1G< 10.3D
IFPAB_TXD5* 10.1G< 10.3D
IFPAB_TXD6 10.1G< 10.3D
IFPAB_TXD6* 10.1G< 10.3D
IFPF_IOVDD 11.2G< 11.3C
IFPF_PLLVDD 11.2C 11.2G<
IFPF_RSET 11.2G< 11.3C
IFPF_TERM 11.2G< 11.5C
IFPF_TXC 11.1G< 11.3D
IFPF_TXC* 11.1G< 11.3D
IFPF_TXC_C 11.1G< 11.3G 11.5D
IFPF_TXC_C* 11.1G< 11.3G 11.5D
IFPF_TXD0 11.1G< 11.3D
IFPF_TXD0* 11.1G< 11.3D
IFPF_TXD0_C 11.1G< 11.3G 11.4D
IFPF_TXD0_C* 11.1G< 11.3G 11.4D
IFPF_TXD1 11.1G< 11.3D
IFPF_TXD1* 11.1G< 11.3D
IFPF_TXD1_C 11.1G< 11.3G 11.4D
IFPF_TXD1_C* 11.1G< 11.3G 11.4D
IFPF_TXD2 11.1G< 11.3D
IFPF_TXD2* 11.1G< 11.3D
IFPF_TXD2_C 11.1G< 11.4D 11.4G
IFPF_TXD2_C* 11.1G< 11.3G 11.4D
IROM_VCC 13.1F 13.1G<
JTAG_TCK 13.1G< 13.2C
JTAG_TDI 13.1G< 13.2C
JTAG_TDO 13.1G< 13.2C
JTAG_TMS 13.1G< 13.2C
JTAG_TRST* 13.1G< 13.2C
NVVDD 17.2H
NVVDD_SENSE 3.4F> 17.1G< 17.4H<
PEX_PLL 15.1G
PEX_PLLVDD 3.1G< 3.4F
PEX_PRSNT* 3.1B 3.1G<
PEX_REFCLK 3.2C 3.5A<
PEX_REFCLK* 3.2C 3.5A<
PEX_RST* 3.1D> 15.1G< 15.2E<
15.3E<
PEX_RST_BUF* 15.1G< 15.2F
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