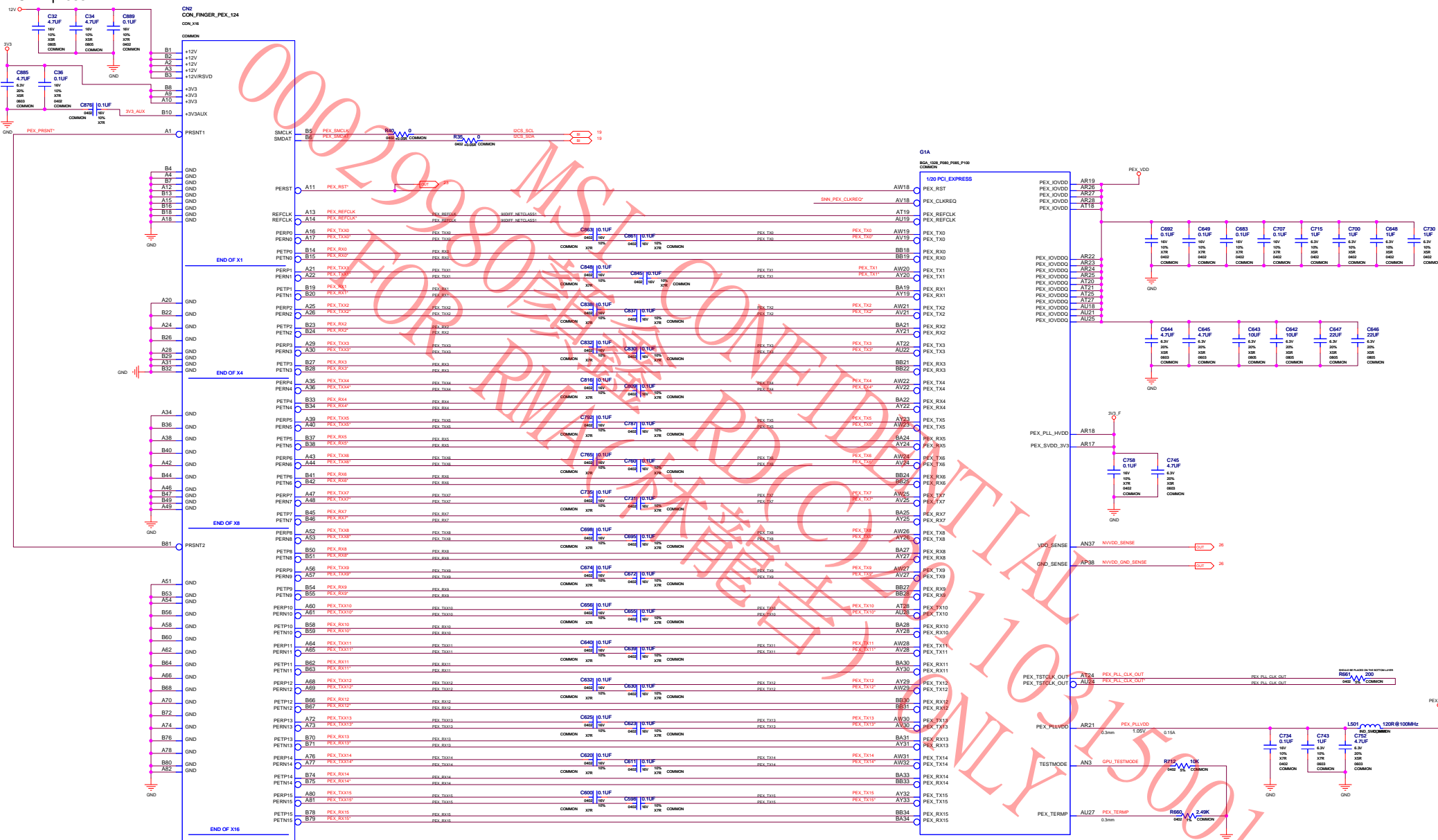



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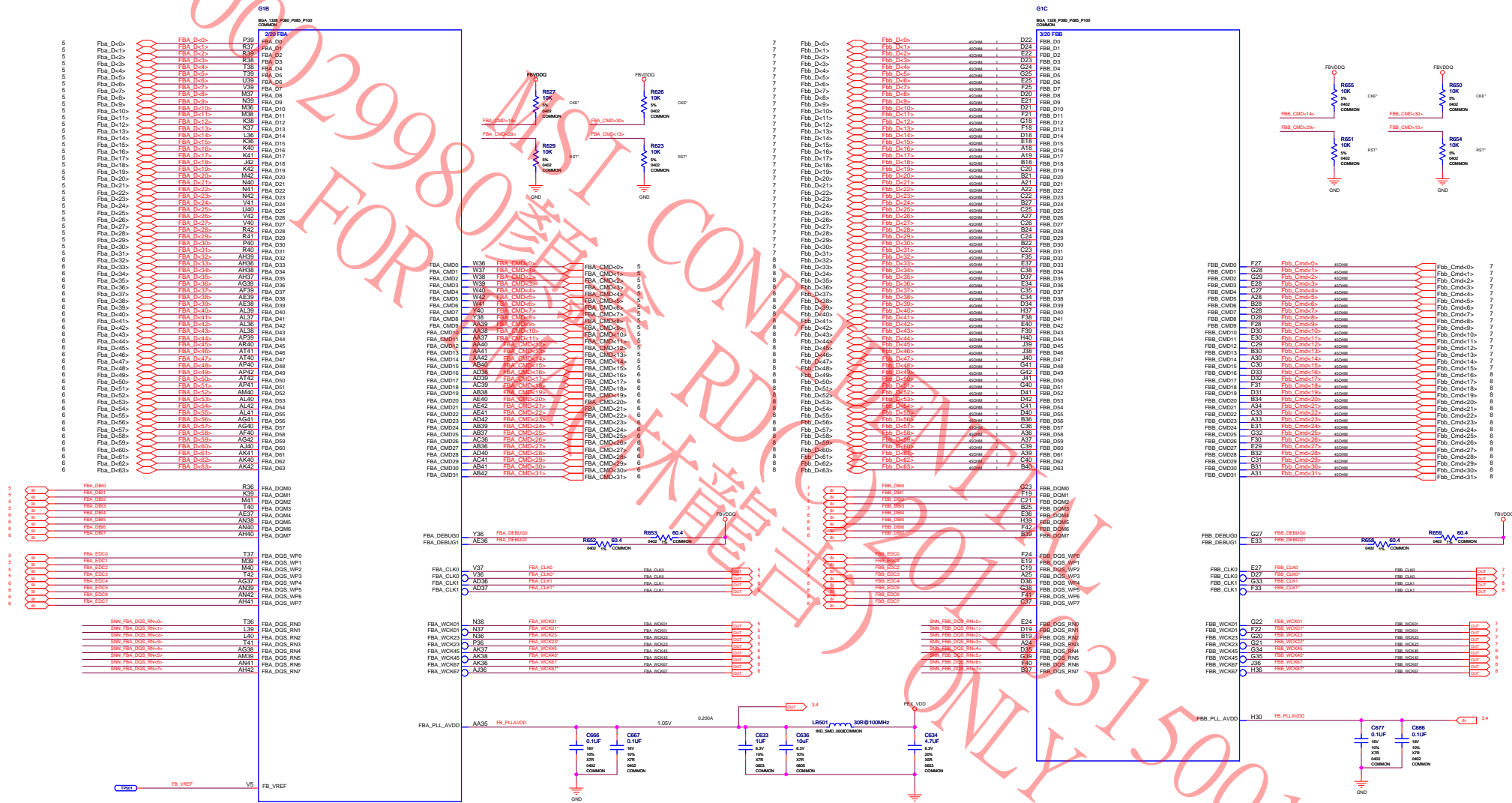


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MEMORY: GPU FB Partition A and B



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ASSEMBLY PAGE: RETAIL
BASE LEVEL GENERIC SCHEMATIC ONLY
GPU FB PARTITION A AND B

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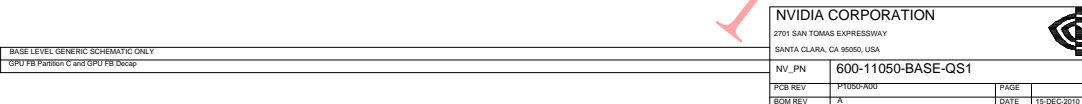
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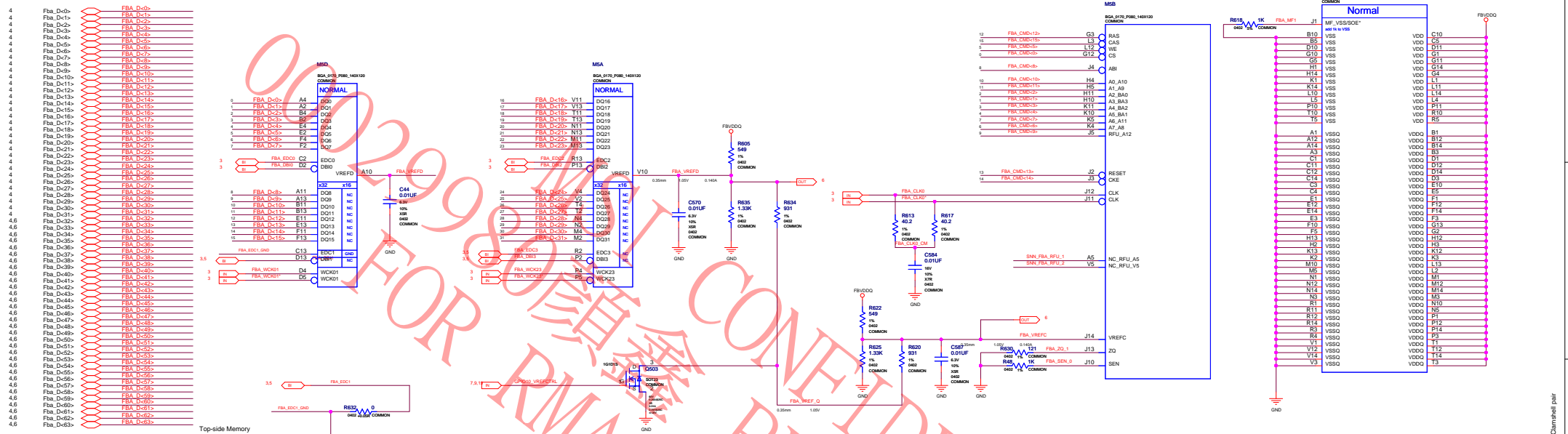
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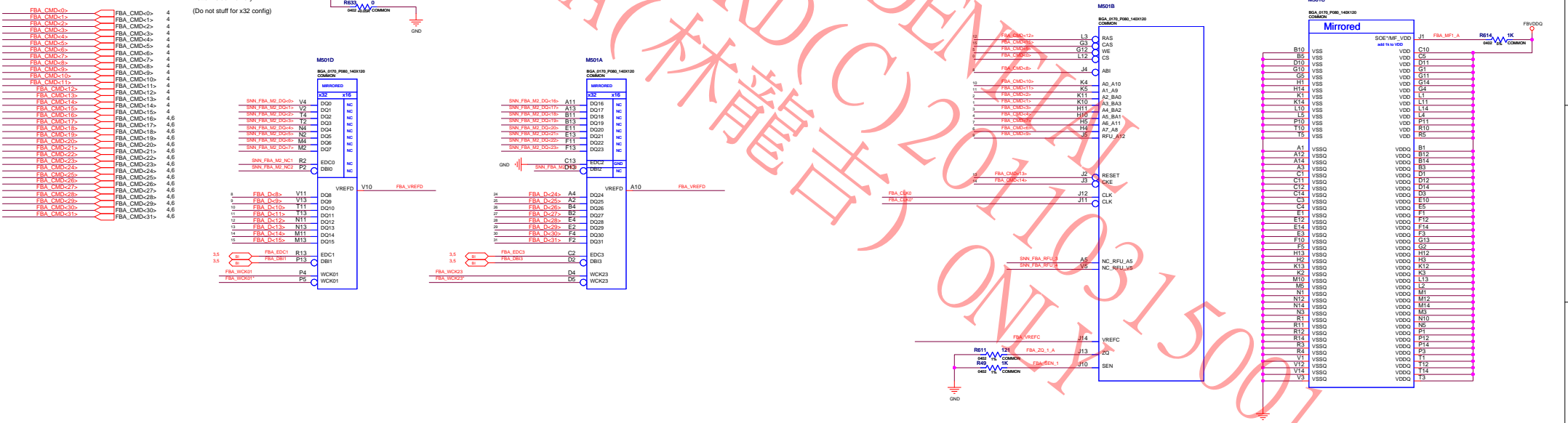
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	GPU FB Partition C and GPU FB Decap

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PCB REV	PT050-ADU	PAGE	
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FBA Partition Memory (1 of 2)



Top-side Memory



Bottom-side memory

(Do not stuff for x32 cont)

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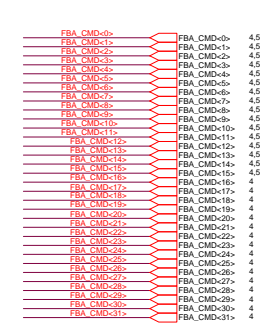
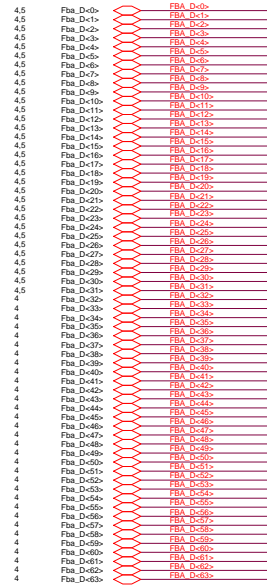
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PAGE DETAIL	FBA MEMORY (1 OF 2)

FBA Partition Memory (2 Of 2)



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FBA MEMORY (2 OF 2)

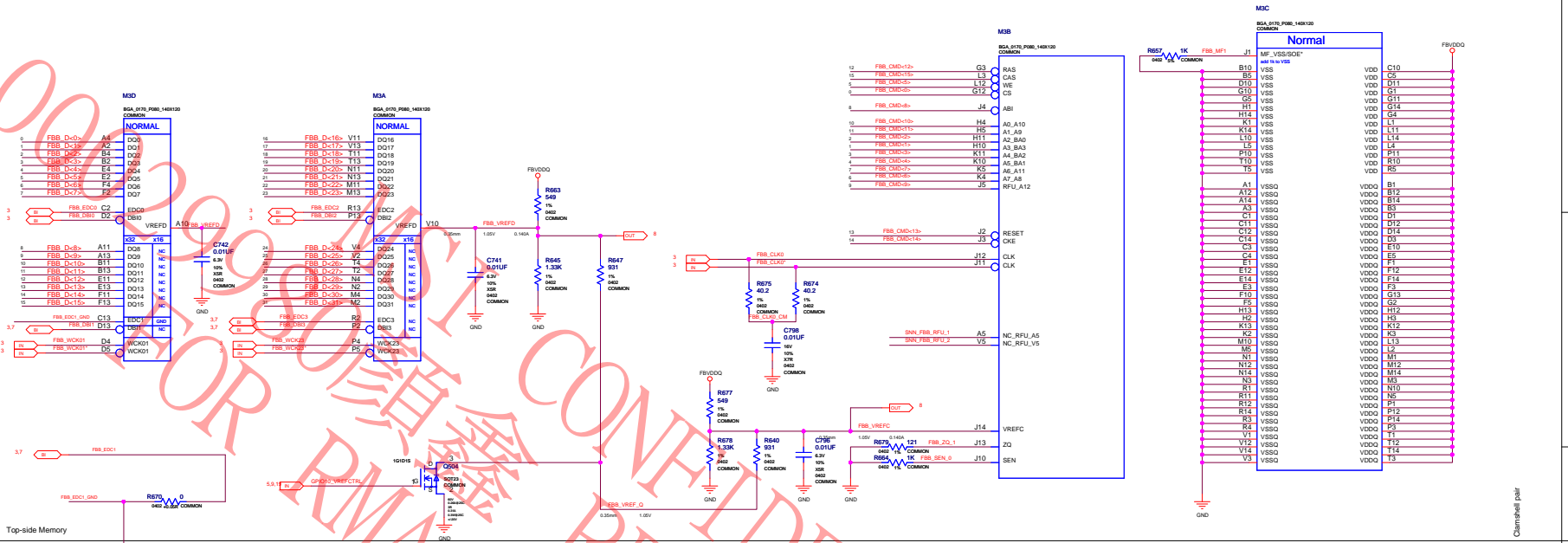
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SV_PN 600-11050-BASE-QS1

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PAGE 1
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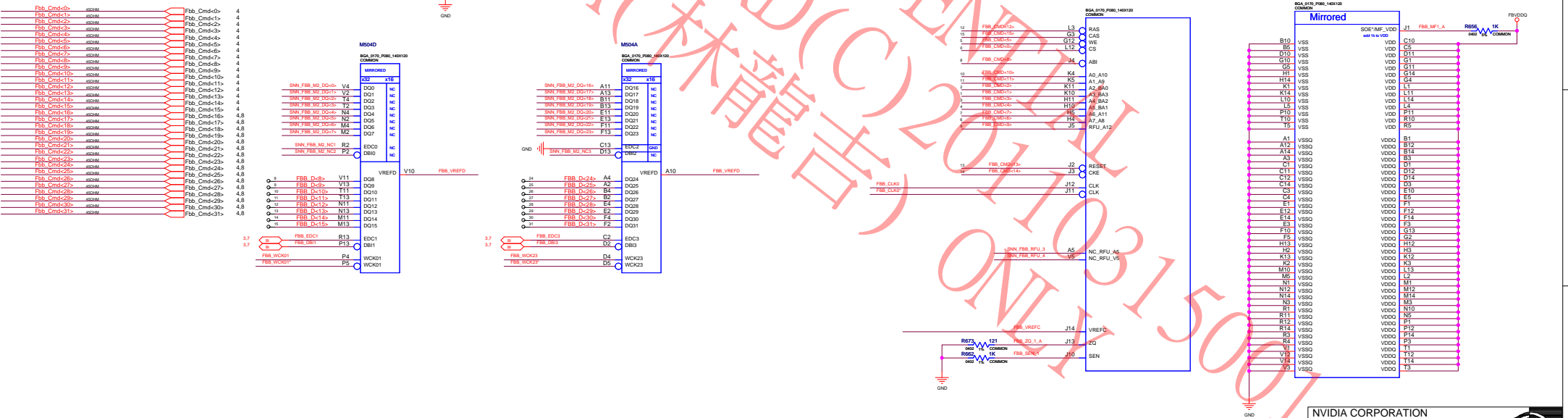
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Top-side Memory

Bottom-side memo

(Do Not Install for x32 config)



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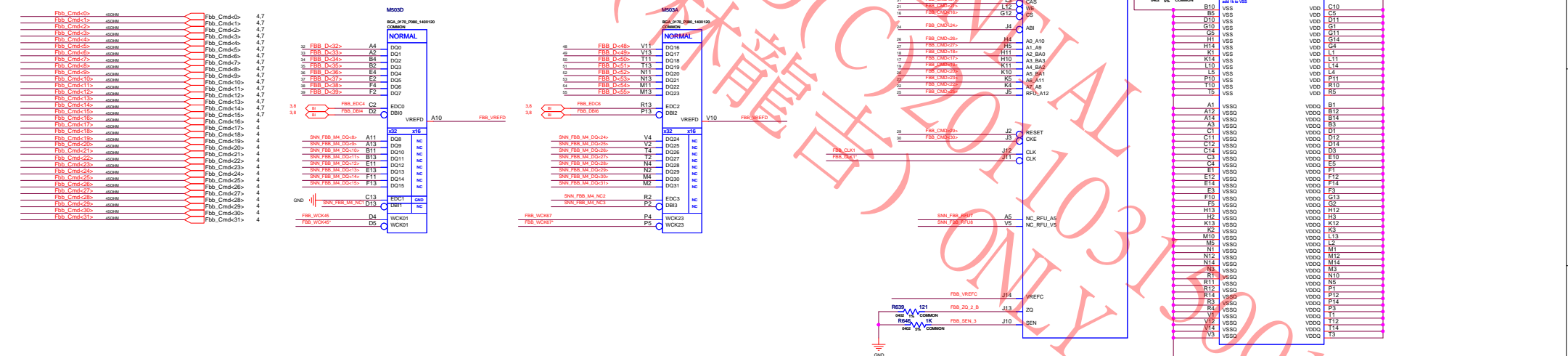
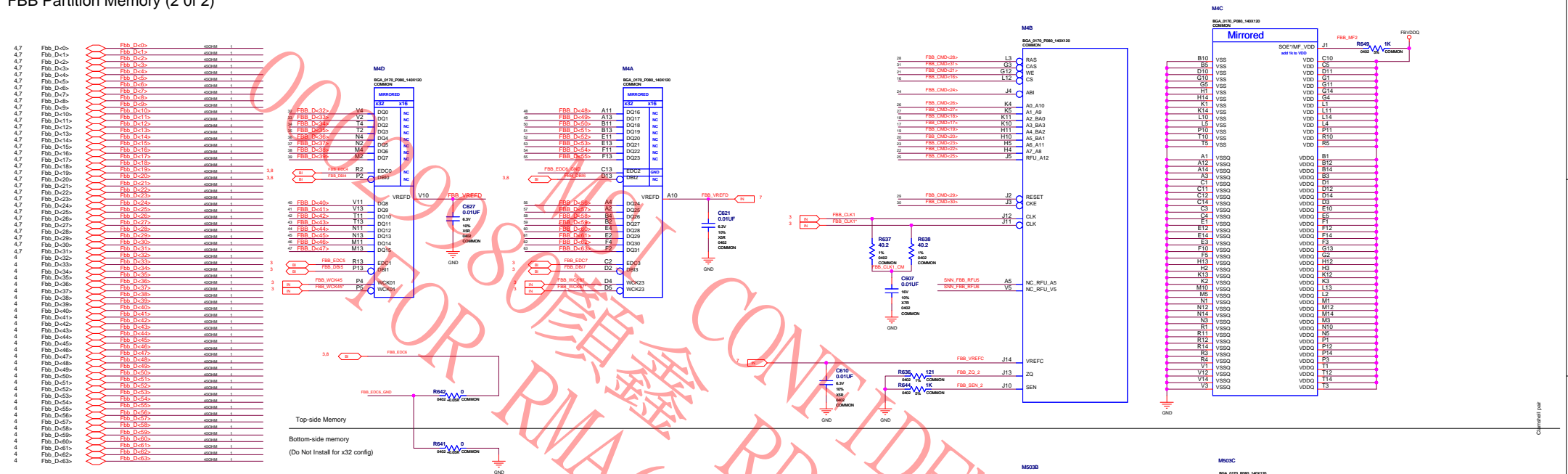
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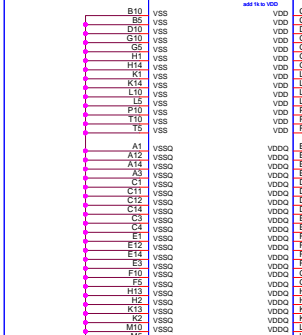
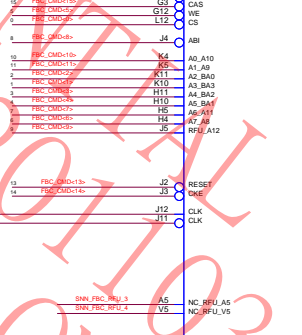
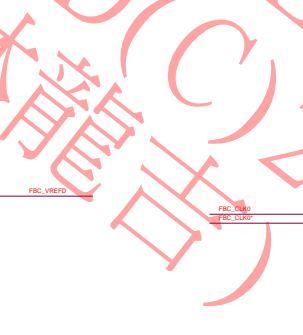
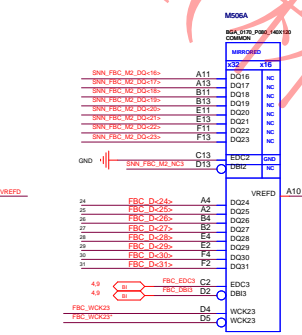
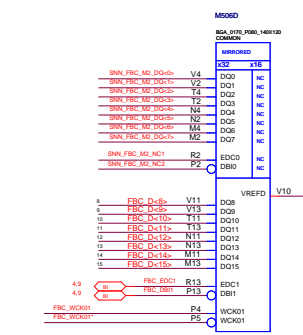
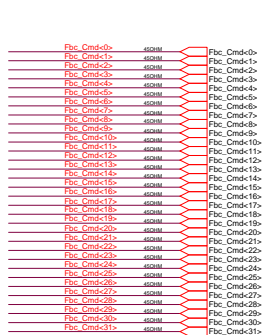
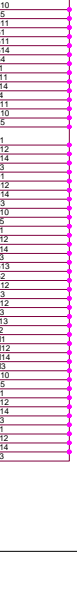
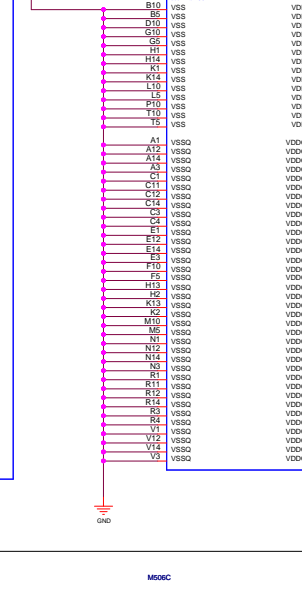
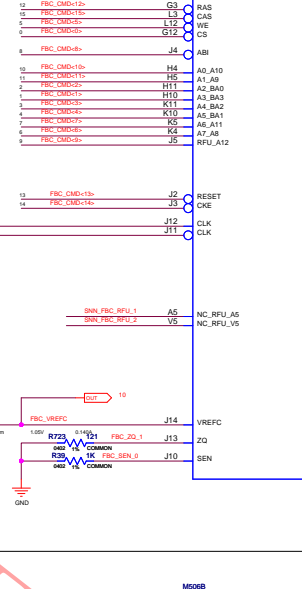
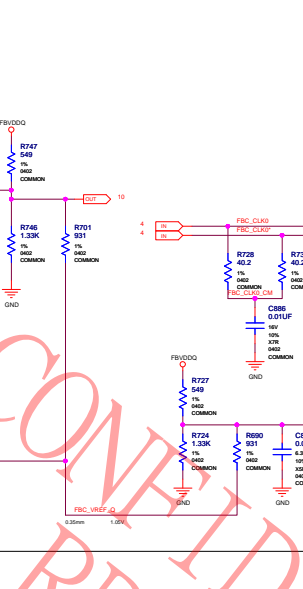
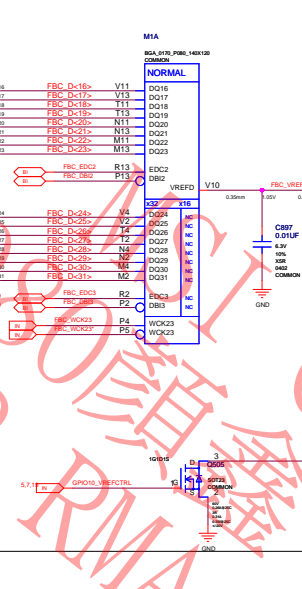
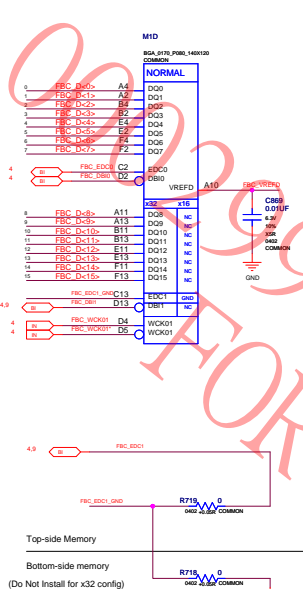
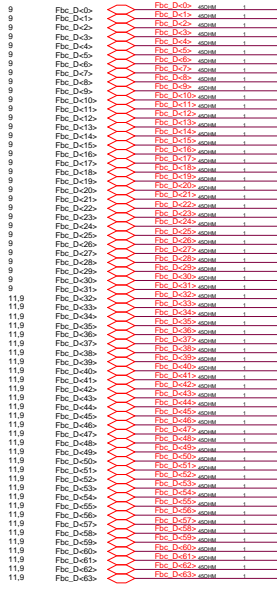
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FBB Partition Memory (2 of 2)



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FBC Partition Memory (1 of 2)



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ASSEMBLY PAGE DETAIL BASE LEVEL GENERIC SCHEMATIC ONLY FBC MEMORY (1 OF 3)

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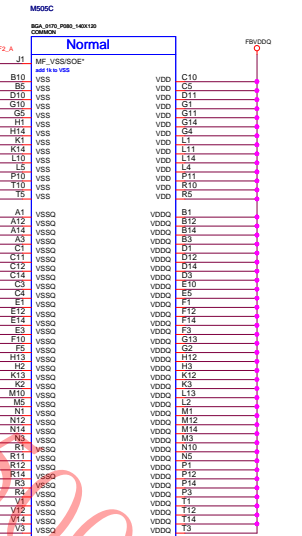
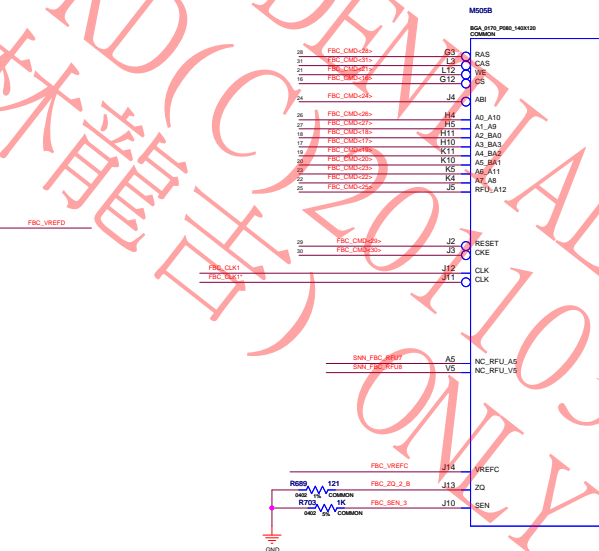
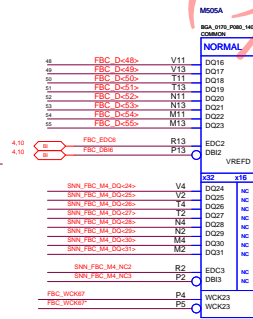
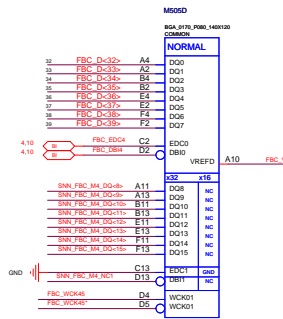
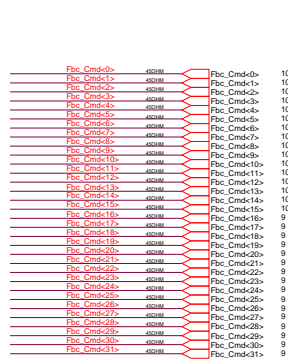
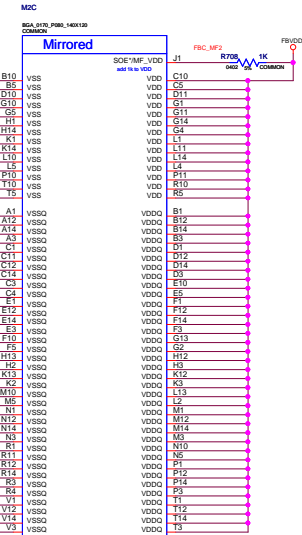
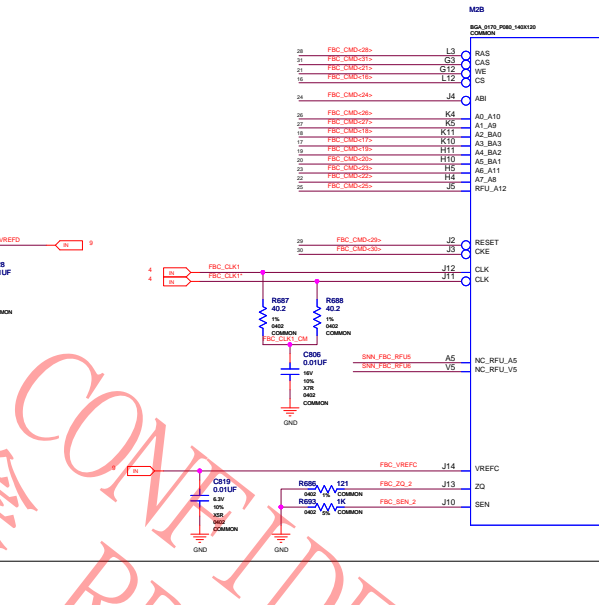
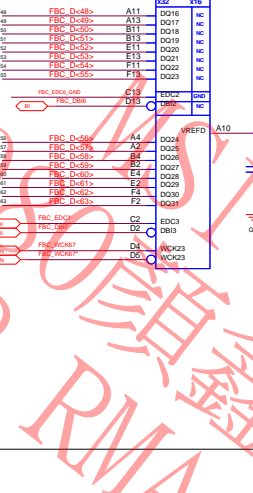
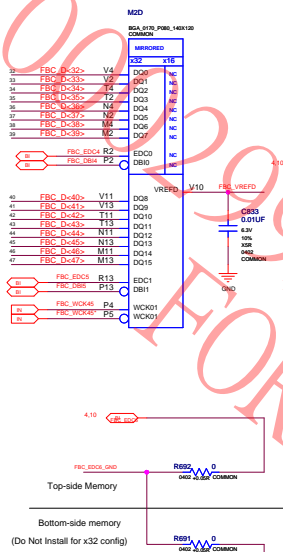
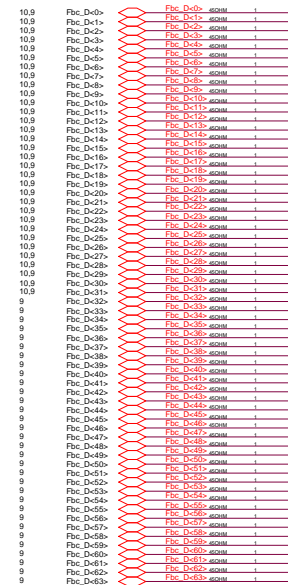
600-11050-BASE-QS1

PCBR REV P10007A01 BOM REV A

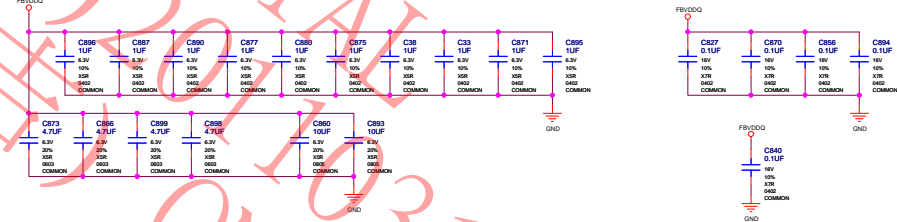
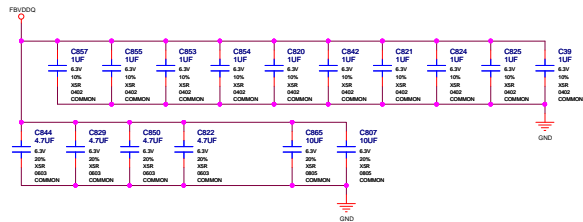
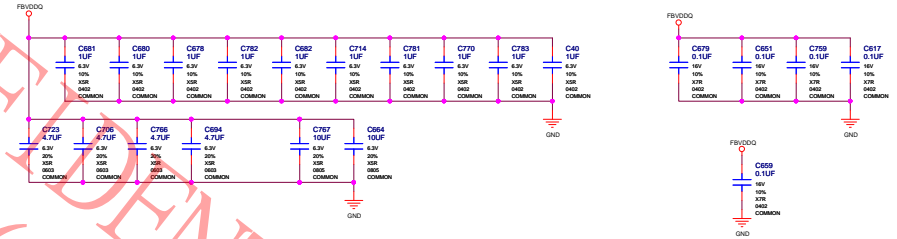
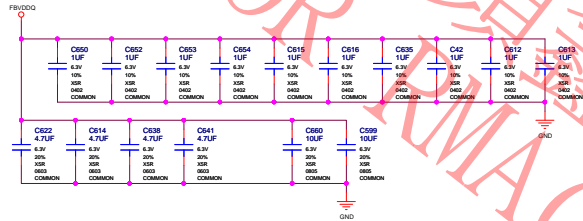
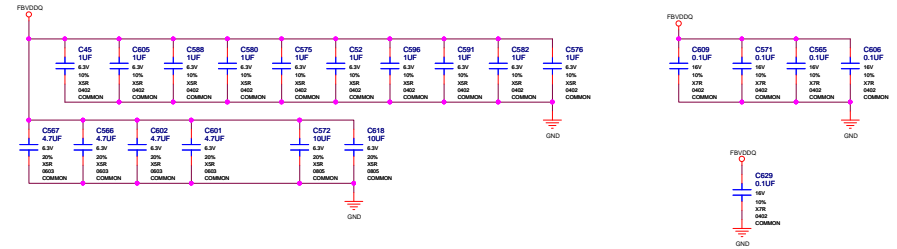
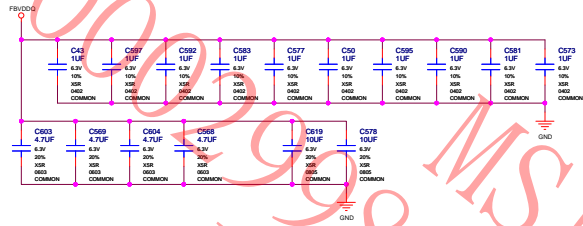
PAGE

DATE 15-DEC-2016

FBC Partition (2 of 2)



FB MEMORY DECAP



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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	FB MEMORY DECAP

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NV_PN	600-11050-BASE-QS1
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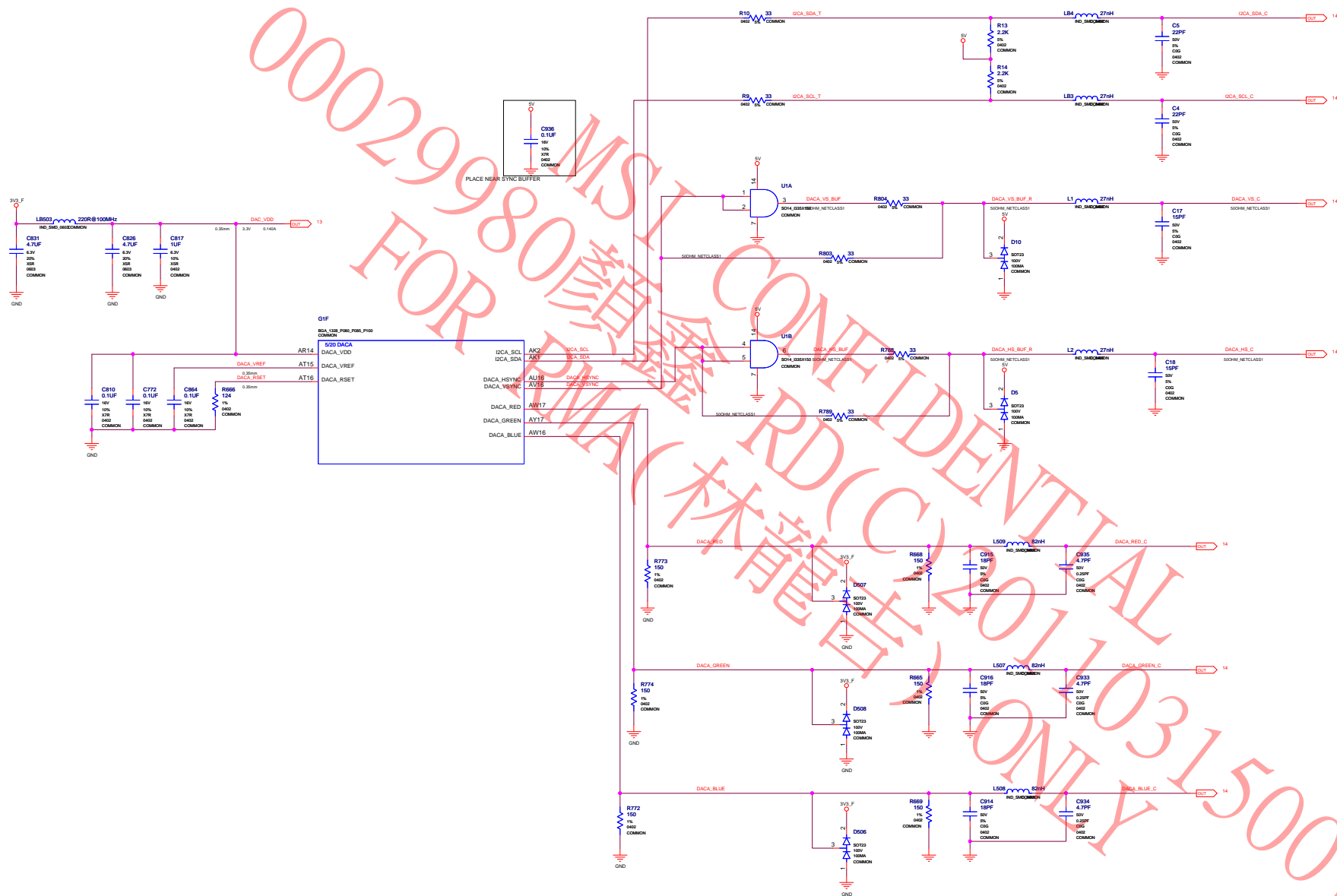
PCB REV	P1050-A00
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BOM REV	A
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[illegible]

DACA for South DVI Interface

DACA RGB-FILTER



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ASSEMBLY BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL DACA (VENDOR TO DVI CONNECTOR) SIGNAL

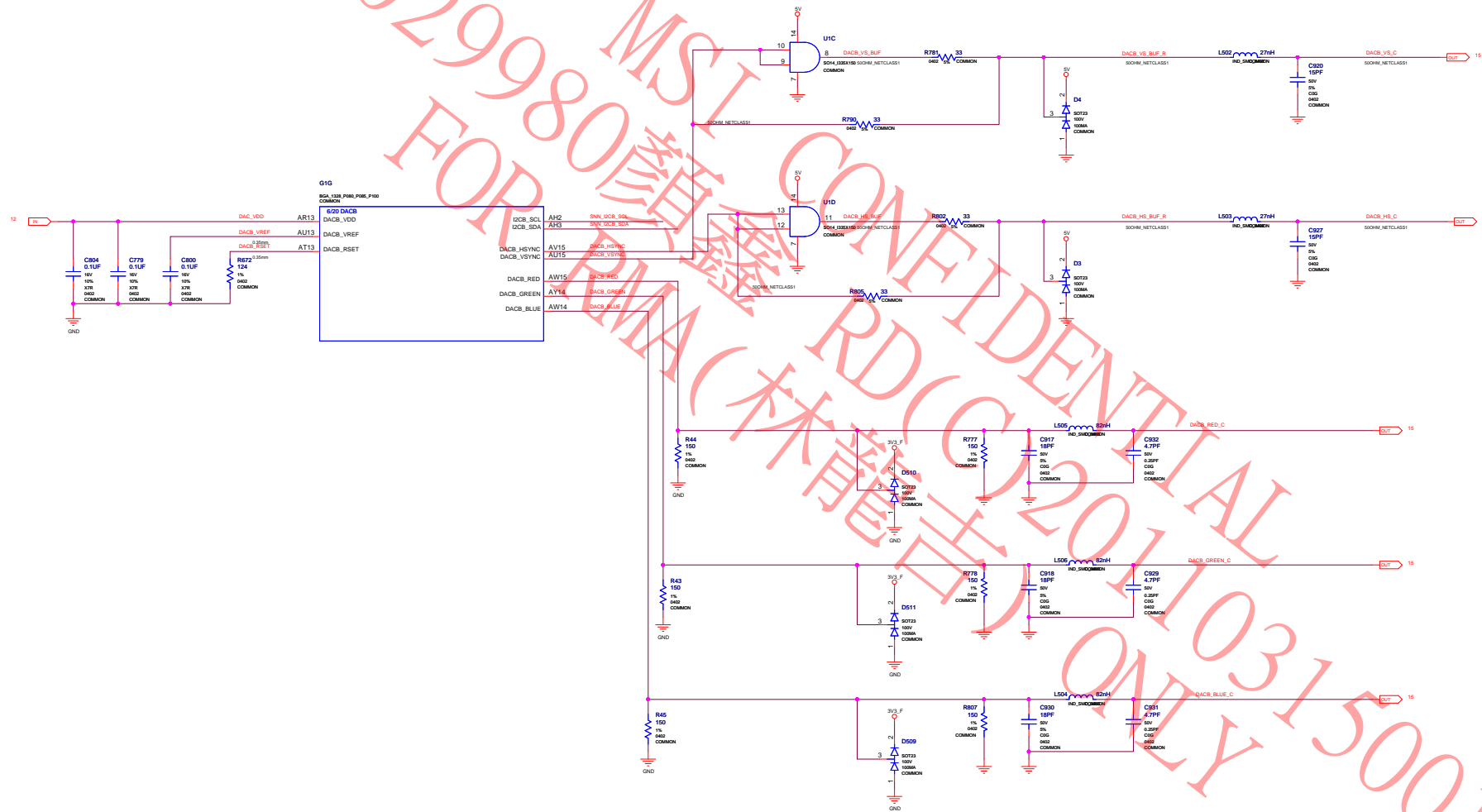
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PCB REV	P1000-000	PAGE	
BOM REV	A	DATE	15-DEC-2016

DACB RGB-FILTER

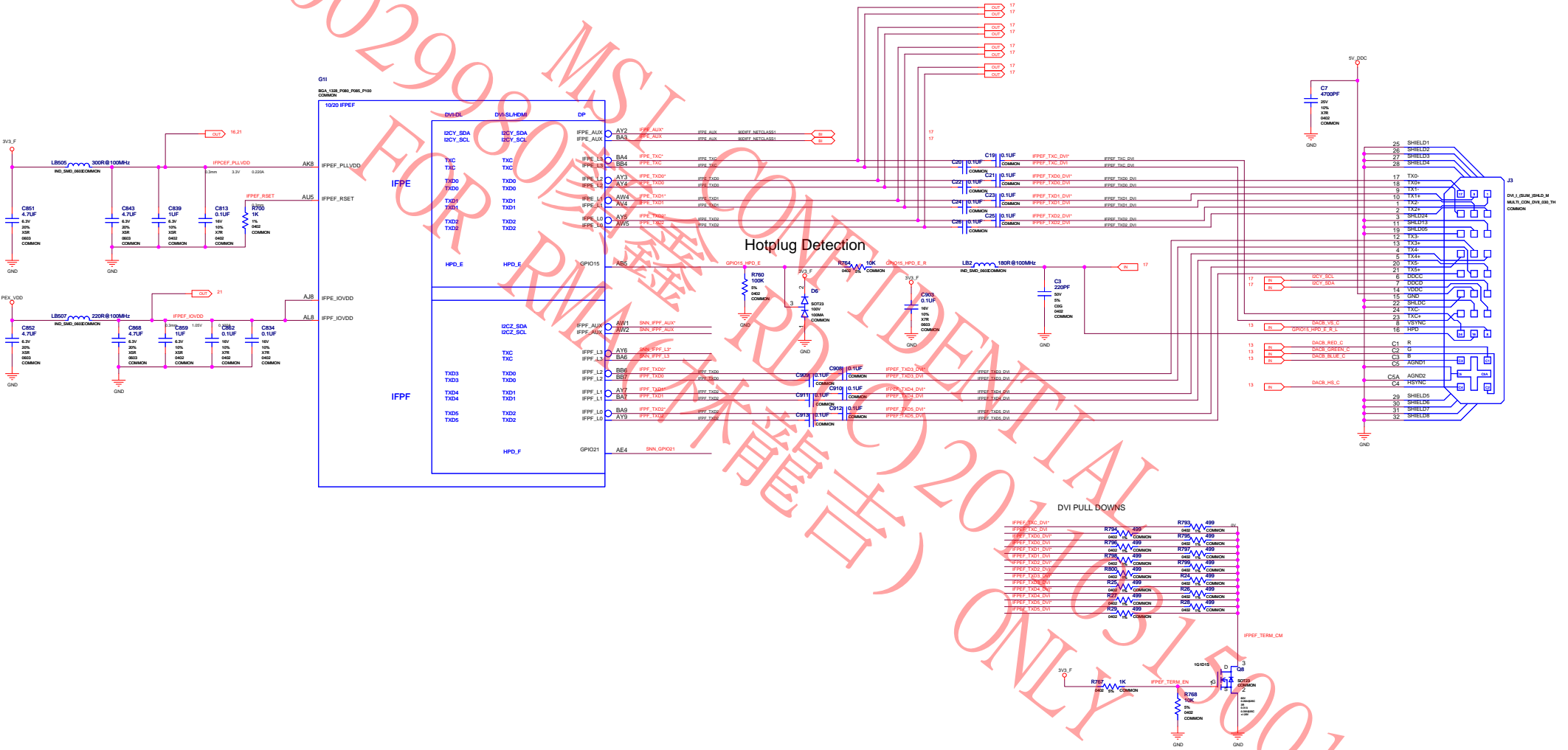


ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	DACB Interface for DVI Connector, Mid

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PCB REV	P1050-A00	PAGE	
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IFP E/F Interface -- DVI Connector, Mid



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ASSEMBLY
PAGE DETAIL

BASE LEVEL GENERIC SCHEMATIC ONLY
IFP E/F Interface -- DVI Connector, Mid

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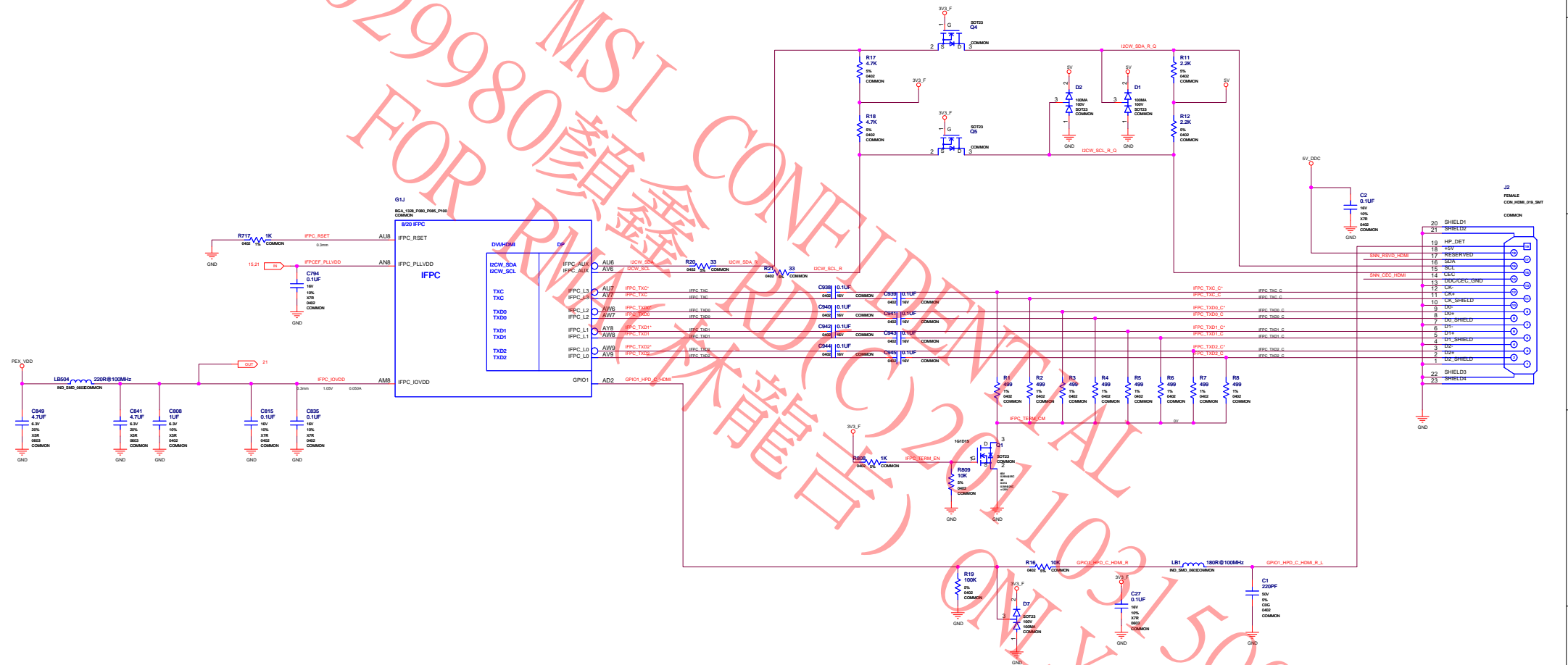


NV_PN 600-11050-BASE-QS1

PCBR REV P1000-ADD
BOM REV A

PAGE
DATE 15-DEC-2016

IFP C Interface -- miniHDMI, North



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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	IFPC Interface to HDMI

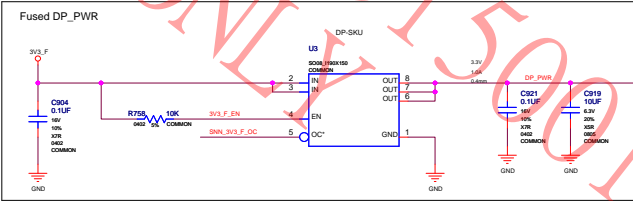
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PCB REV	P1050-A00	PAGE	
BOM REV	A	DATE	15-DEC-2010

Three cases to be considered:

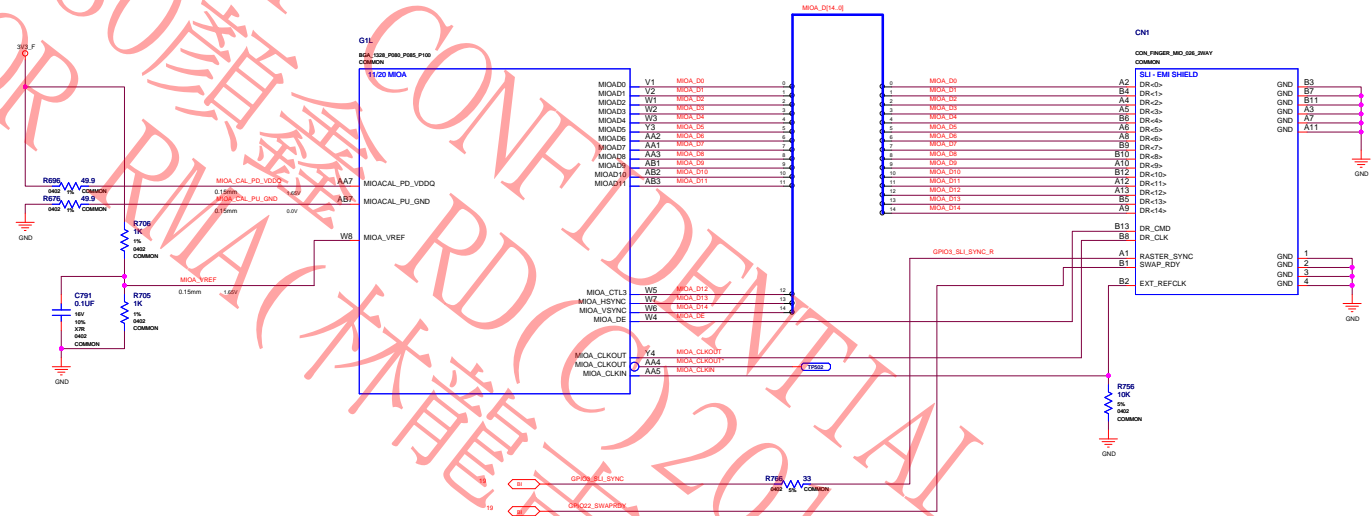
1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through
3. DP AUX to DVI connector: AUX level shifted to DDC



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	DP FROM IFPE, and IFPD (UNUSED)

PAGE	
DATE	15-DEC-2010

MIO A Interface, SLI

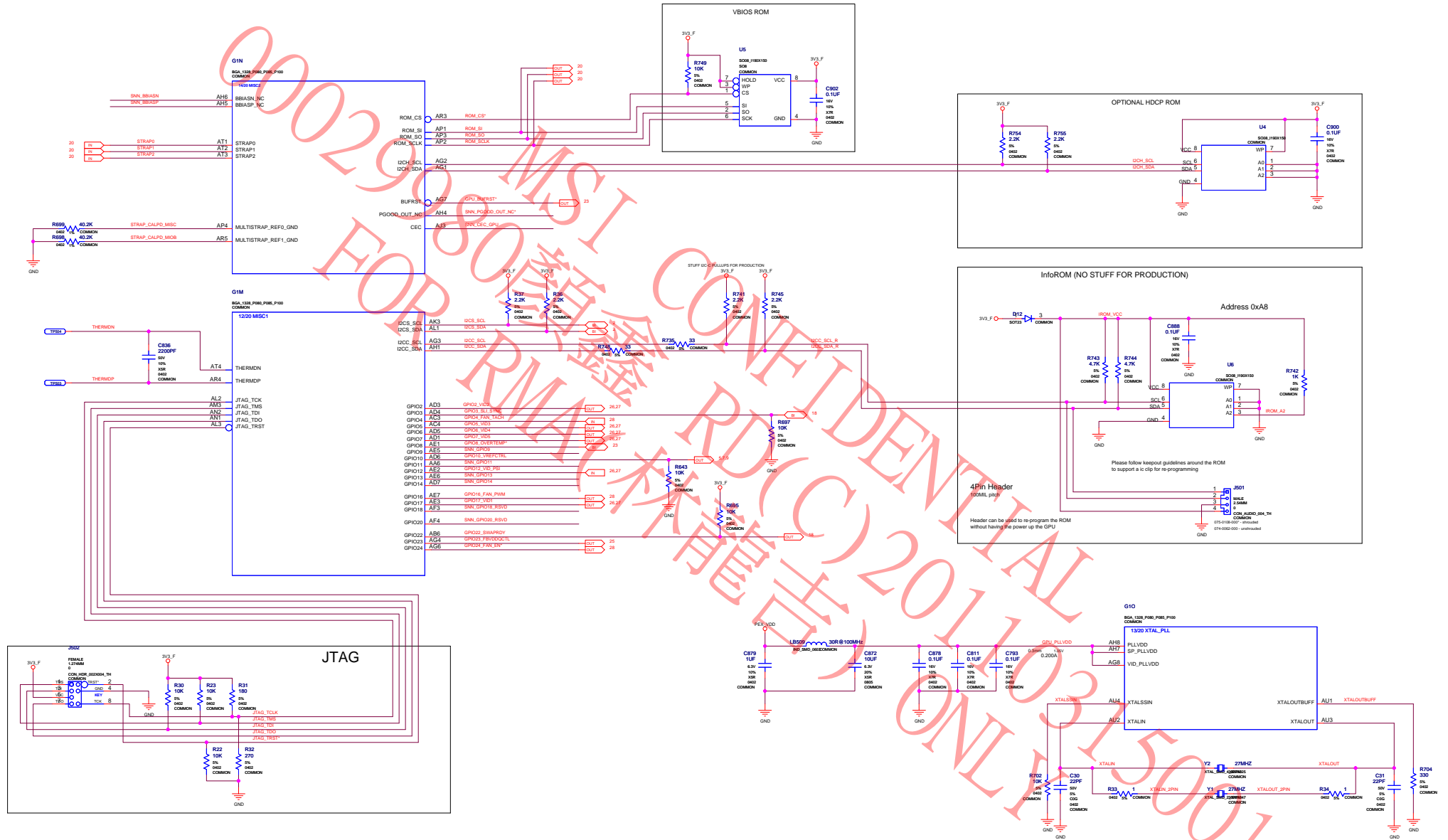


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ASSEMBLY: BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE: 1/1
PAGE DETAIL: MIO A INTERFACE, SLI

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MISC: GPIO, I2C, ROM, HDCP, and XTAL



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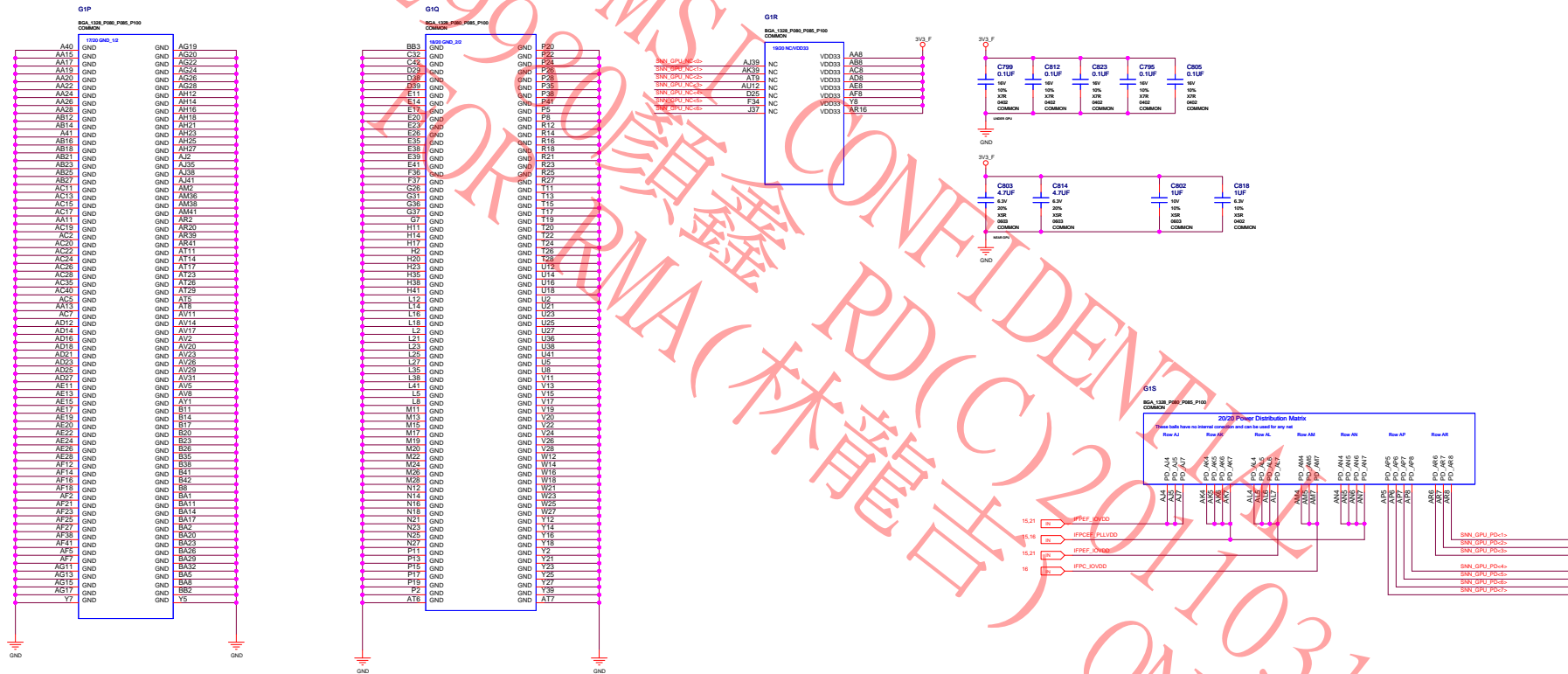


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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	MISC: GPIO, I2C, ROM, HDCP, and XTAL

PCB REV	P1050-A00	PAGE	
BOM REV	A	DATE	15-DEC-2010

PWR and GND Signals

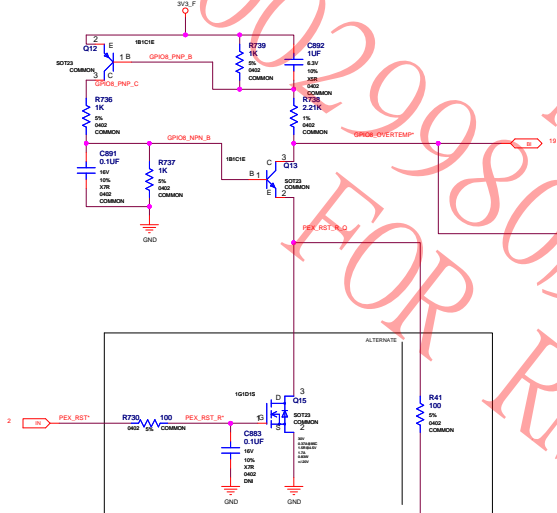


NVDD Decoupling

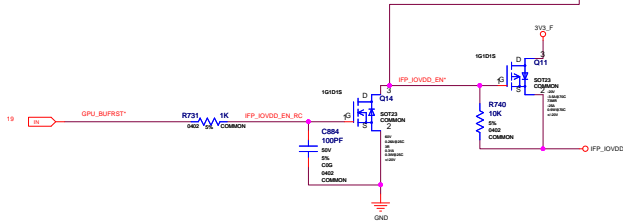
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Power Sequencing, Thermal Protection, and IFP_IOVDD Power Supply

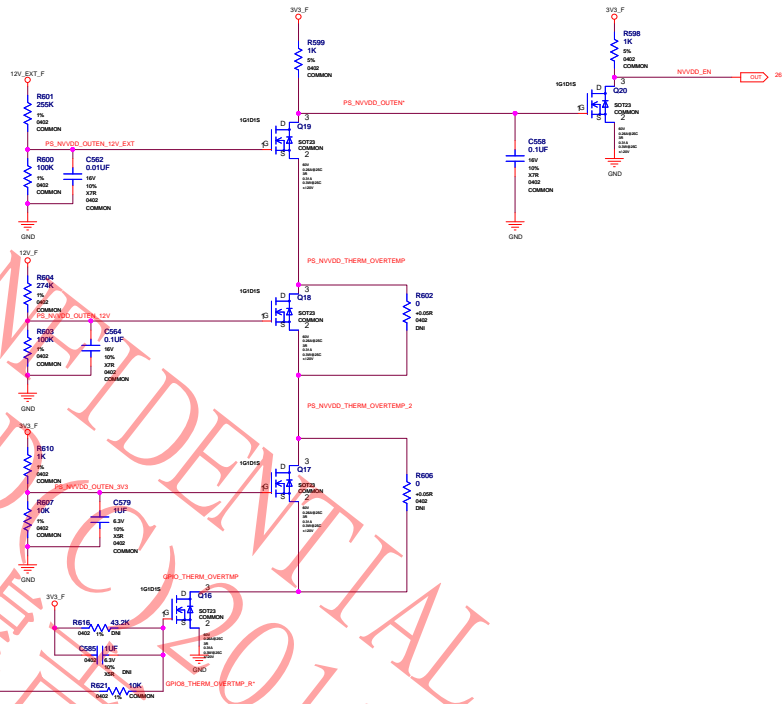
Thermal Protection (latch)



IFP_IOVDD Backdrive Prevention



NVVD Sequence



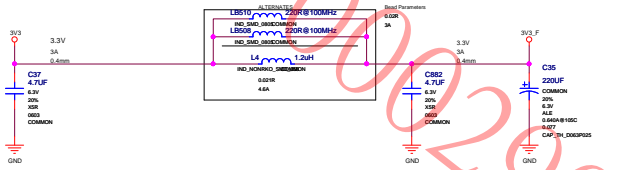
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PS I: 3V3, 12V, and 12V_EXT Power Supply Filter, 5V, DDC VOLTAGE

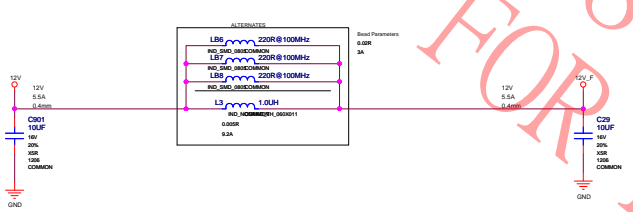
3V3 Power Supply Filter

3V3_F = 3.3V @ 2.4A

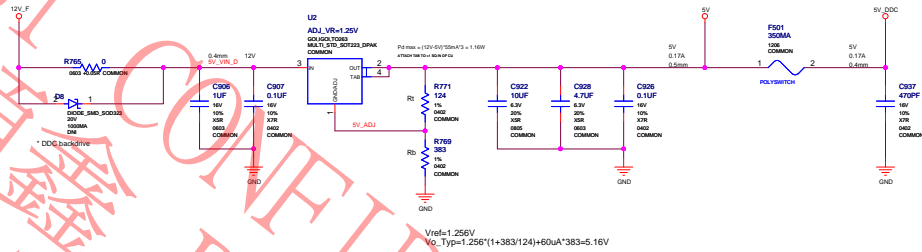


12V Power Supply Filter

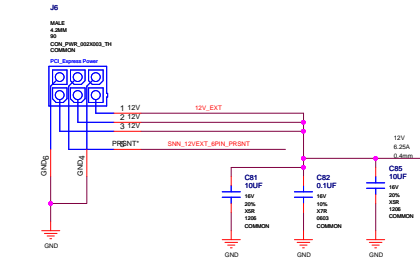
12V_F = 12V @ 5.5A



5V POWER SUPPLY, DDC POWER

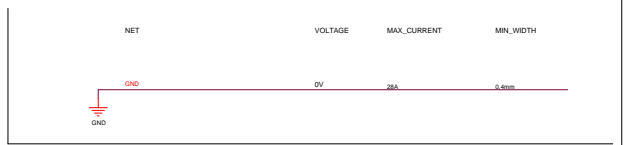
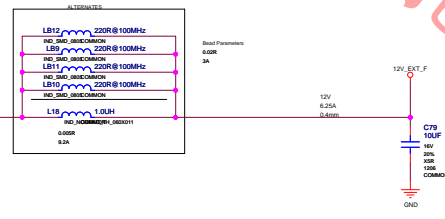


12V_EXT Power Connector



12V_EXT Power Supply Filter

12V_EXT_F = 12V @ 6A



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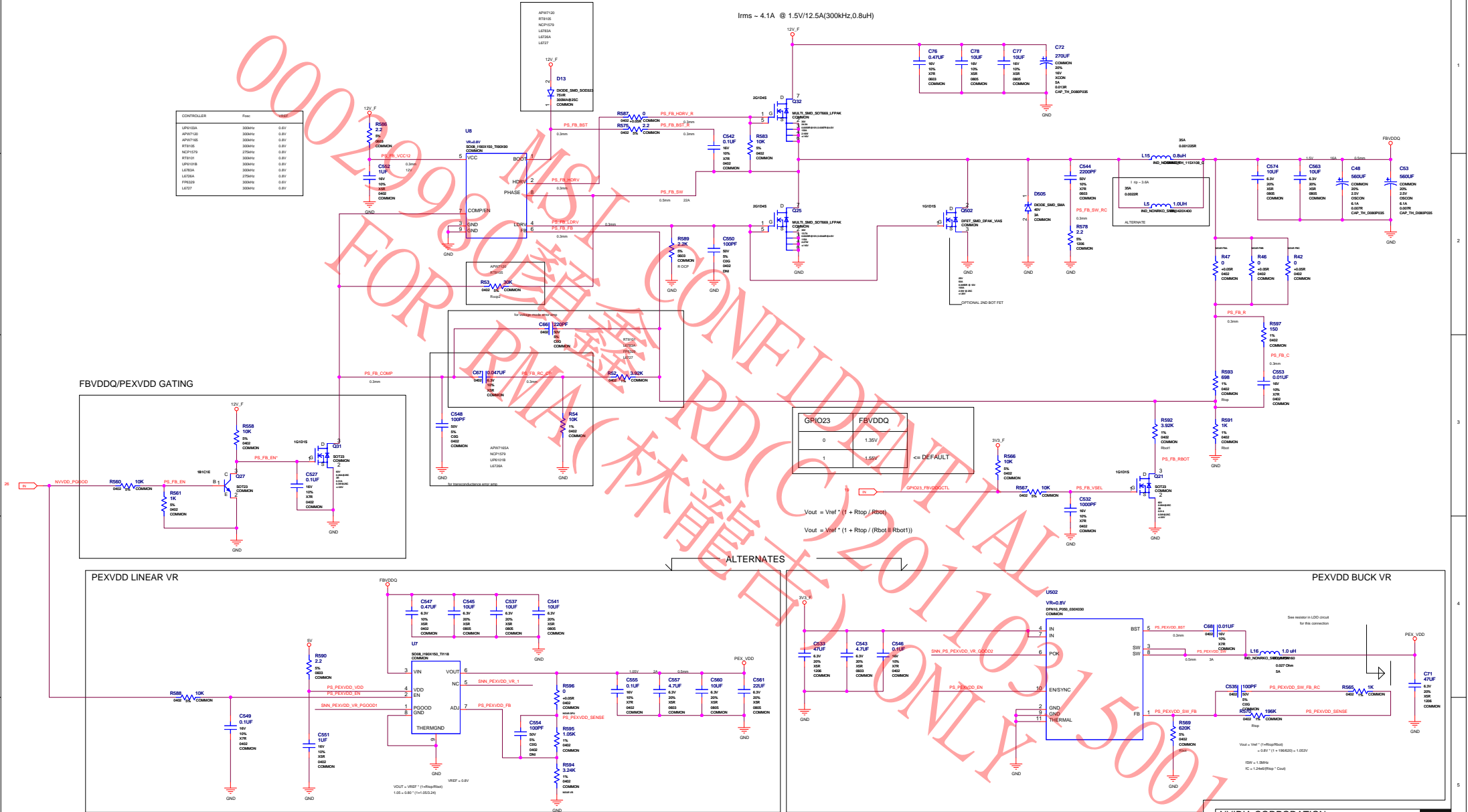
ASSEMBLY BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL P81: 12V Power Supply Filter, 5V AND DDC VOLTAGE

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PS II: FBVDDQ, PEX_VDD

FBVDDQ, 10.3A @ 1.5V
PEXVDD, 2.1A @ 1.05V

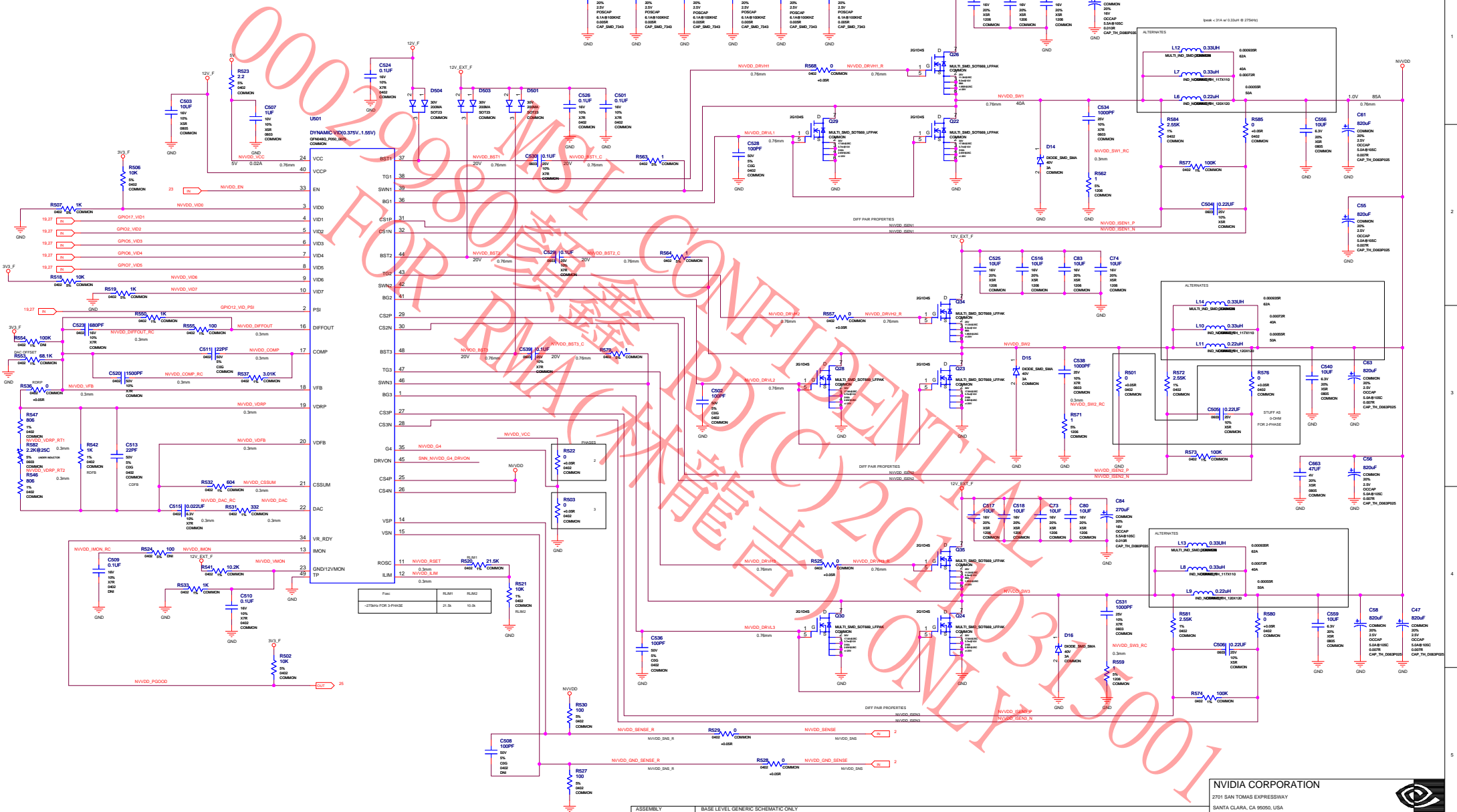
CONTROLLER	Part	Value
UPR102A	300kΩ	0.0V
APR102B	300kΩ	0.0V
APR102C	300kΩ	0.0V
APR102D	300kΩ	0.0V
APR102E	300kΩ	0.0V
APR102F	300kΩ	0.0V
APR102G	300kΩ	0.0V
APR102H	300kΩ	0.0V
APR102I	300kΩ	0.0V
APR102J	300kΩ	0.0V
APR102K	300kΩ	0.0V
APR102L	300kΩ	0.0V
APR102M	300kΩ	0.0V
APR102N	300kΩ	0.0V
APR102O	300kΩ	0.0V
APR102P	300kΩ	0.0V
APR102Q	300kΩ	0.0V
APR102R	300kΩ	0.0V
APR102S	300kΩ	0.0V
APR102T	300kΩ	0.0V
APR102U	300kΩ	0.0V
APR102V	300kΩ	0.0V
APR102W	300kΩ	0.0V
APR102X	300kΩ	0.0V
APR102Y	300kΩ	0.0V
APR102Z	300kΩ	0.0V



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ASSEMBLY PAGE DETAIL BASE LEVEL GENERIC SCHEMATIC ONLY PS II: FBVDDQ, PEXVDD (Linear and Switcher Options)

PS III: NVVDD Power Supply
2-PHASE OR 3-PHASE DESIGN
(UNSTUFF PHASE 2 FOR 2-PHASE CONFIG)

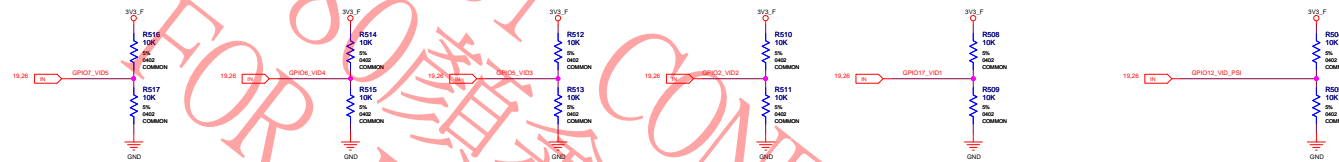


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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	PS III: NVDD Power Supply

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NV_PN	600-11050-BASE-QS1		
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PS IV: NVAPI VID



VR in single-phase diode-emulation mode
when PSl is asserted low

[illegible]

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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	PS IV: NVAPI VID

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