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ÉİİÂÎİİßÇÐ»»·½° , DPDT

MMPA

ソッス KY77643キヲヲ

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ANT1304

ANT1302 16021 INI- H=1.6

ANT1303 16021 INI- H=1.6

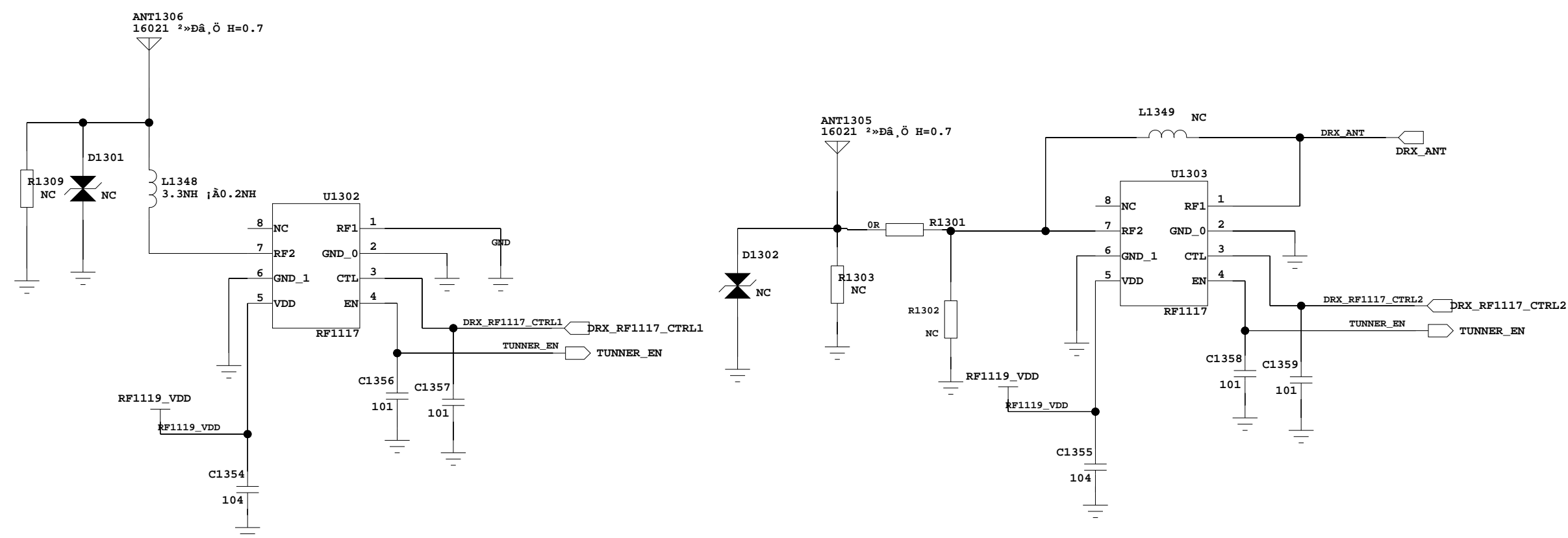
ANT1301 16021 INI- H=0.7

16021 INI- H=1.6

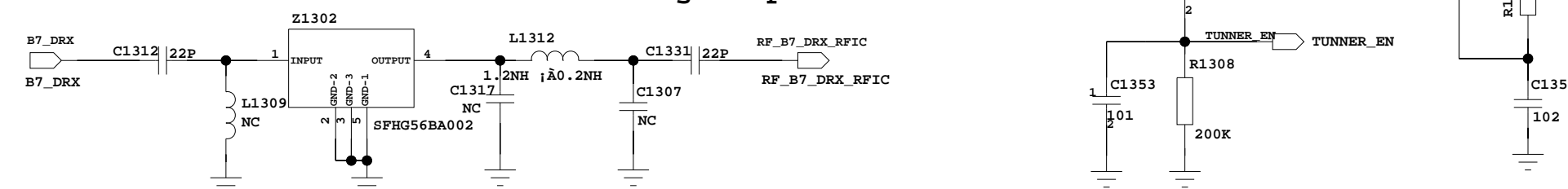
16021 INI- H=0.7

L1340 NC

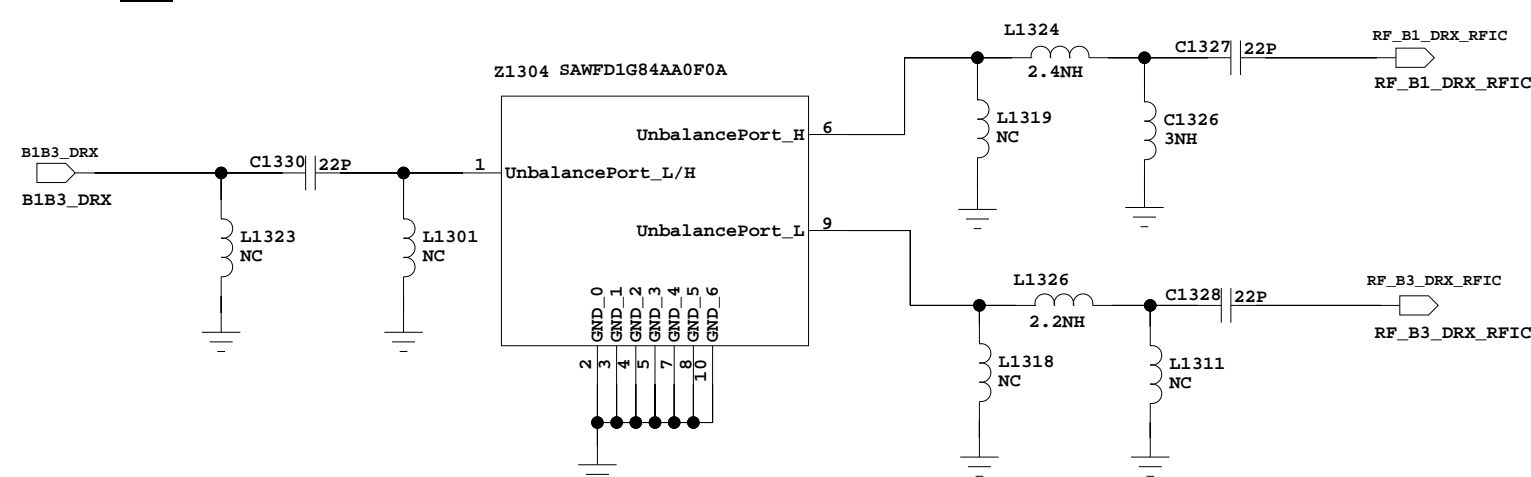
GND



Place LNA matching components close to MT6176



Place LNA matching components close to MT6176



The schematic diagram illustrates the internal circuitry of the U1309 SKY13418-485LF antenna tuner. Key components and connections include:

- Input Matching Network:** Consists of inductor L1331, capacitor C1339, inductor L1332, and capacitor C1316.
- Variable Capacitor:** MM8030-2610, connected to the input signal path.
- Diode:** D1334, connected to the input signal path.
- U1309 Chip:** SKY13418-485LF, with pins for VDD, V1, V2, V3, ANT, and RF1 through RF8.
- Output Matching Network:** Consists of inductor L1320 and capacitor C1316.
- Connectors:** DRX_ASM and a 15-pin connector with pins 3, 9, 2, 10, 1, 11, 14, and 12.

B39/41 DRX

The schematic diagram illustrates the B39/41 DRX circuit. It features a SAW filter (Z1301, SAW-FD1G90AB0F0A) with UnbalancePort_L/H and UnbalancePort_H. The input is DRX_B39_B41, which is connected to the UnbalancePort_L/H through a series of components: a capacitor C1301 (22P), an inductor L1325 (NC), and an inductor L1314 (2.4NH). The output of the UnbalancePort_L/H is connected to the UnbalancePort_H through a series of components: a capacitor C1302 (1NH), an inductor L1307 (6.2NH), a capacitor C1338 (22P), and a capacitor C1333 (10NH, 15%). The output of the UnbalancePort_H is connected to the RF_B39_DRX_RFIC through a series of components: an inductor L1304 (22P), a capacitor C1341 (2.2NH), a capacitor C1322 (22P), and an inductor L1305 (NC). The circuit is powered by GND pins 2, 3, 4, 5, 7, 8, 9, 10, and 11.

Schematic diagram of the B8-DRX circuit. The circuit starts with an input B8_DRX connected to a 22pF capacitor C1340. This is followed by an inductor L1329 (NC) connected to ground. The signal then enters the input of a Z1308 SPH942AA002 component. The output of the component is connected to an inductor L1327 (NC) connected to ground. The signal then passes through a 1.2nH inductor L1337 and a 0.2nH capacitor to a 33pF capacitor C1343. Finally, the signal is connected to an output RF_B8_DRX_RFIC through a 3.3nH capacitor C1342 and a 0.2nH capacitor.

B28 DRA

SAW-B39781B8323P810

B28_DRX

C1305

0R

Z1305

1

2

3

4

1.2NH

1A0.25P

25V

6.8P

C1303

RF_B28_DRX_RFIC

C1304

1A0.25P

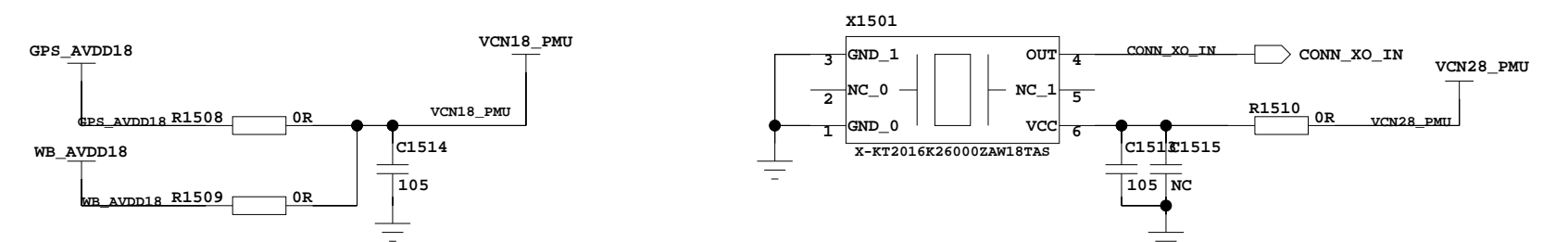
RF_B28_DRX_RFIC

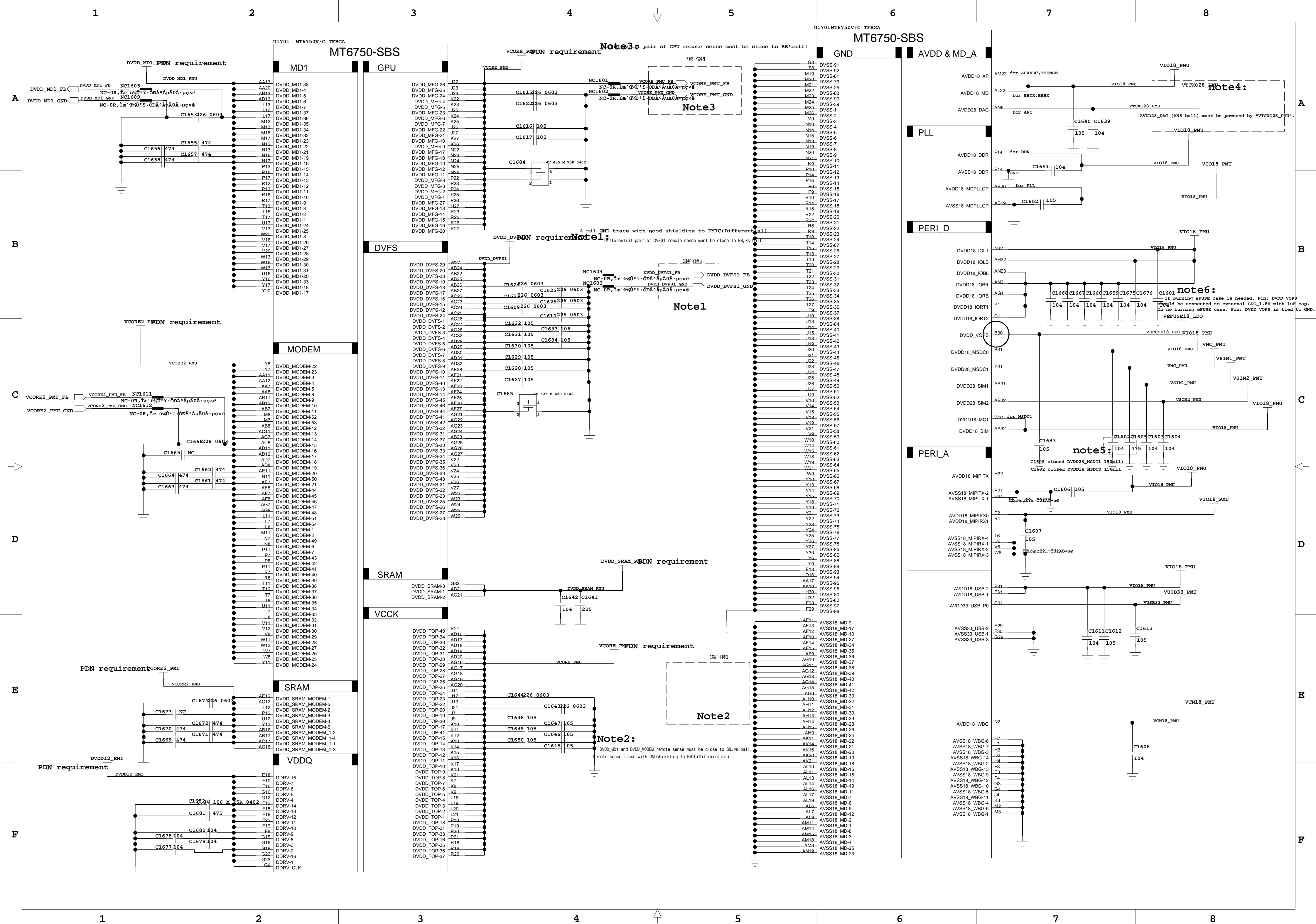
L1341

NC



VCN33μçÔ´ ç 1¶Ê²»Âú×ãÒª ÇÓ»áμ¼ÖÂWiFi TX POWER²»×¼or EVM¶ñ»-


$$\frac{3}{4}\S\tilde{\text{O}}\tilde{\text{n}}^{\frac{3}{4}}\gg\text{¿}\tilde{\text{O}}^2\hat{\text{I}}\text{¿}^{\frac{1}{4}}150$$



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A

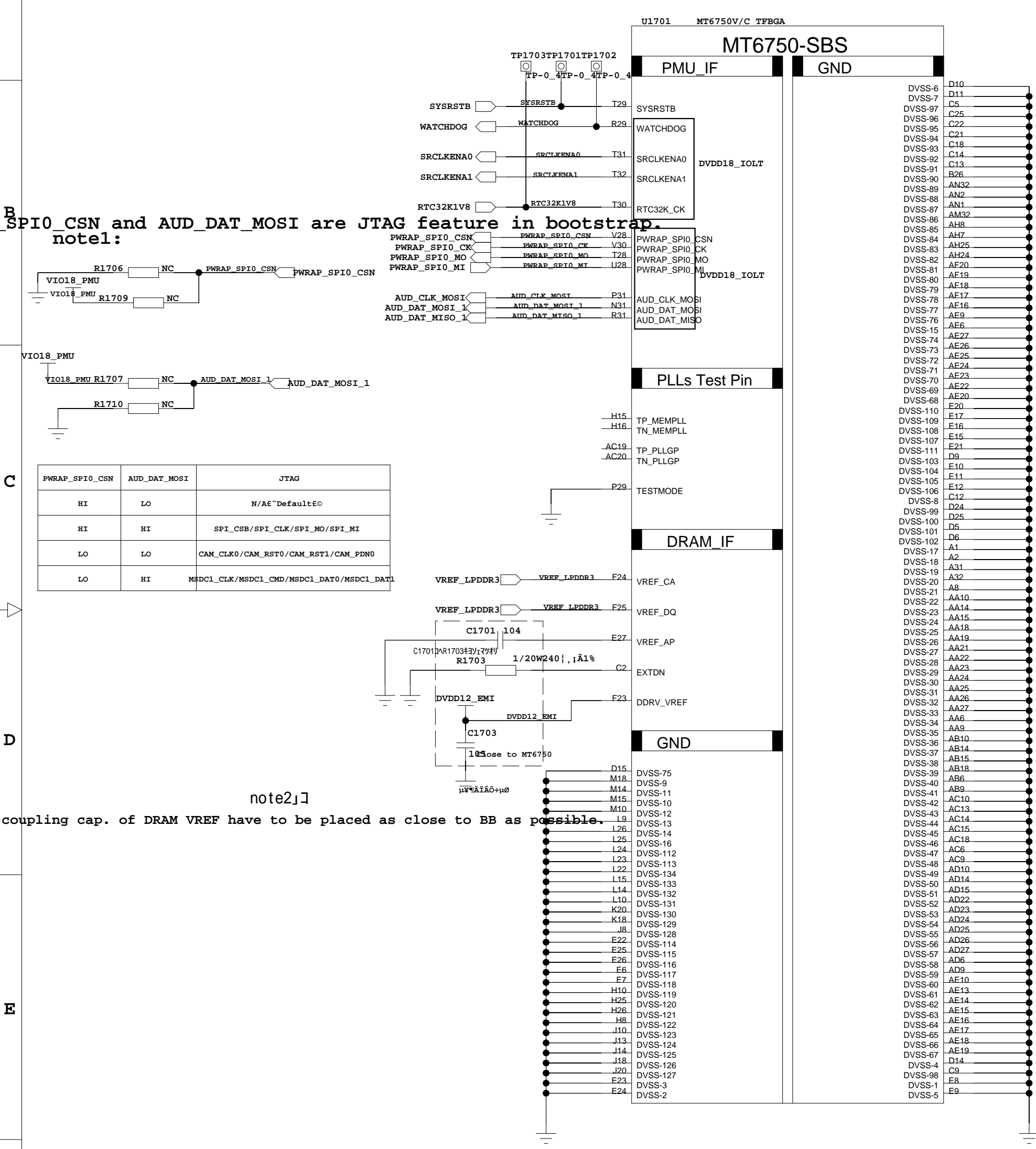
B

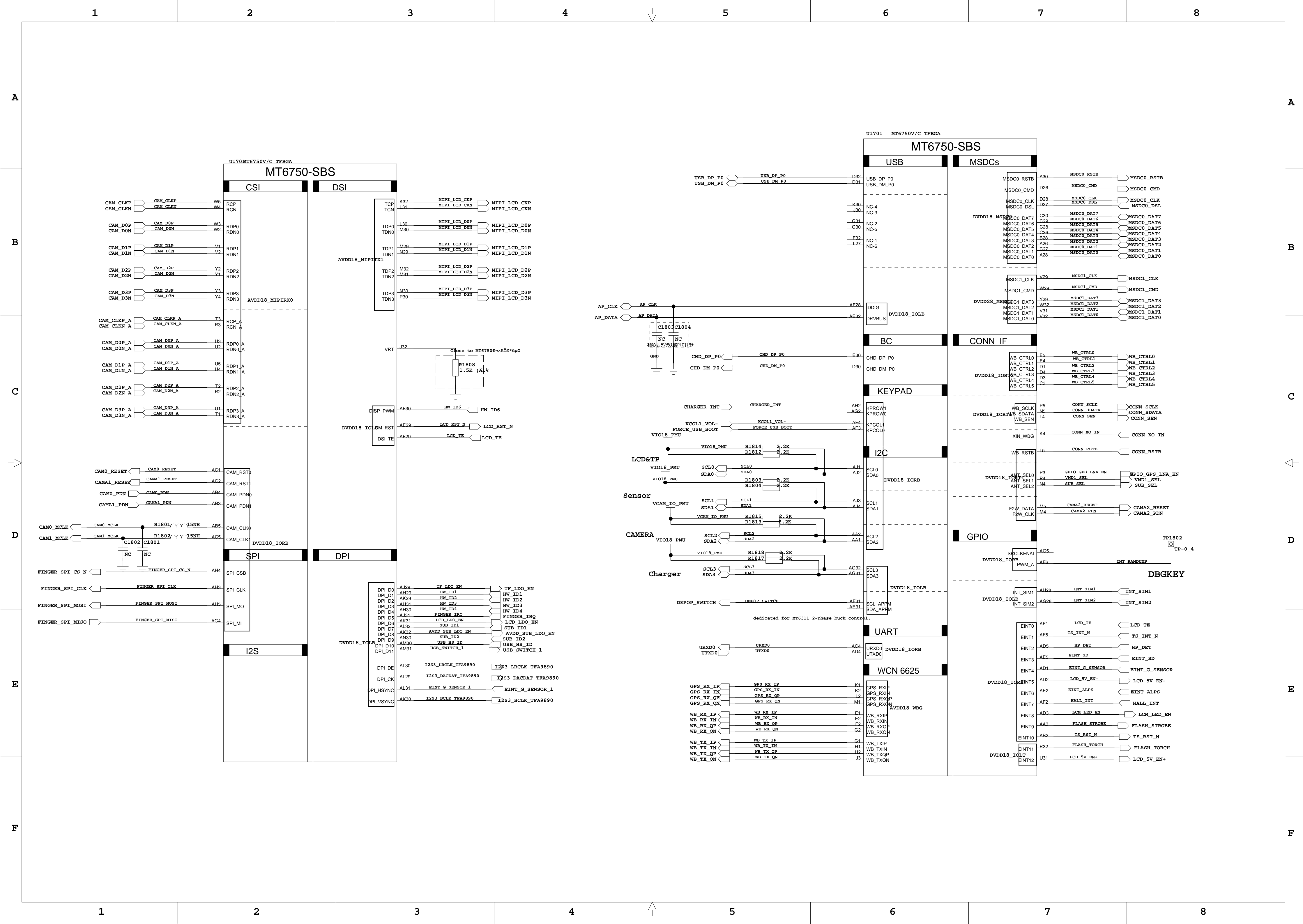
C

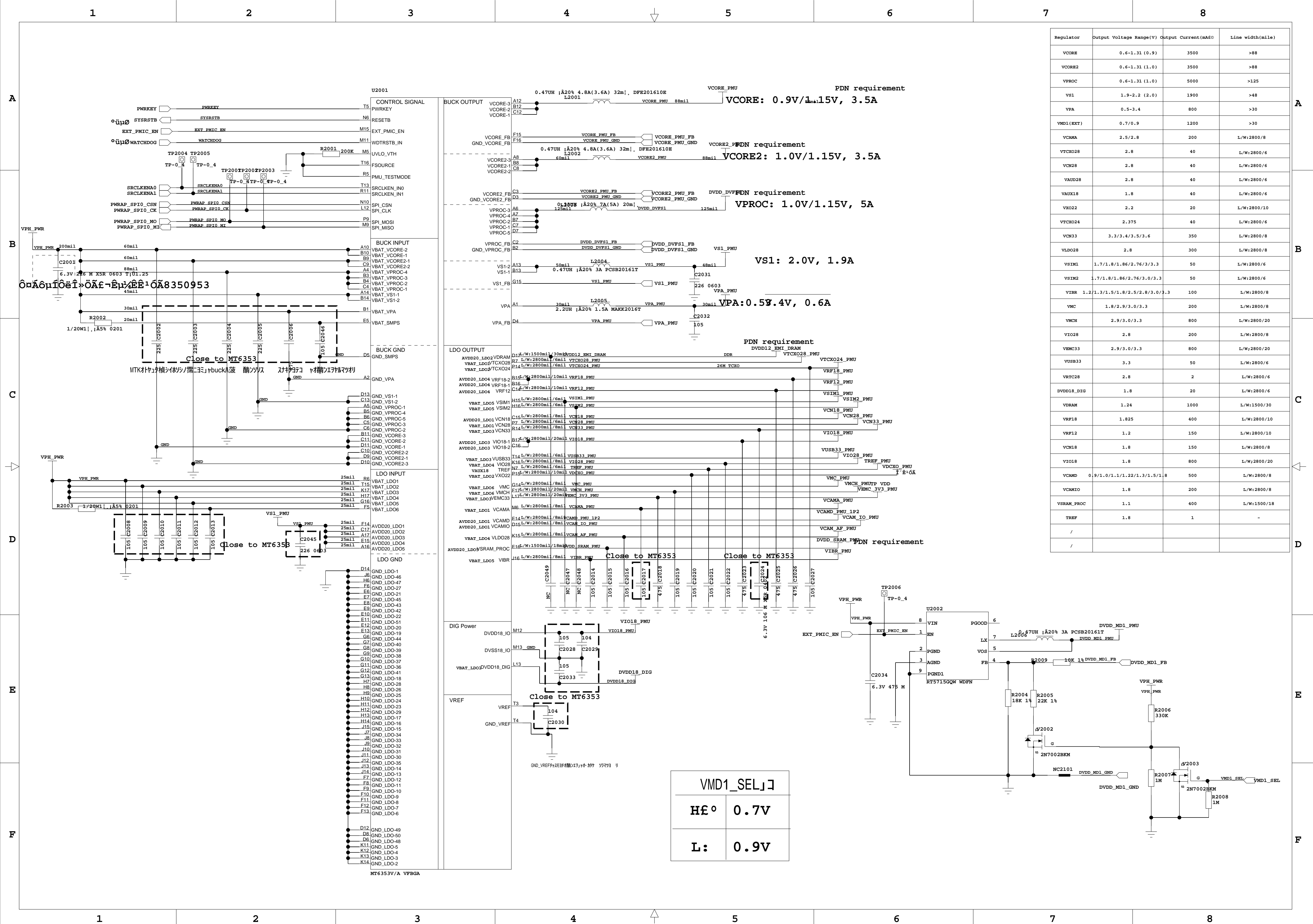
D

E

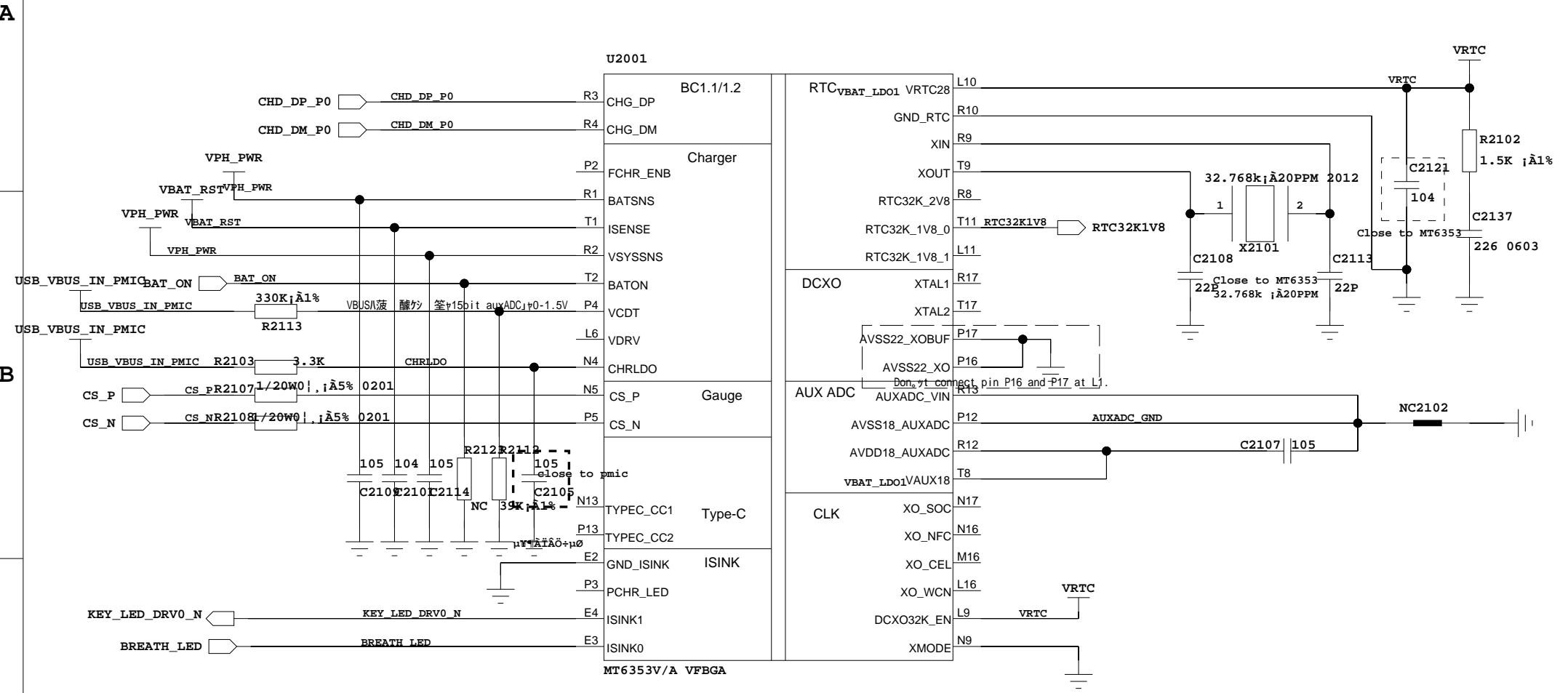
F







MT6353 HS KEEP

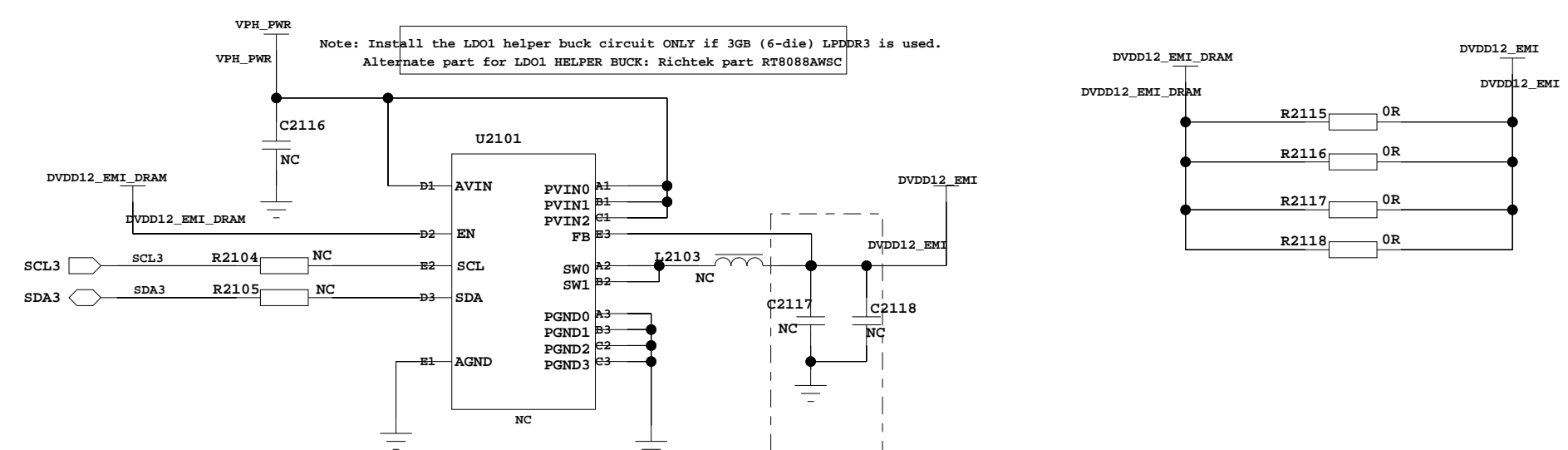
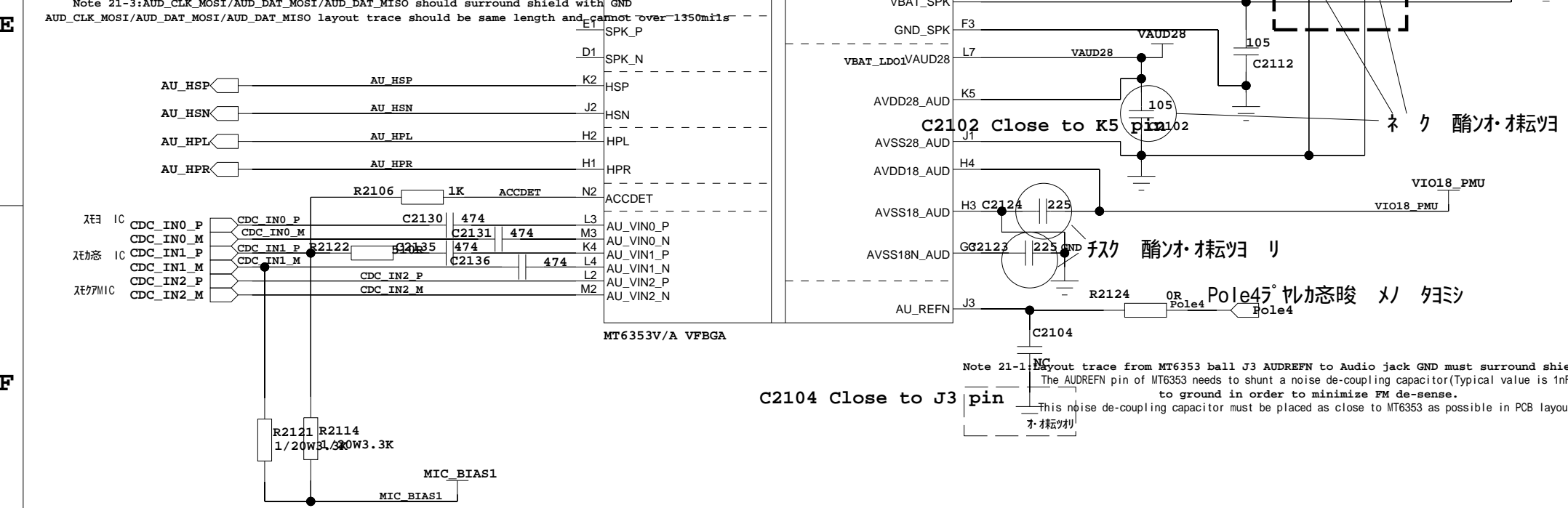


Charger	R2103
Ext. Switching Charger	3.3K
5V charger + MTK Pump Express	5K

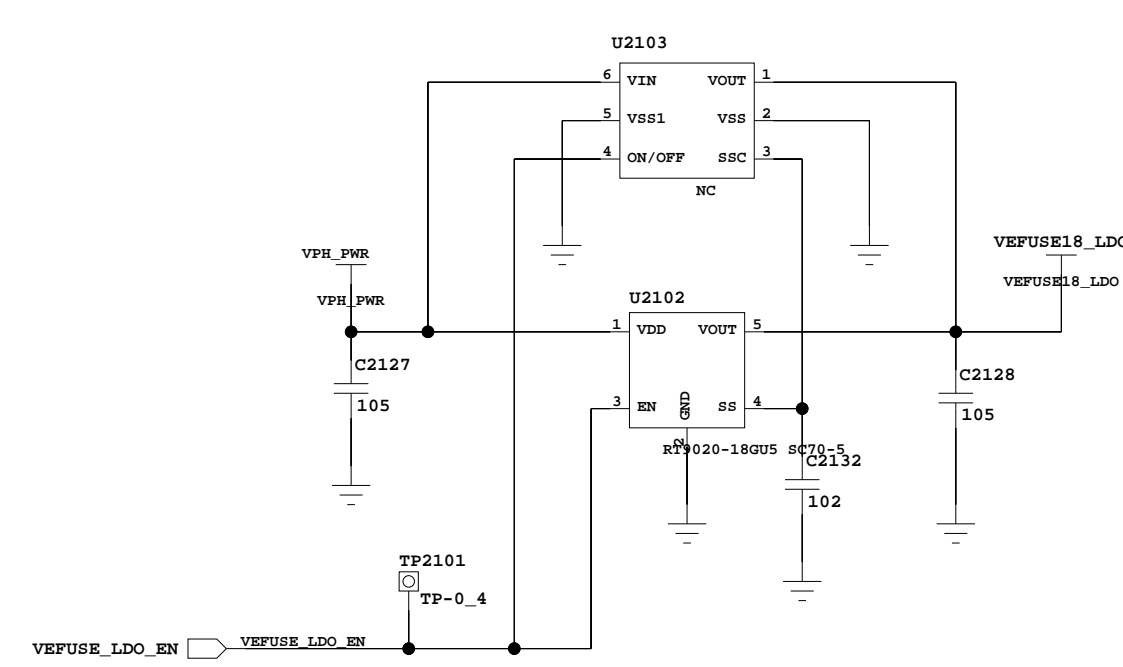
DCXO32K_EN	XMODE	CLKOUT
1	0	VTCXO on MT6176 OFF XO in MT6353

DCXO32K_EN	XMODE	CLK OUT
0	0	TCXO on MT6353
1	0	VTCXO on MT6176 (OFF XO in MT6353)
0	1	TSX on MT6353, w/ RTC
1	1	TSX on MT6353, w/o RTC

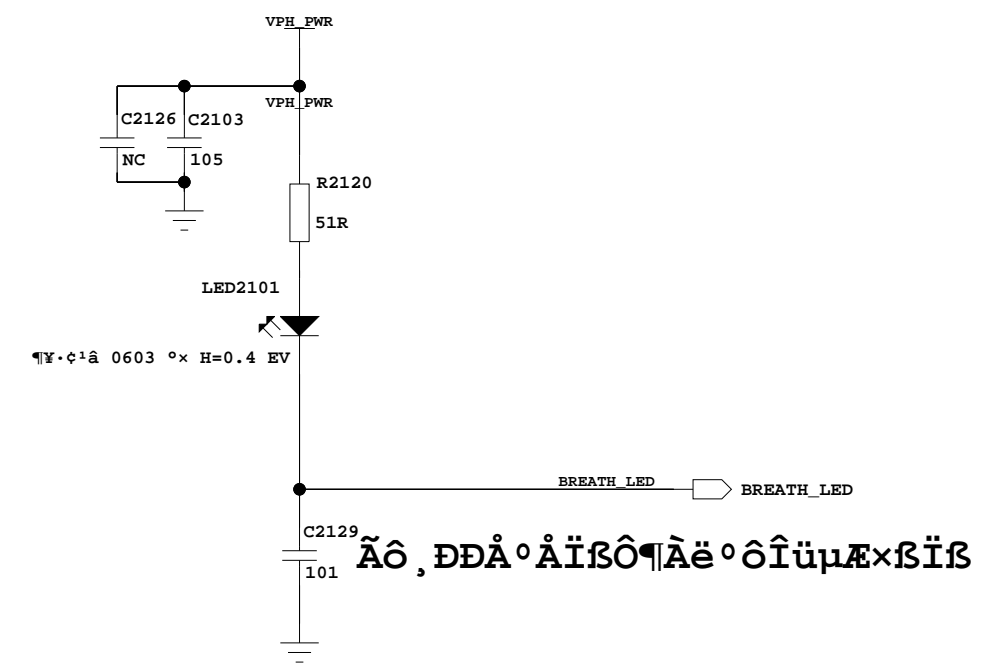
MT6353 Audio

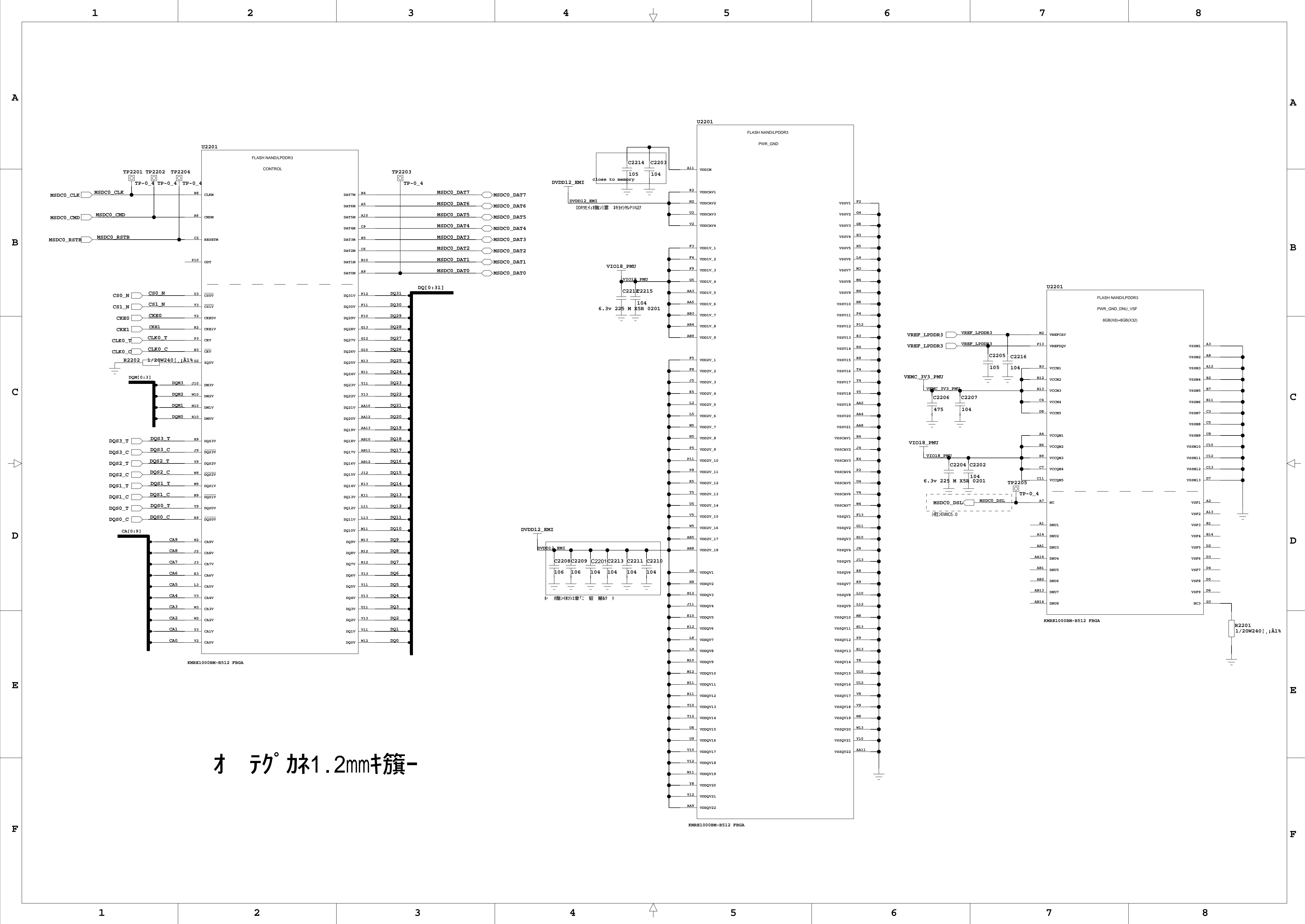


NCP6343B slave Address is 0x1C



Breath LED





B



D



F

B

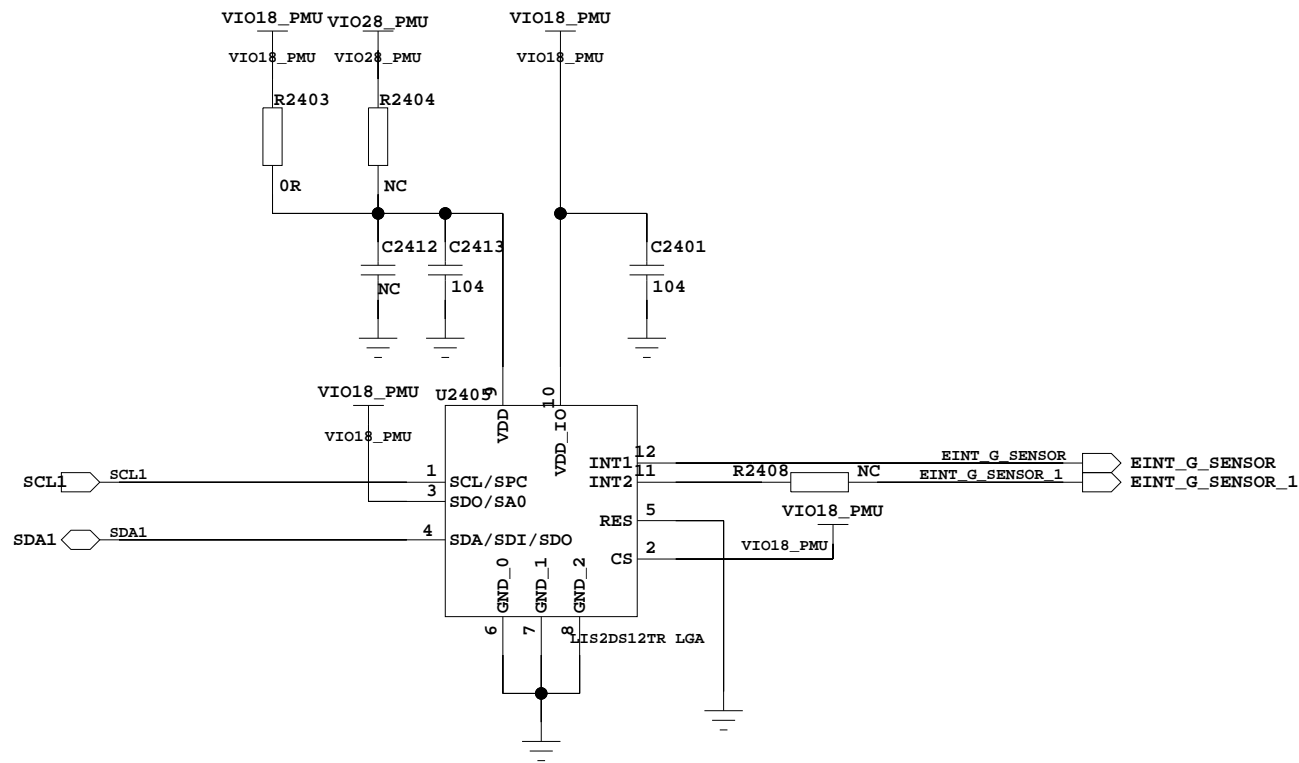


D



F

G-Sensor

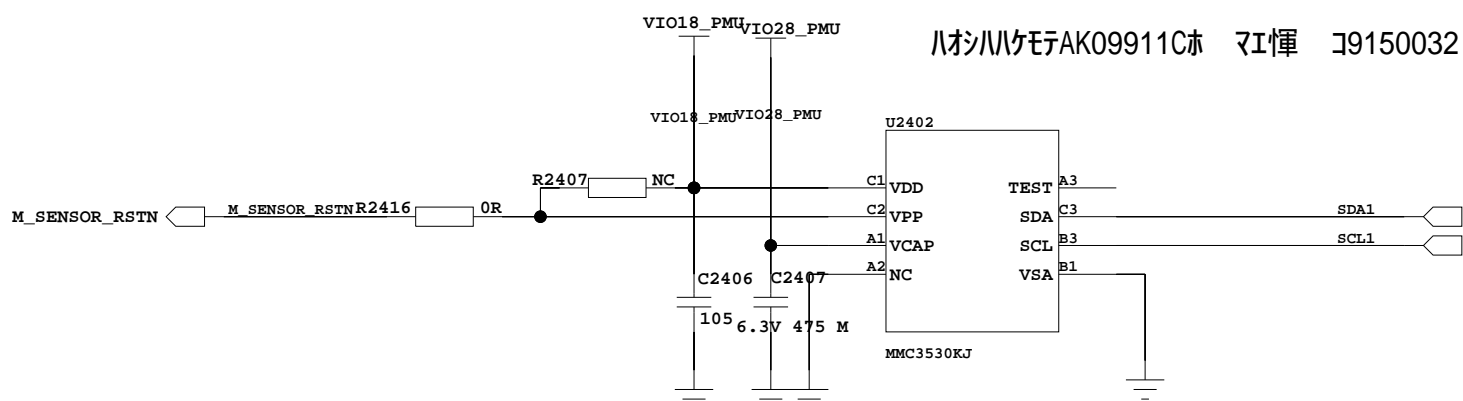


I2CµØÖ.£¬¶Á0x3B£¬Ð´0x3A

LIS2DS12 slave Address is 0x1D

SA0 slave ADDR	
0	0x1E
1(VDD)	0x1D

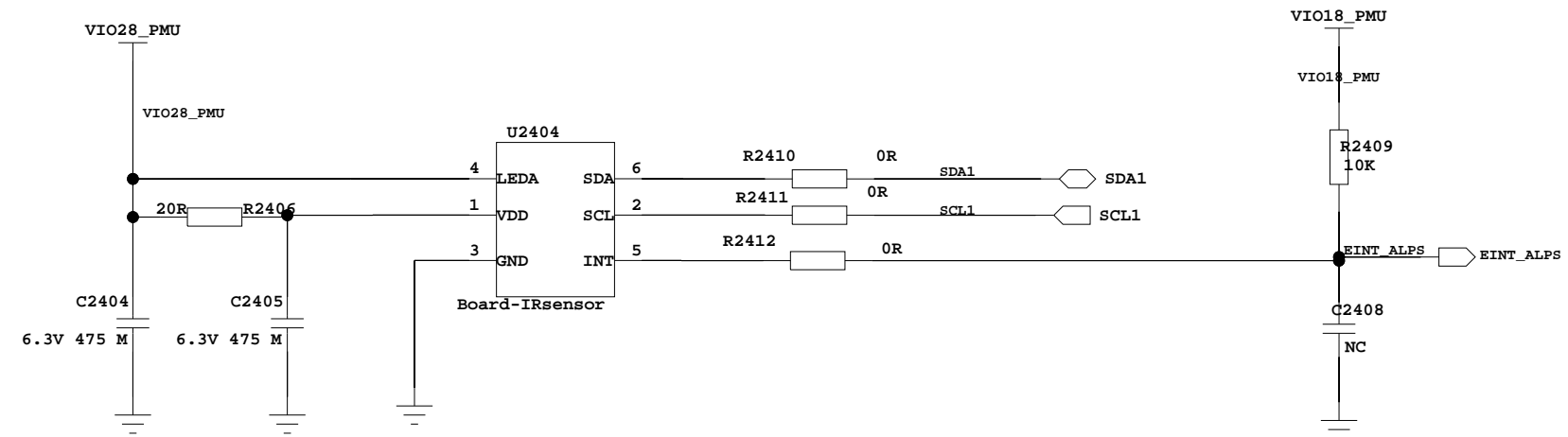
M Sensor



AK09911 slave Address is 0x0C

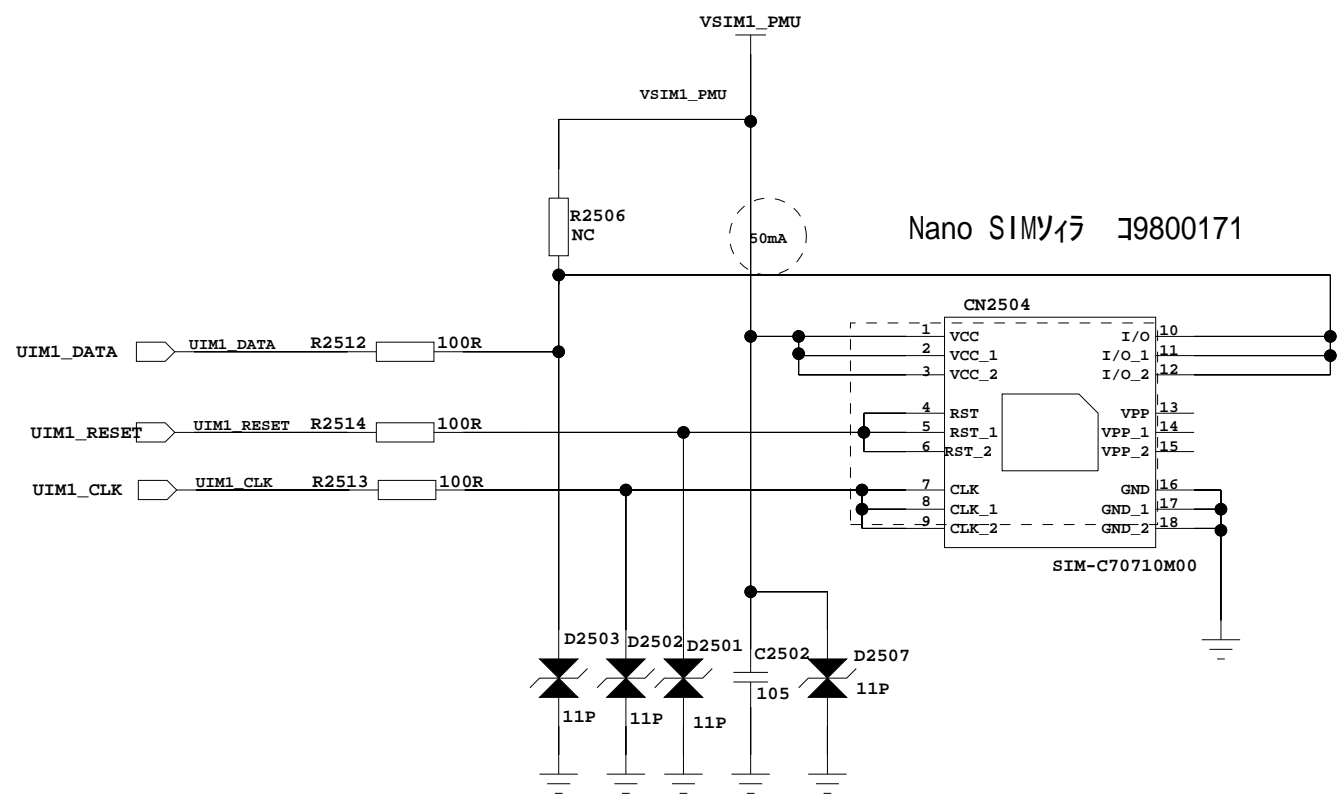
CAD slave ADDR	
0	0x0C
1(VDD)	0x0D

A.L.S.+P.S sensor

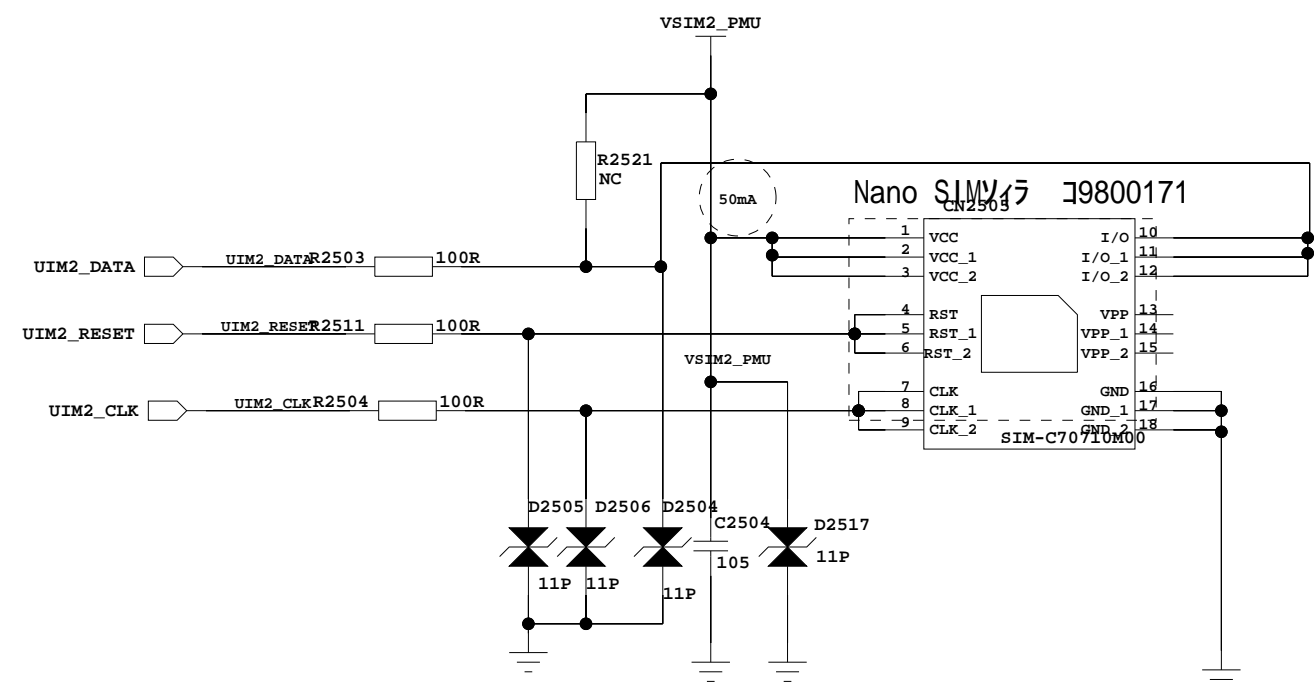


CAD slave ADDR	
0	0x0C
1(VDD)	0x0D

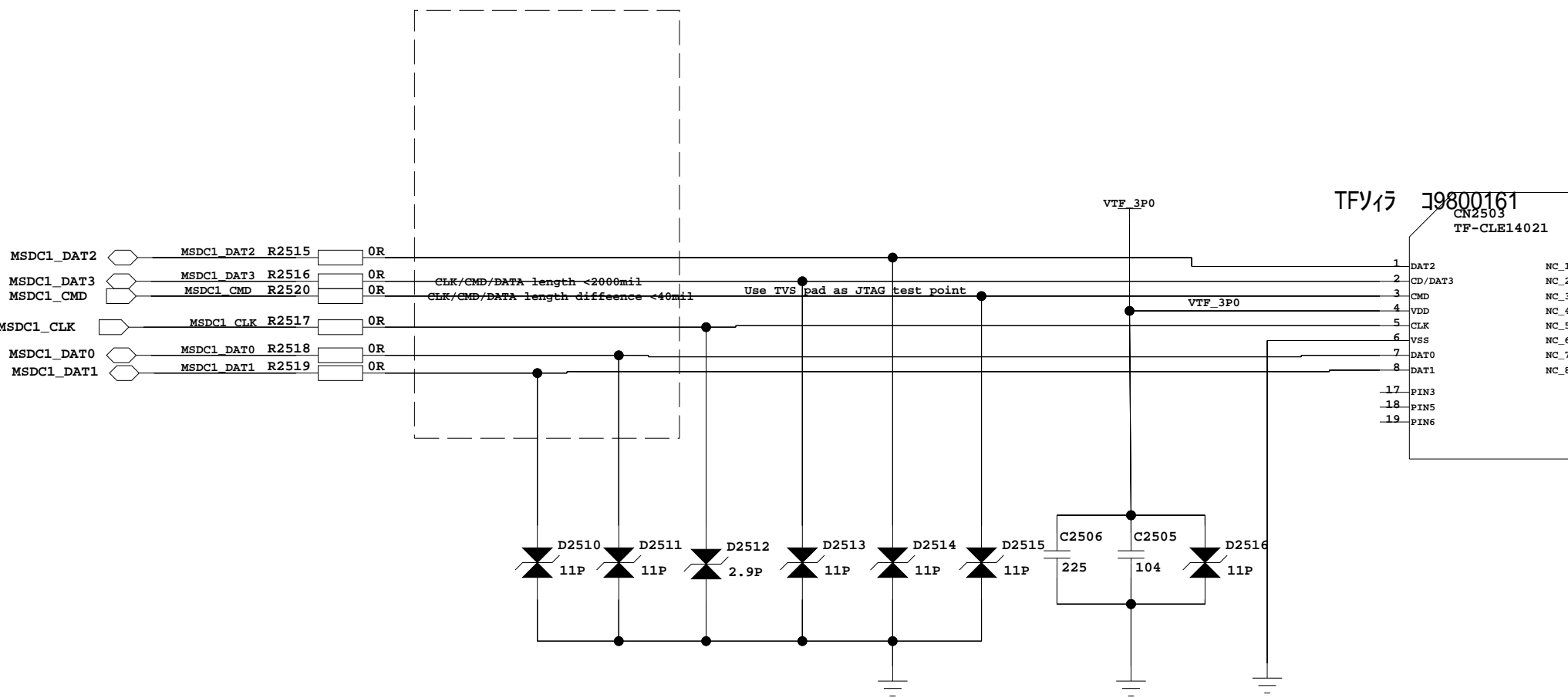
SIM1 Card



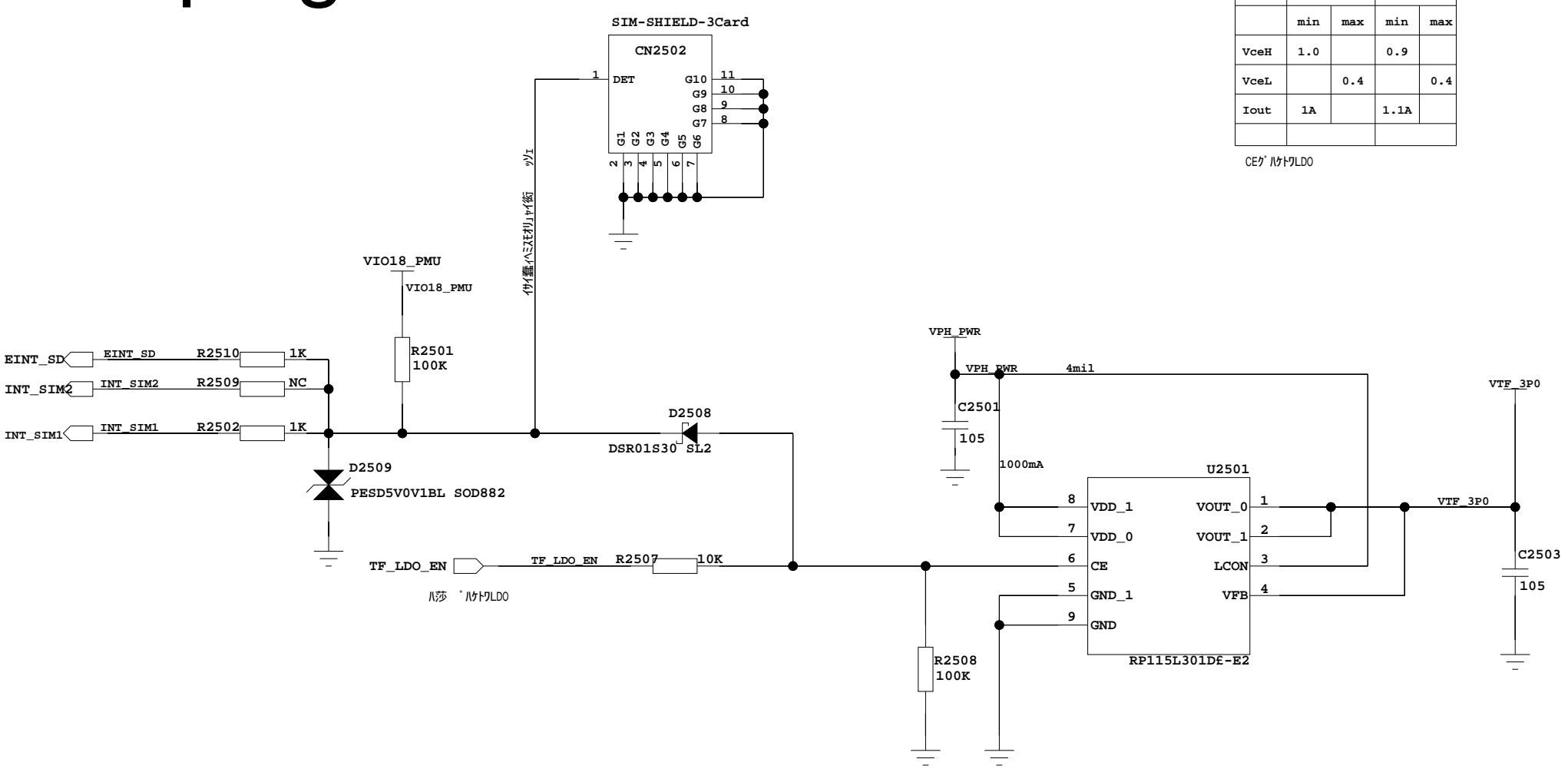
SIM2 Card



TF_CARD



Hot plug: Ext 2.95V supply



	RP115L301D 9170125		NCP706A 9170224	
	min	max	min	max
VceH	1.0		0.9	
VceL		0.4		0.4
Iout	1A		1.1A	

CEP / 1019LDO

Main CAMERA

A

B

C

D

E

F

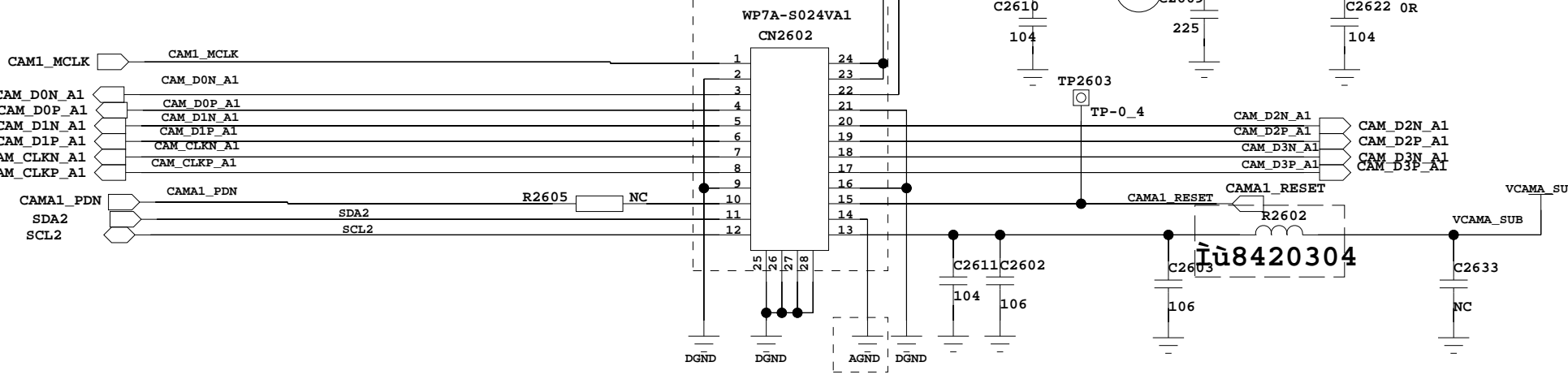
CAMERA SWITCH

SEL	/OR	
LOW	LOW	CLKP=CLKAP, CLKN=CLKAN, Dn(P/N)=DAn(P/N)
HIGH	LOW	CLKP=CLKBP, CLKN=CLKBN, Dn(P/N)=DBn(P/N)
X	HIGH	DAn(P/N), DBn(P/N) Data Ports High Impedance

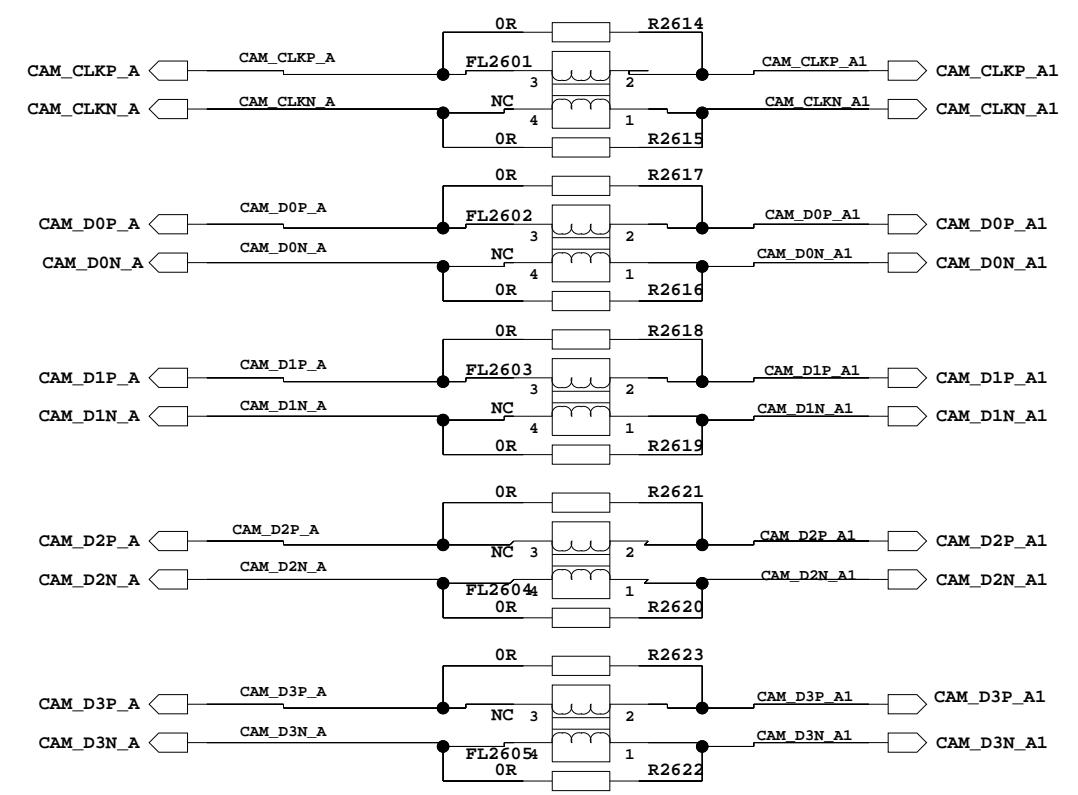
ÃÜİÜ	İaİÜ
R2641-----2660=OR	R2641-----2660=NC
U2604+NC	U2604+IC=PSA644

SUB_Camera I 16M

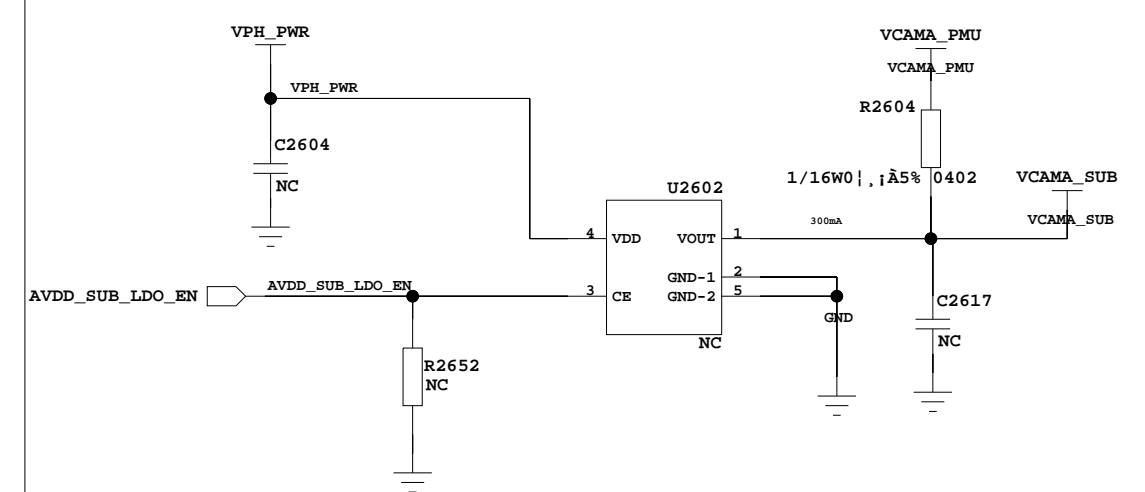
SUB_Camera I 16M



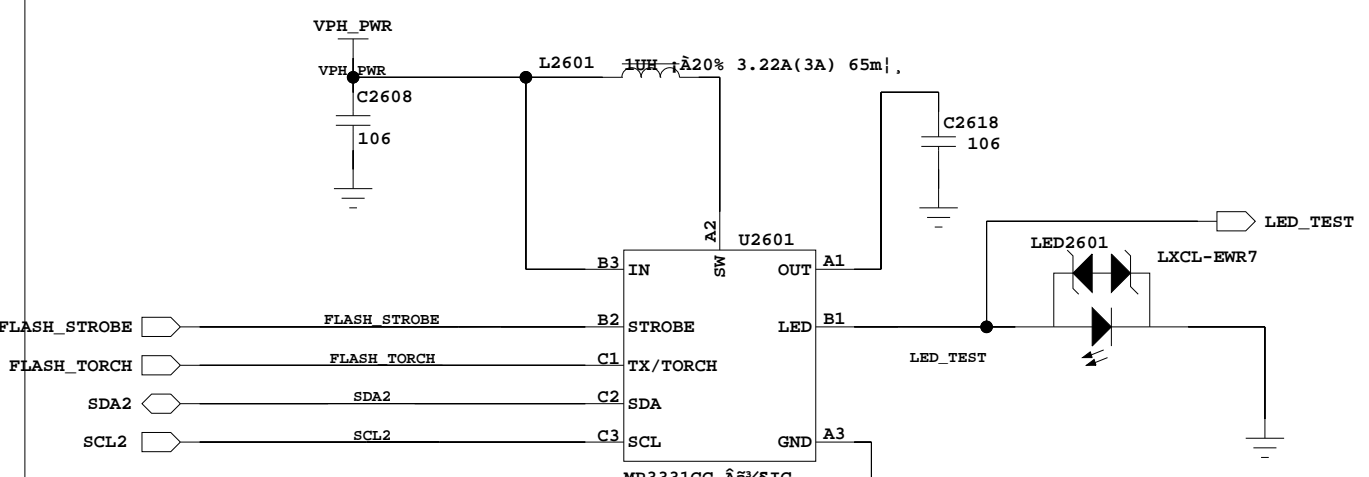
CSI CMF



SUB Camera I AVDD(3V) SUB Camera I DVDD(1.2V)



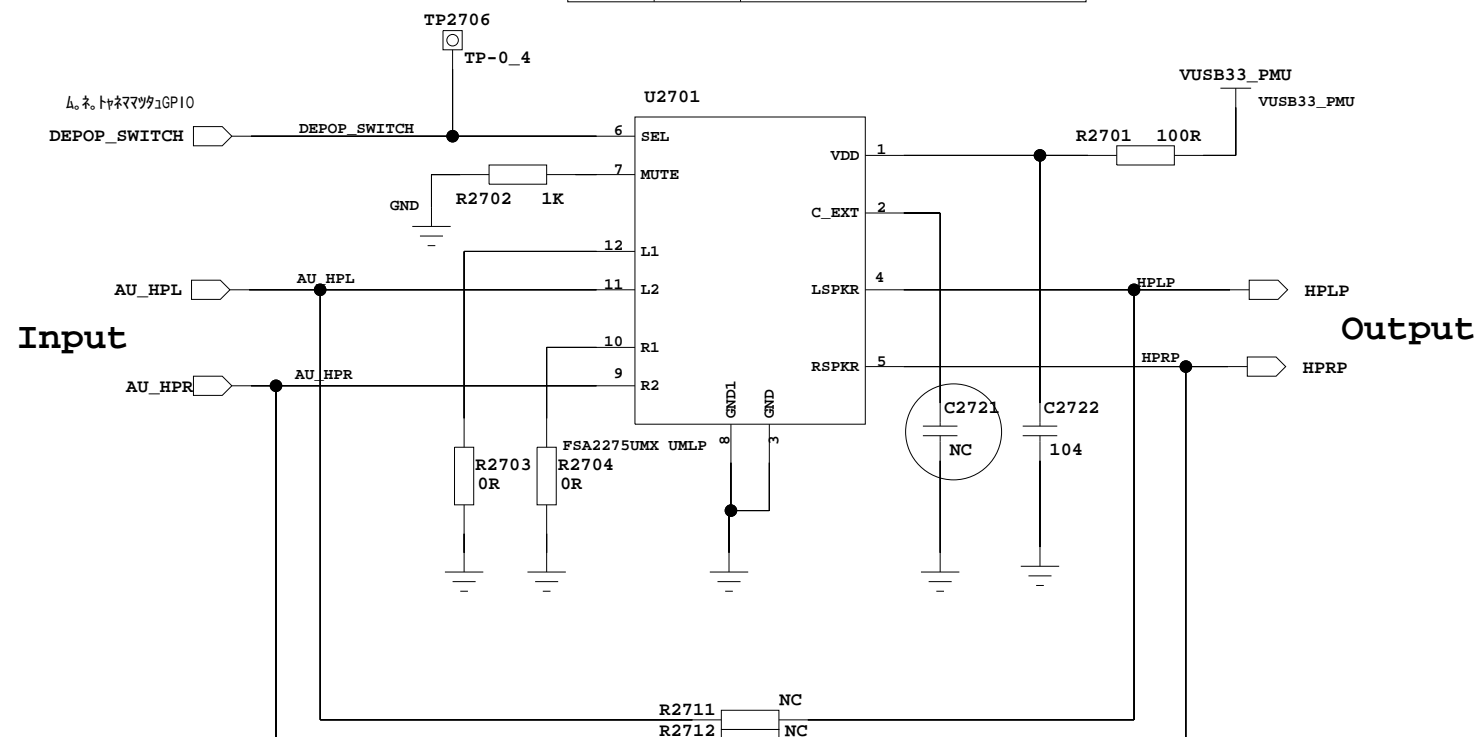
Flash LED Driver



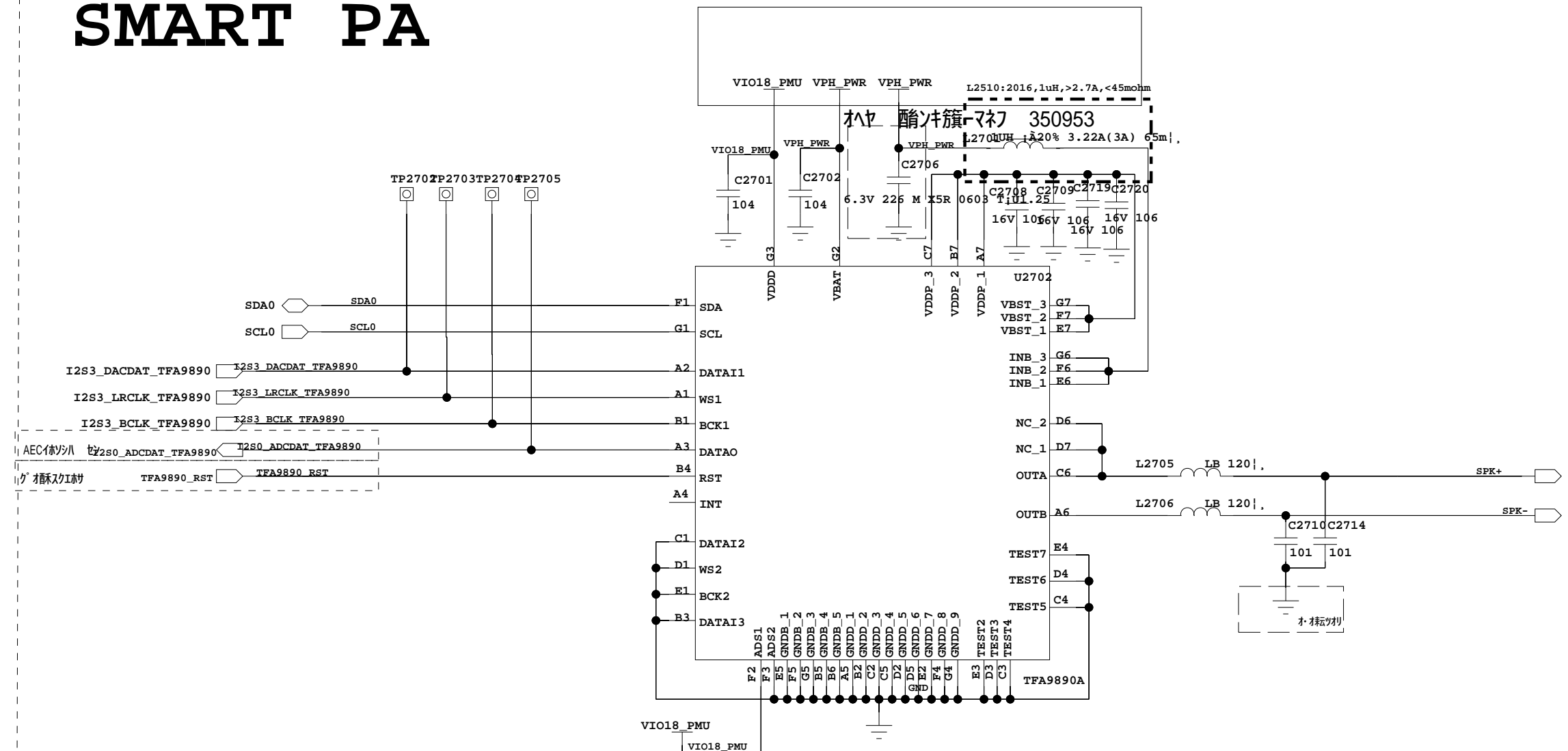
MP3331 slave Address is 0x0X67

CODEC

MUTE	SEL	FOUNTION
0	0	L1=LSPKR; R1=RSPKR
0	1	L2=LSPKR; R2=RSPKR

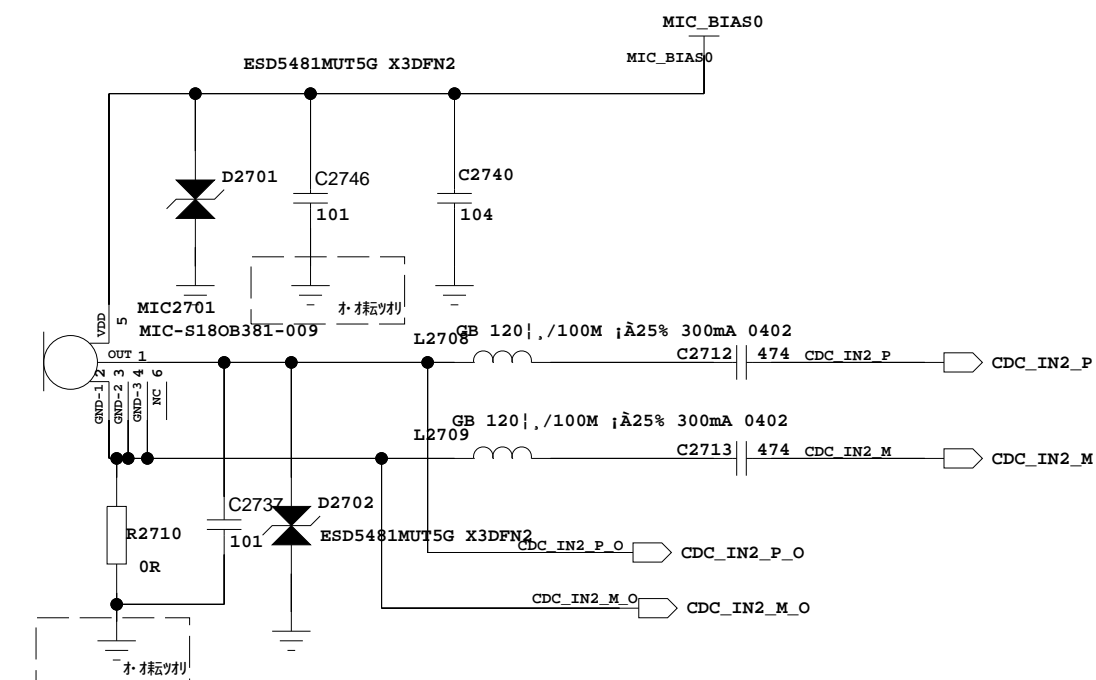


SMART PA

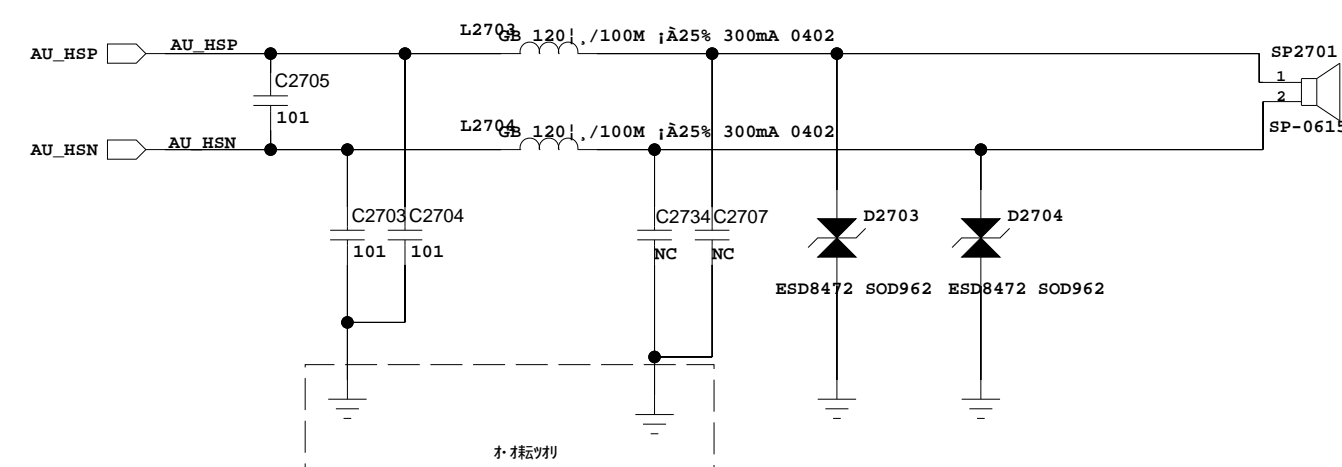


TFA9890A slave Address is 0x35

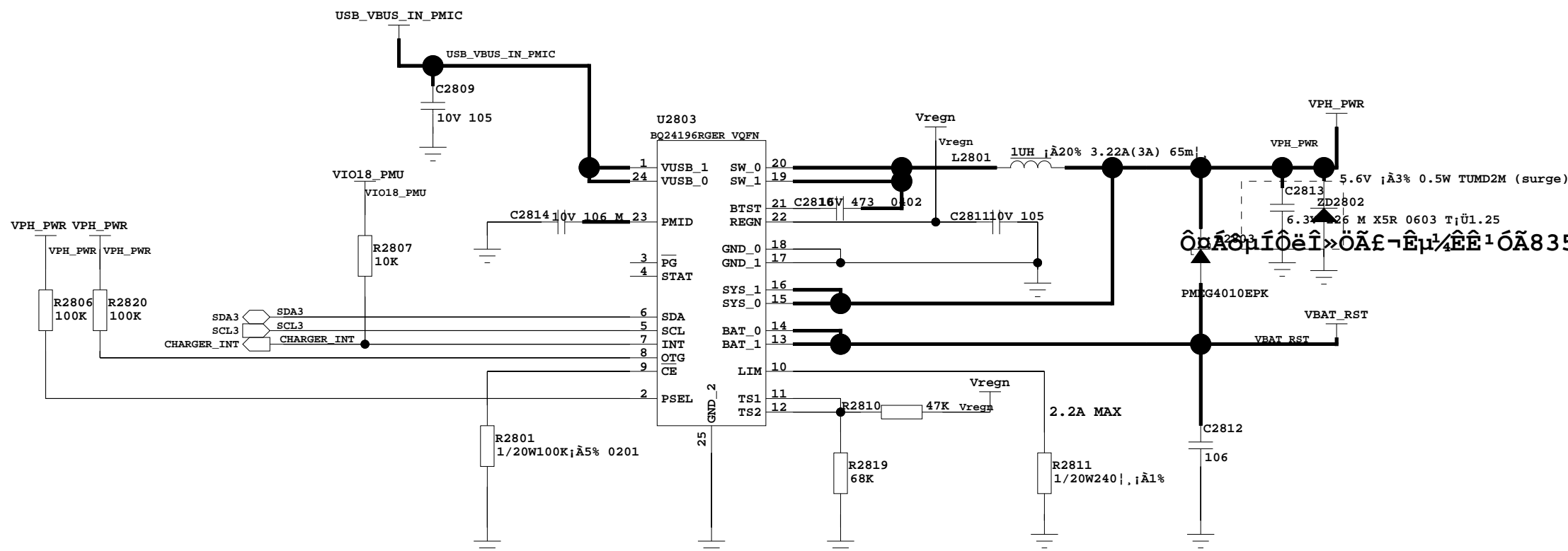
Ant-Noise MIC



Receiver

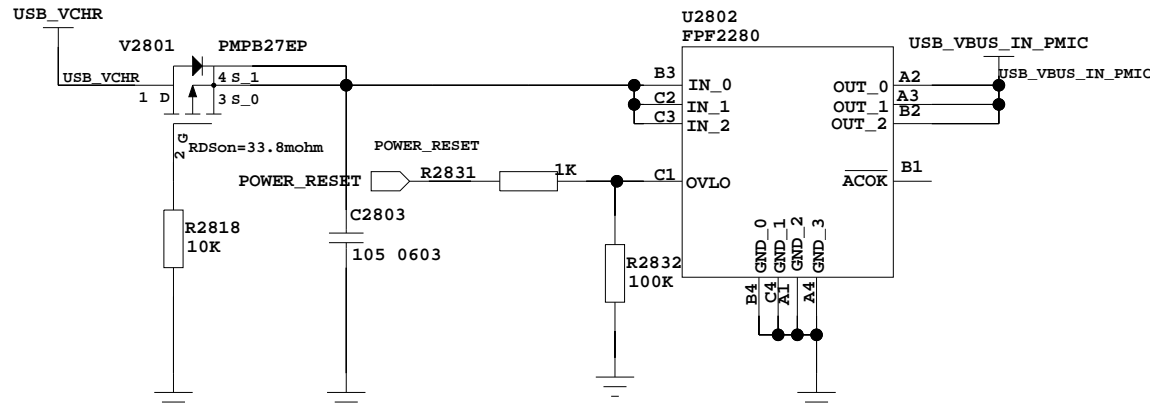


Internal Switching Charge



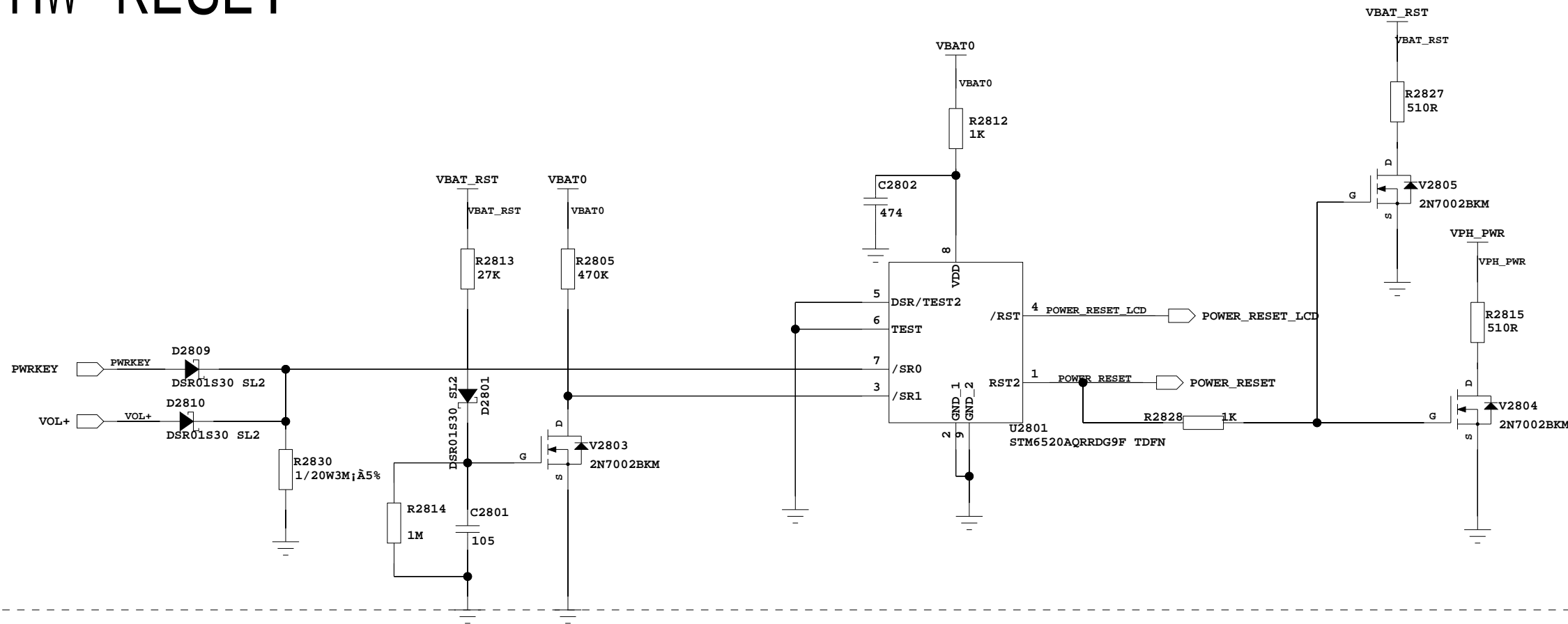
BQ24196 slave Address is 0x6B

NVP&OVP

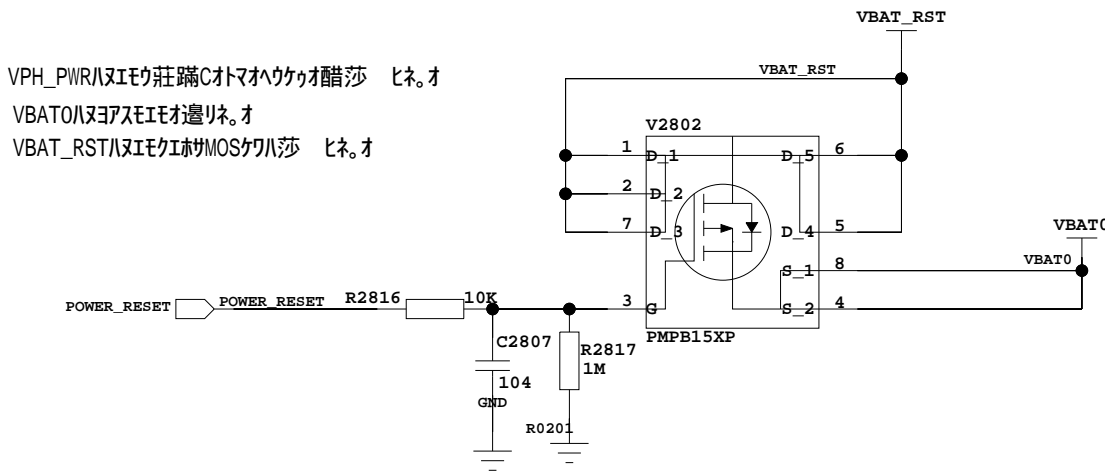


Note:
1)5Vの端子に33.8mohmの抵抗を付与し、
2)9Vの端子に2826Ω、2825Ω、50Kの抵抗を付与し、

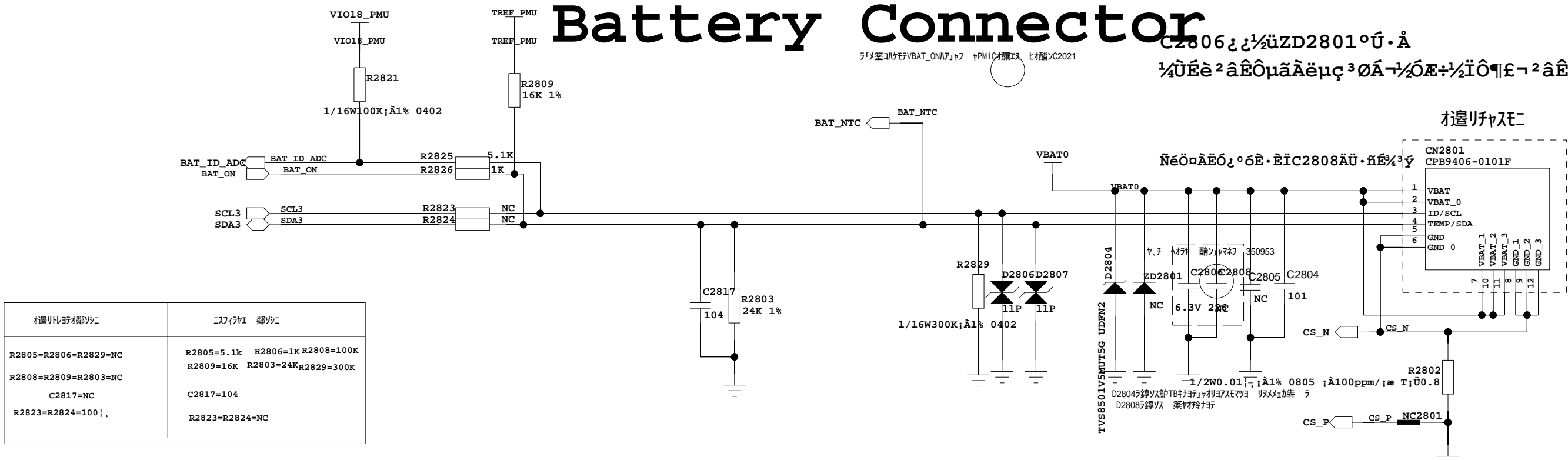
HW RESET



BAT_MOSFET

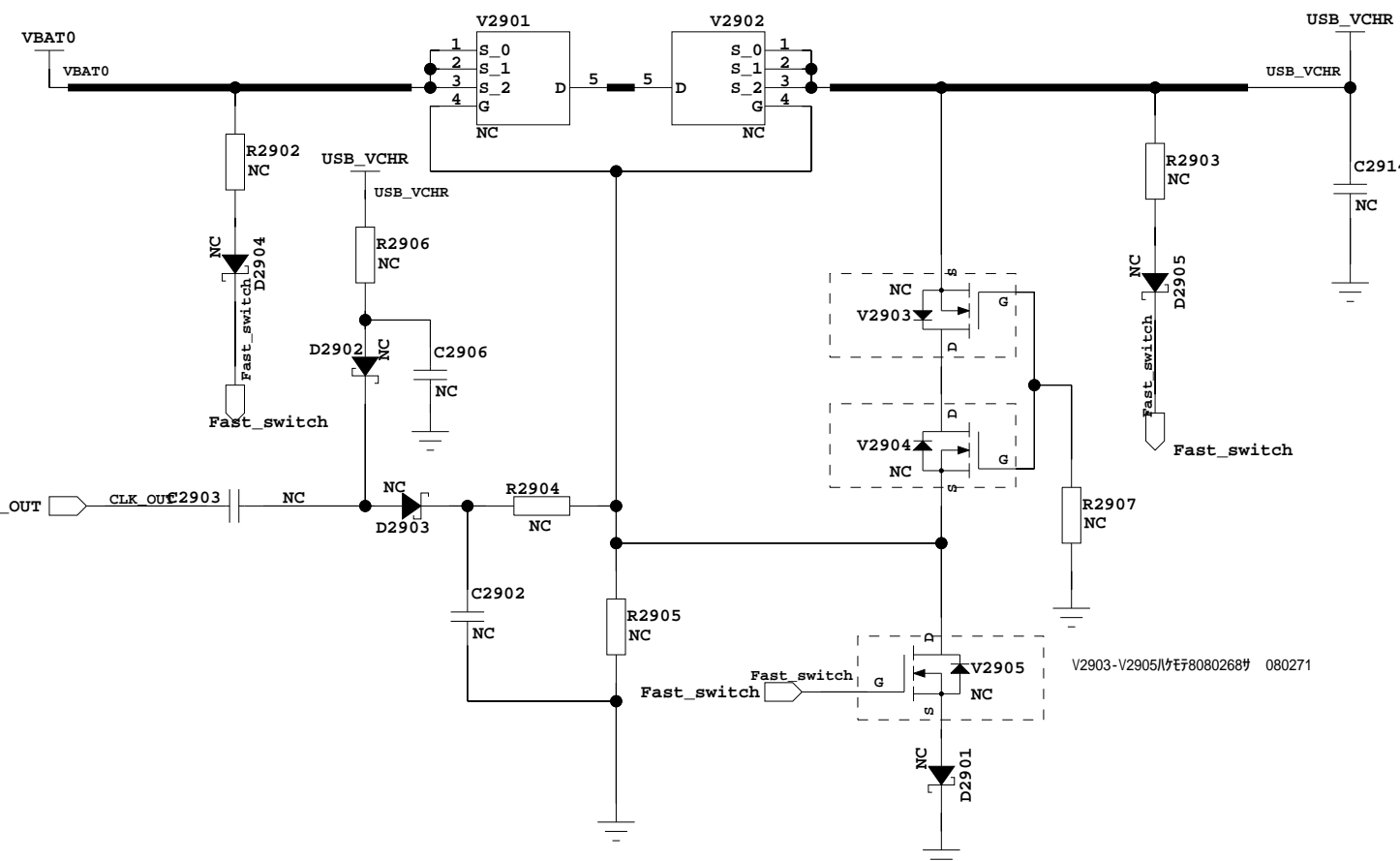


Battery Connector

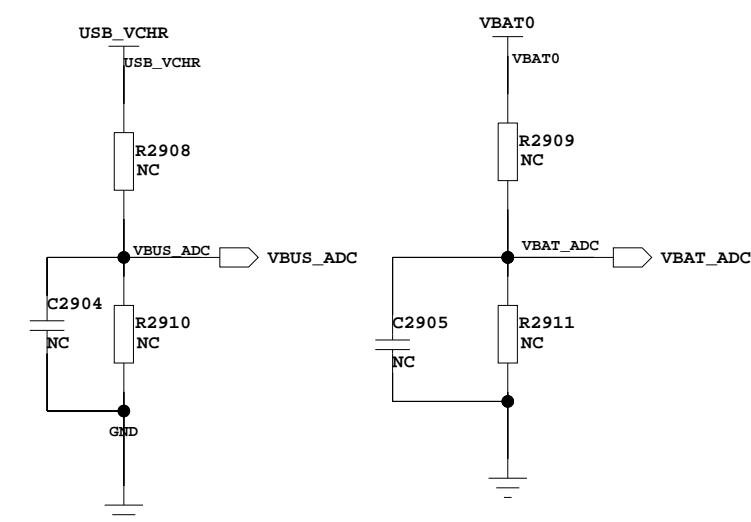
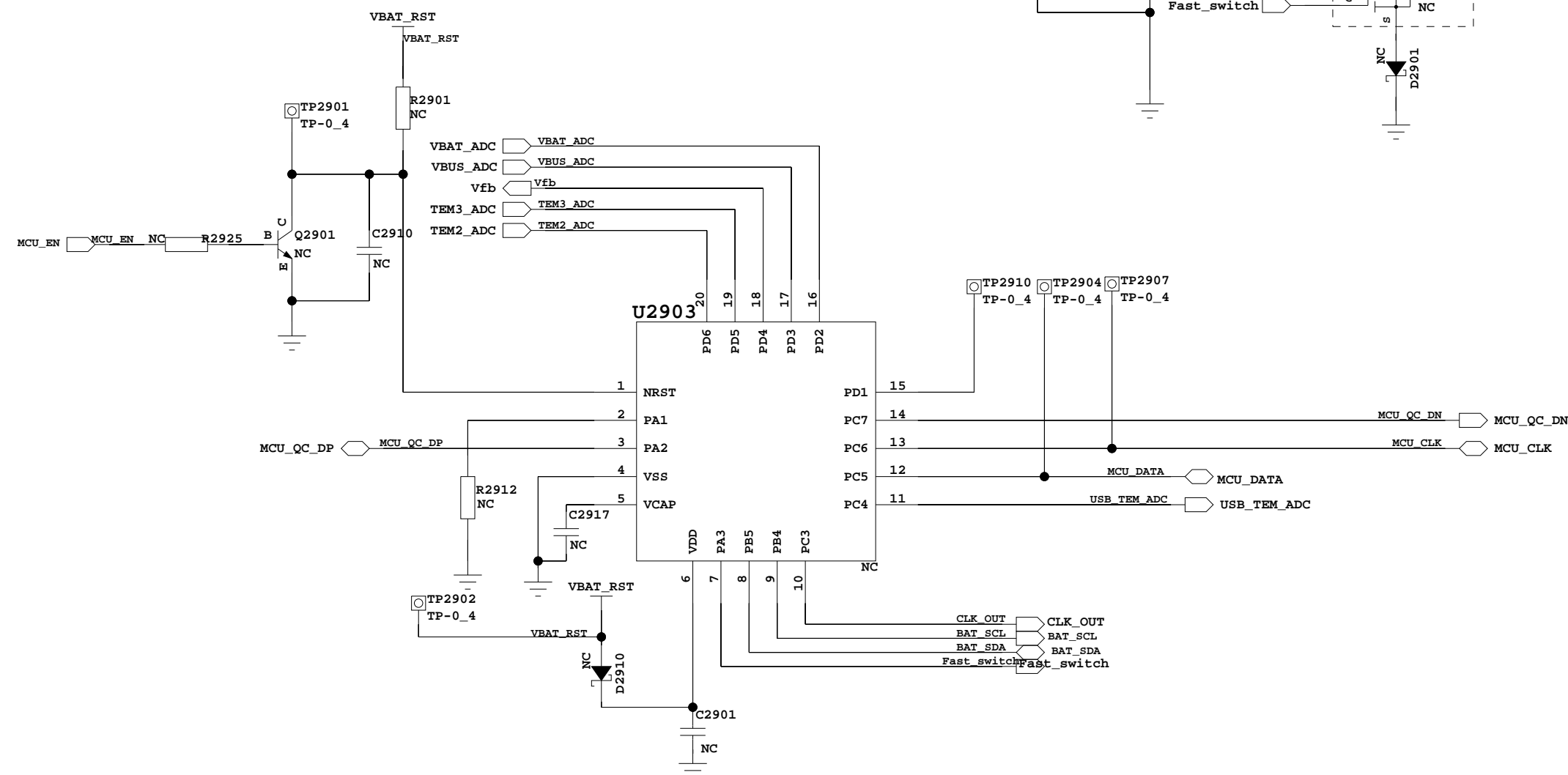
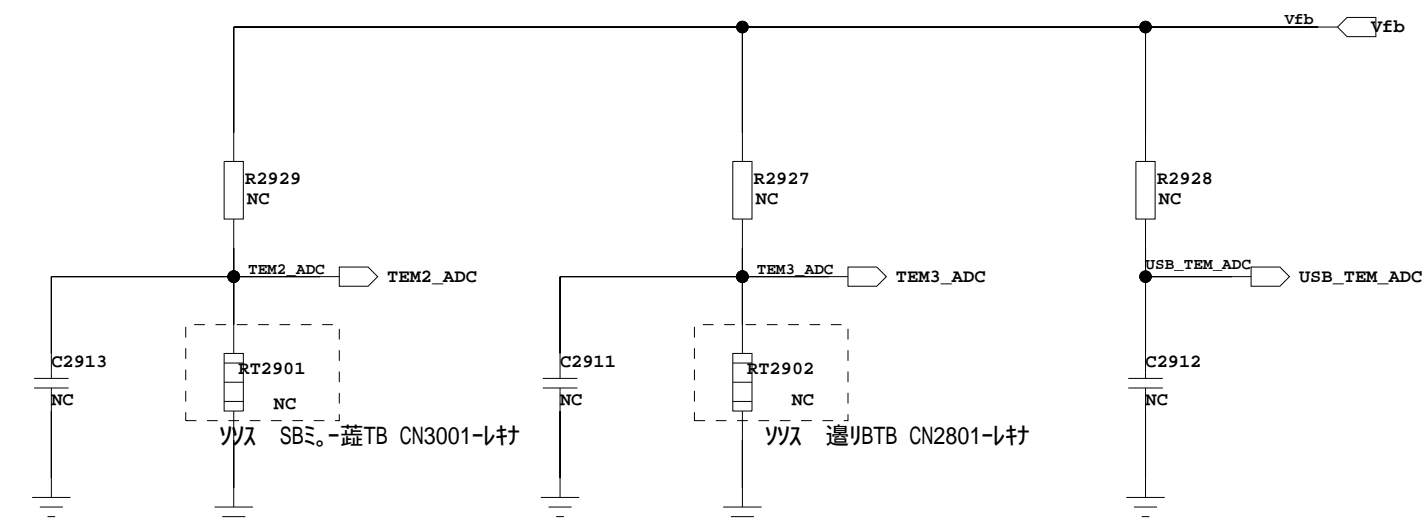


部品名	値
R2805=R2806=R2829=NC	R2805=5.1k R2806=1k R2808=100k
R2808=R2809=R2803=NC	R2809=16k R2803=24k R2829=300k
C2817=NC	C2817=104
R2823=R2824=100	R2823=R2824=NC

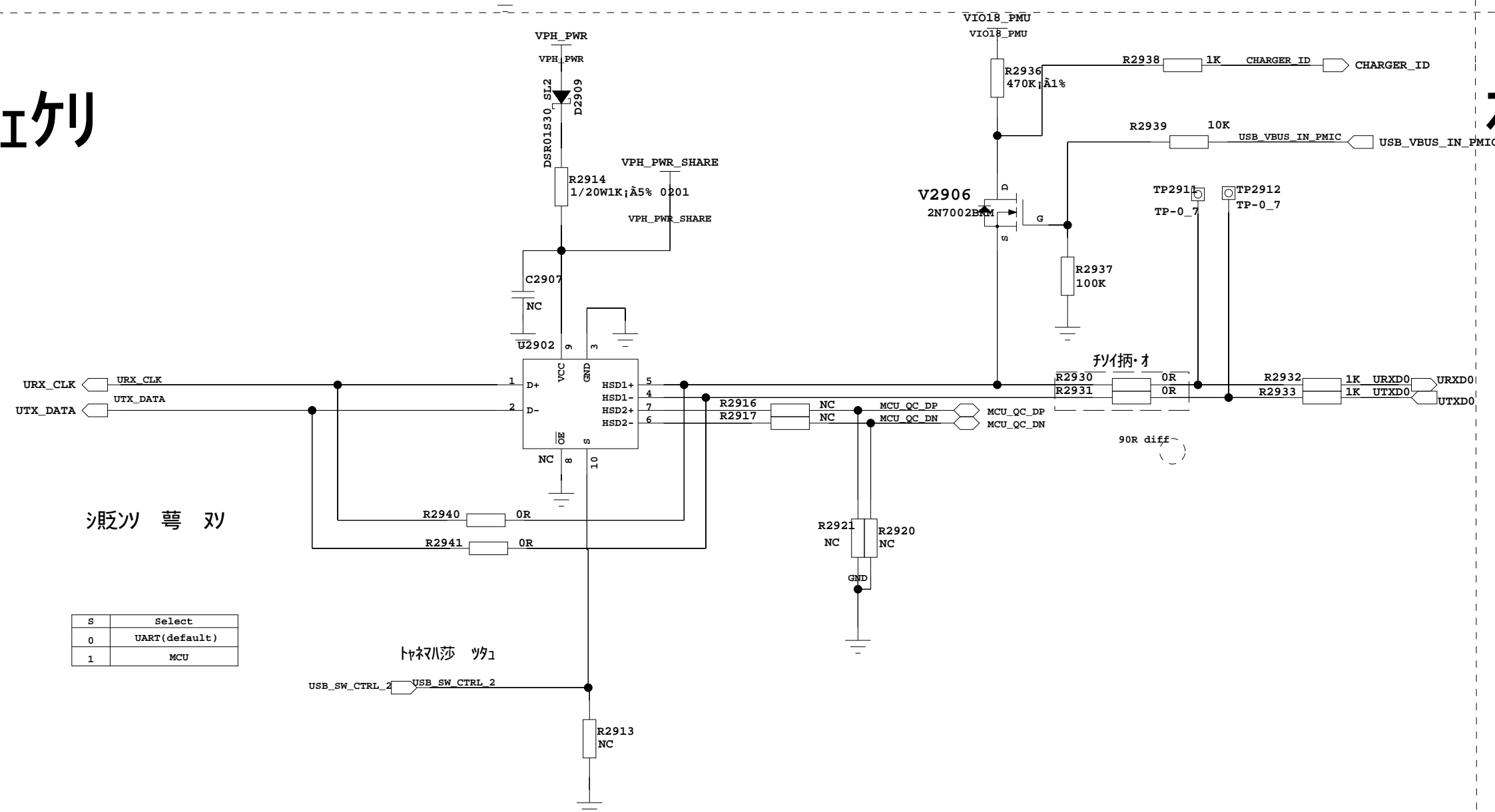
FAST CHARGE



ADC, xβİβxçÒâ°üμØ

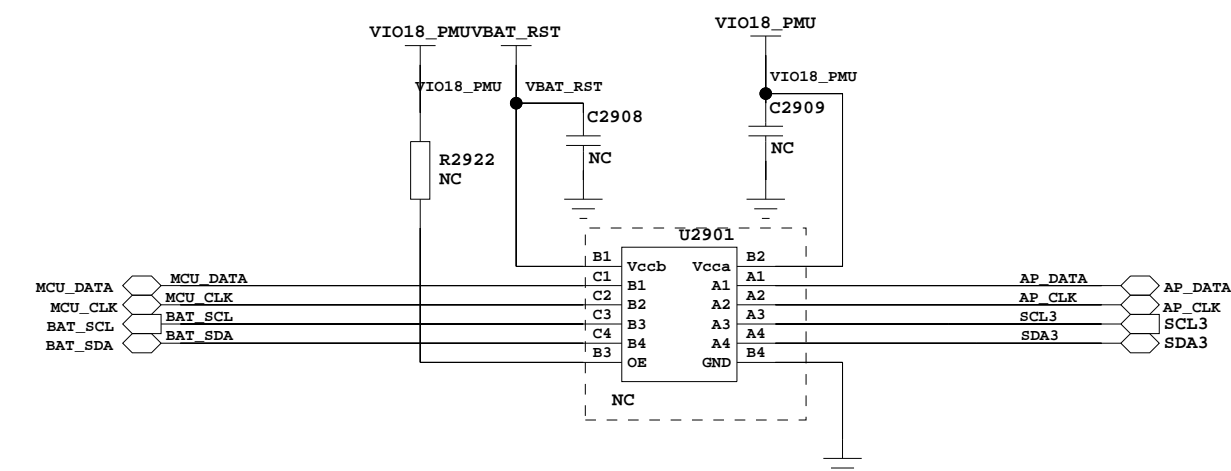


USBソケット



S	Select
0	UART(default)
1	MCU

オ酥禾スラエササIC



B



C



D

**E**

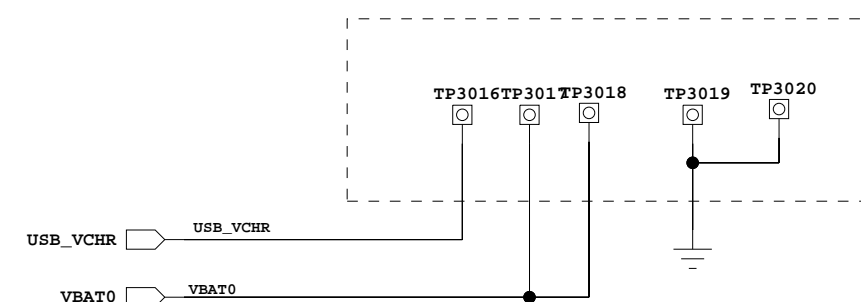
5



(c)



F



The schematic diagram illustrates the electrical connection of the four Mark3101, Mark3102, Mark3103, and Mark3104 components to the ground plane. Each component is represented by a square symbol with a cross inside, labeled with its part number. The components are connected to a common ground line, which is then connected to the ground plane. The ground plane is represented by a large rectangle with a cross inside, labeled 'GND'.

