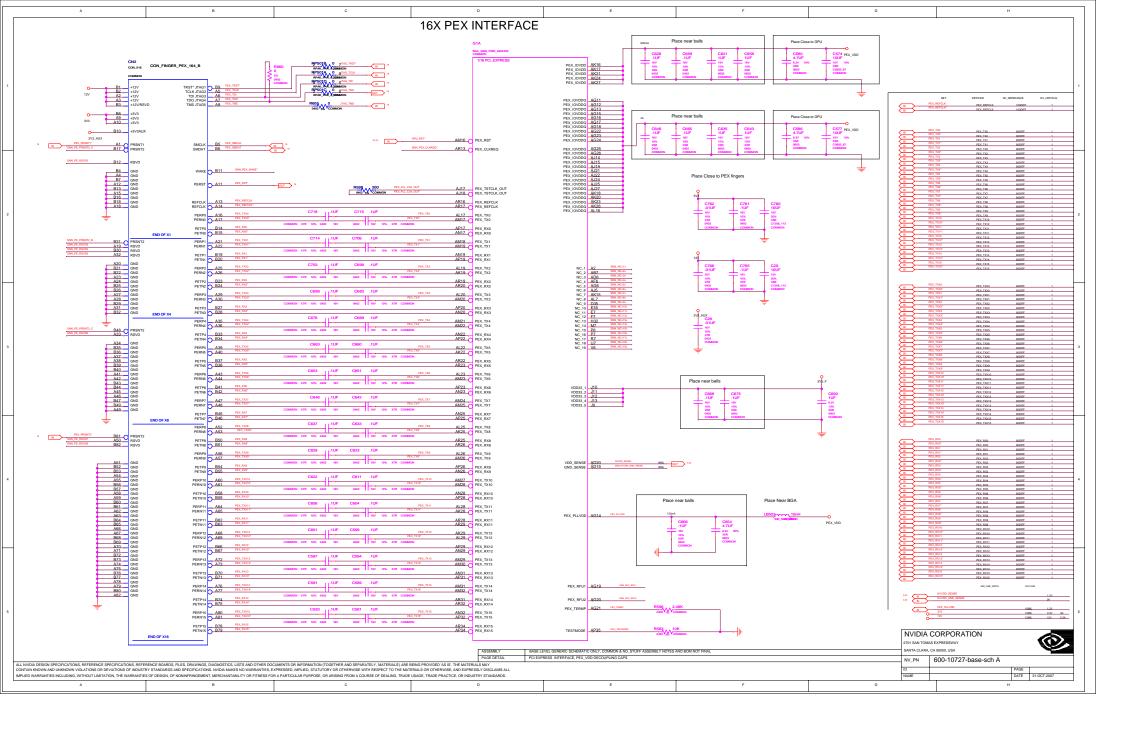
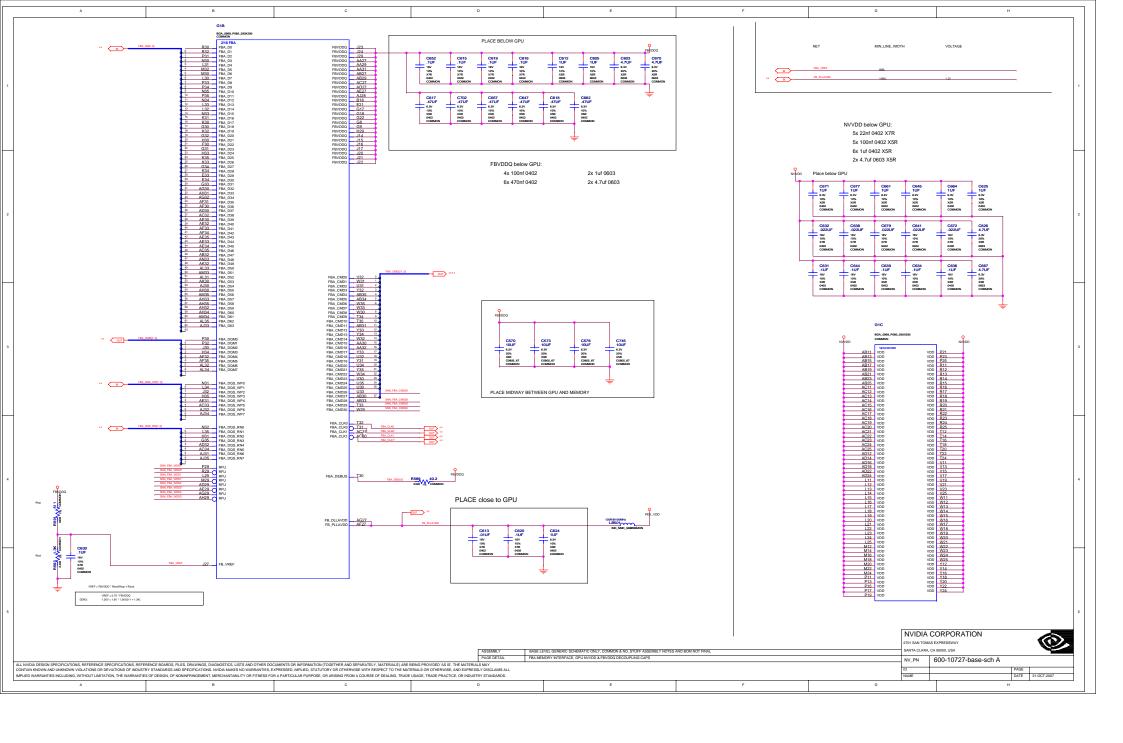
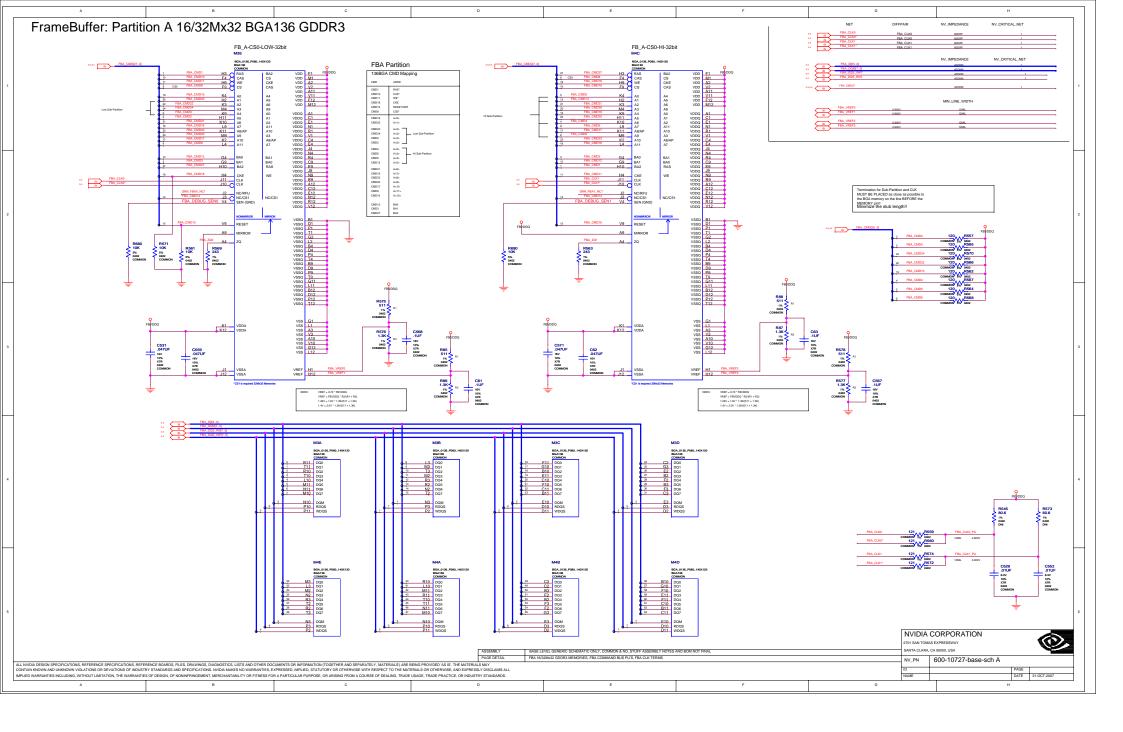
P727-A01: G96, GB1-128, GDDR3, DL-DVI, DL-DVI/VGA, SD/HDTV V129-40 **REV 1.2 HISTORY** 04/01 PAGE SUMMARY: Page 1: TABLE OF CONTENTS PAGE 12.Remove J1 Slim-DSUB connector Page 2: PCI EXPRESS INTERFACE, PEX\_VDD DECOUPLING CAPS PAGE 15.Add R761 and R762 to reserve pull down DP AUX Page 3: FBA MEMORY INTERFACE, GPU NVVDD & FBVDDQ DECOUPLING CAPS Change F503 and F504 footprint to 1812 size Page 4: FBA 16/32Mx32 GDDR3 MEMORIES, FBA COMMAND BUS PU'S, FBA CLK TERMS Remove DP J8 Pin25, Pin26 Page 5: FBA MEMORY FBVDDQ DECOUPLING CAPS PAGE 18.Change U514~U517 to EM1~EM6 Page 6: FBA 16/32Mx32 GDDR3 A1 MEMORIES, FBA COMMAND BUS PU'S, FBA CLK TERMS Page 7: FBC MEMORY INTERFACE Page 8: FBC 16/32MX32 GDDR3 MEMORIES, FBC CMD BUS PU'S, FBC CLK TERMS PAGE 19.Add 2pin Fan control circuit Page 9: FBC MEMORY FBVDDQ DECOUPLING CAPS, GPU GND CONNECTIONS Page 10: FBC 16/32MX32 GDDR3 C1 MEMORIES, FBC CMD BUS PU'S, FBC CLK TERMS PAGE 23.Add C120,C149 10uF 1206 footprint to reserve PEX VDD power Page 11: DACA FILTERS, DACA SYNC BUFFERS & DB15 SOUTH 04/08 Page 12: DACC FILTERS, DACC SYNC BUFFERS & DB15 MID Page 13: TMDS LINK A/B, DVI CONNECTOR SOUTH PAGE 22. Add R92 0805 0ohm Resistor for 3V3 bypass to 2V5 power Page 14: TMDS LINK C/D, AC COUPLING, HDMI Page 15: TMDS LINK E/F, AC COUPLING, DP **REV 1.21 HISTORY** Page 16: MIOA & MIOB, SLI CONNECTOR Page 17: DACB FILTERS, MINIDIN CONNECTOR NORTH, SD/HD VIDEO OUTPUT CONNECTOR 06/04 Page 18: SPDIF-IN, XTAL, MECHANICALS, THERMALS PAGE 20.Add R774 10K pull down resistor for HDA issue Page 19: EXTERNAL THERMAL SENSOR, 4PIN FAN CONTROL, GPIO Page 20: BIOS ROM, HDCP ROM, STRAPPING OPTIONS **REV 4.0 HISTORY** Page 21: HYBRID POWER CIRCUIT Page 22: POWER SUPPLY LINEARS: 5V, DDC5V, IFP PLLVDD, IFP IOVDD, MIO VDD, 3V3 FILTER, 12V FILTER 06/06 change form 1.21 version Page 23: POWER SUPPLY: FBVDDQ +PEX\_VDD SINGLE PHASE SWITCHER Page 24: POWER SUPPLY: NVVDD DUAL PHASE SWITCHER PAGE 15.Remove DP I/O and function Page 25: POWER SUPPLY: Dynamic NVVDD PAGE 14&19 change I2CD bus for HDMI PAGE 19&24 Add Thermal protect circuit PAGE 17 Remove DACB TV I/O and function 06/12 Page 18 remove EM2 ASSEMBLY

ASSEMB BASE
SKU0000
SKU0001
SKU0001
SKU0002
«UNDEFINED.
«UNDEFINED.» NVIDIA CORPORATION ANTA CLARA CA 95050 LISA PAGE DETAIL 600-10727-base-sch A NV PN L MIGHA DESIGNA SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR REFORMATION (TOGETHER AND SEPARATELY, MATERIALS) ARE BEING PROVIDED AS IS: THE MATERIALS MAY

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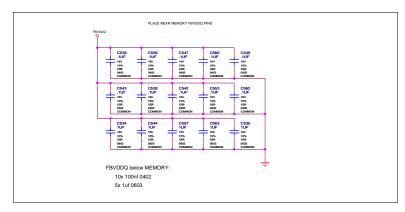




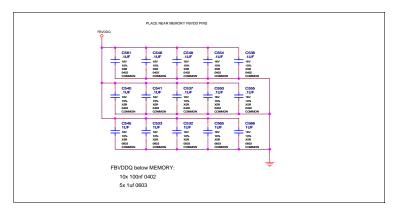


# FRAME BUFFER: PARTITION A DECOUPLING

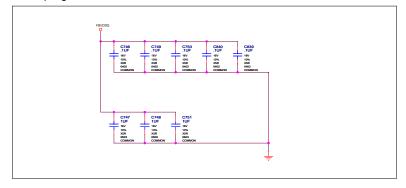
# Decoupling for FBA 0..31



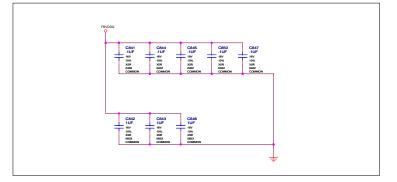
# Decoupling for FBA 32..63



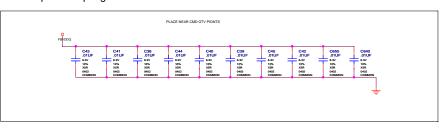
## Decoupling for FBA A1 0..31



# Decoupling for FBA A1 0..31



## Return path coupling GND/FBVDDQ for FBA



NVIDIA CORPORATION SANTA CLARA CA 95050 LISA PAGE DETAIL 600-10727-base-sch A PAGE DATE 31-OCT-2007

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