

change list


- P13 D-sub add DACB SCL SDA結隔
- P15 埃J3场 I/O power 獐瘁
- P16 mHDMI copy V229 SMD HDMI
- P17 埃DP結隔 6262 IC
- P19 埃U6 J501. 钡I2CC倒6262
- P22 add Hi-C CAP(C943)
- P24 12V\_PEX choke change to R50
- P25 FBVDD output choke ㄟR68
- P26 NVVDD output choke常ㄟR68 箇瘁 葺 筓臂C82
- P28 埃mec1 旣撻 κ兵 ñ舢兵
- 0813
- P13 I/O power net nameㄟ5V\_VDD
- P24 L17 稍 礪璫ㄟ R50
- P25 memory MOSㄟ0903Bk ㄟfootprint
- P25 26 R68ㄟタA 腹
- P26 NVVDD MOSㄟ4939+4935
- P28 FAN foot\_print ㄟ ñ硃奎

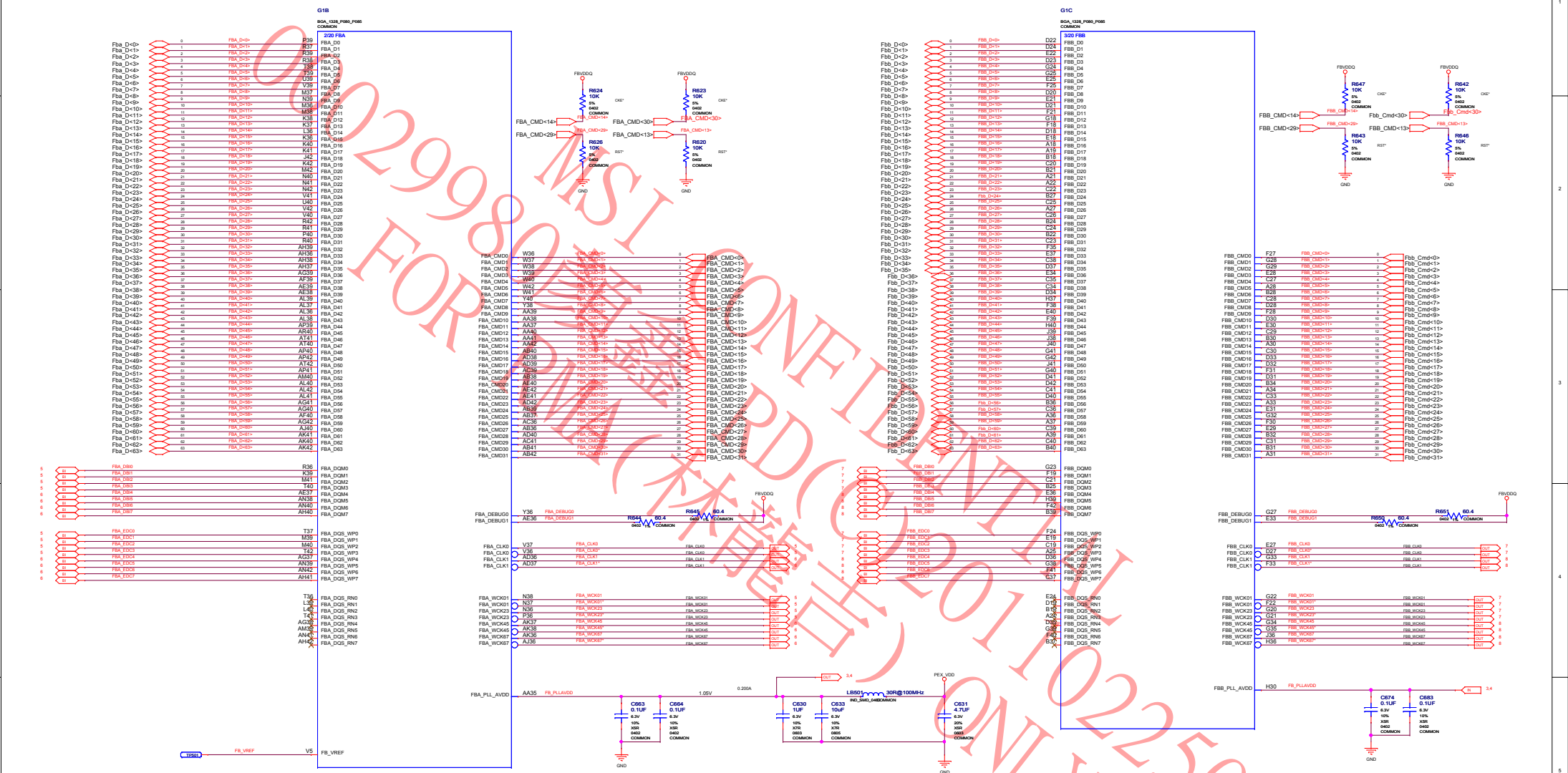
Rev	VARIANT	NVPN	ASSEMBLY
B	BASE	600-11060-base-200	P1060 BASE LEVEL GENERIC SCHEMATIC ONLY. COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU0001	600-11060-0001-200	GEI06J7E 432 1000MHz/2000MHz 1024MB (8pcs)64Mx16 BGA170 GDDR5 (iron grade), DVI+DVI+miniHDMI, 140W
2	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
3	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
4	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
5	SKU0005	600-11060-0005-200	GF106-275 432 1600MHz/2000MHz 1024MB (8pcs)64Mx16 BGA170 GDDR5 on FBB/FBC, DVI+DVI+miniHDMI, 140W
6	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
7	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
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11	SKU0011	600-11060-0011-200	GEI06-250 432 1400/2000MHz 1024MB (8pcs) 64Mx16 BGA170 GDDR5 (iron grade), DVI+DVI+miniHDMI, 95W
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13	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
14	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
15	SKU0021	600-11060-0021-200	GF106-TBD 3/3 1400/2000MHz 1536MB (12pcs) 64Mx16 BGA170 GDDR5 (iron grade), DVI+DVI+miniHDMI, 125W

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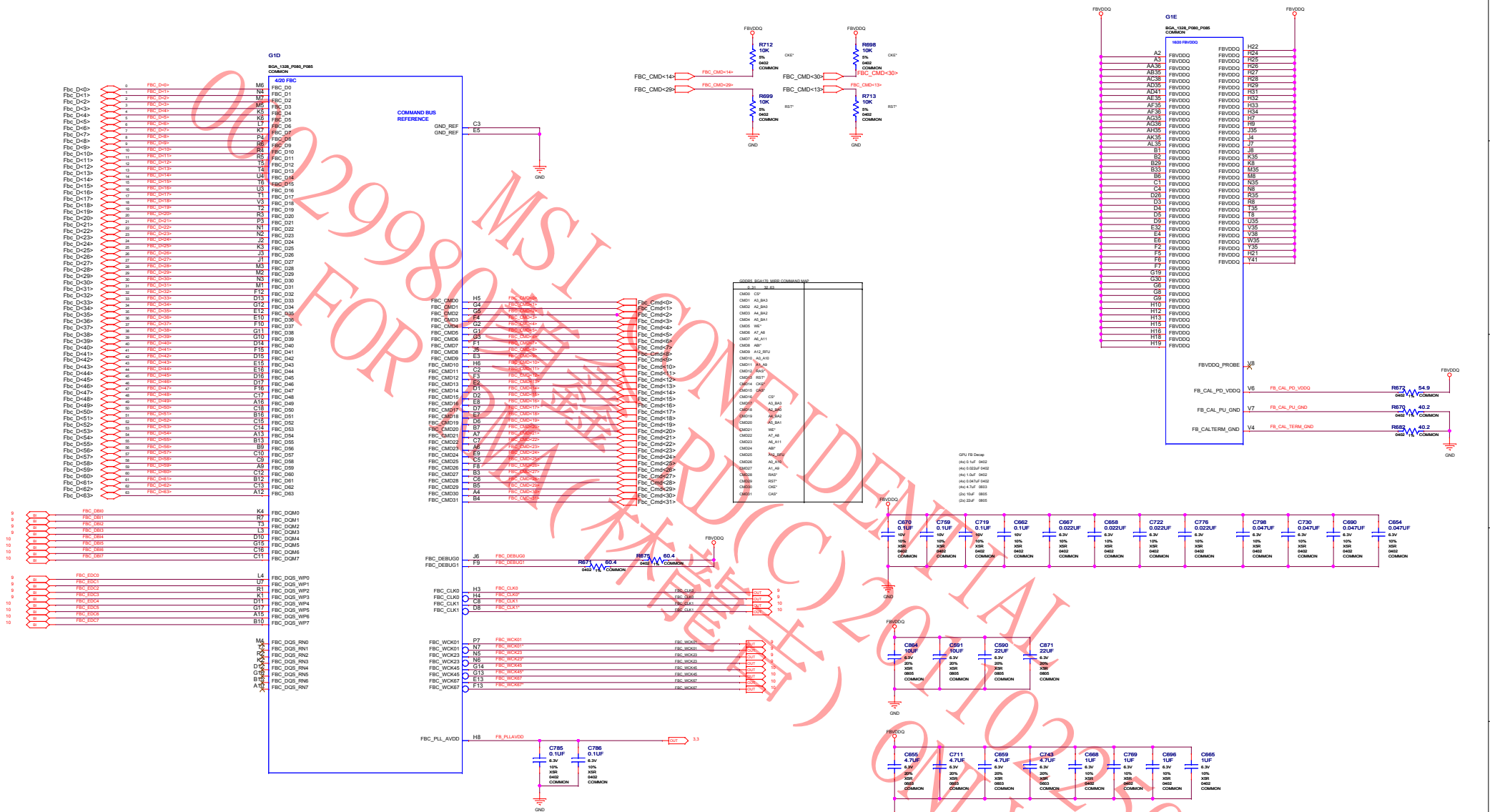
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 PCB DETAIL GPU FB PARTITION A AND B

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MEMORY: GPU FB Partition C AND GPU FB Decap



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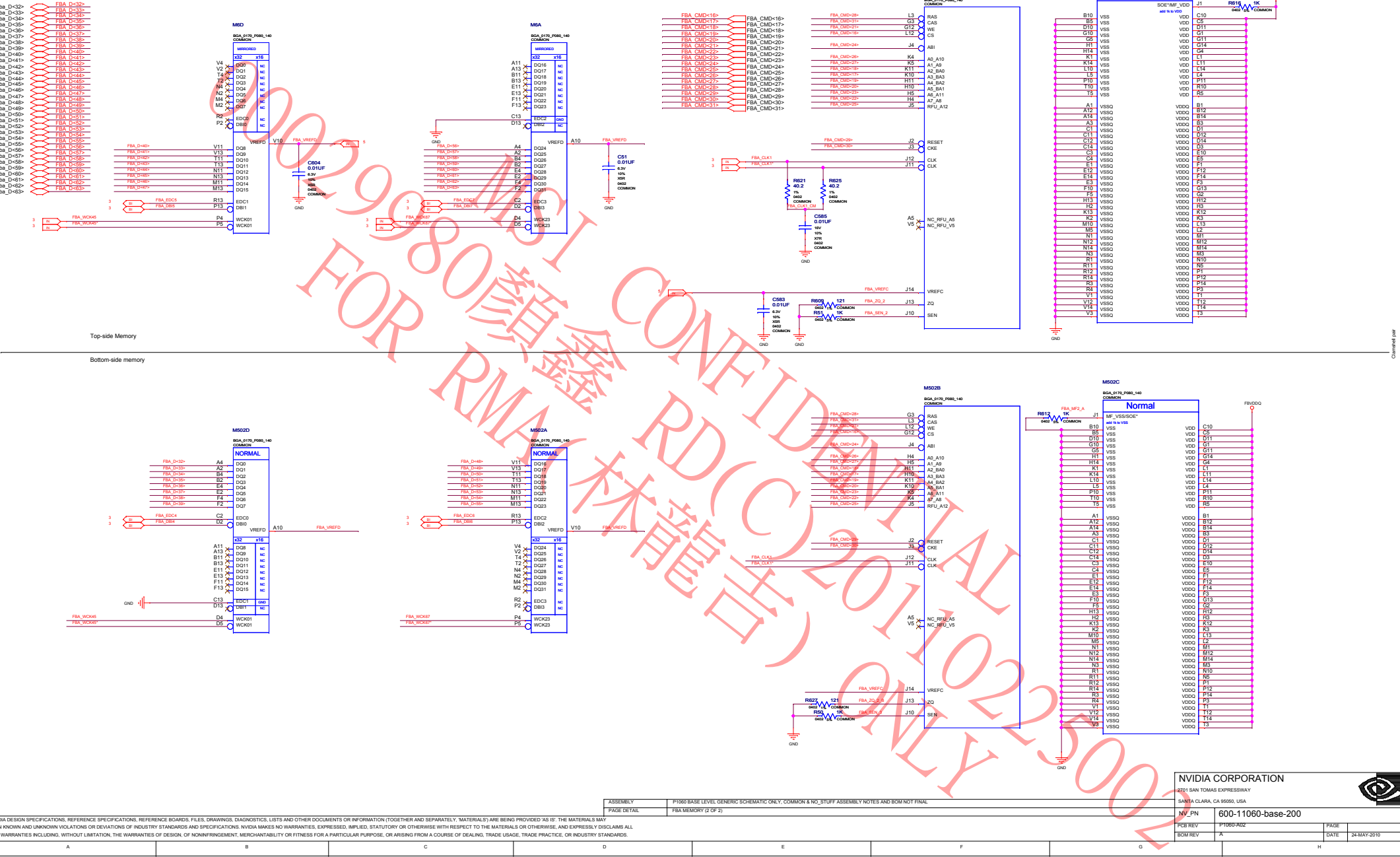
PCB REV	P1060-A02	PAGE	
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PAGE DETAIL	GPU FB Partition C and GPU FB Decap

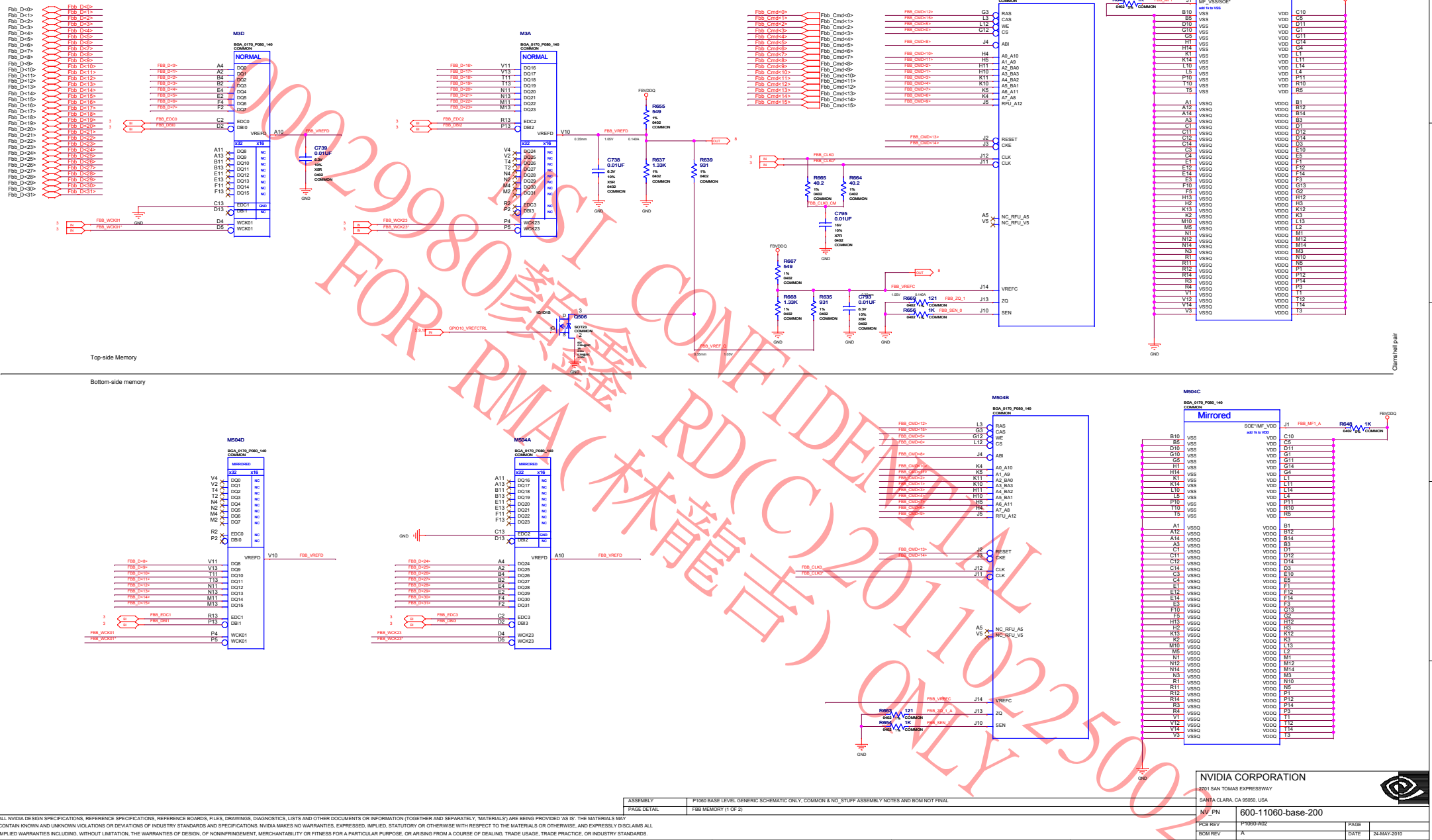


FBA Partition Memory (2 of 2)





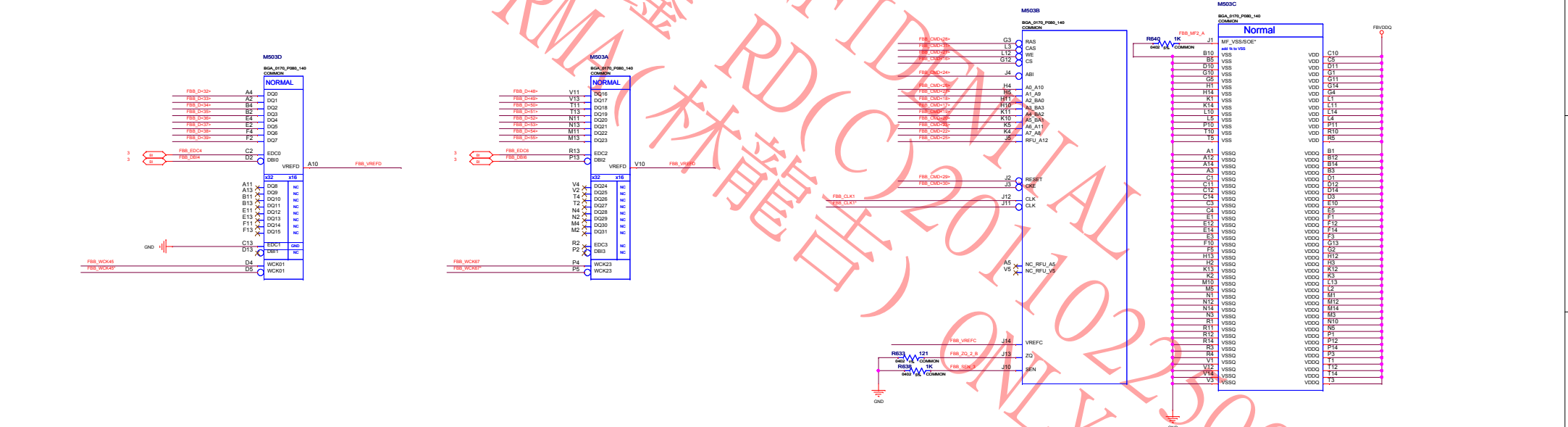
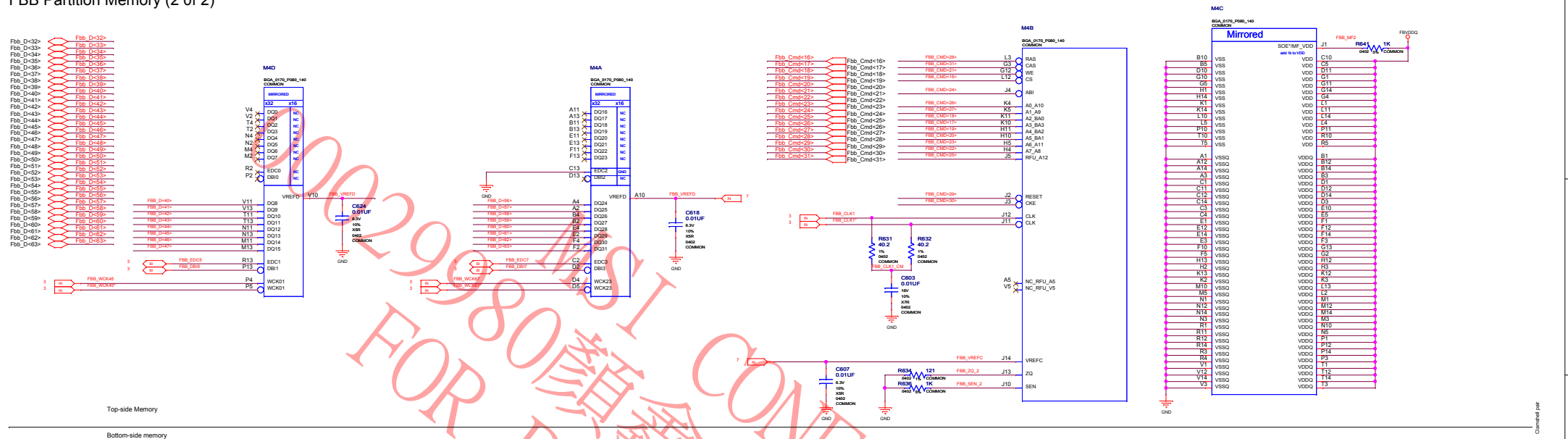
# FBB Partition Memory (1 of 2)



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PAGE DETAIL FBB MEMORY (1 OF 2)

## FBB Partition Memory (2 of 2)



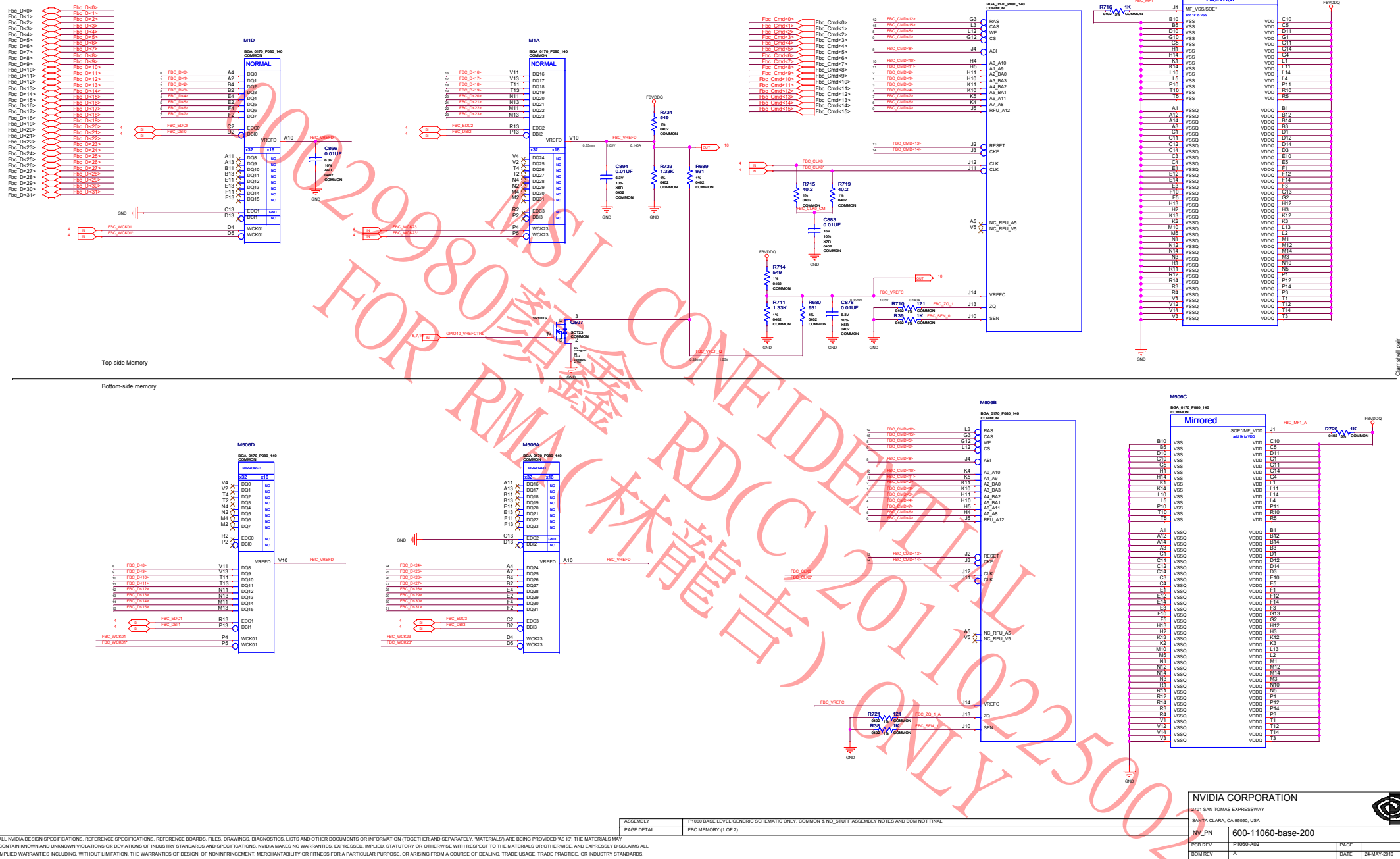
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PAGE DETAIL	FBB MEMORY (2 OF 2)

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## FBC Partition Memory (1 of 2)



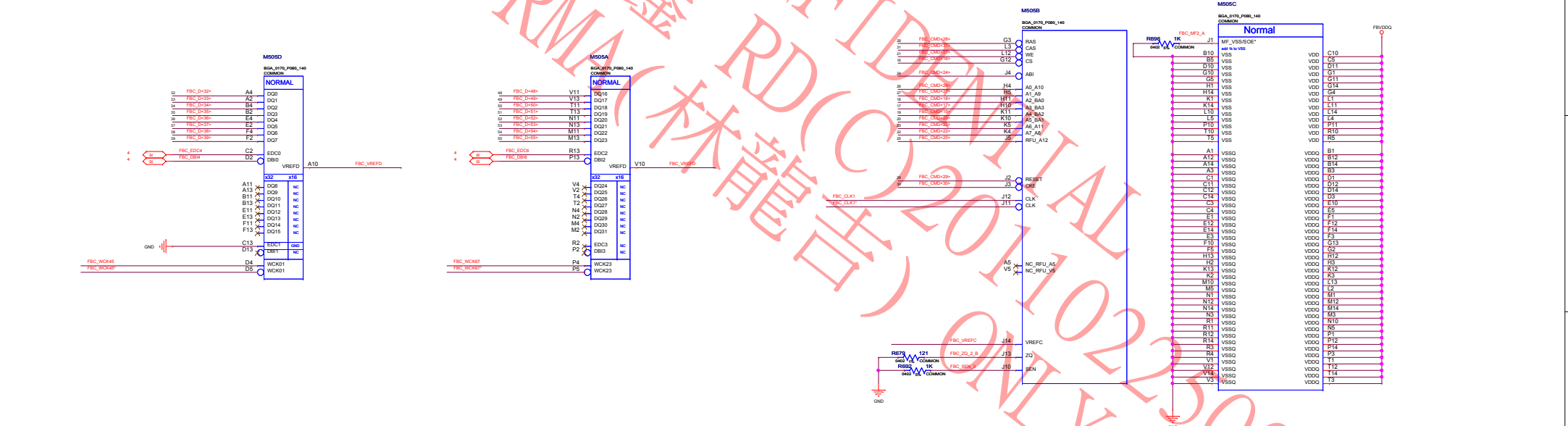
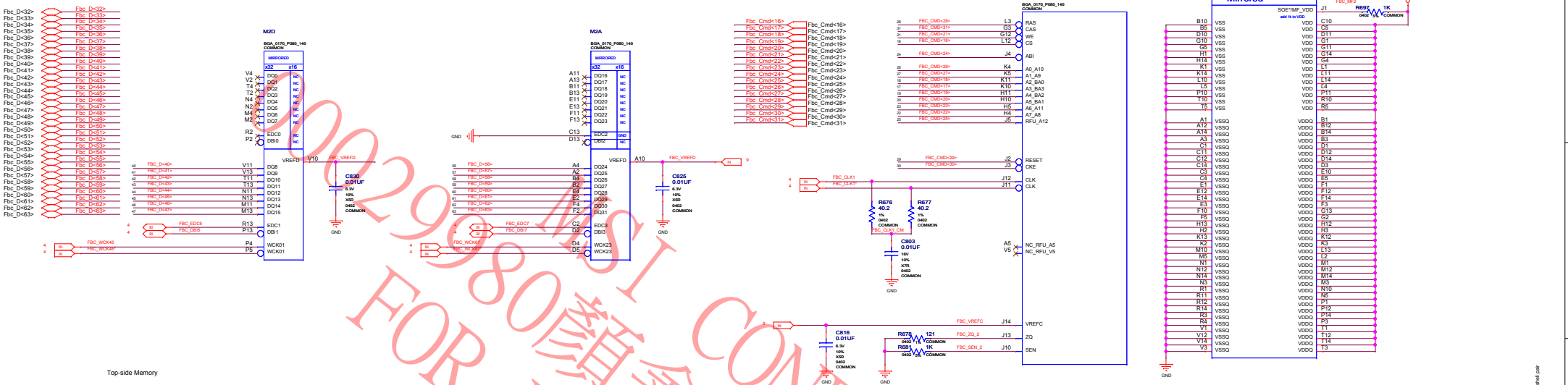
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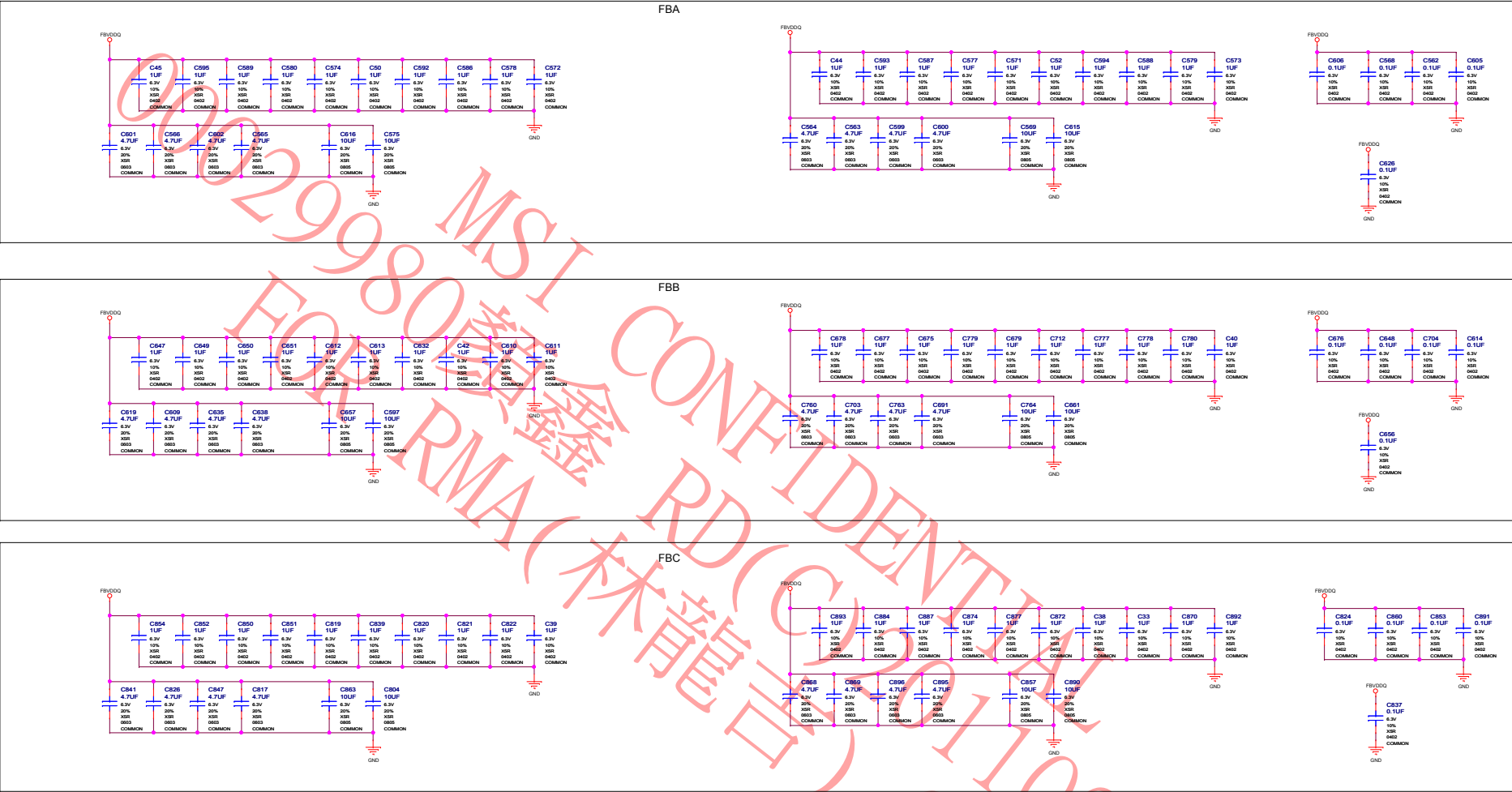
## FBC Partition (2 of 2)



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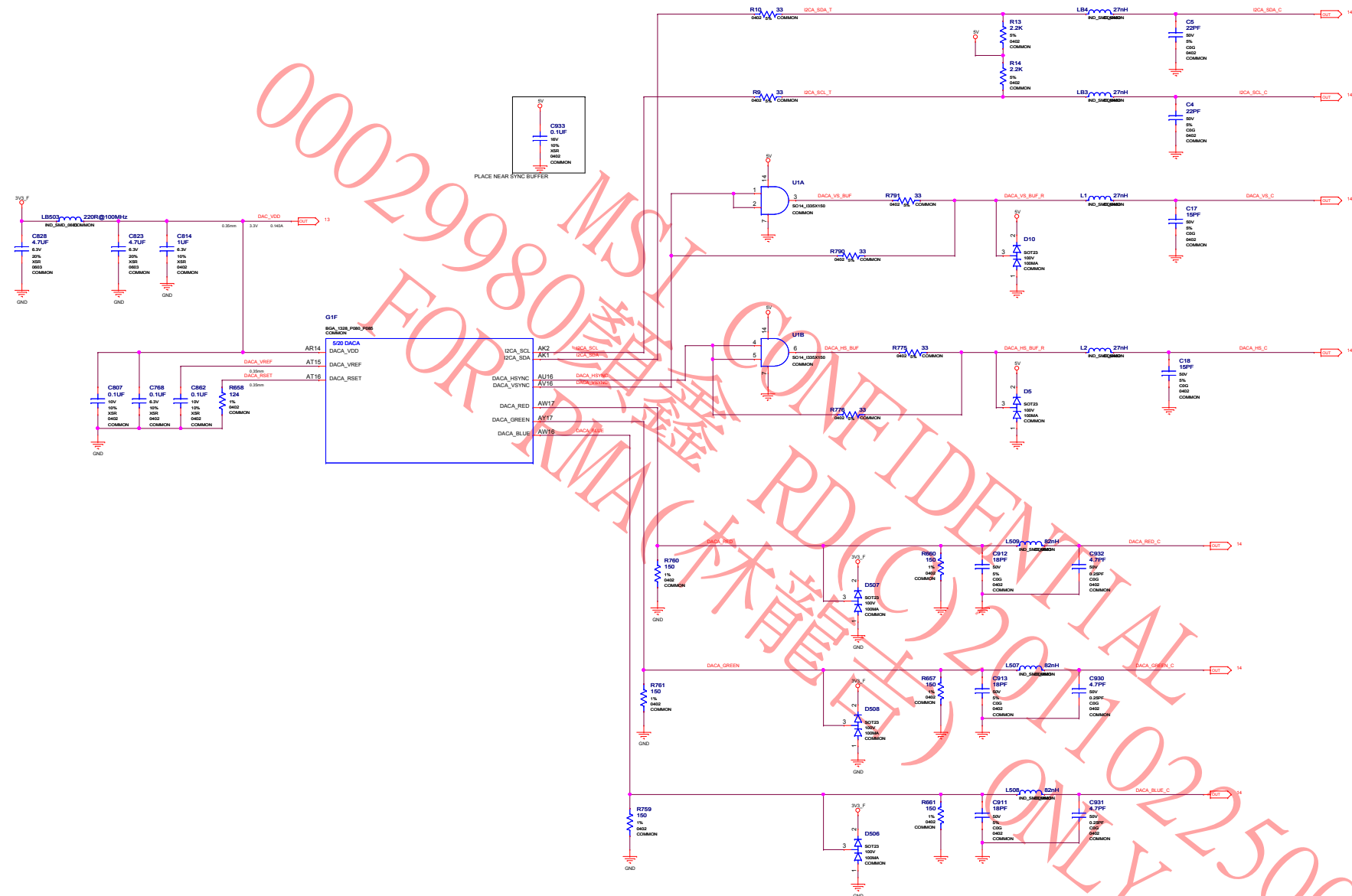
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### DACA for South DVI Interface

## DACA RGB-FILTER



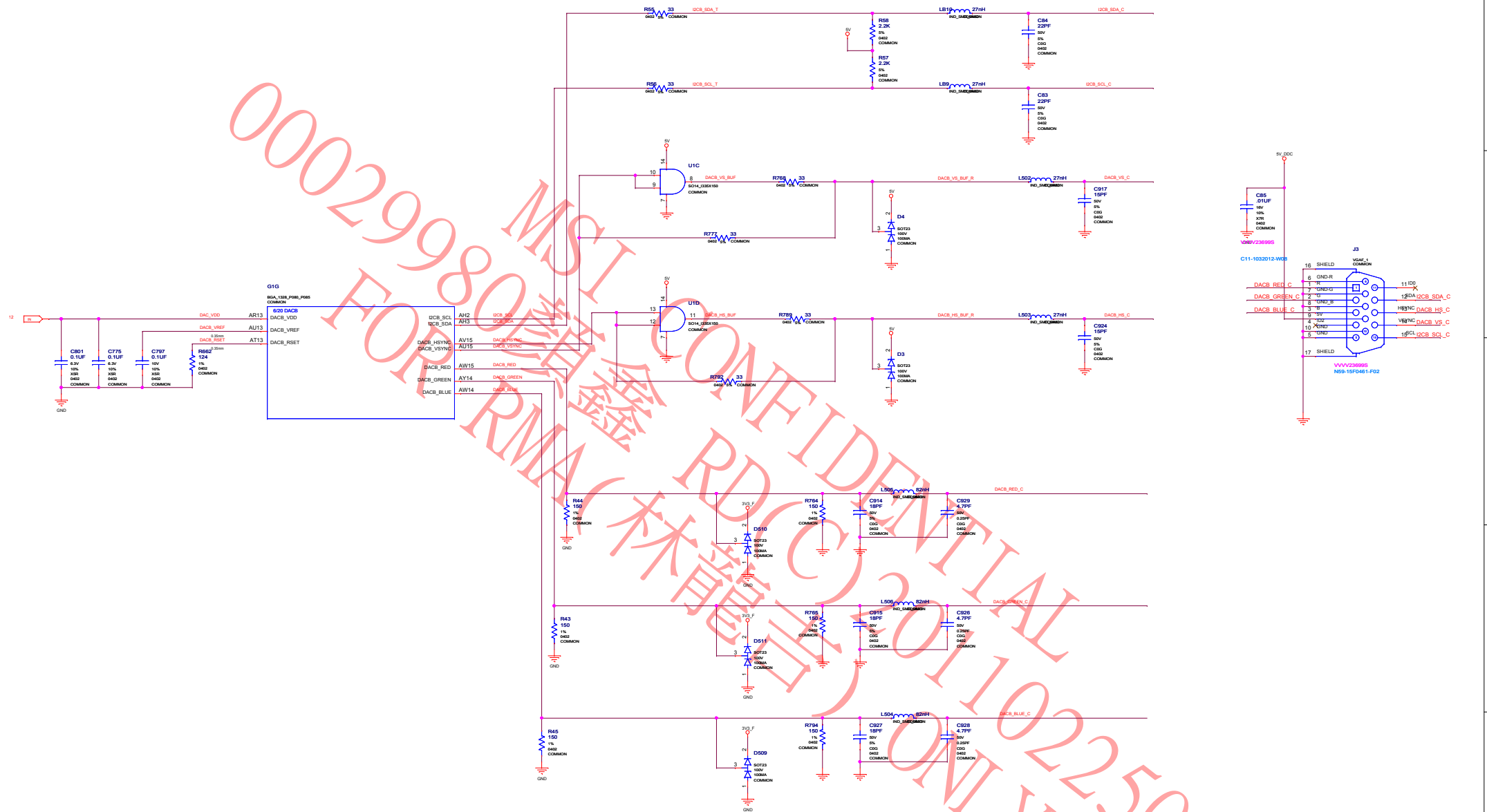
ASSEMBLY	P1000 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACA Interface for DVI Connector, South

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### DACB for Mid DVI Interface

## DACB RGB-FILTER

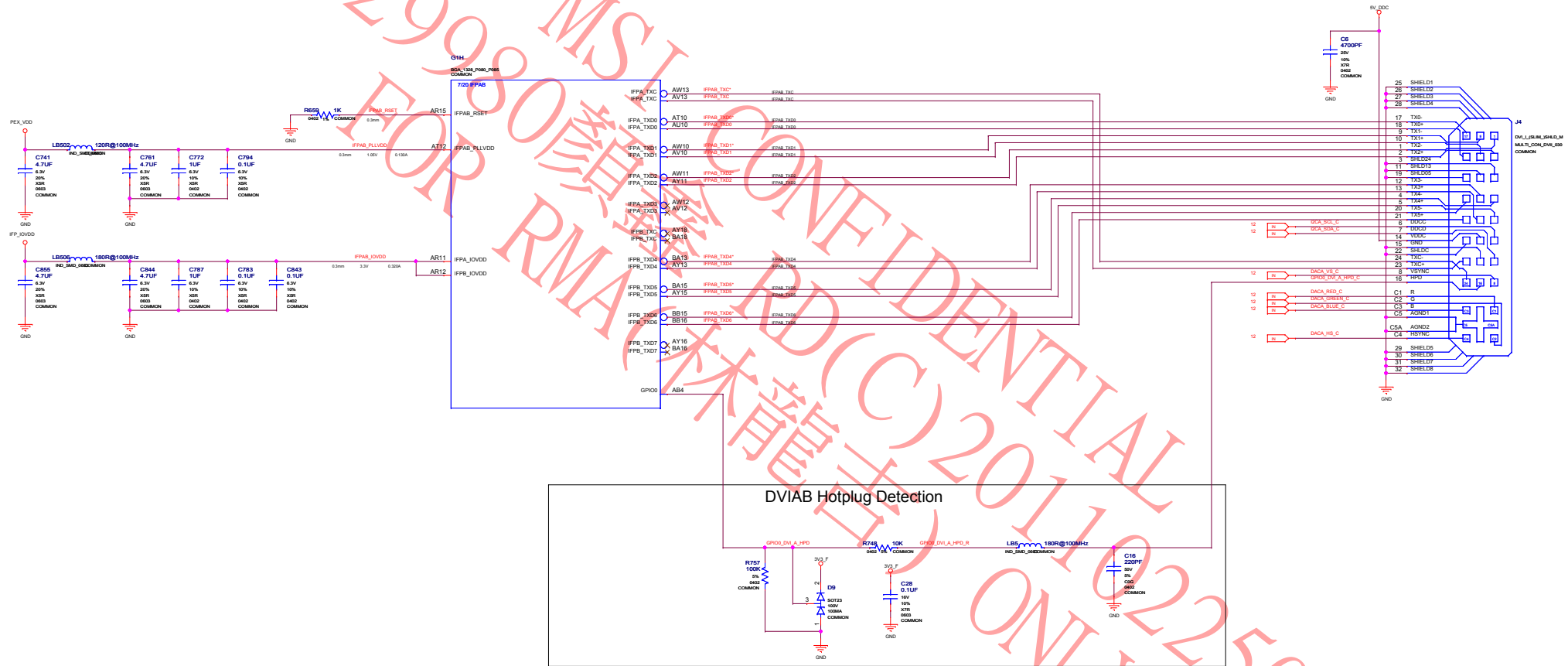


ASSEMBLY	P1605 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACB Interface for DVI Connector, Mid

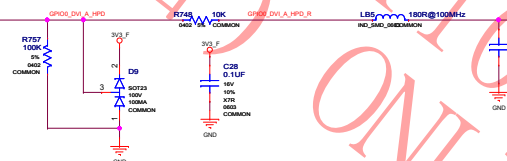
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# IFP A/B Interface -- DVI Connector, South



## DVIAB Hotplug Detection



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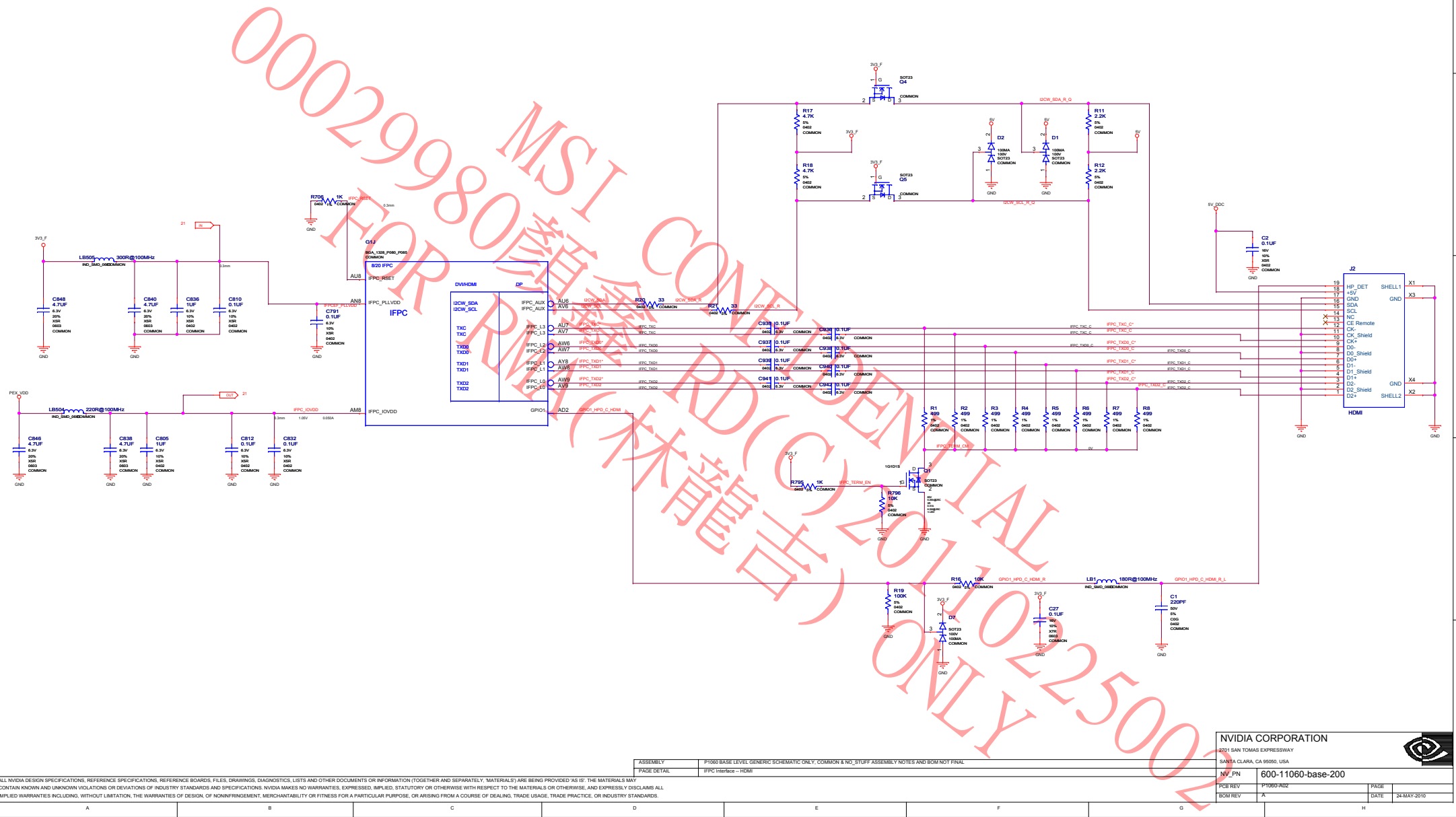
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PAGE DETAIL	IFP A/B Interface -- DVI Connector, South





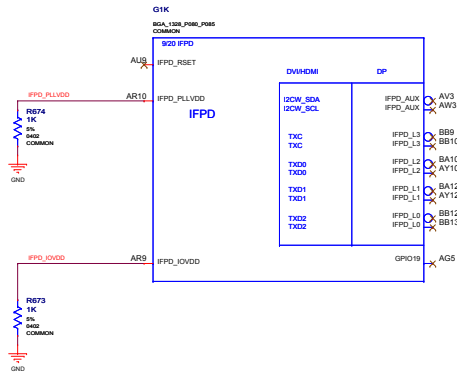
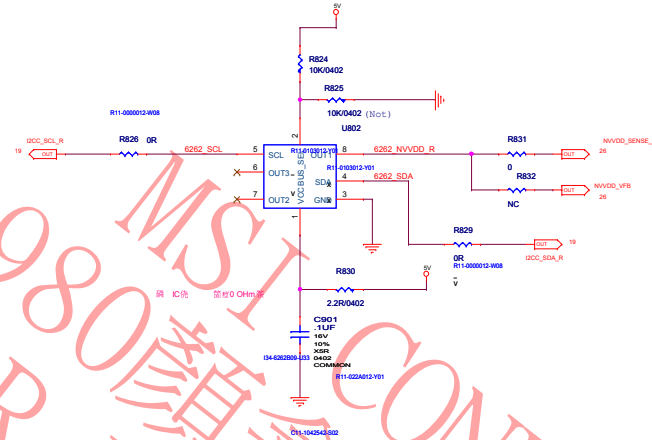
# IFP C Interface -- HDMI, North



IFP D INTERFACE (UNUSED), DP from IFPE, Mid

Three cases to be considered:

1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through
3. DP AUX to DVI connector: AUX level shifted to DDC



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PAGE DETAIL	DP FROM IFPE, and IFPD (UNUSED)

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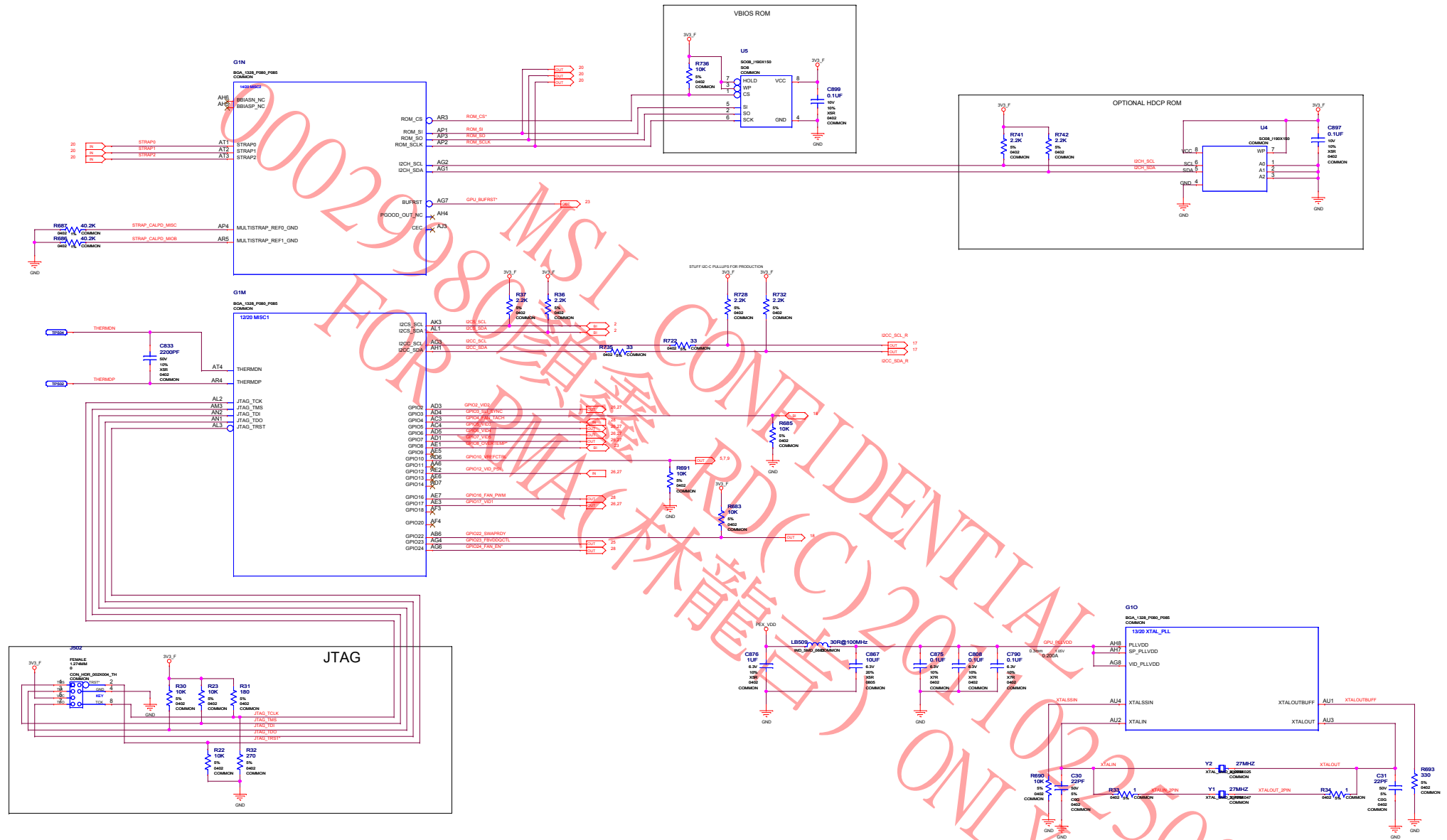


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## MISC GPIO I2C ROM HDCP and XTAL

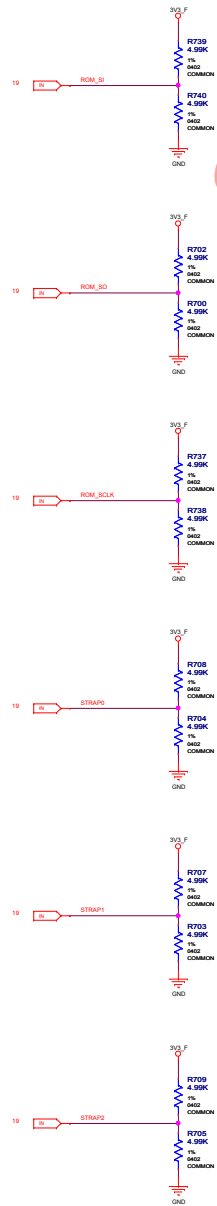


ASSEMBLY	P1060 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	MISC: GPIO, I2C, ROM, HDQP, and XTAL

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## Strap Configuration


[illegible]

Multi-lined Strip Detector Vectors  
Strip PUPD Row (xCol)

0	PD 5
1	PD 10
2	PD 15
3	PD 30
4	PD 25
5	PD 30
6	PD 35
7	PD 45
8	PU 5
9	PU 10
10	PU 15
11	PU 30
12	PU 25
13	PU 30
14	PU 35
15	PU 45

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PAGE DETAIL	Strap Configuration

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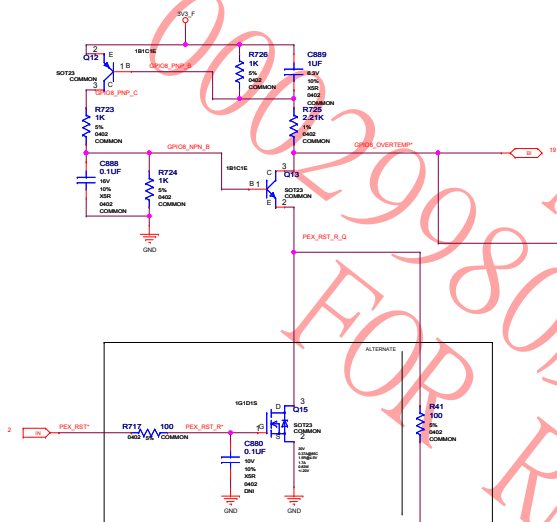




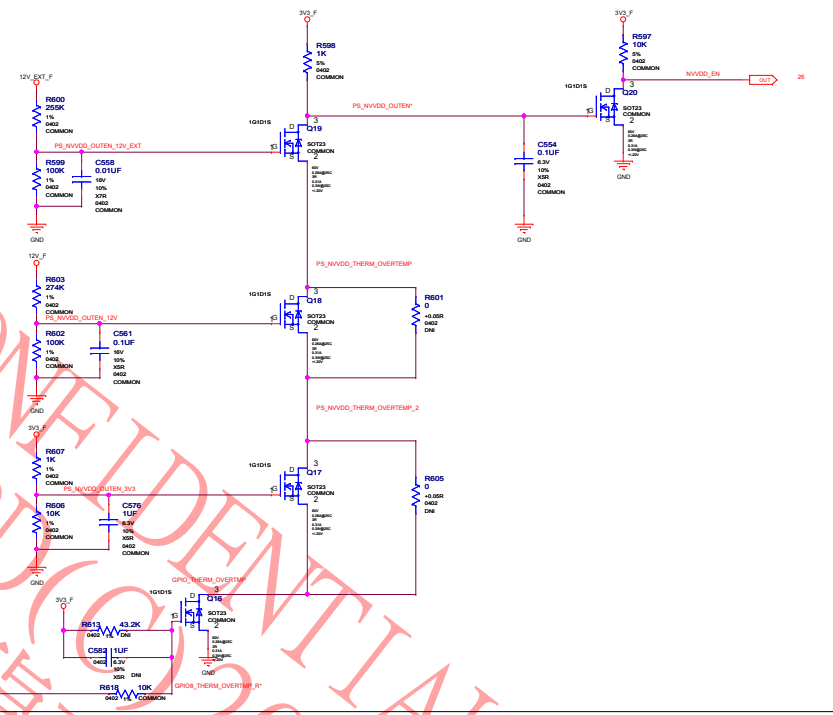


Power Sequencing, Thermal Protection, and IFP\_IOVDD Power Supply

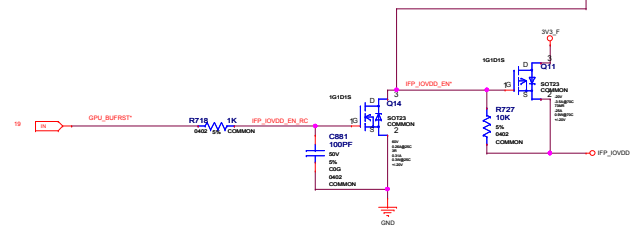
Thermal Protection (latch)



NVDD Sequence



IFP\_IOVDD Backdrive Prevention



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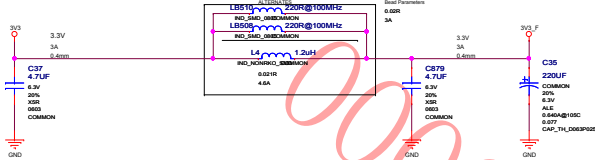
ASSEMBLY	P1060 BASE LEVEL GENERIC SCHEMATIC ONLY. COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Backdrive and Thermal Protection, Power Sequencing

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# PS I: 3V3, 12V, and 12V\_EXT Power Supply Filter, 5V, DDC VOLTAGE

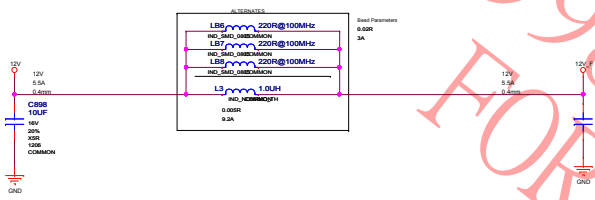
## 3V3 Power Supply Filter

3V3\_F = 3.3V @ 2.4A

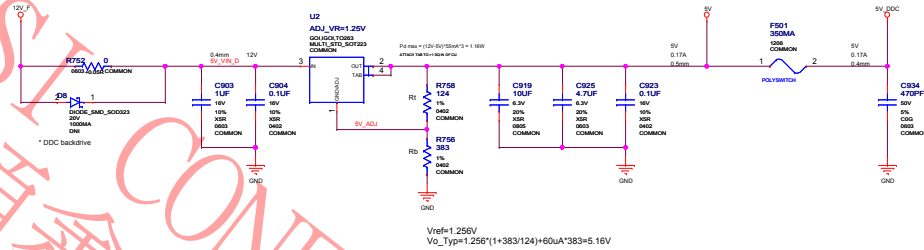


## 12V Power Supply Filter

12V\_F = 12V @ 5.5A



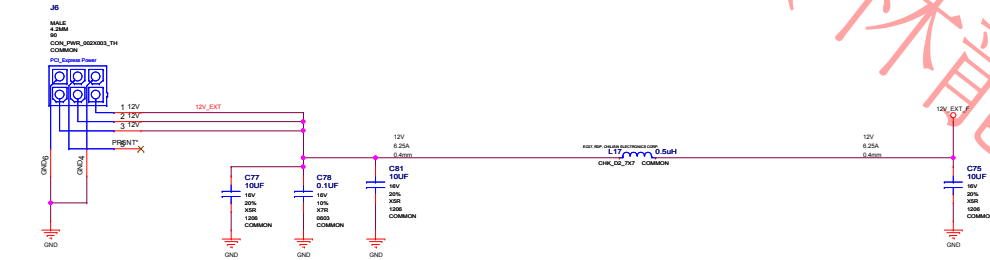
## 5V POWER SUPPLY, DDC POWER



## 12V\_EXT Power Connector

## 12V\_EXT Power Supply Filter

12V\_EXT\_F = 12V @ 6A

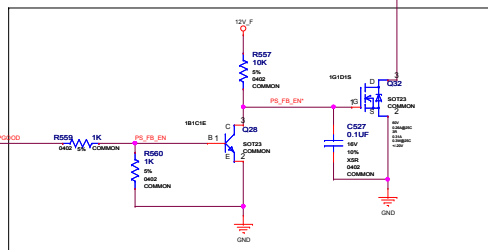


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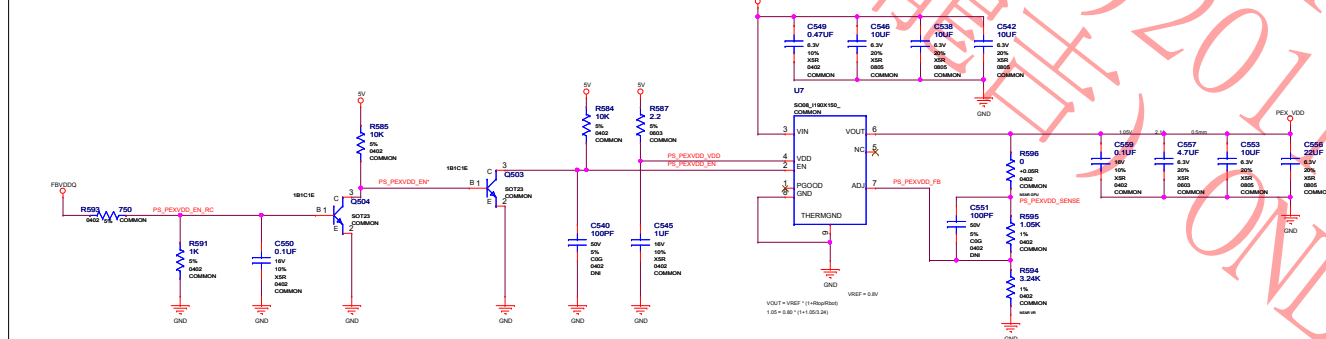
ASSEMBLY	P1060 BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PS I Input Power Supply Filters, 5V AND DDC VOLTAGE

NVIDIA CORPORATION	
2701 SAN TOMAS EXPRESSWAY	
SANTA CLARA, CA 95050, USA	
NV_PN	600-11060-base-200
PCB REV	P1060-A02
BOM REV	A
DATE	24-MAY-2010

## FBVDDQ/PEXVDD GATING



## PEXVDD LINEAR VR

ASSEMBLY P1060 BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO\_STUFF ASSEMBLY NOTES AND BOM NOT FINAL  
PAGE DETAIL PS II: PEX\_VDD; FBVDDQ

NVIDIA CORPORATION

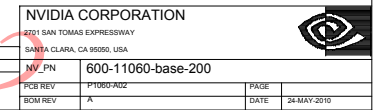
2701 SAN TOMAS EXPRESSWAY  
SANTA CLARA, CA 95050, USA

NV\_PN 600-11060-base-200

PCB REV P1000-A02  
BOM REV APAGE 1  
DATE 24 MAY 2010

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2-PHASE OR 3-PHASE DESIGN  
(UNSTUFF PHASE 2 FOR 2-PHASE CONFIG)



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