

EAH4870 Repair Guide

May/18/2008

Aray_Chen

CSC-GRMA

Vendor Kit	B507PML
ASUS model name	EAH4870/HTDI/512M/A(B507PML)
p/n	80-C1CLGA-J0A01
Chip	RV770 XT
GPU p/n	02G110016500
Memory	16M*32 -0.5 GDDR5*8pcs (512MB---256 bit)GPU(256bit)
Memory p/n	03G15134J210 / Qimonda
CoreClock	750
MemoryClock	900

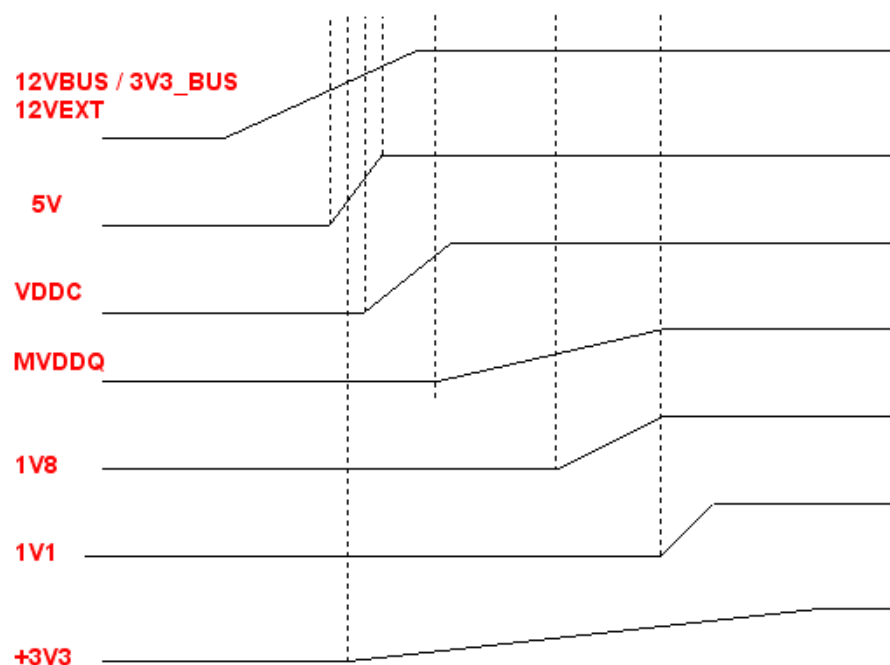
Page1. Basic Information about the Model

Page2. Power Sequence and Power Flow

Page3. Power Point Map (Top Side)

Page4. Power Point Map (Bottom Side)

Power sequence



External +12V
Connector

POWER REGULATORS

From +12V

+VDDC (MPVDD, VDDCI), +MVDD
(MVDDC, VDDR1/VDDRH), VDDM

From +12V LINEAR:

+5V, +5V_VESA,
+5V_VESA2,

From +12V DIRECT:

FAN

From +3.3V: Direct or Linear (1.8V)

VDD_CT, DPLL_PVDD, TPVDD,
T2PVDD, TXVDDR, T2XVDDR/
T2XVDDC, AVDD, VDD1DI,
VDD2DI, PCIE_VDDR, PCIE_PLL,
VDDR4, VDDR5
VDDR3, A2VDD, PCIE_PVDD
Option for VDDCI

From MVDDC to Linear (+1.1V):

PCIE_VDDC, DPLL_VDDC

From MVDDC to Linear (+VDDCI_LDO):

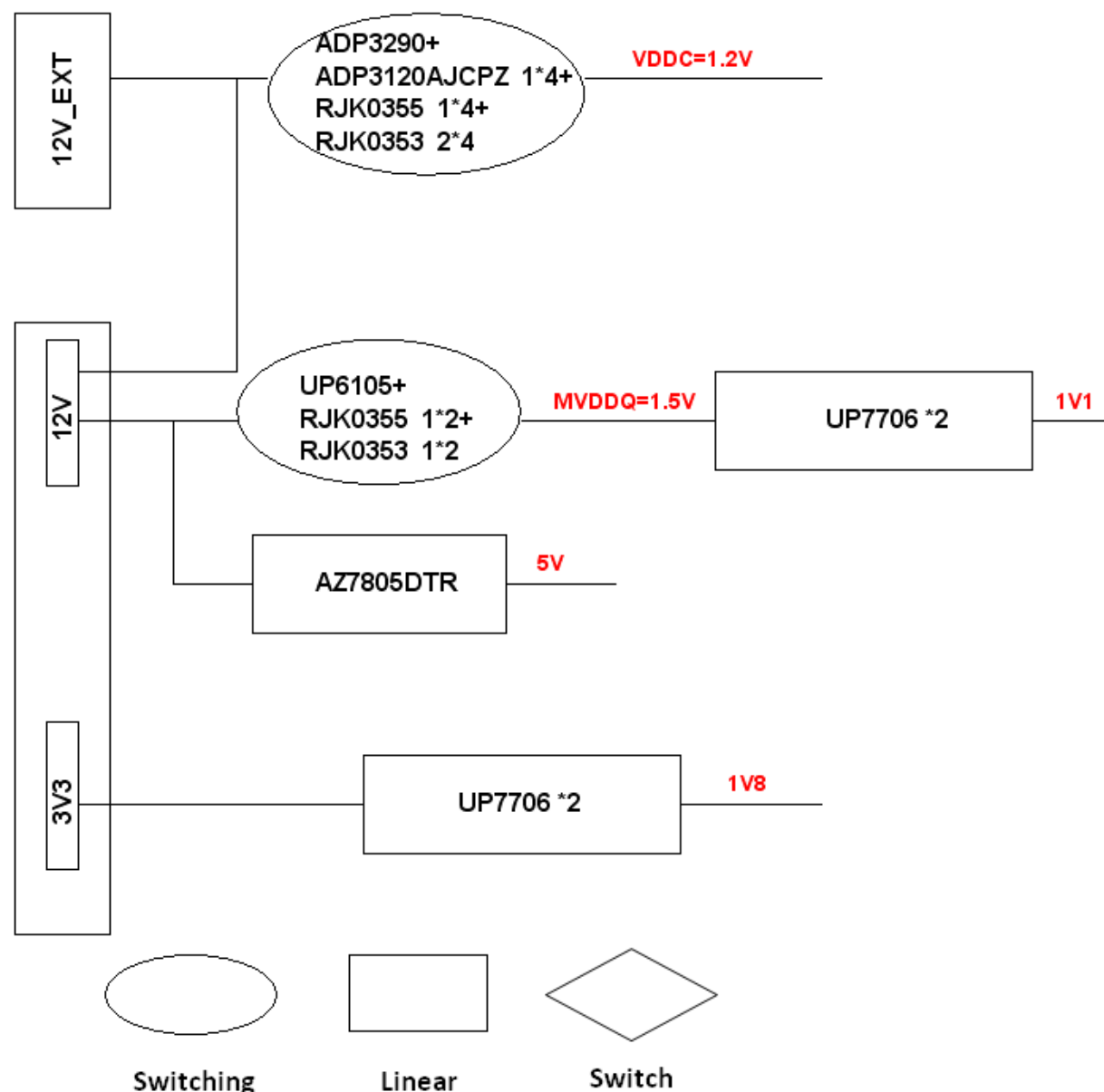
Option for VDDCI, MPVDD

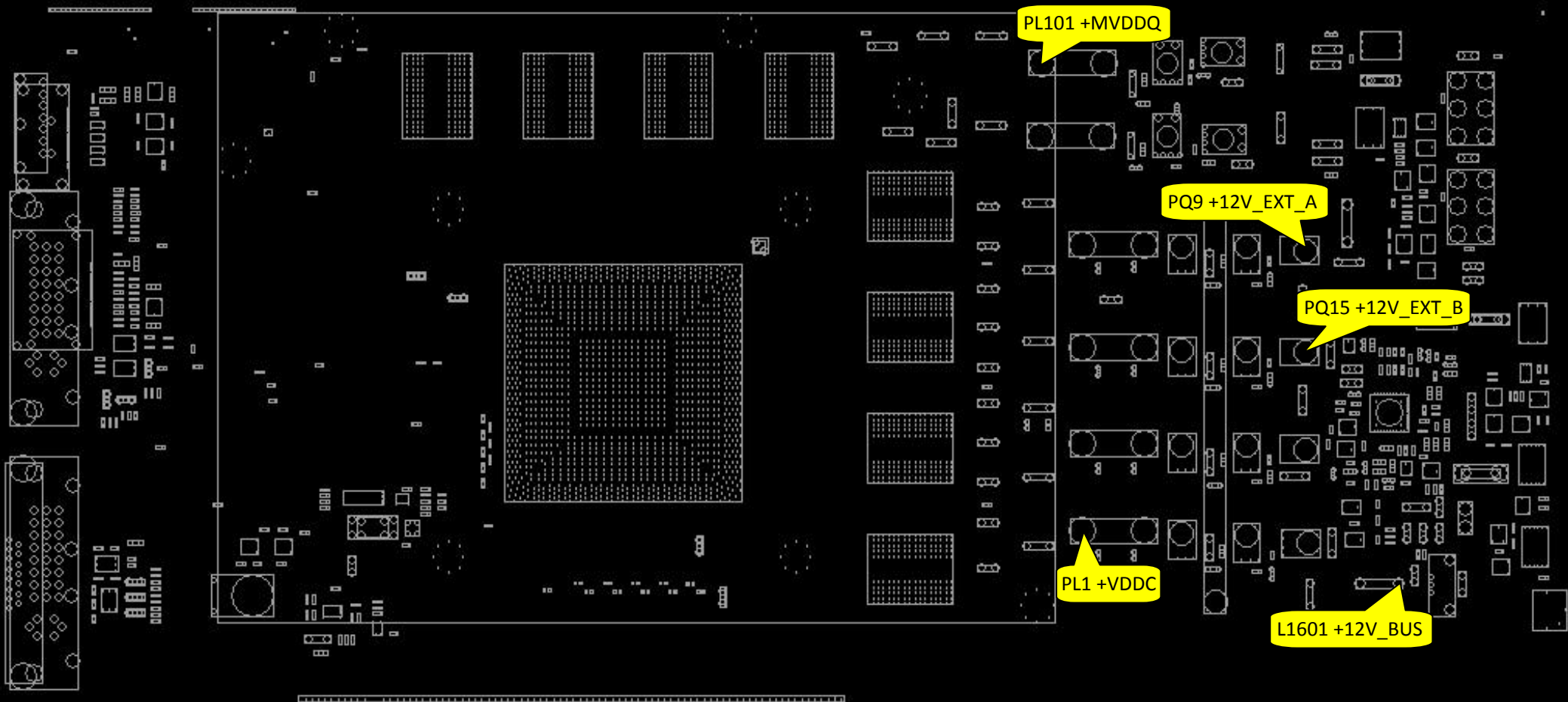
+PCIE_SOURCE

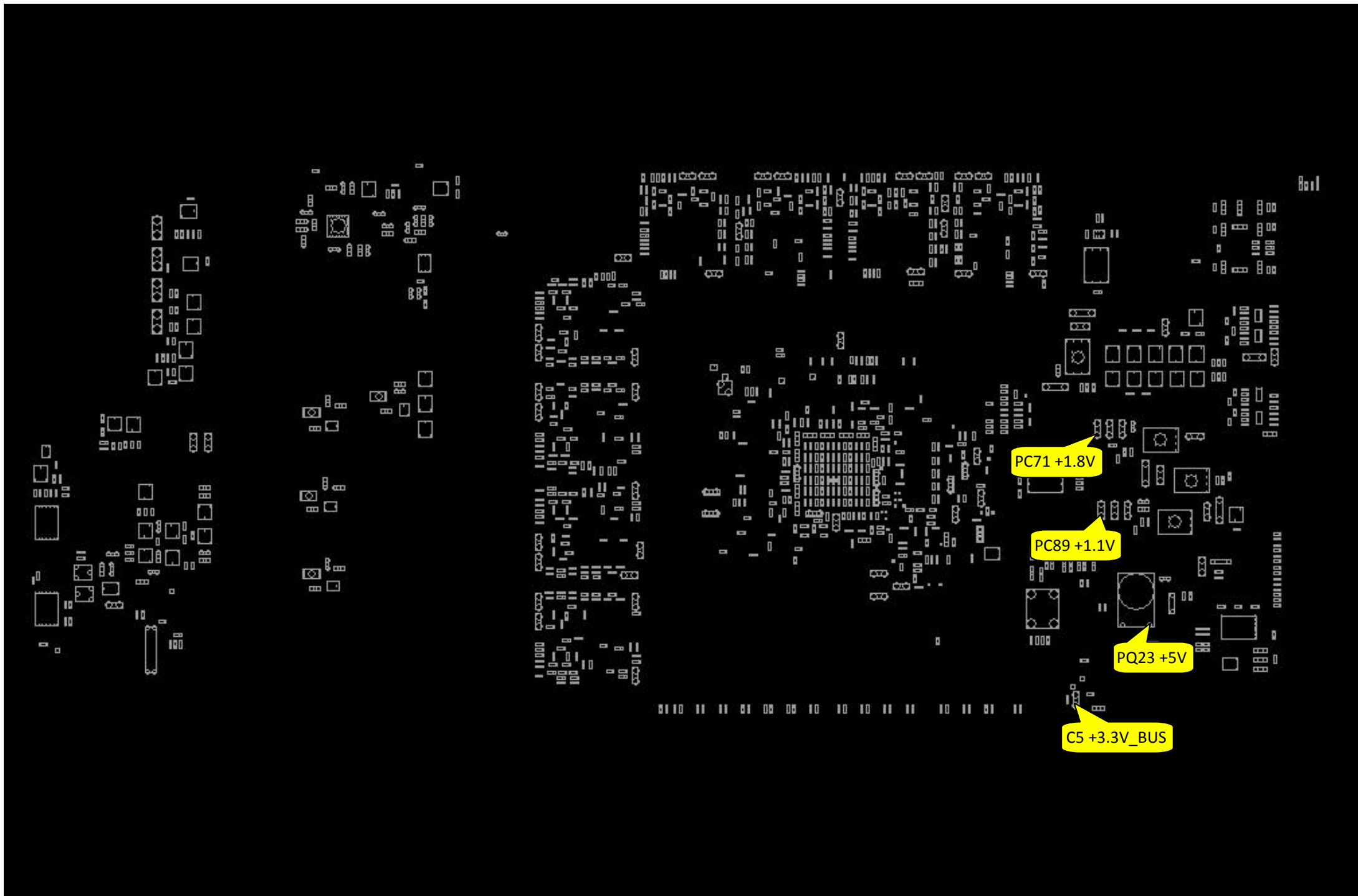
+3.3V_BUS

+12V_BUS

Power flow







PC71 +1.8V

PC89 +1.1V

PQ23 +5V

C5 +3.3V_BUS