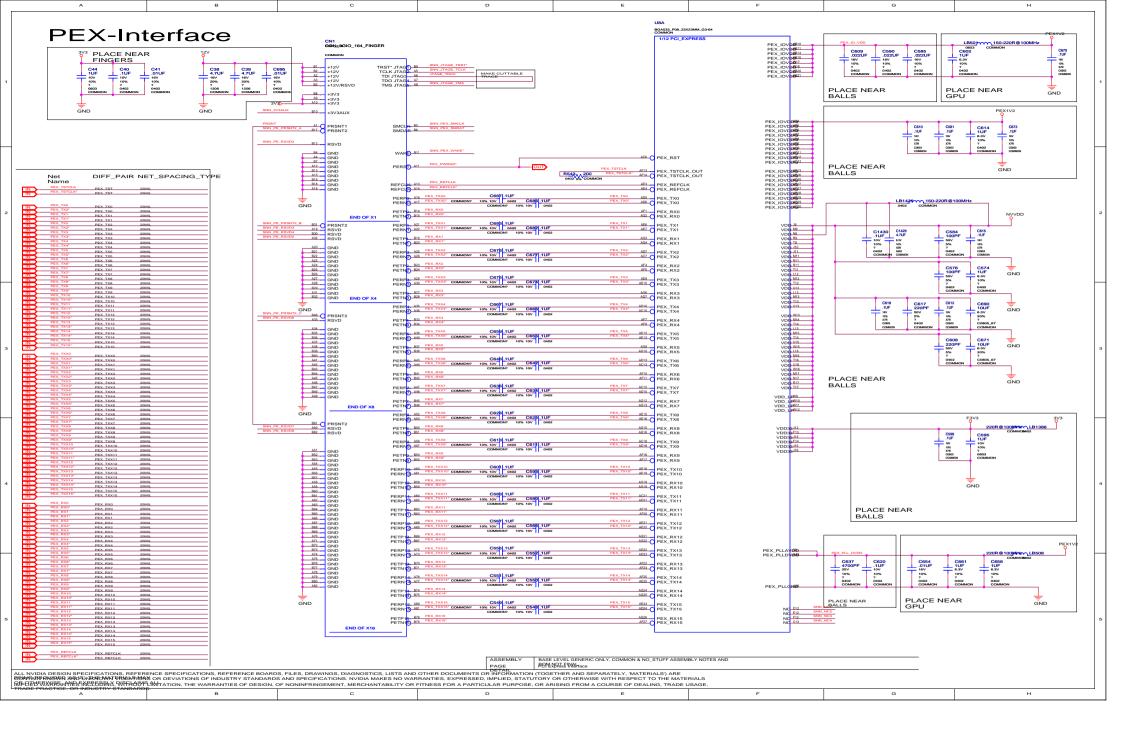
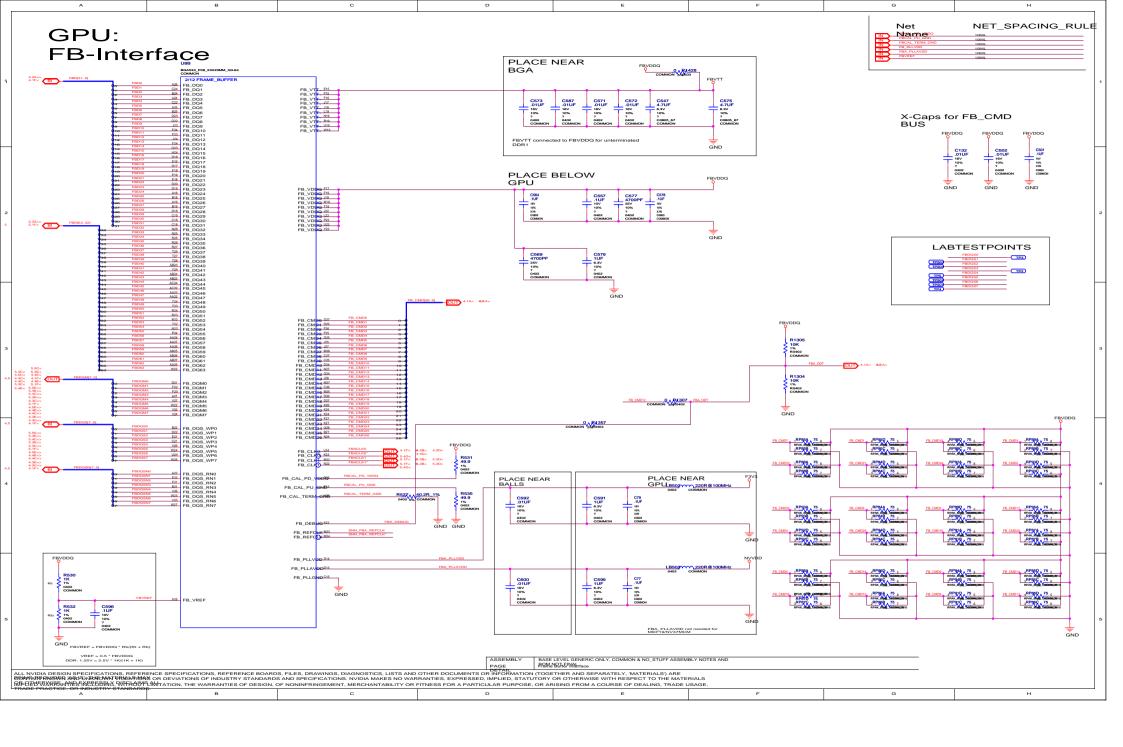
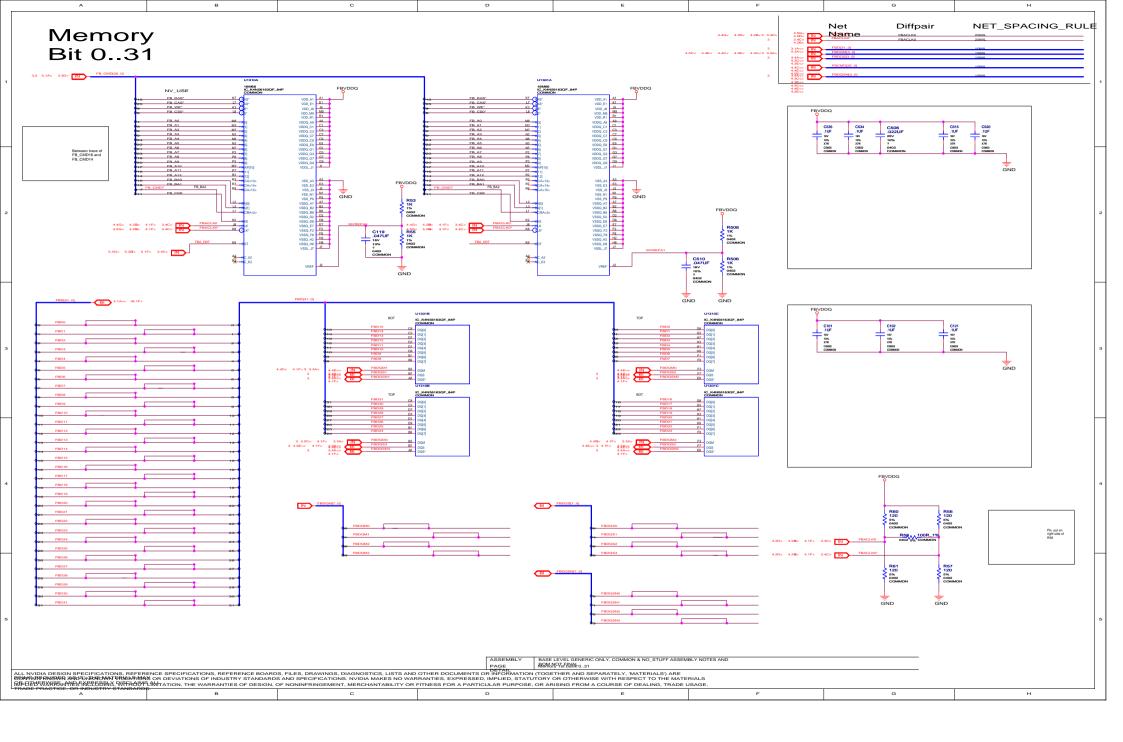
V034-1A NV44 128 MB DDR2, VGA, DVI-I, SD/H良武 HISTORY 01. Cover page 0A base on v001 1.1 modify 02. PEX interface removed TSOP-66 DDR change to BGA-84 DDR2. 03. GPU FB-interface add 2nd RT9218 for FBVDD/Q. 04. MEMORY Bit 0..31 05. MEMORY Bit 32..64 A01 add APL5331 for FBVTT. 10 removed RT9218 and VTT. 06. DAC-A, DB15 Con. 07. DAC-B, MUX, DB15 add op+mos circuit. - 9/22/2004: and cost down 0402 parts change to 0603. 08. Internal TMDS Changed TMDS\_PLLVDD to PLLVDD Added bead option to PLLVDD rail of NV44 11 pin header change to box header for samsung request. 09. MIOA. MIOB 10. Straps 11. BIOS, GPIO, FAN Con. 12. Mini DIN Changed RSET values for DACA and PACE THE PAC 13. Power :NVVDD/FBVDD Added F3V3 bypass to PLLVDD linear regulator 14. Power others Changed location of R62 ASSEMBLY 600-f0pp-xxxx-vvv 600-f0262-0000-000 600-f0262-0001-000 600-f0262-0002-000 <UNDEFINED> <UNDEFINED> ASSEMBLY
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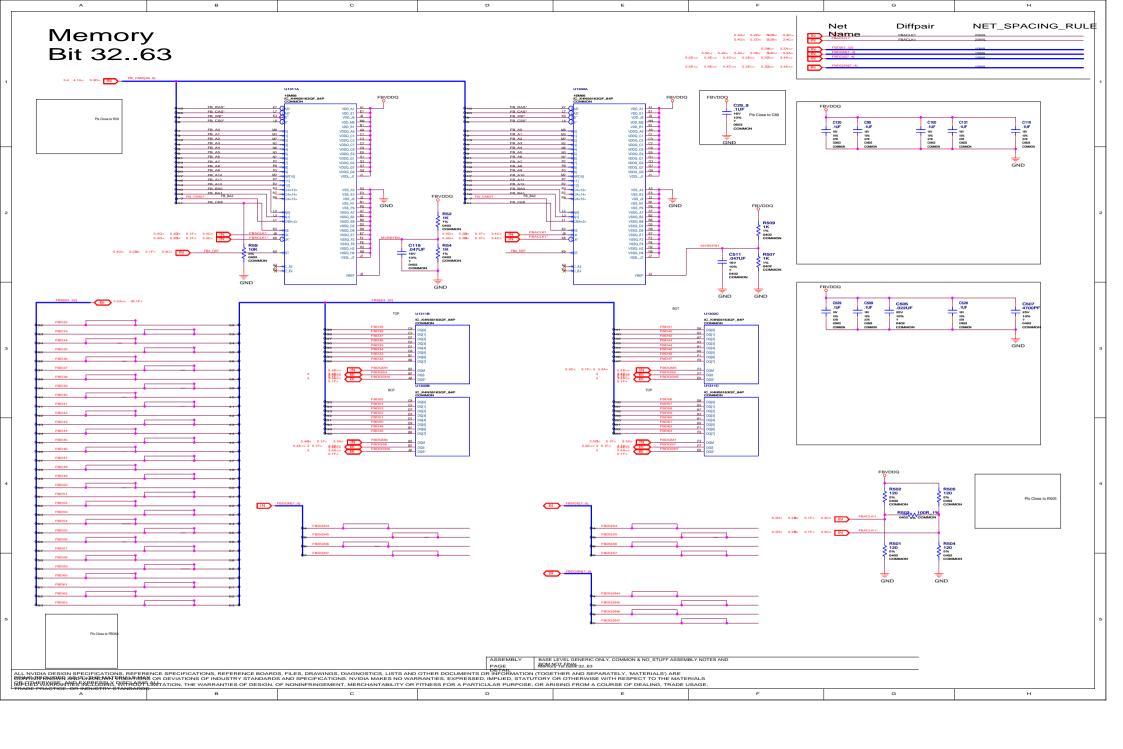
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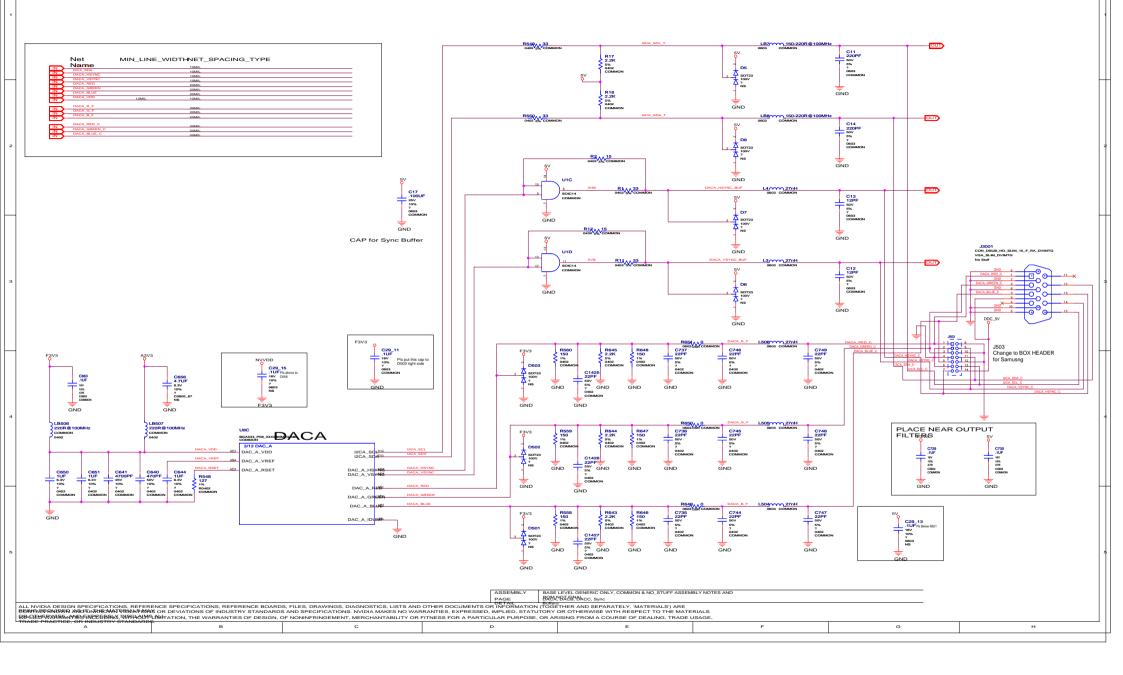


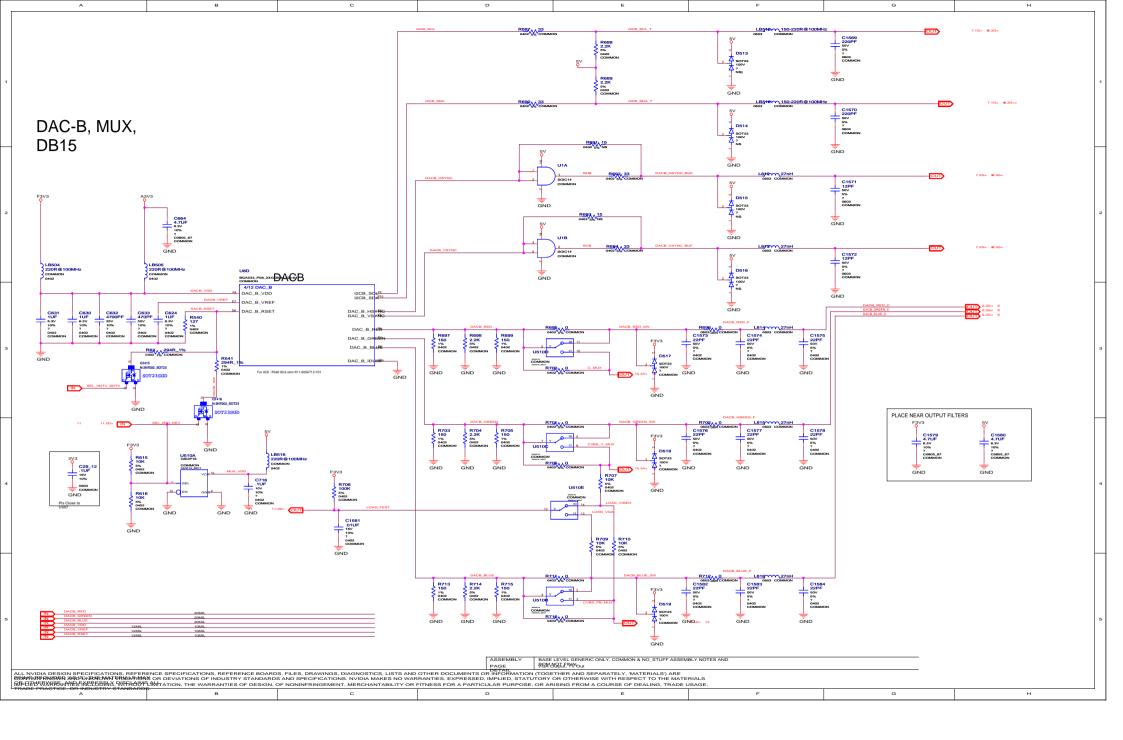


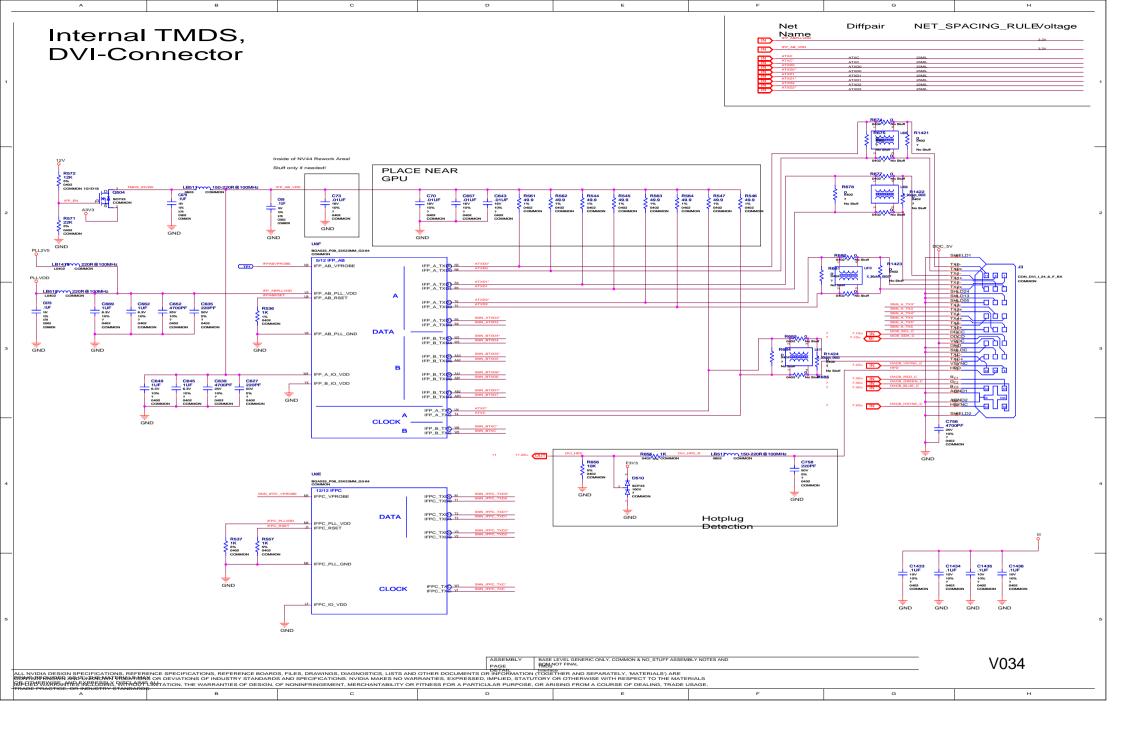


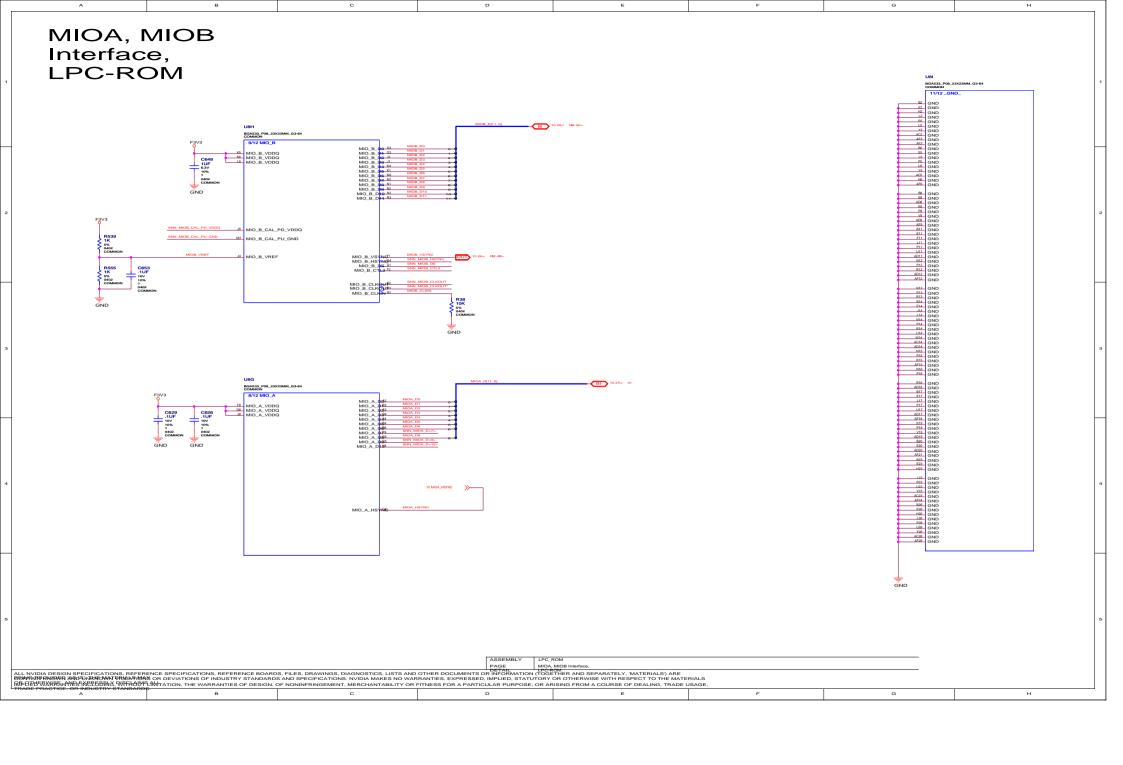


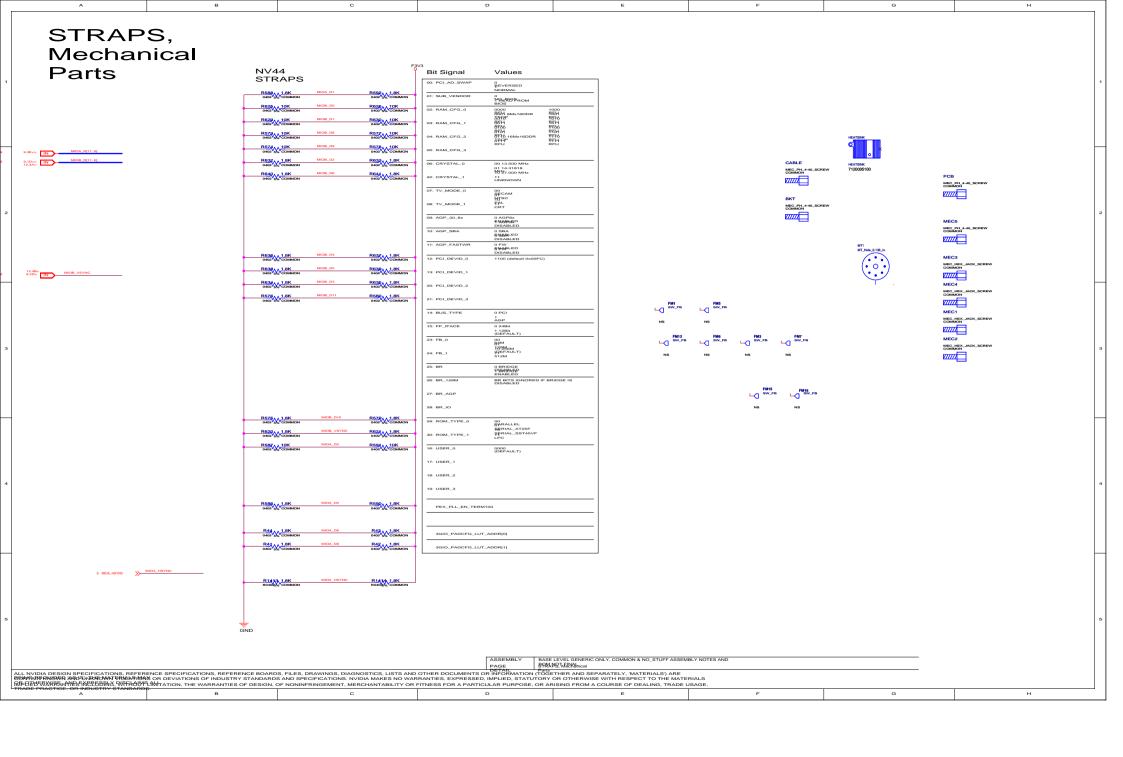
## DAC-A, DB15 Connector

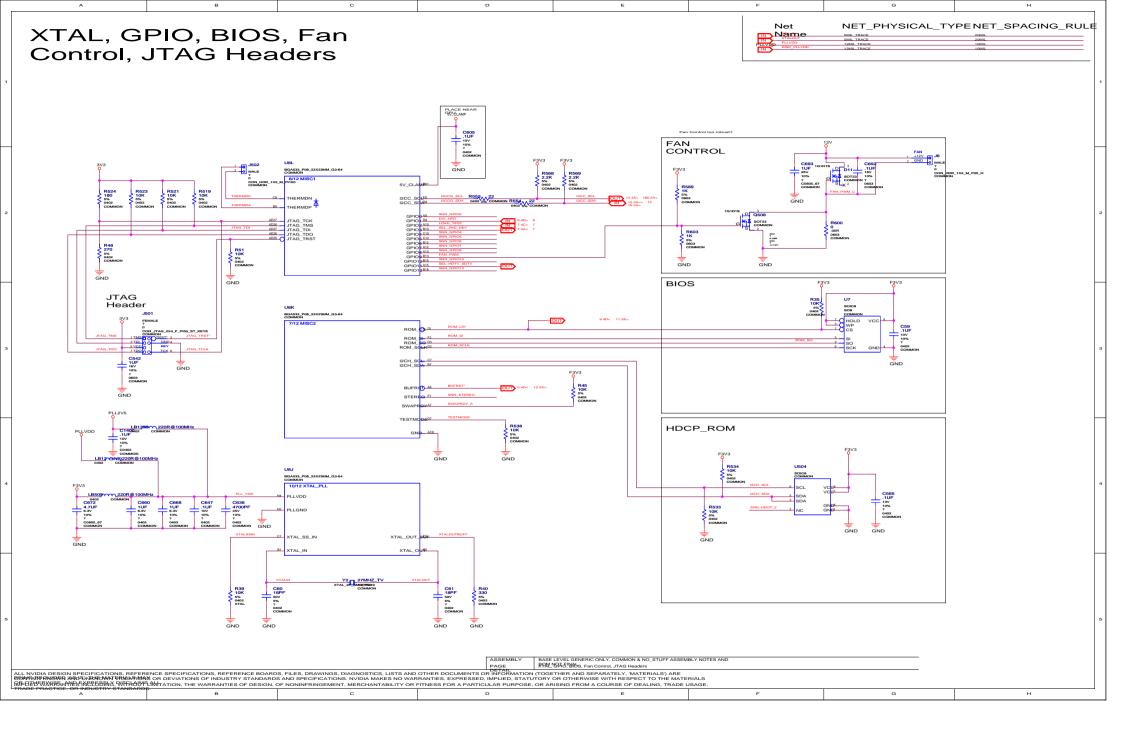


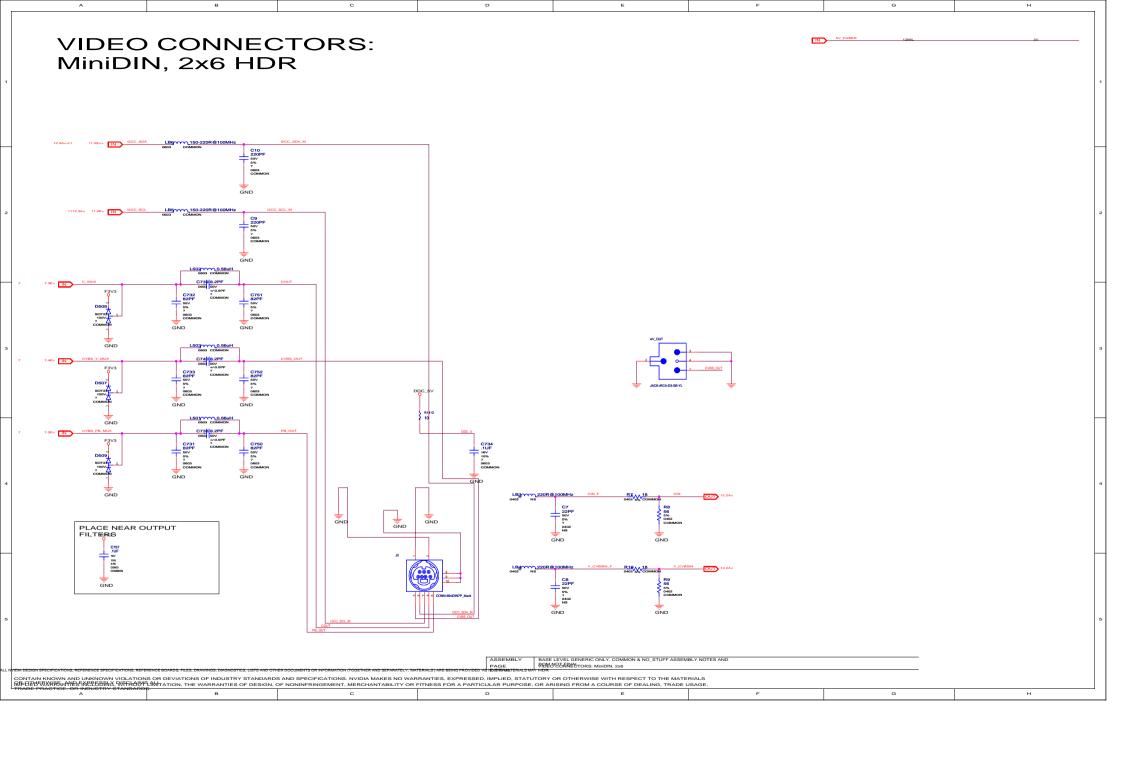


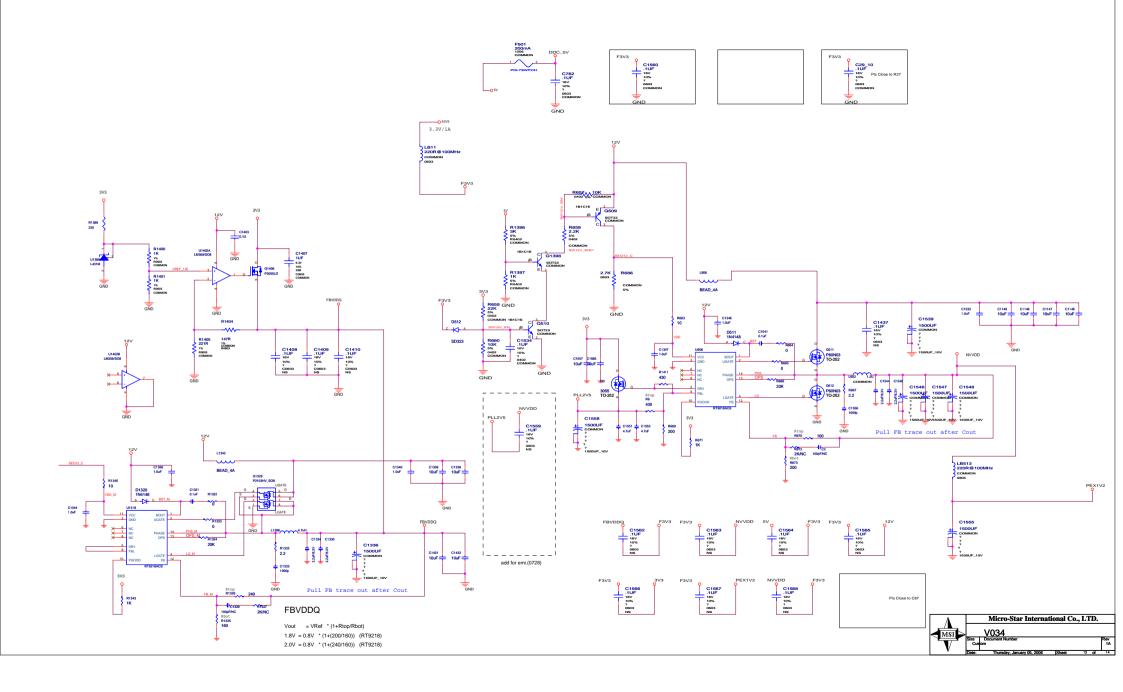




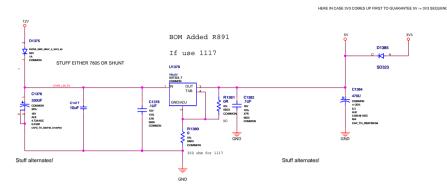




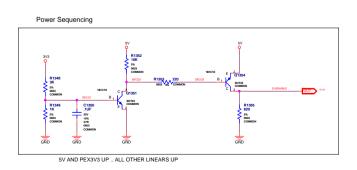




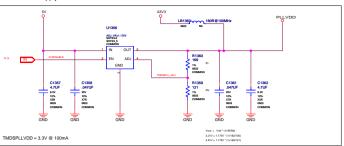
# 14 Others Power Supply (Linears) 5V,FBVDDQ,A3V3,3V3,TMDS\_PLLVDD,TMDS\_IOVDD,FBVTT



### **5V LOW COST REGULATOR**



#### TMDS PLL Supply



#### A3V3 Power Supply

