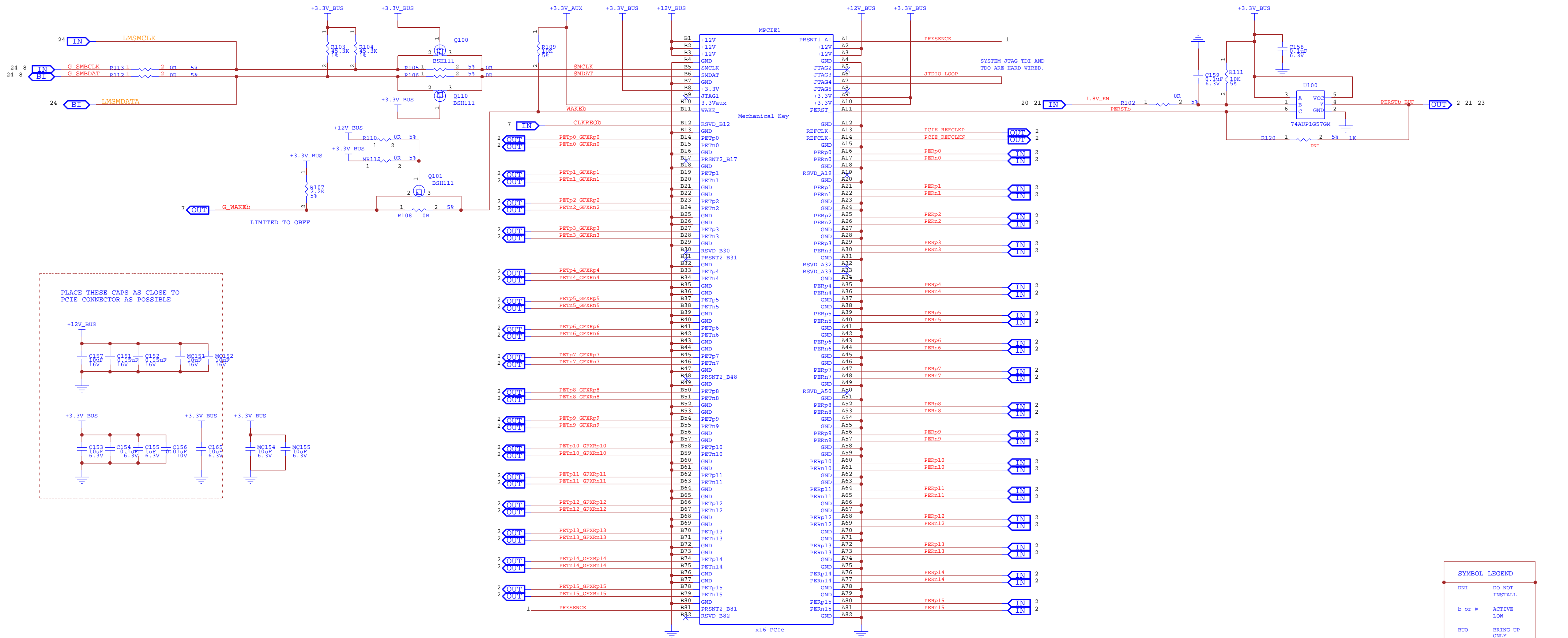


(1) PCI-EXPRESS EDGE CONNECTOR



AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: PCIE EDGE CONNECTOR

DATE: Mon Sep 19 17:21:37 2016

SHEET NUMBER: 1 OF 26

DOCUMENT NUMBER: 105_C940XX_00D

REV: 1.0

TITLE:

TITLE

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A



0.8V

1.8V

IN

PERSTD_BUF

AV42

PERSTD

AM25 VDD_18

AN25 VDD_18

AN26 VDD_18

AP26 VDD_18

AP27 VDD_18

AR27 VDD_18

MC141 40uF

C141 40uF

C136 6.3V

C139 6.3V

C140 6.3V

C137 1uF

C138 6.3V

C30 6.3V

C133 6.3V

C145 6.5V

C146 1uF

C147 1uF

C150 6.3V

C1284 1uF

C1319 6.3V

C161 6.3V

C162 6.3V

C163 6.3V

C160 10uF

C148 10uF

MC148 4uF

AA34 VDD_08

AA35 VDD_08

AD36 VDD_08

AE35 VDD_08

AF35 VDD_08

AG36 VDD_08

AJ35 VDD_08

AK35 VDD_08

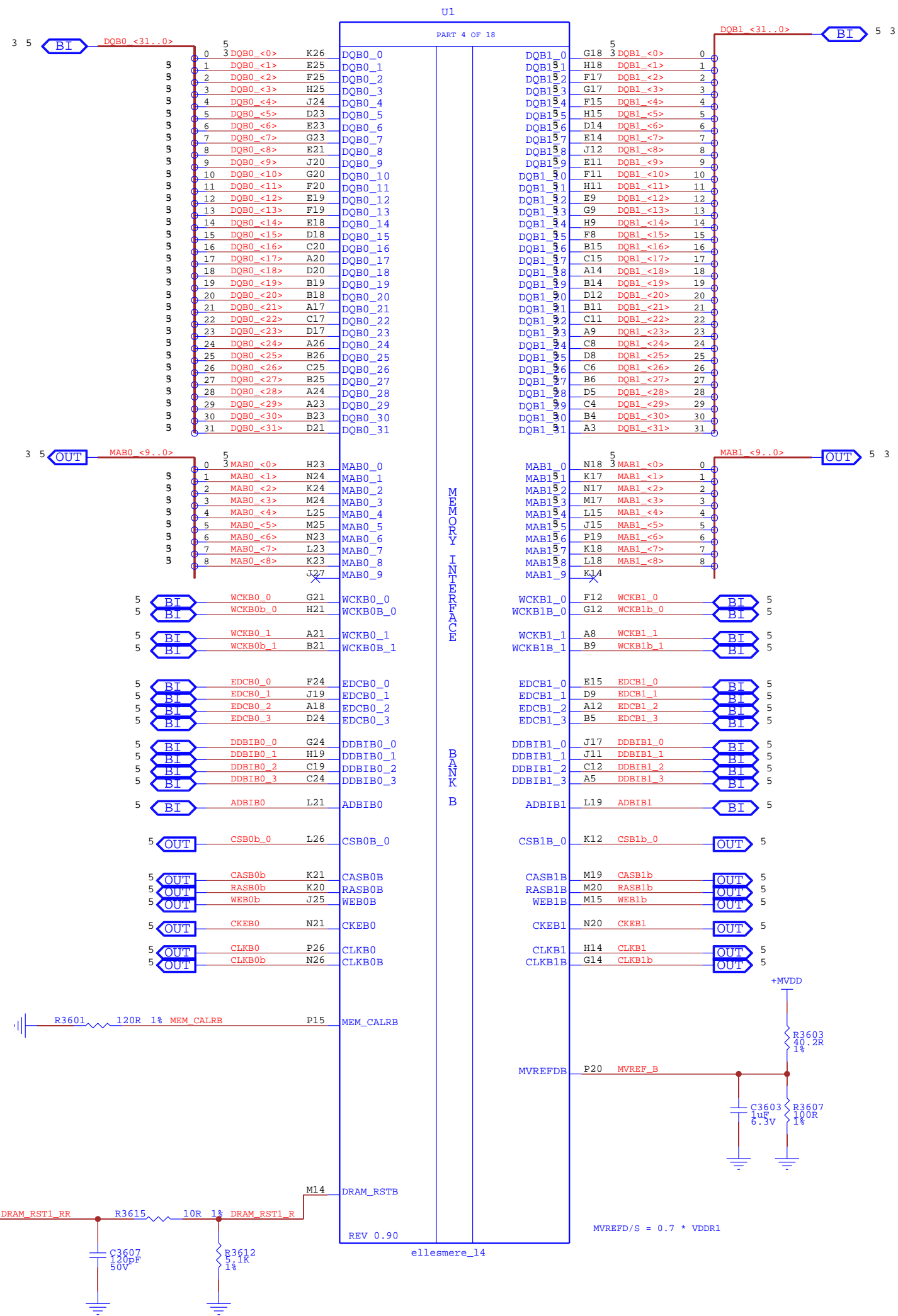
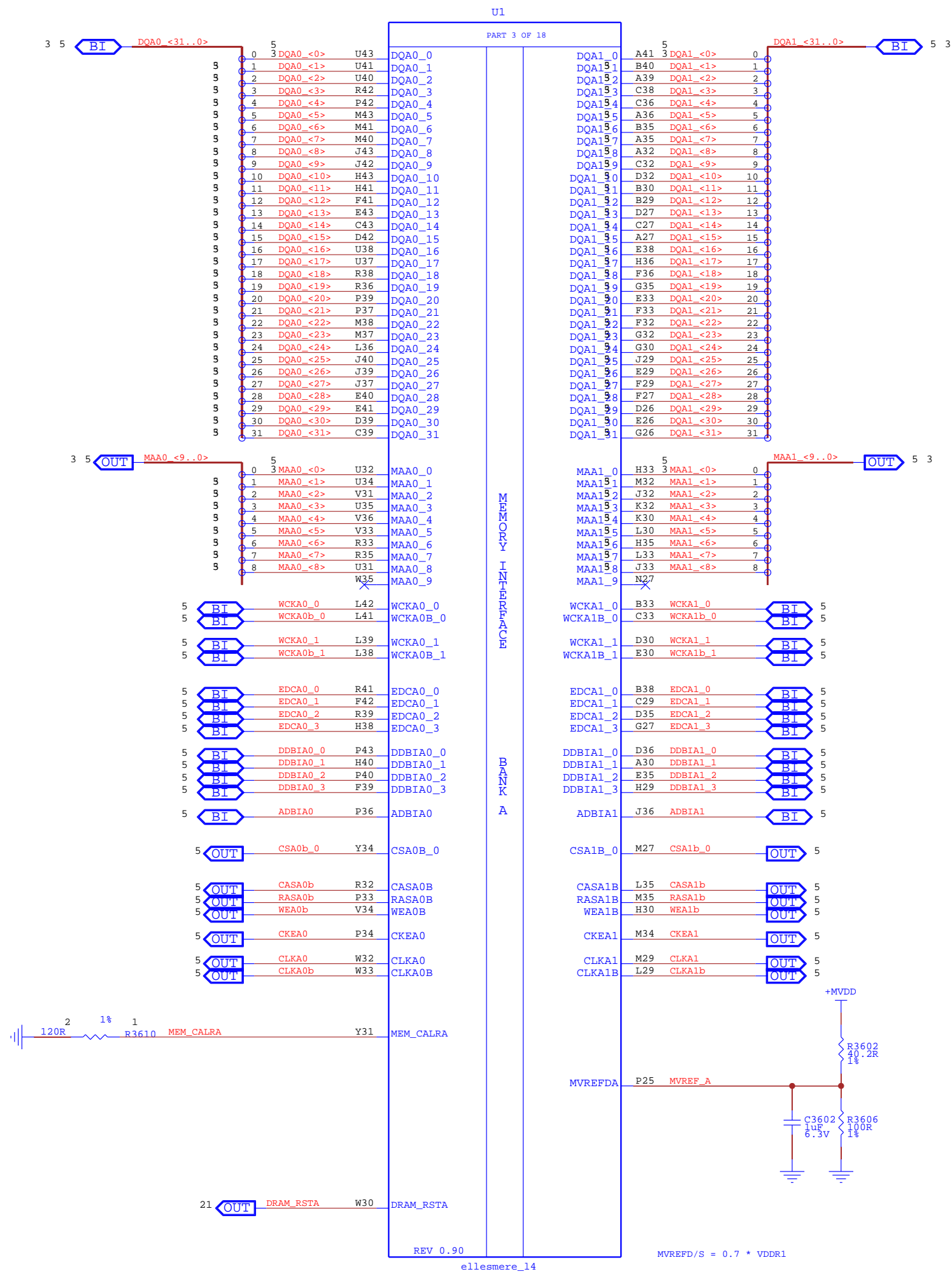
AM36 VDD_08

AN35 VDD_08

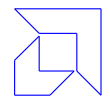


TITLE

(3) ELLESMERE MEM INTERFACE CH A/B



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SHEET: ELLESMERE MEM CH AB

DATE: Mon Sep 19 17:21:47 2016

REV: 1.0

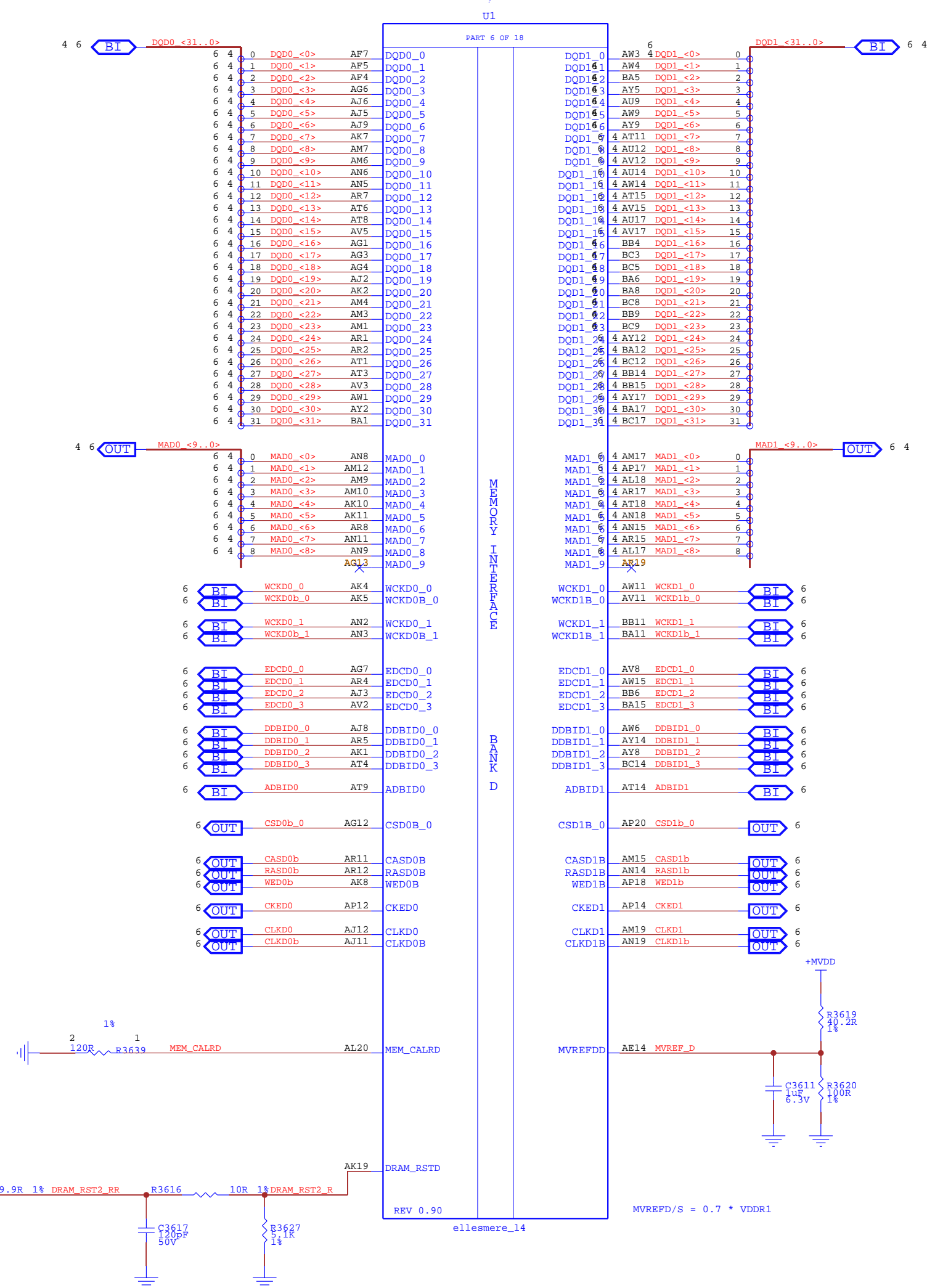
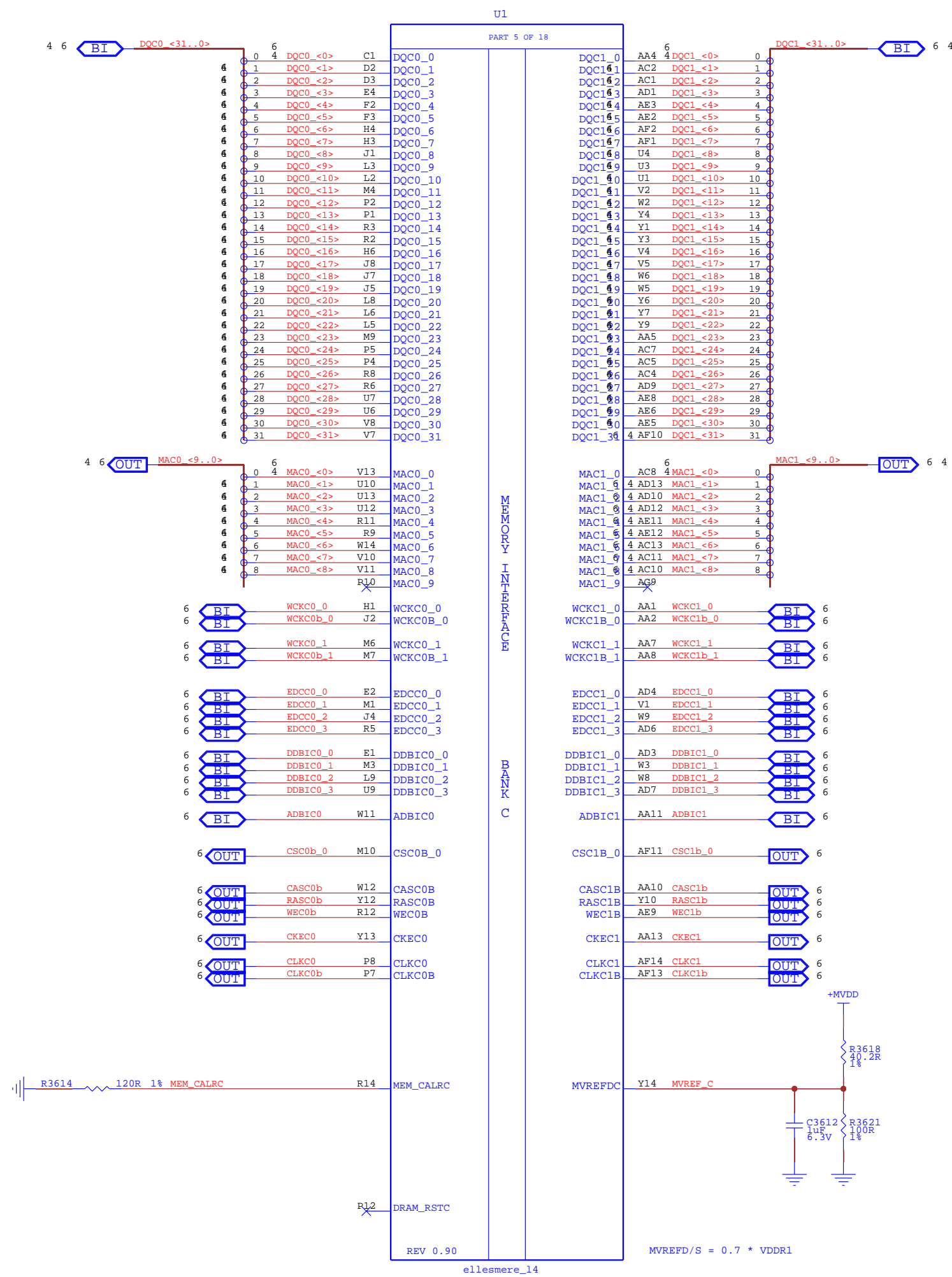
SHEET NUMBER: 3 OF 26

TITLE:

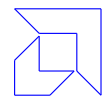
DOCUMENT NUMBER: 105_C940XX_00D

TITLE

(4) ELLESMERE MEM INTERFACE CH C/D



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SHEET: ELLESMERE MEM CH CD

DATE: Mon Sep 19 17:21:48 2016

REV: 1.0

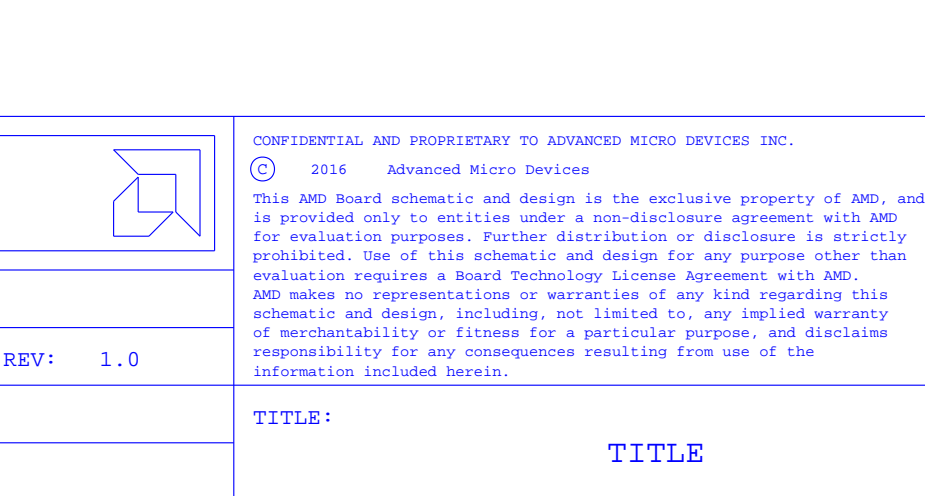
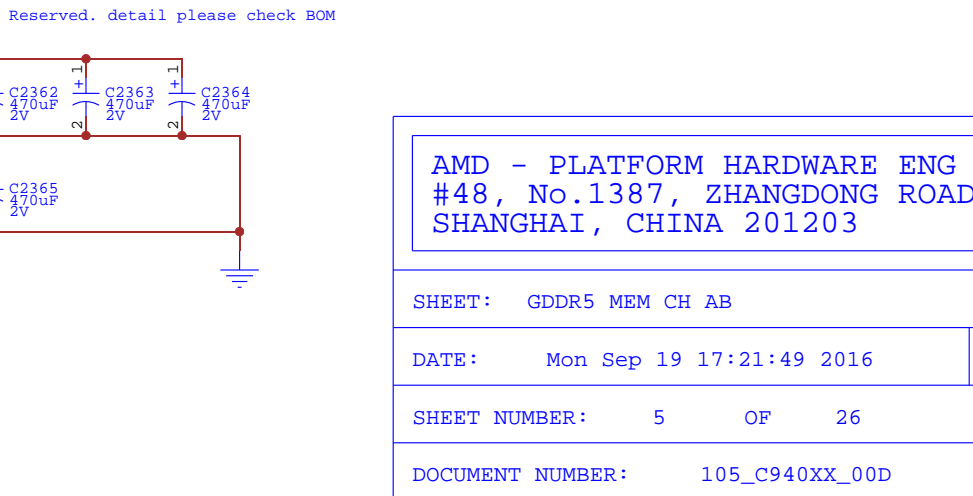
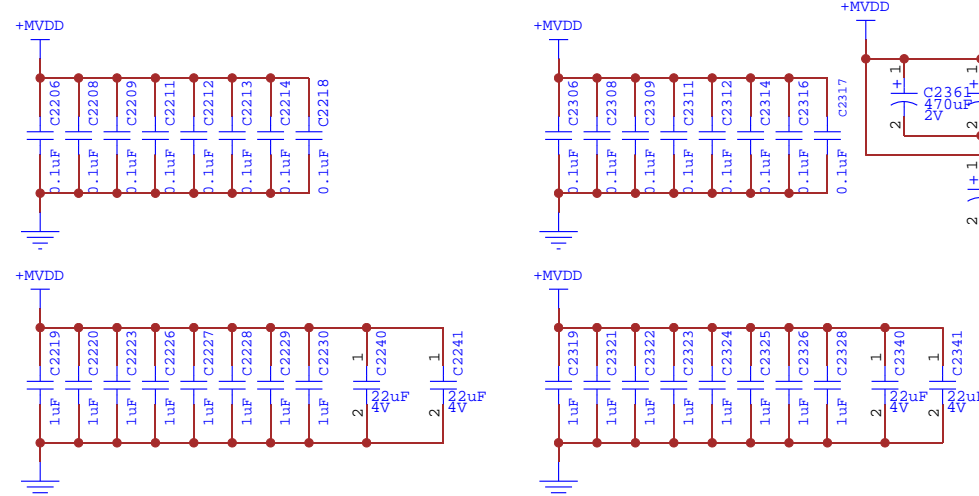
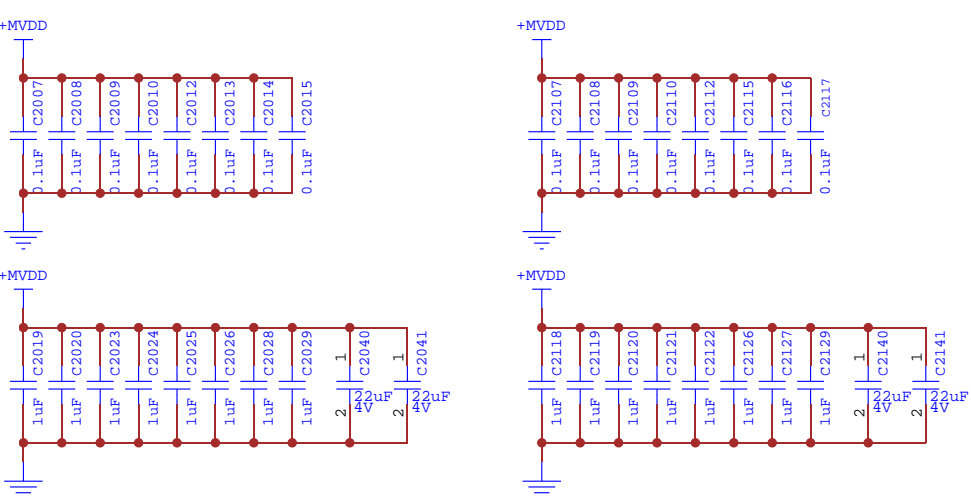
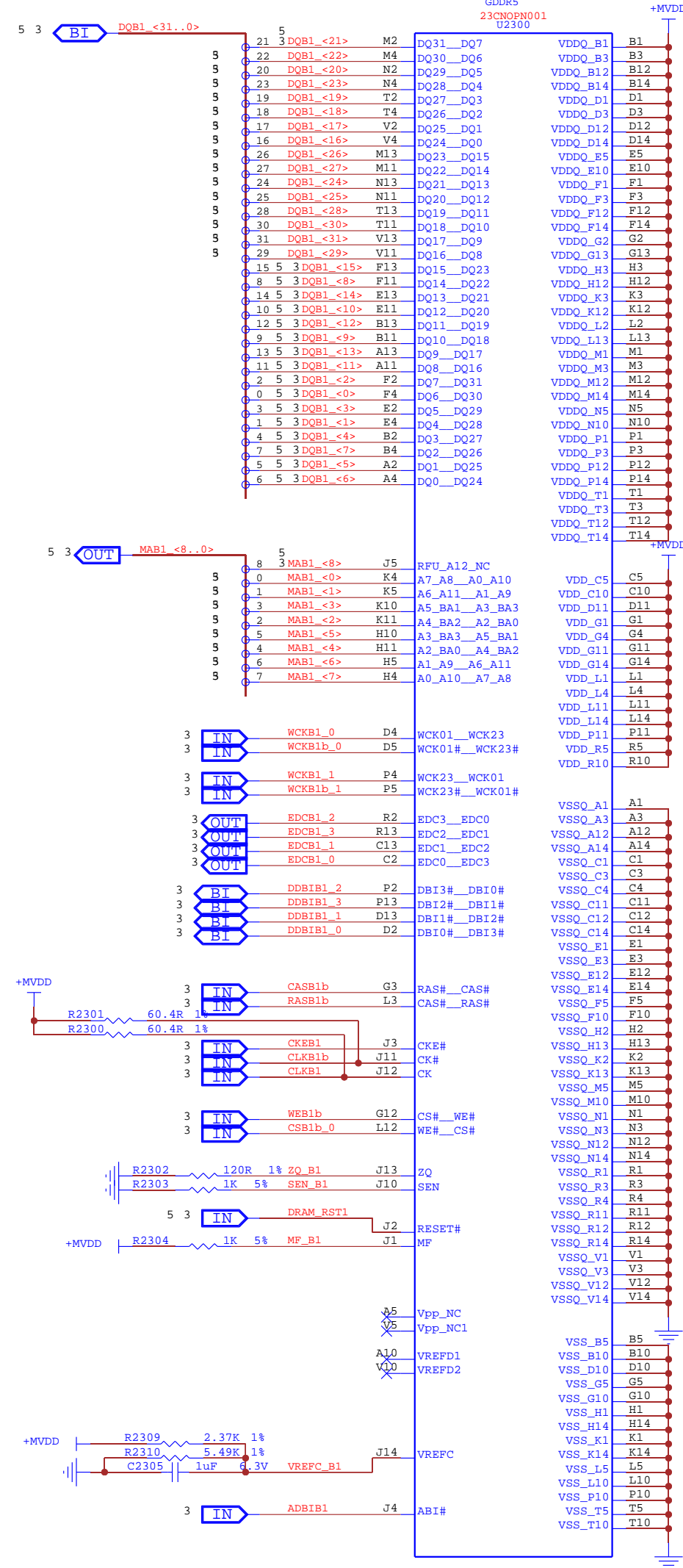
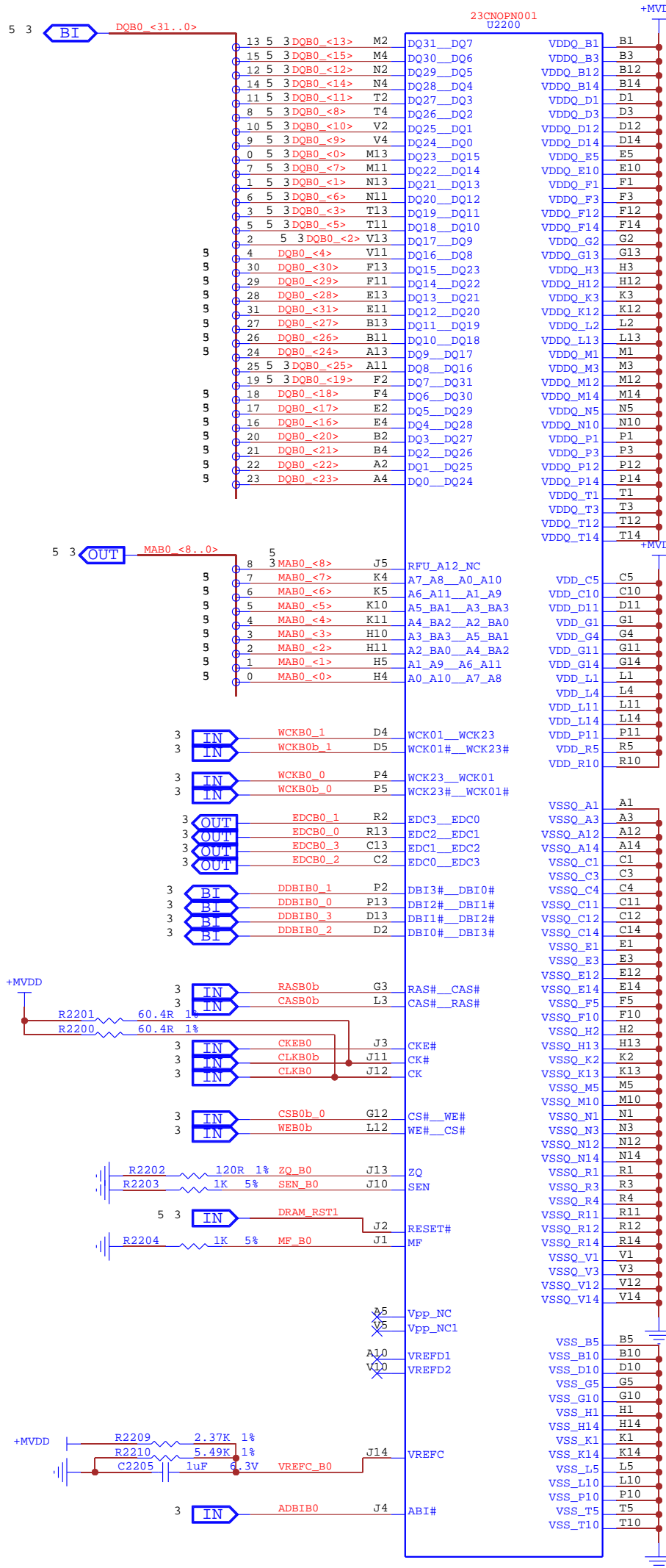
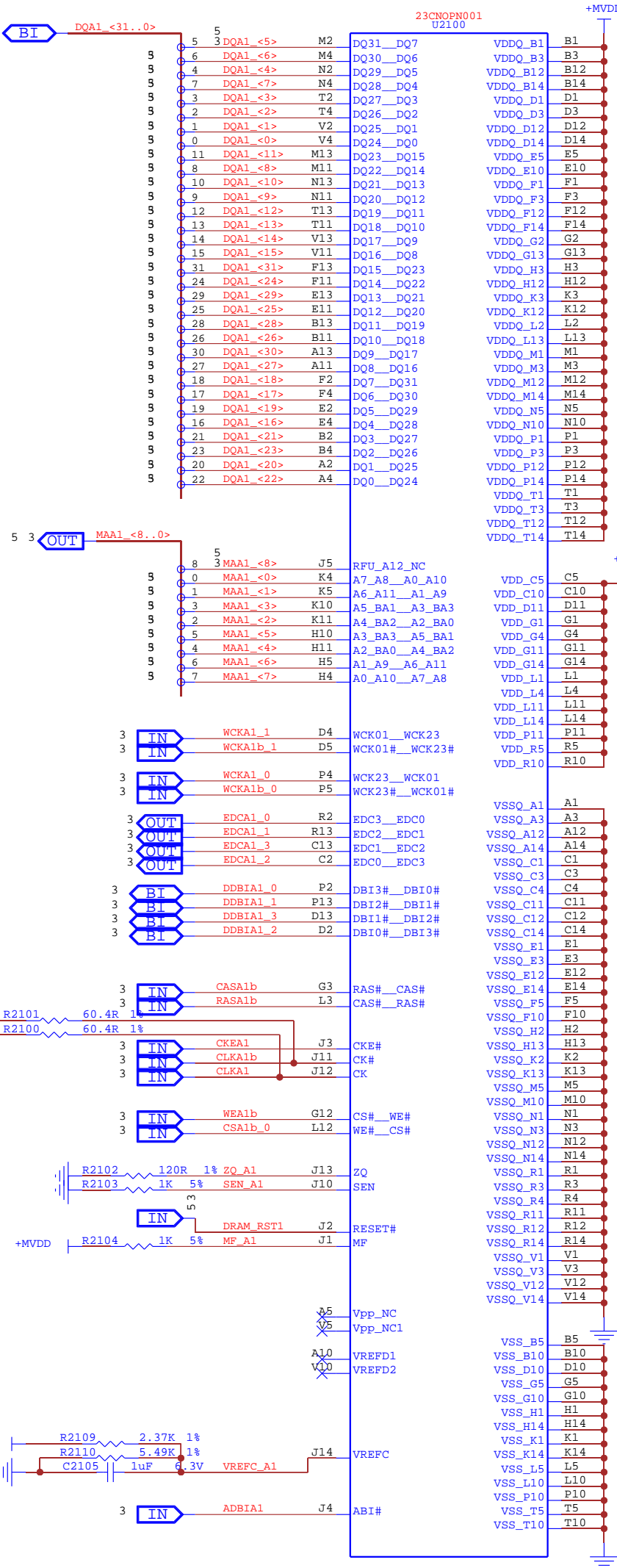
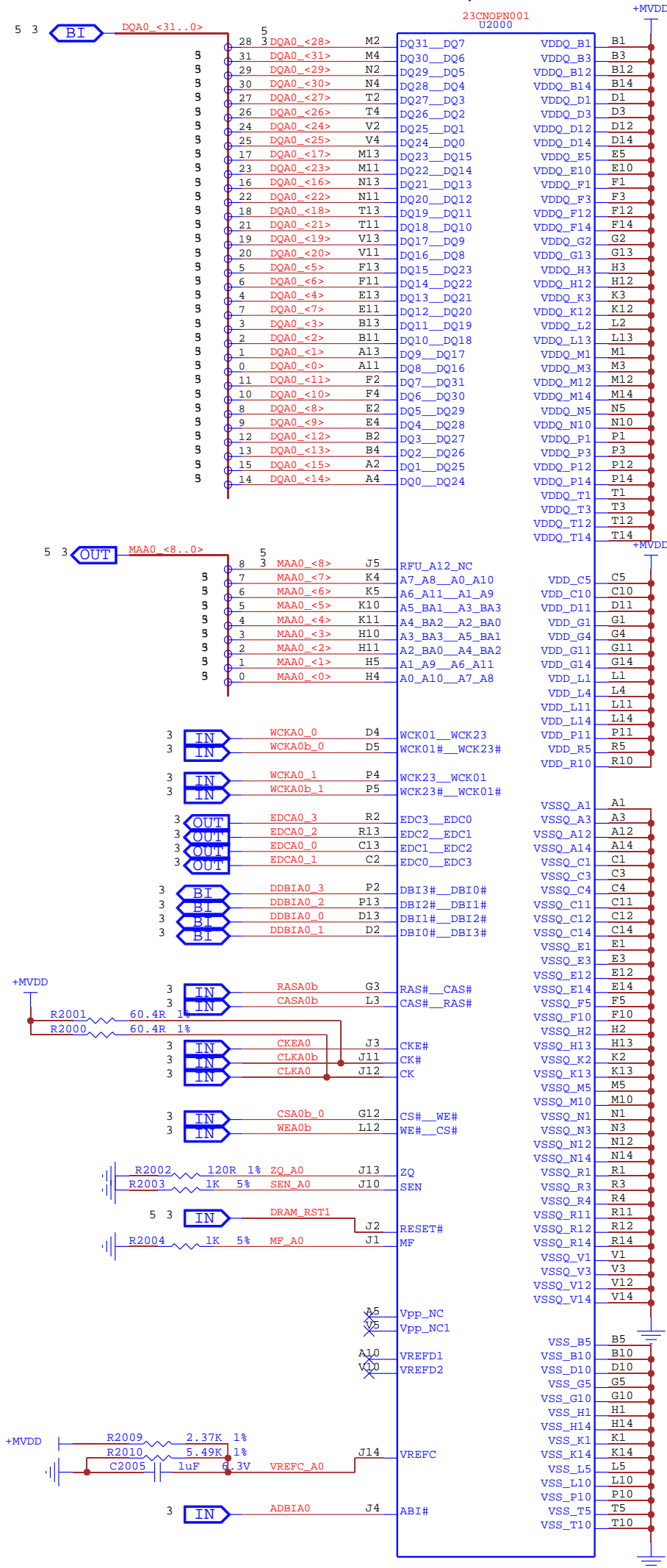
SHEET NUMBER: 4 OF 26

TITLE:

TITLE

DOCUMENT NUMBER: 105_C940XX_00D

(5) GDDR5 MEMORY CH A/B



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SHANGHAI, CHINA 201203

SHEET: GDDR5 MEM CH AB

DATE: Mon Sep 19 17:21:49 2016

REV: 1.0

SHEET NUMBER: 5 OF 26

DOCUMENT NUMBER: 105_C940XX_00D

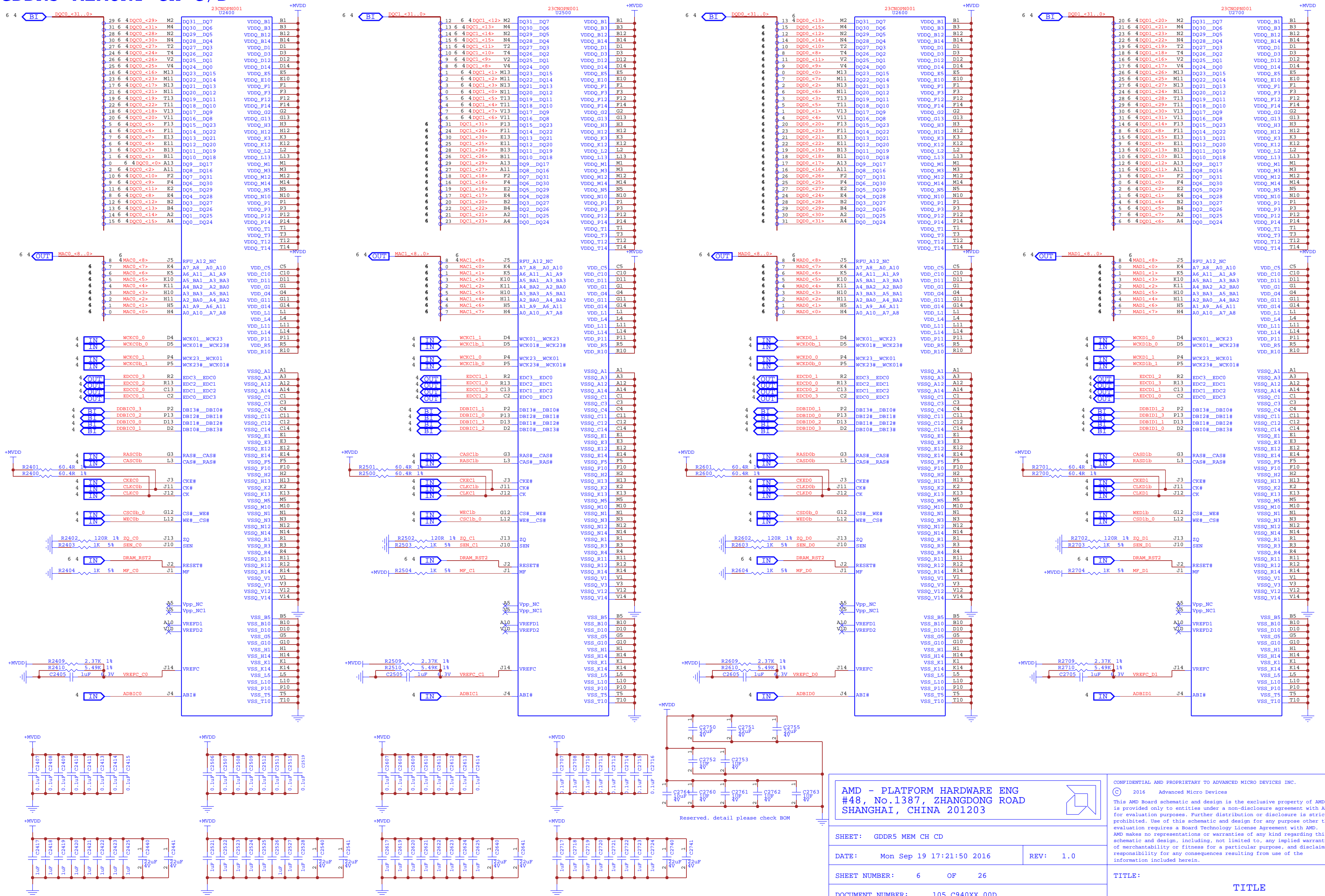
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TITLE

(6) GDDR5 MEMORY CH C/D



D



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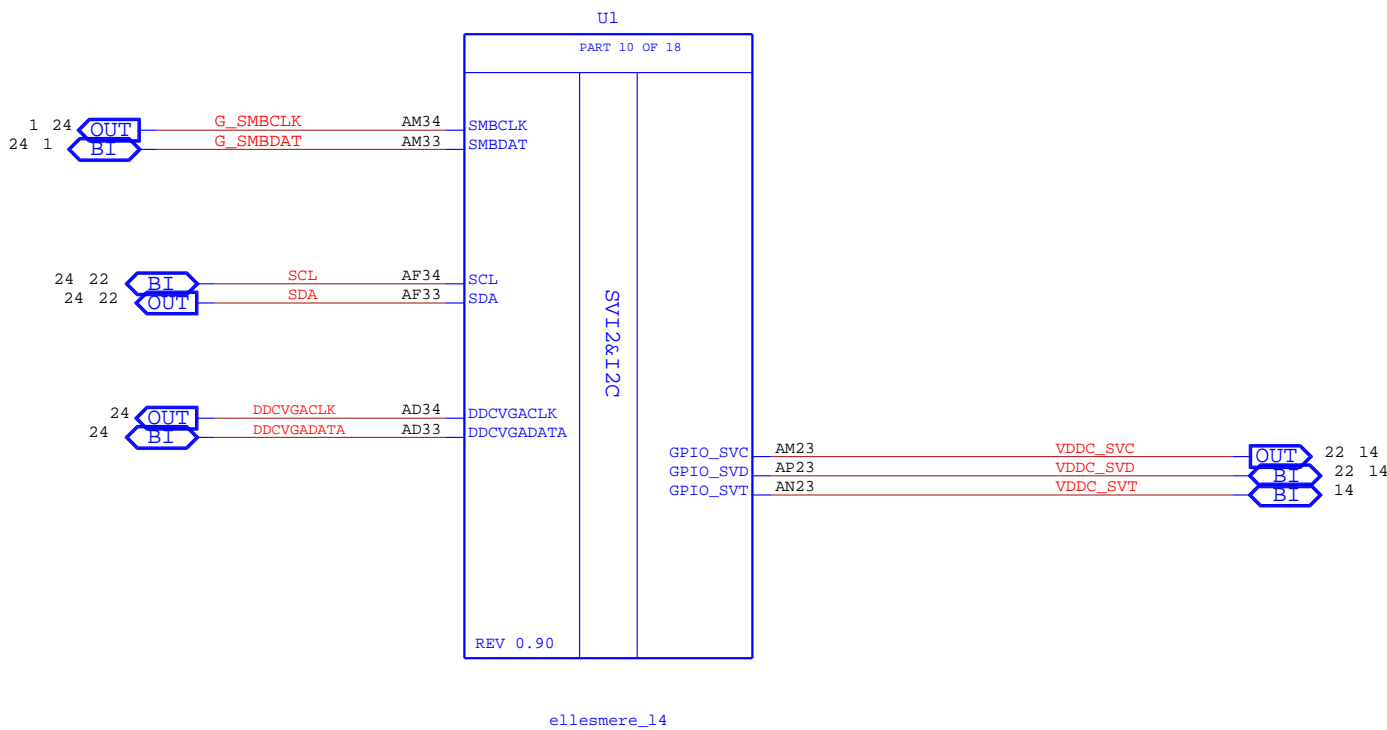
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TITLE:

TITLE

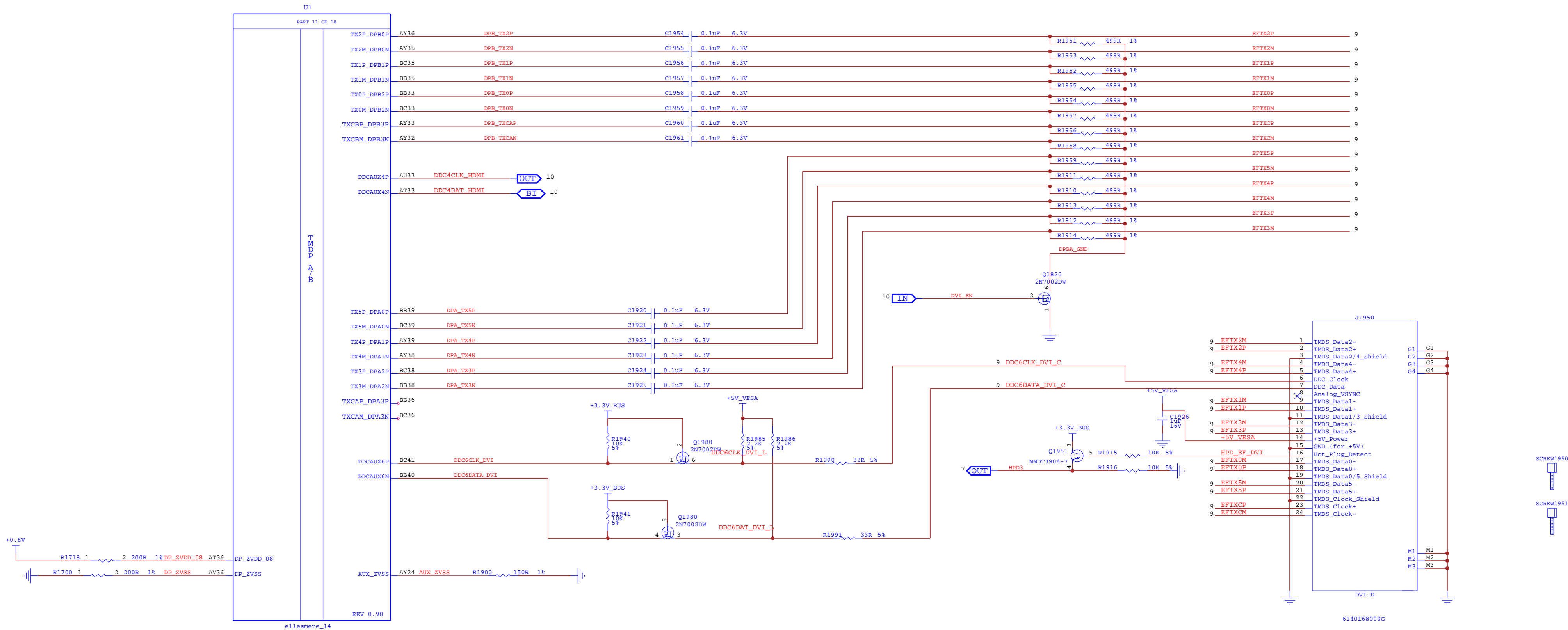
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(8) ELLESMERE DAC1 LOCK

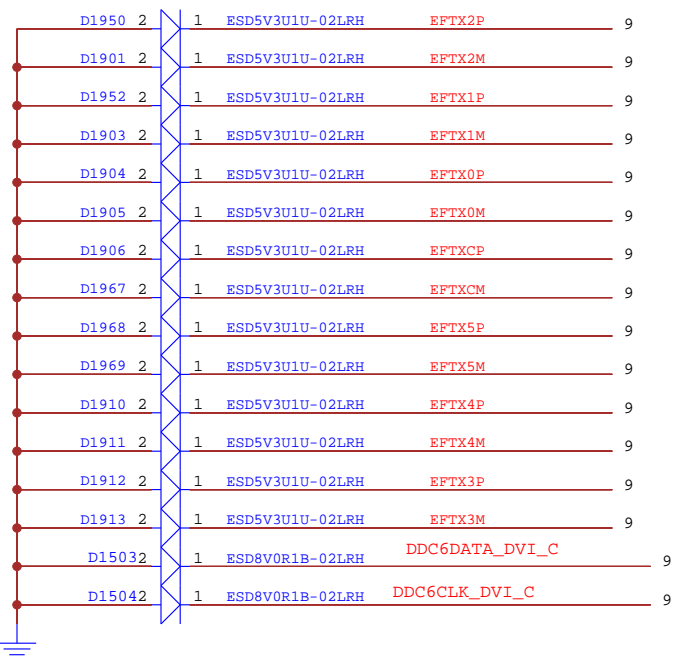


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SHEET: ELLESMERE SV12&I2C		TITLE:	
DATE: Mon Sep 19 17:21:50 2016	REV: 1.0	TITLE	
SHEET NUMBER: 8 OF 26	DOCUMENT NUMBER: 105_C940XX_00D		

(9) ELLESMERE TMDP A/B



OPTIONAL ESD PROTECTION DIODES



AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203



SHEET: ELLESMERE TMDPAB dDVI

DATE: Mon Sep 19 17:21:40 2016

REV: 1.0

SHEET NUMBER: 9 OF 26

DOCUMENT NUMBER: 105_C940XX_00D

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TITLE:

TITLE

A



D1851

DTXP0 1

2

DTXCAN 3

DTXCAP 4

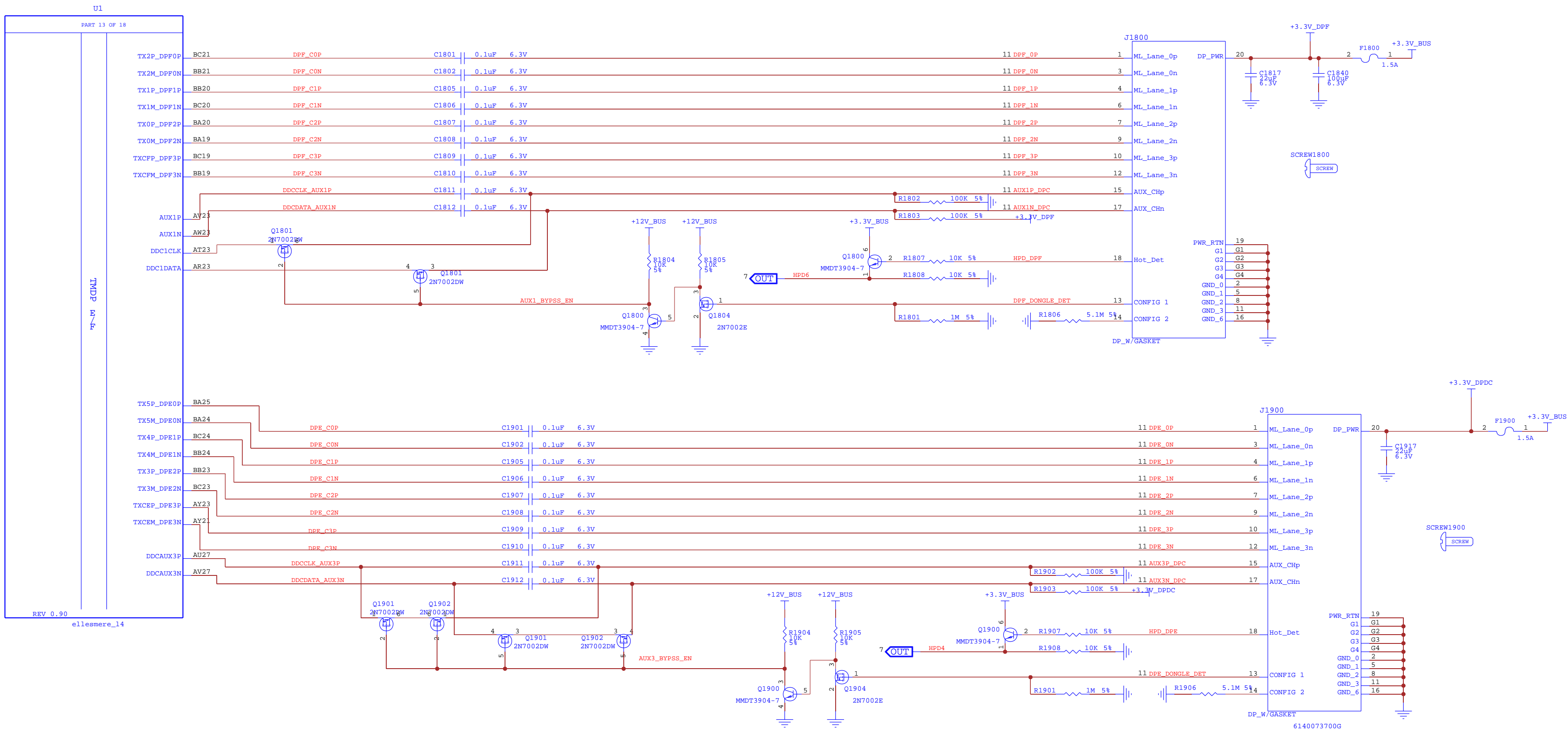
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RCLAMP0564P.TNT

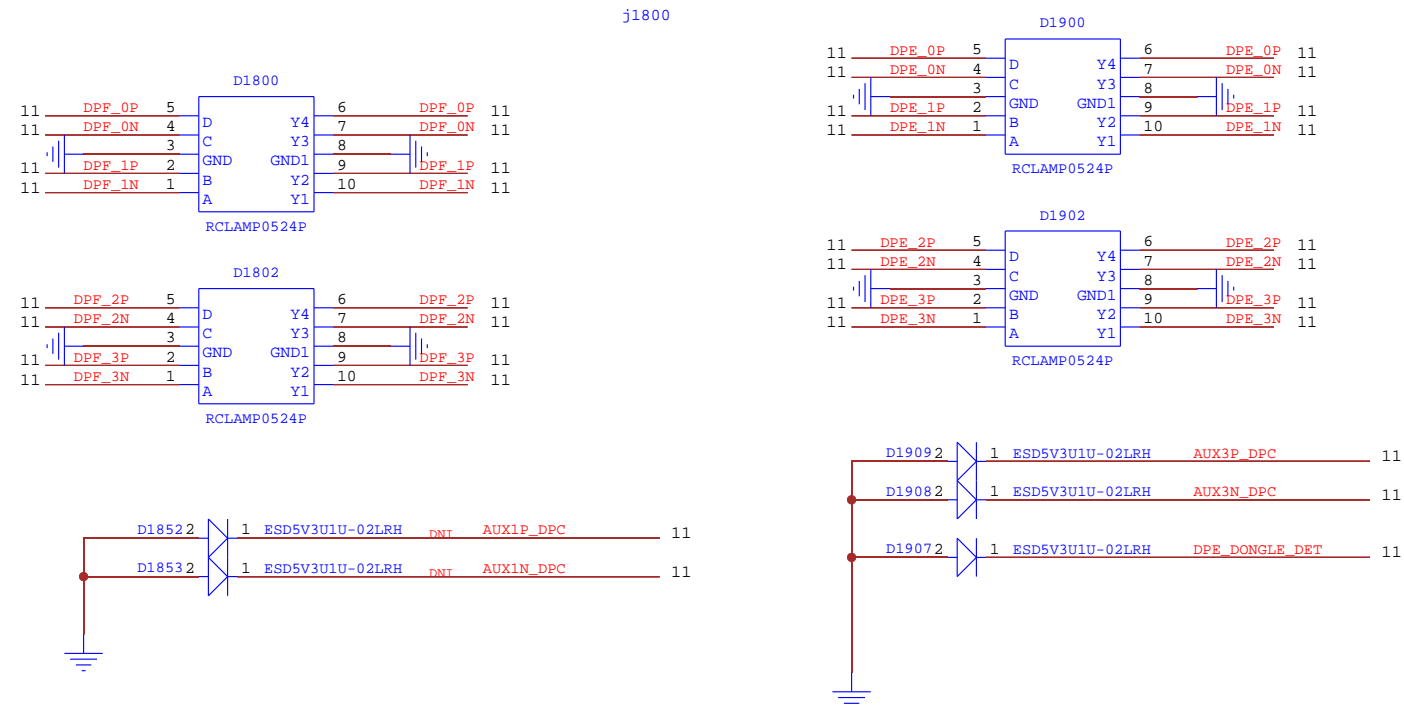
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TITLE

(11) ELLESMERE LVTMDP E/F

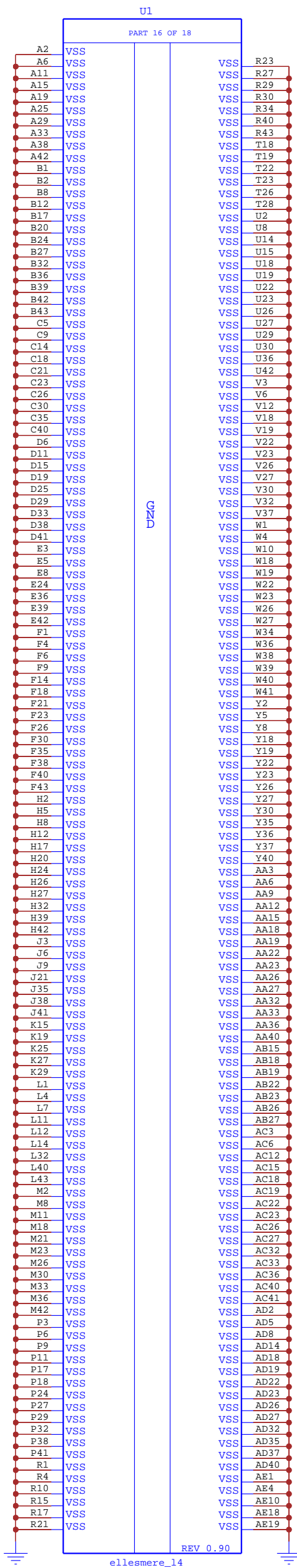


OPTIONAL ESD PROTECTION DIODES

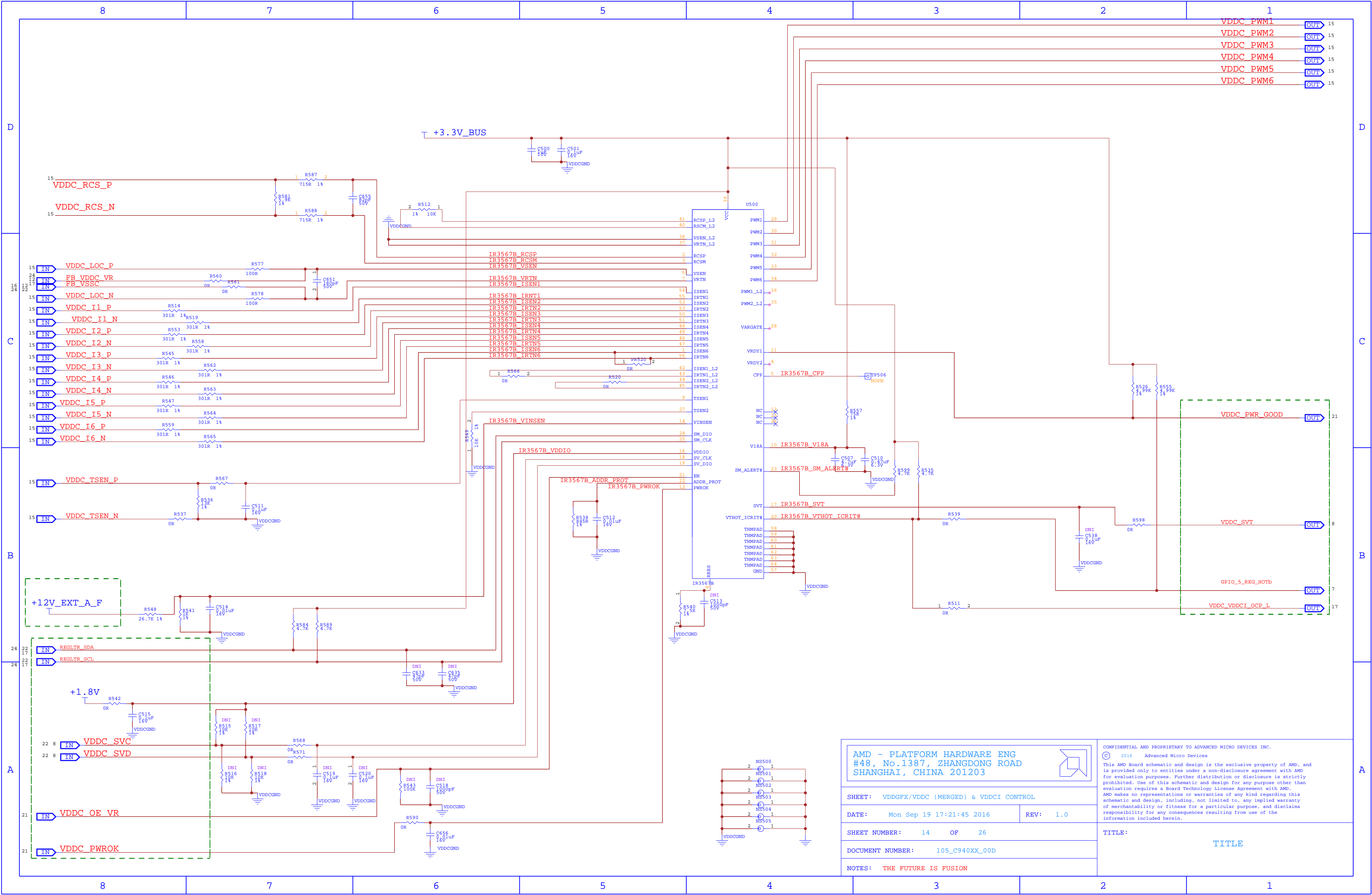


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SHEET: ELLESMERE TMDP DP DP		TITLE:	
DATE: Mon Sep 19 17:21:41 2016		REV: 1.0	
SHEET NUMBER: 11 OF 26		TITLE:	
DOCUMENT NUMBER: 105_C940XX_00D		TITLE:	

(13) ELLESMERE GROUND



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SHEET: ELLESMERE GND		TITLE:	
DATE: Mon Sep 19 17:21:52 2016	REV: 1.0	DOCUMENT NUMBER: 105_C940XX_00D	
SHEET NUMBER: 13 OF 26		TITLE	



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SHANGHAI, CHINA 201203

SHEET: VDDGFX/VDDC (MERGED) & VDDCI CONTROL

DATE: Mon Sep 19 17:21:45 2016

SHEET NUMBER: 14 OF 26

DOCUMENT NUMBER: 105_C940XX_00D

NOTES: THE FUTURE IS FUSION

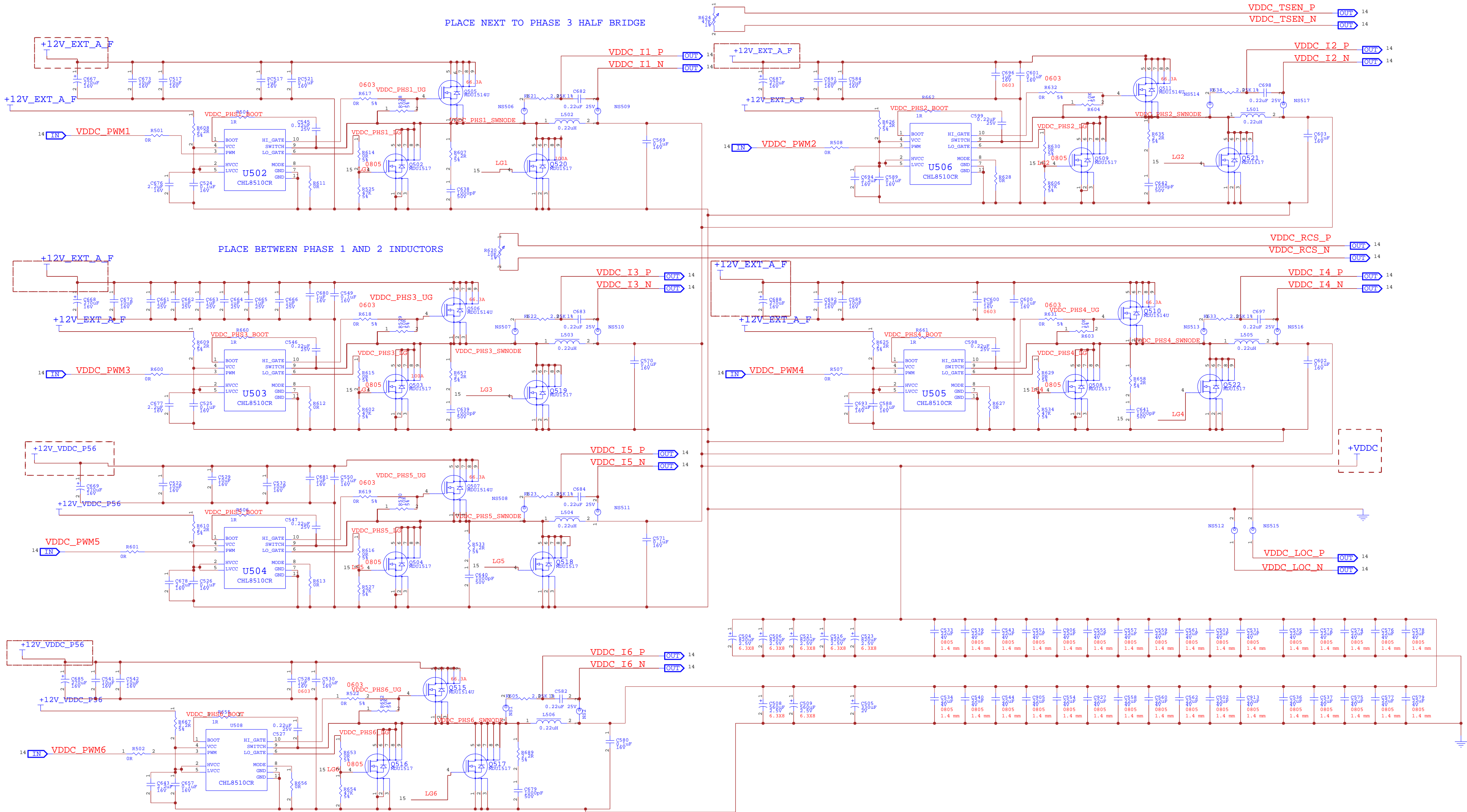
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TITLE: TITLE

VDDC

PLACE NEXT TO PHASE 3 HALF BRIDGE



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SHEET: VDDC

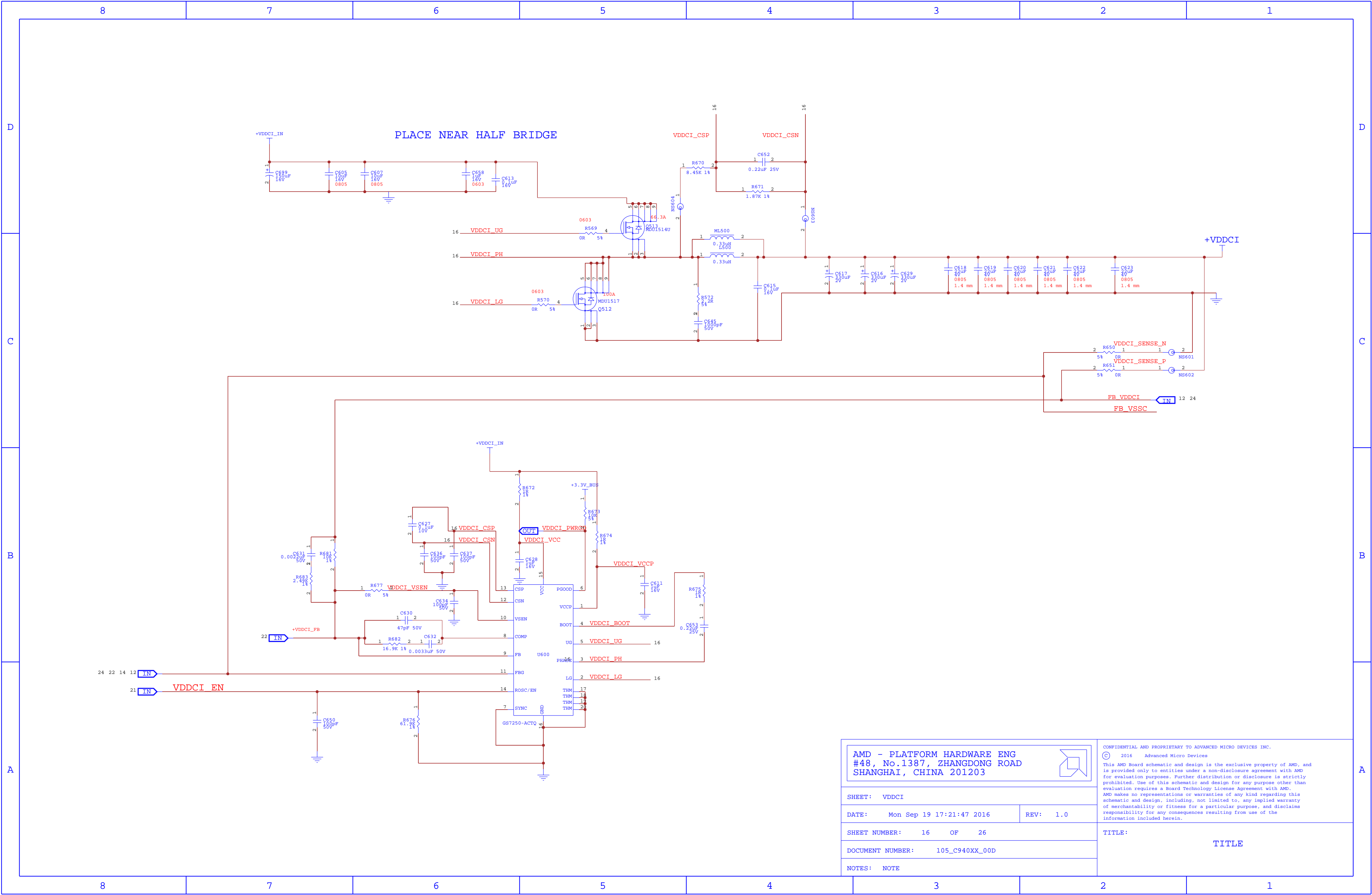
DATE: Mon Sep 19 17:21:46 2016 REV: 1.0

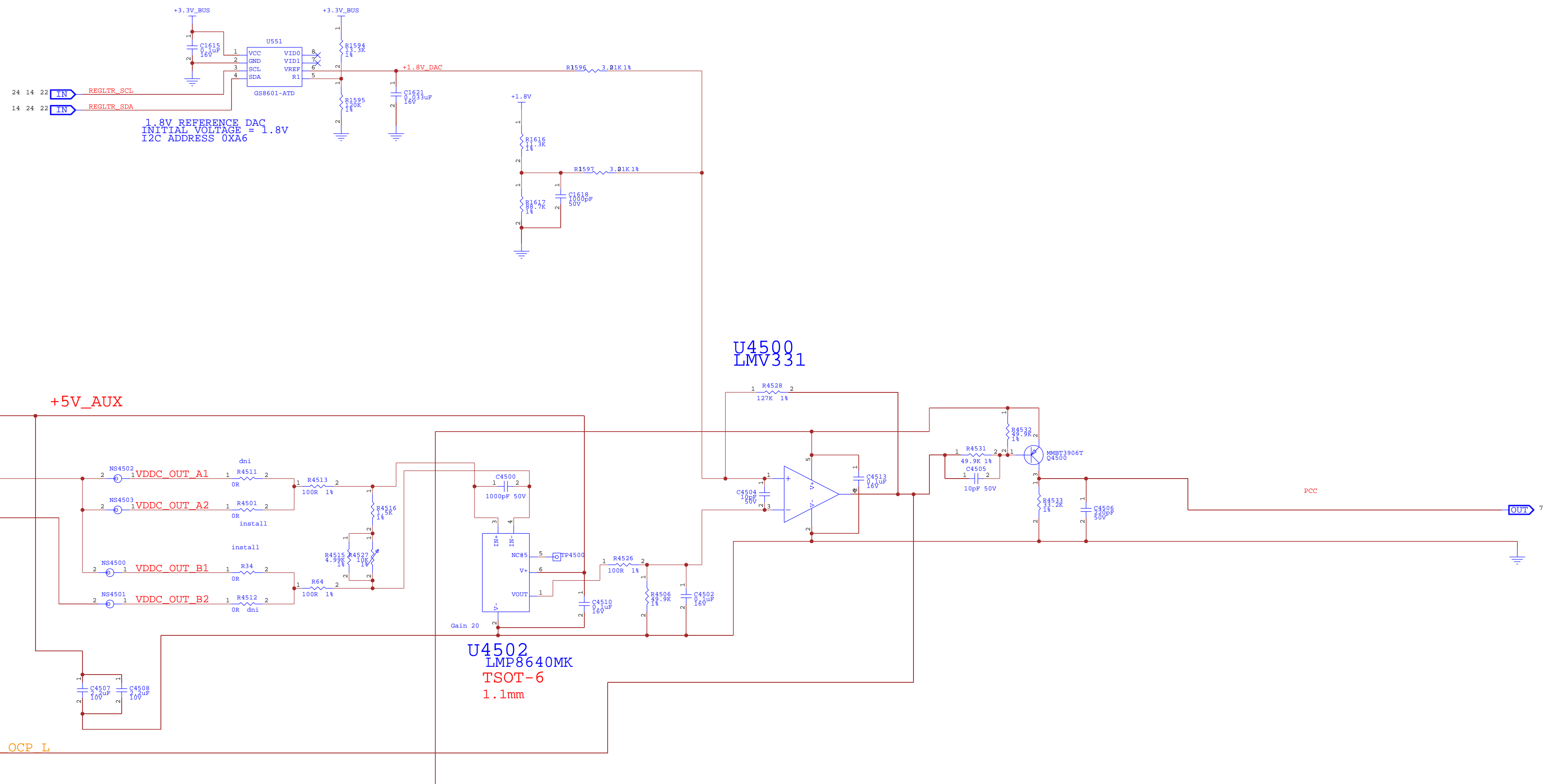
SHEET NUMBER: 15 OF 26

DOCUMENT NUMBER: 105_C940XX_00D

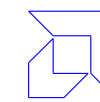
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TITLE: TITLE





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SHEET: PCC VDDC

DATE: Mon Sep 19 17:21:46 2016

REV: 1.0

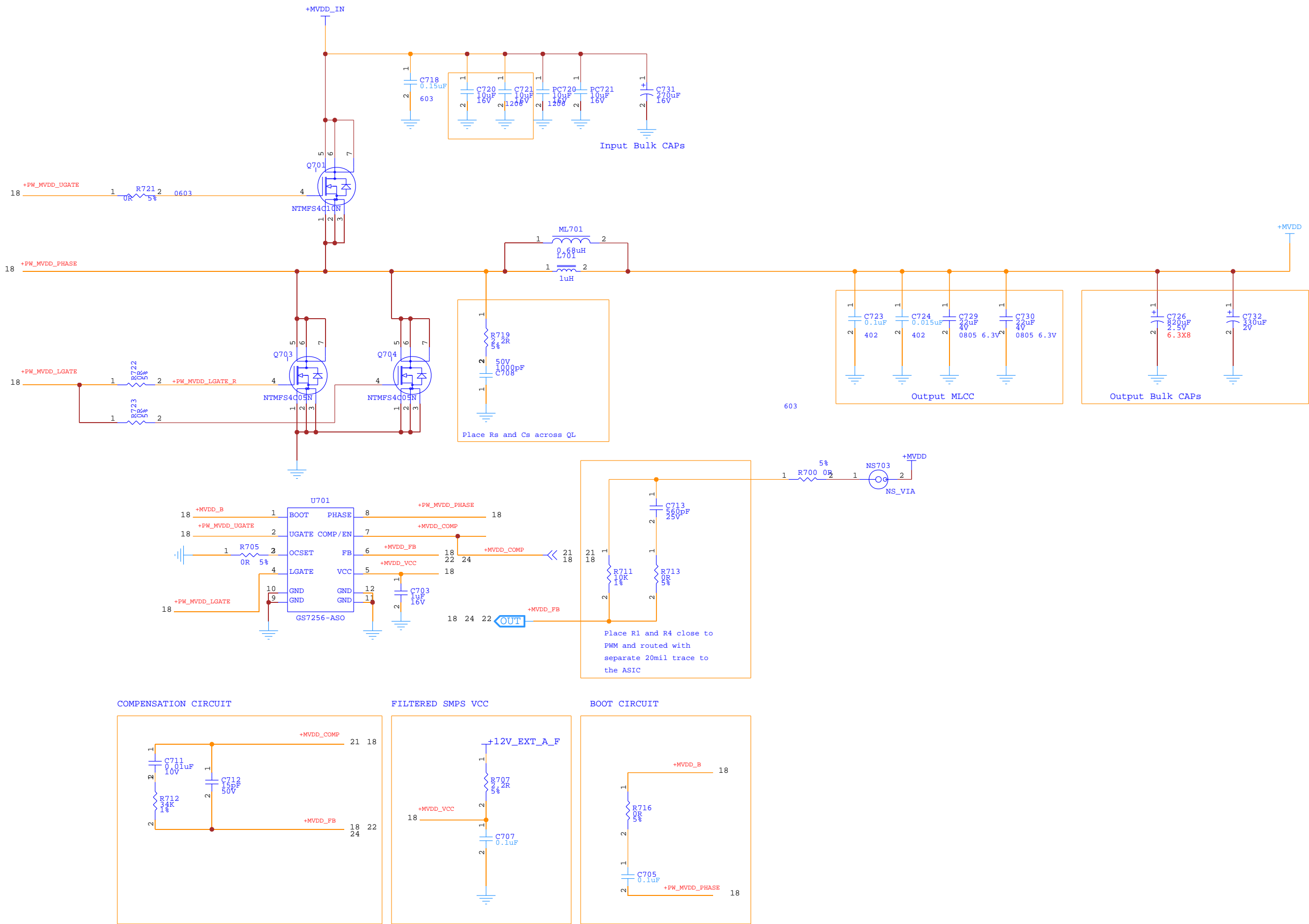
SHEET NUMBER:	17	OF	26
DOCUMENT NUMBER:	105_C940XX_00		

NOTES: NOTE

TITLE:

TITLE

(18) MVDD

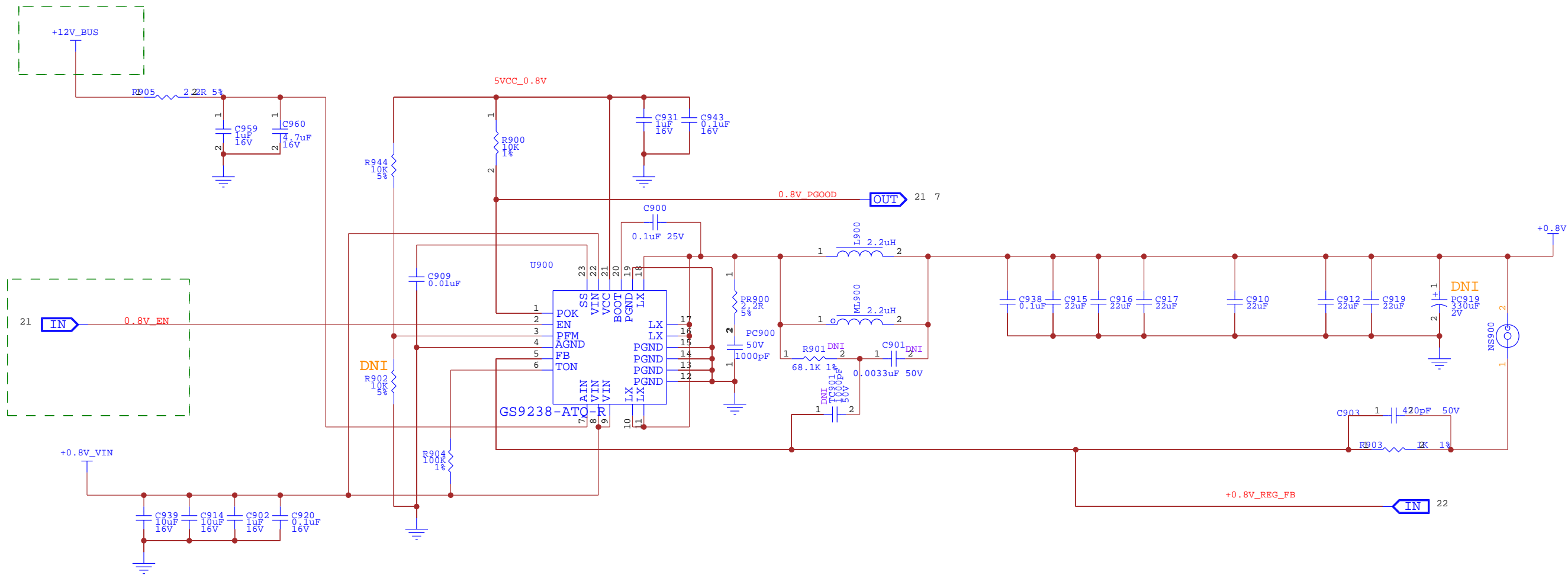


COMPENSATION CIRCUIT

FILTERED SMPS VCC

BOOT CIRCUIT

(17) 0.8V



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SHANGHAI, CHINA 201203



SHEET: 0.8V REG

DATE: Mon Sep 19 17:21:42 2016

REV: 1.0

SHEET NUMBER: 19 OF 26

DOCUMENT NUMBER: 105_C940XX_00D

NOTES: NOTE

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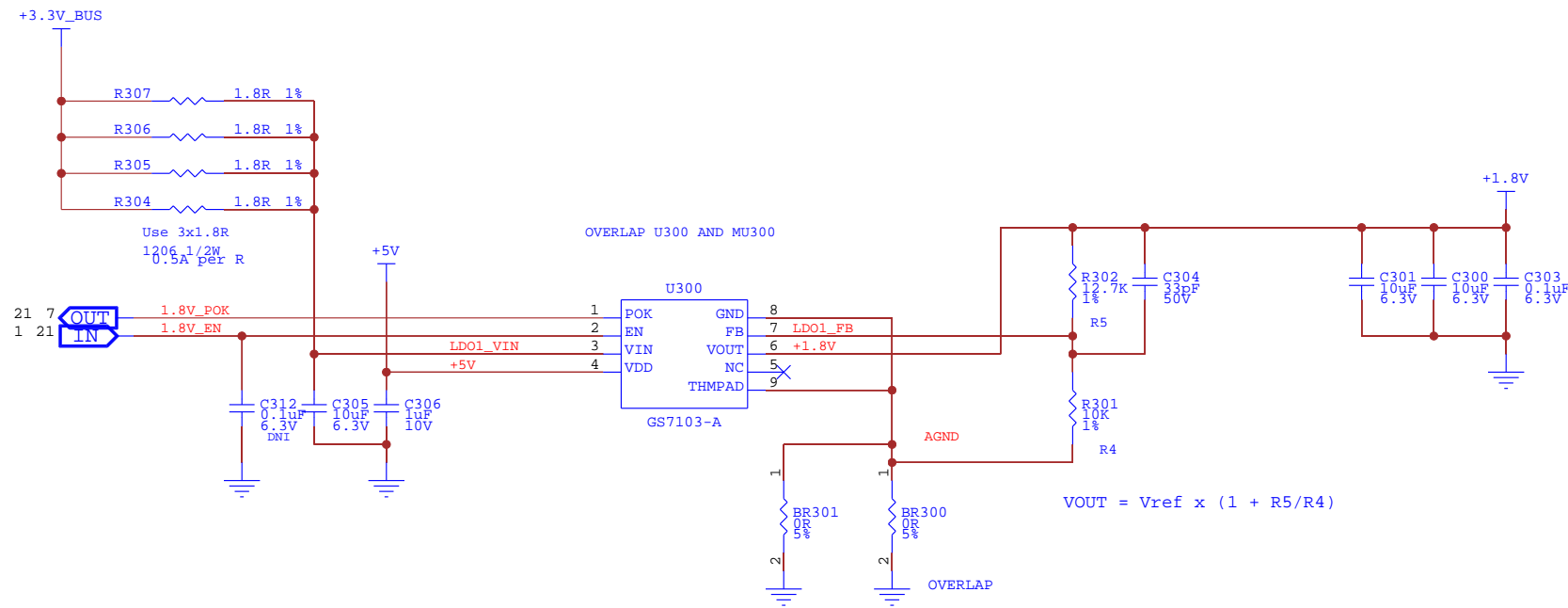
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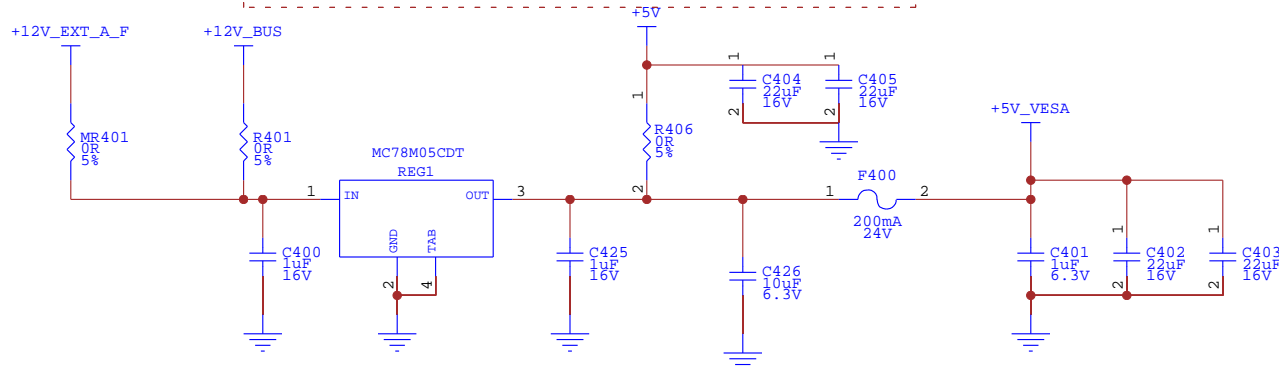
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(20) SMALL RAIL REGULATORS

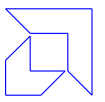
LDO #1: VIN = 3.0V TO 3.6V MAX VOUT = +1.8V +/- 2% IOUT = 1.3A RMS MAX
PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING



REGULATOR FOR +5V RAILS



AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203



SHEET: SMALL RAIL REGULATORS

DATE: Mon Sep 19 17:21:42 2016

REV: 1.0

SHEET NUMBER: 20 OF 26

DOCUMENT NUMBER: 105_C940XX_00D

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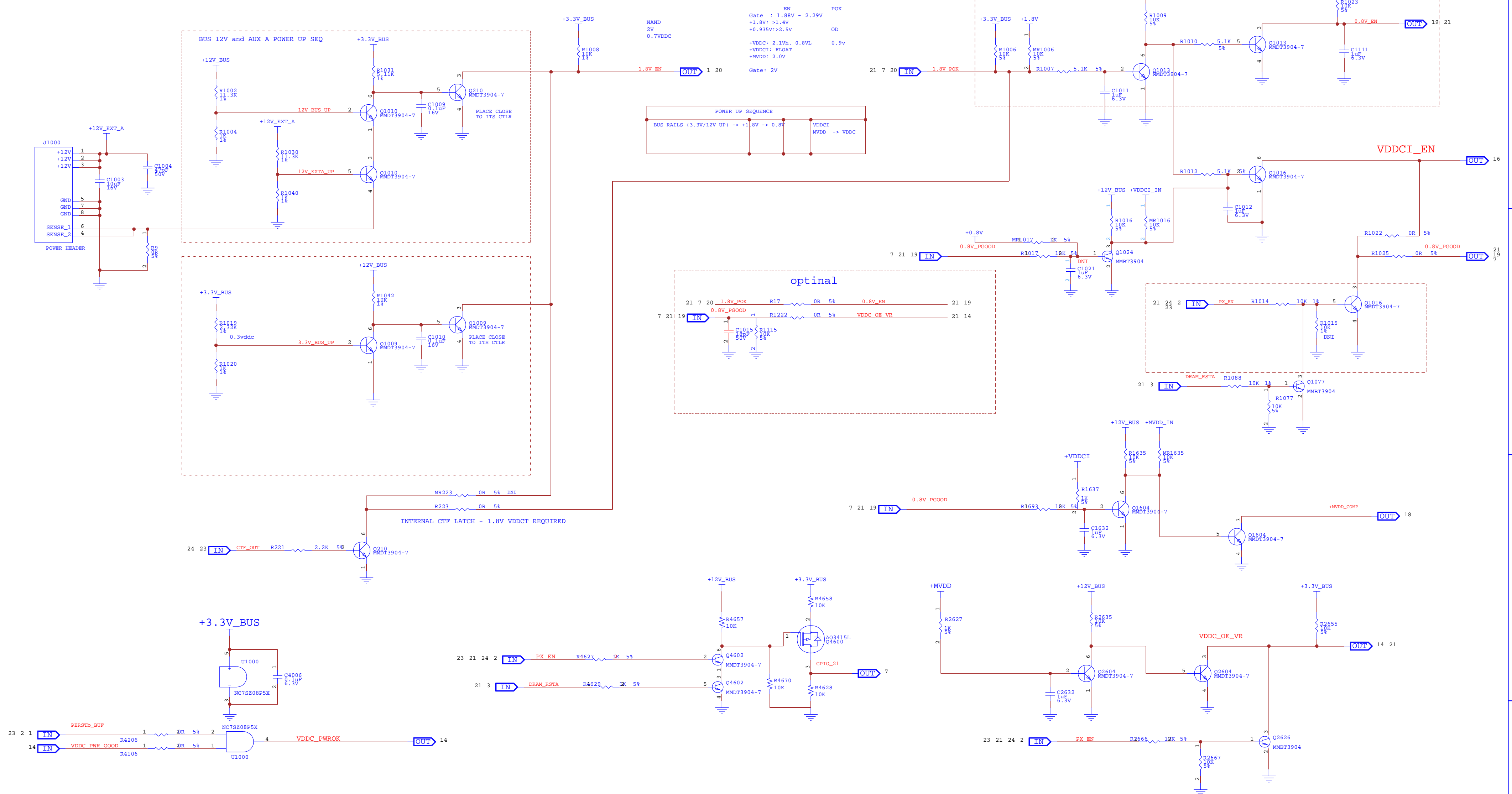
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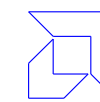
TITLE:

TITLE

(21) POWER MANAGEMENT



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SHEET: POWER MANAGEMENT1	
DATE: Mon Sep 19 17:21:44 2016	REV: 1.0
SHEET NUMBER: 21 OF 26	
DOCUMENT NUMBER: 105_C940XX_00D	

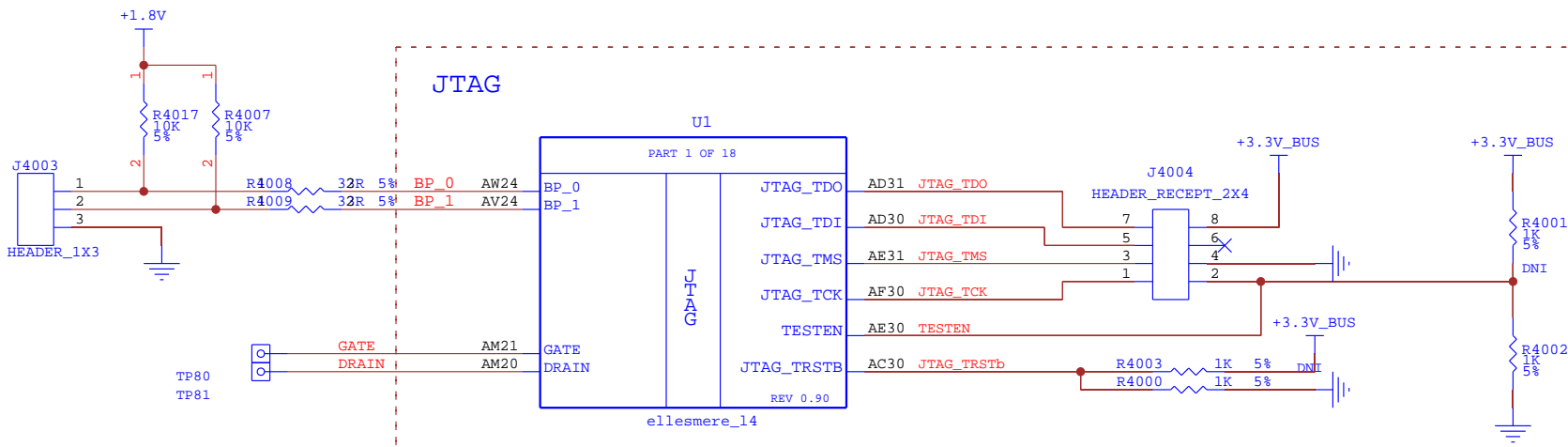
TITLE:	
	TITLE

E

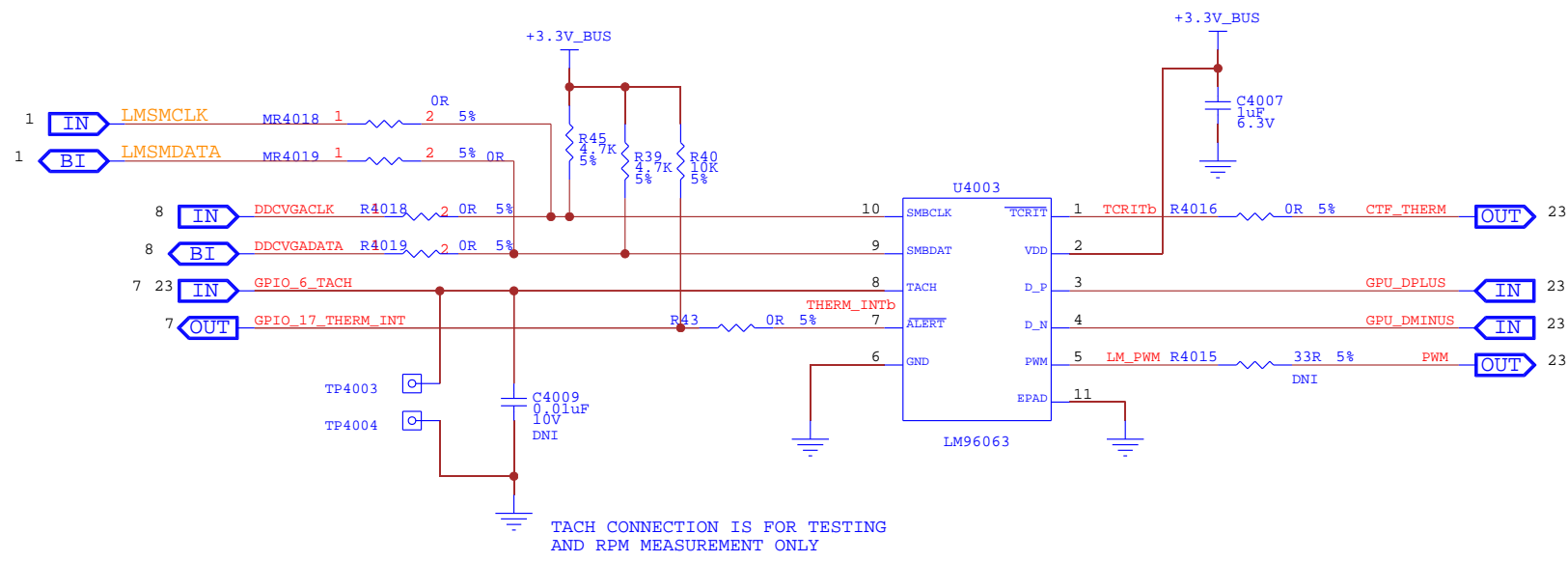
A

(23) DEBUG CIRCUITS

D

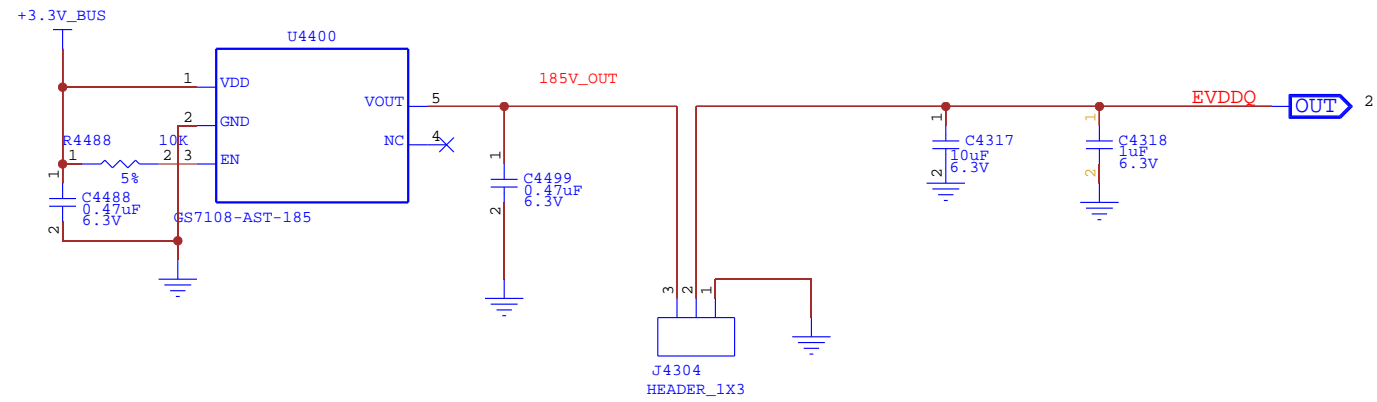


LM96163 FOR BACKUP THERMAL CONTROL

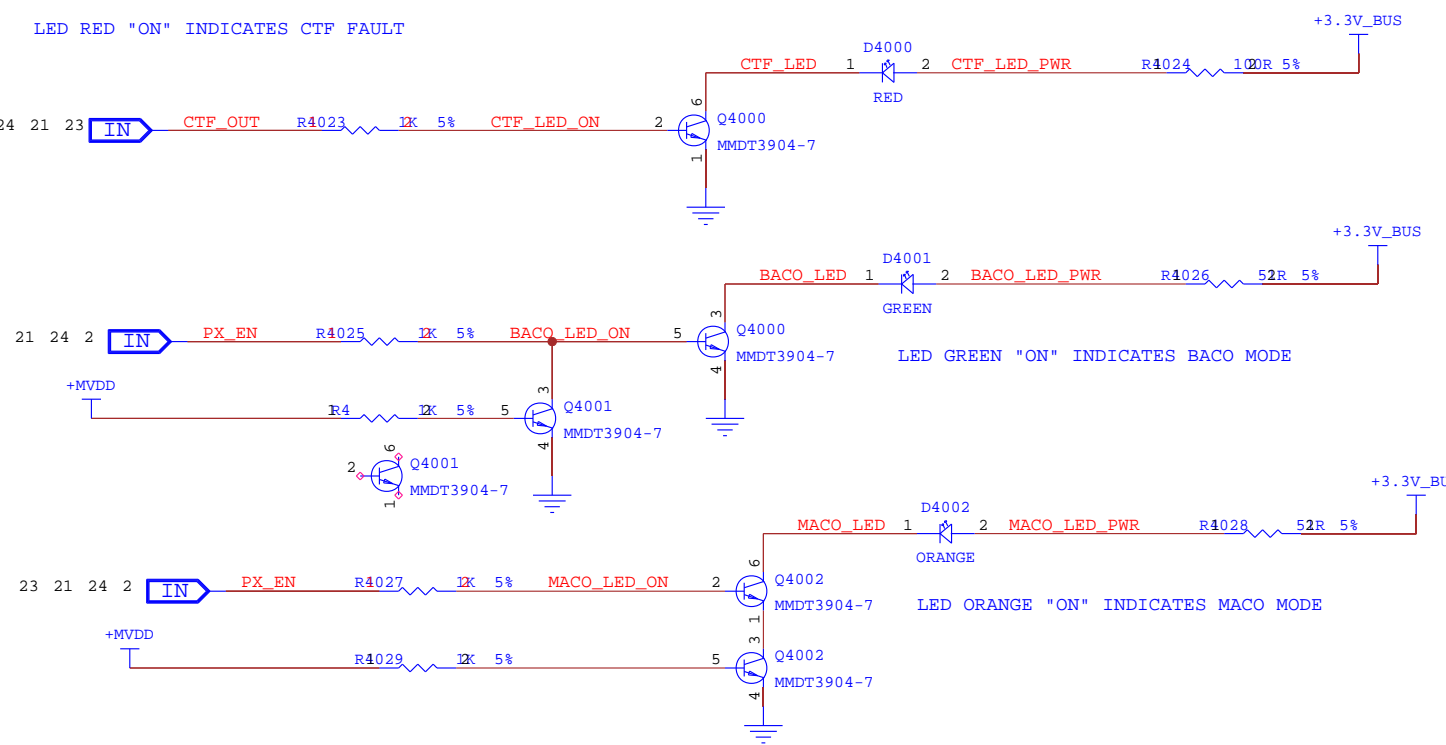


C

E-FUSE CAPABILITY

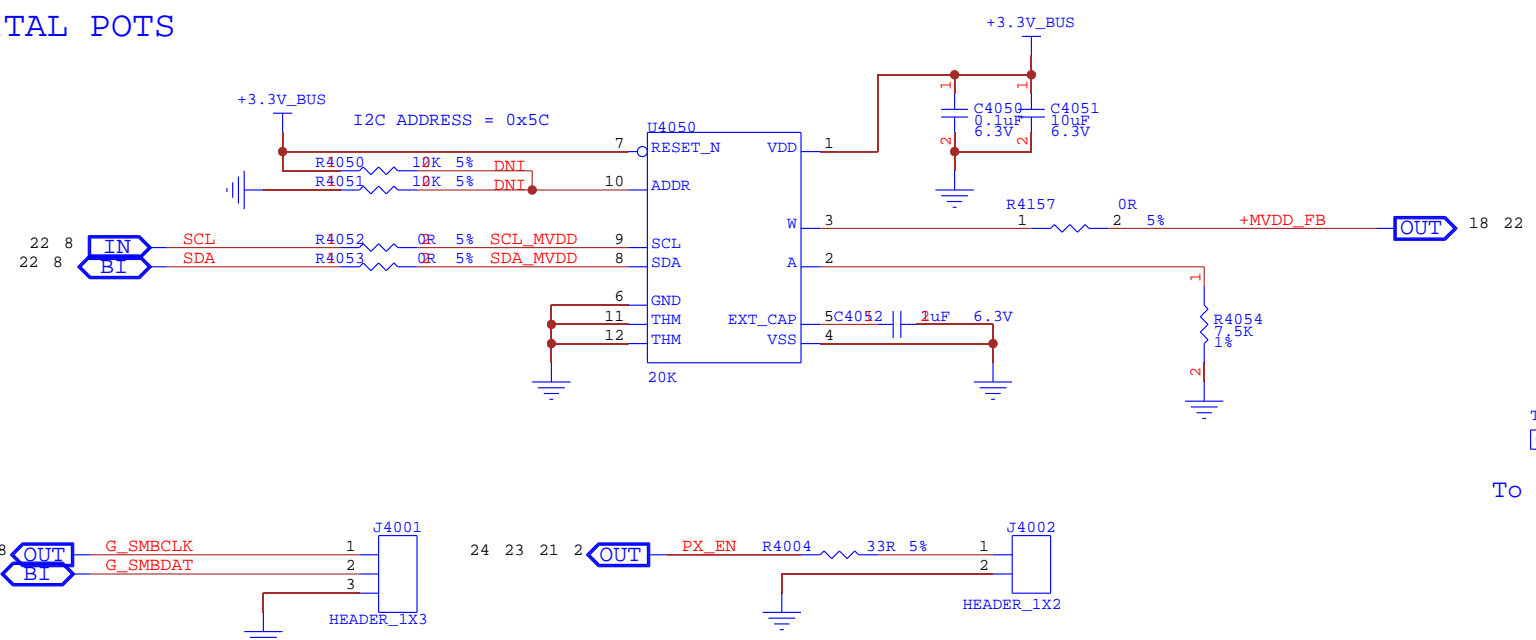


LED LIGHTS

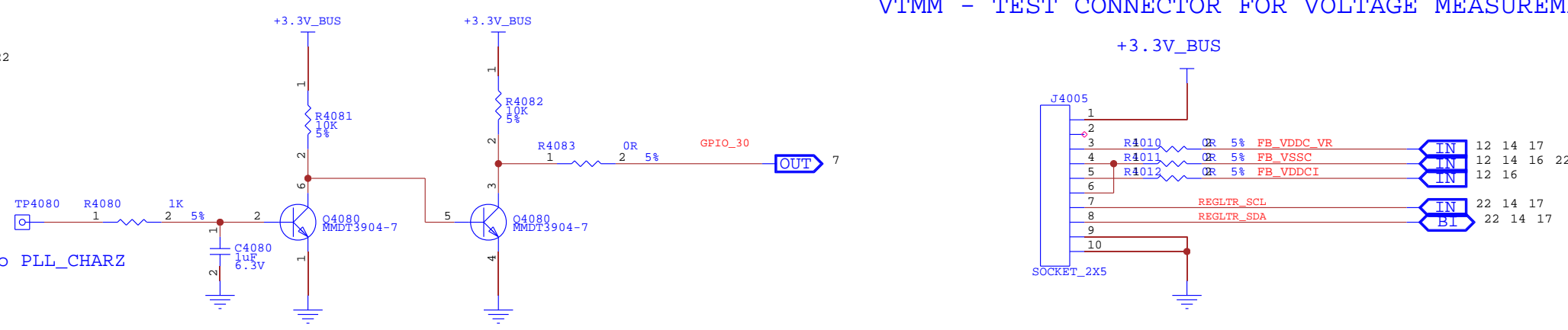


B

DIGITAL POTS

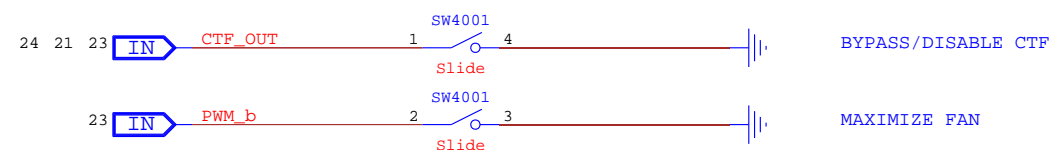


VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS



A

SWITCHES



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SHEET: DEBUG CIRCUITS/ E-FUSE	
DATE: Mon Sep 19 17:21:44 2016	REV: 1.0
SHEET NUMBER: 24	OF 26
DOCUMENT NUMBER: 105_C940XX_00D	

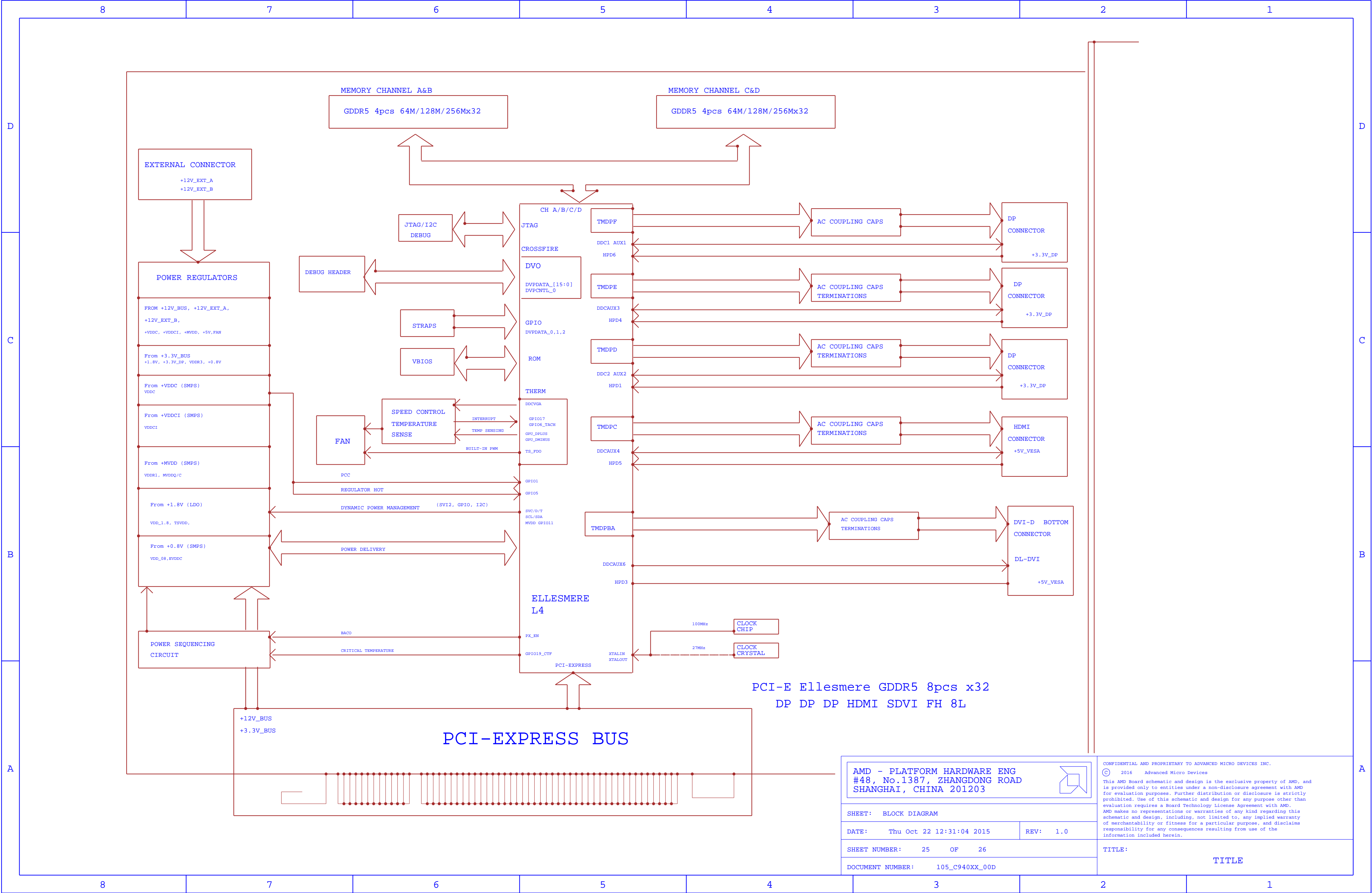
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AMD 		TITLE: TITLE				DOCUMENT NUMBER: 105_C940XX_00D			DATE: Mon Sep 19 12:33:14 2016			SHEET NUMBER: 26 OF 26			REV: 1.0	
REVISION HISTORY				ENGINEER: XXX		NOTES: NOTE			CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC. C • 2016 Advanced Micro Devices This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representations or warranties of any kind regarding this schematic and design, including, not limited to, any implied warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information included herein.				AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203			
D	SCH Rev	PCB Rev	Date	REVISION DESCRIPTON												
	0	00A	04/22/2015													
	1	00B	07/15/2015	1. add level shift for SCL/SDA on page8 2. add 100M generator U22 on page7 3. layout update to meet new e-rule(DP/MEMORY)												
	2	00C	04/05/2016	HDMI: - add series resiator R1880 ~R1887 - add pull down inductor L1880 ~L1887 - Q1950, Q1820 combied as Q1820 (DVI) MACO - change VDDCI from SVI2 to GPIO - PWR sequence LDO: - VR400 removed VDDCI: - change from SVI2 to seperate VDDCI. SMBUS: - add R113,R112, MR4018,MR4019 1.8v level shift: - remove R1111,R1113,R1112,R1114,MQ1105,MQ1103												
C	3	00d	29/08/2016	HDMI: - add aviod for HDMI - Add option FB_VSSC for VDDCI (R815/R816) - add optinal GND for LDO_1.8V (BR301)												
	4	00e	13/09/2016	- change 2x6pin external connector to 1x8pin - change VDDCI controller - add C63 22u for 100M clk pwr - VDDC 12V input caps change												
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