

P407-A00: G84M/G86M MXM-II
256/512MB 128-BIT GDDR2
LVDS, DVI -A, DVI -B, TV-OUT, VGA
SLI , HDMI , HDCP

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MSIS-RMA
羅覓林
20090826

SKU	VARIANT	NVPN	ASSEMBLY
B	BASE	600-10407-9998-000	BASE LEVEL GENERIC SCHEMATIC ONLY. COMMON & NO-STUFF ASSEMBLY NOTES AND BOM NOT FINAL.
1	SKU0001	600-10407-0001-000	G84M-600 450/400 256MB 128bit t GDDR2 16Mx16 84FBGA, LVDS + DVI_A/DVI_B + TV_OUT + VGA, MXM-II, HDCP.
2	SKU0002	600-10407-0002-000	G84M-600 450/400 512MB 128bit t GDDR2 32Mx16 84FBGA, LVDS + DVI_A/DVI_B + TV_OUT + VGA, MXM-II, HDCP.
3	SKU0003	600-10407-0003-000	G84M-700 TBD/400 512MB 128bit t GDDR2 32Mx16 84FBGA, LVDS + DVI_A/DVI_B + TV_OUT + VGA, MXM-II, HDCP.
4	SKU0004	600-10407-0004-000	G86M_770 500/400 256MB 128bit t GDDR2 16Mx16 84FBGA, LVDS + DVI_A/DVI_B + TV_OUT + VGA, MXM-II, HDCP.
5	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
6	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
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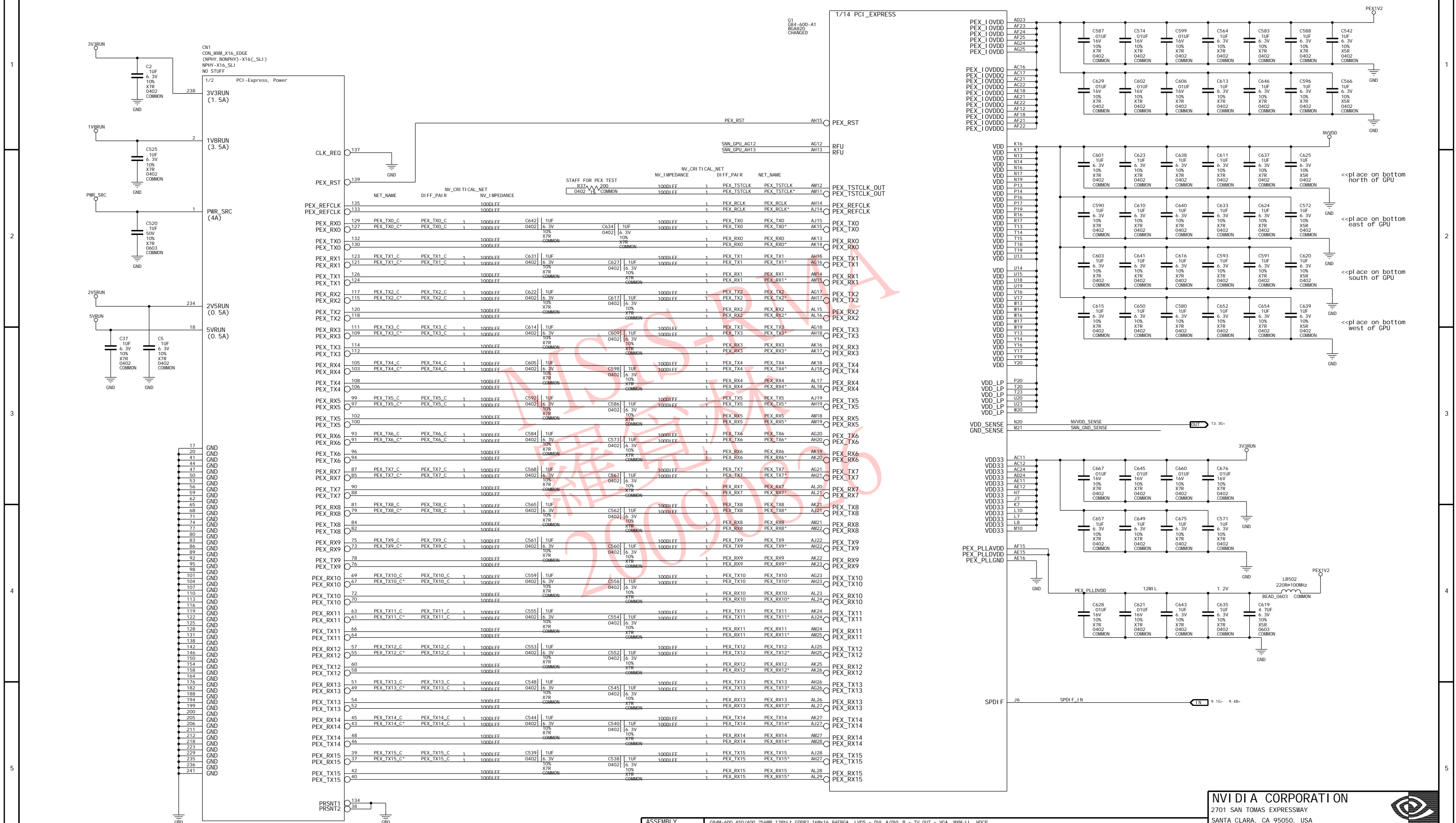
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PAGE 2) MXM-11 GOLDEN EDGE, PCI EXPRESS INTERFACE



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ASSEMBLY	G84M-600 450/400 256MB 128bit GDDR2 16Mx16 84FBGA, LVDS + DVI_A/DVI_B + TV_OUT + VGA, MXM-II, HDCP
PAGE DETAIL	PCI EXPRESS Interface

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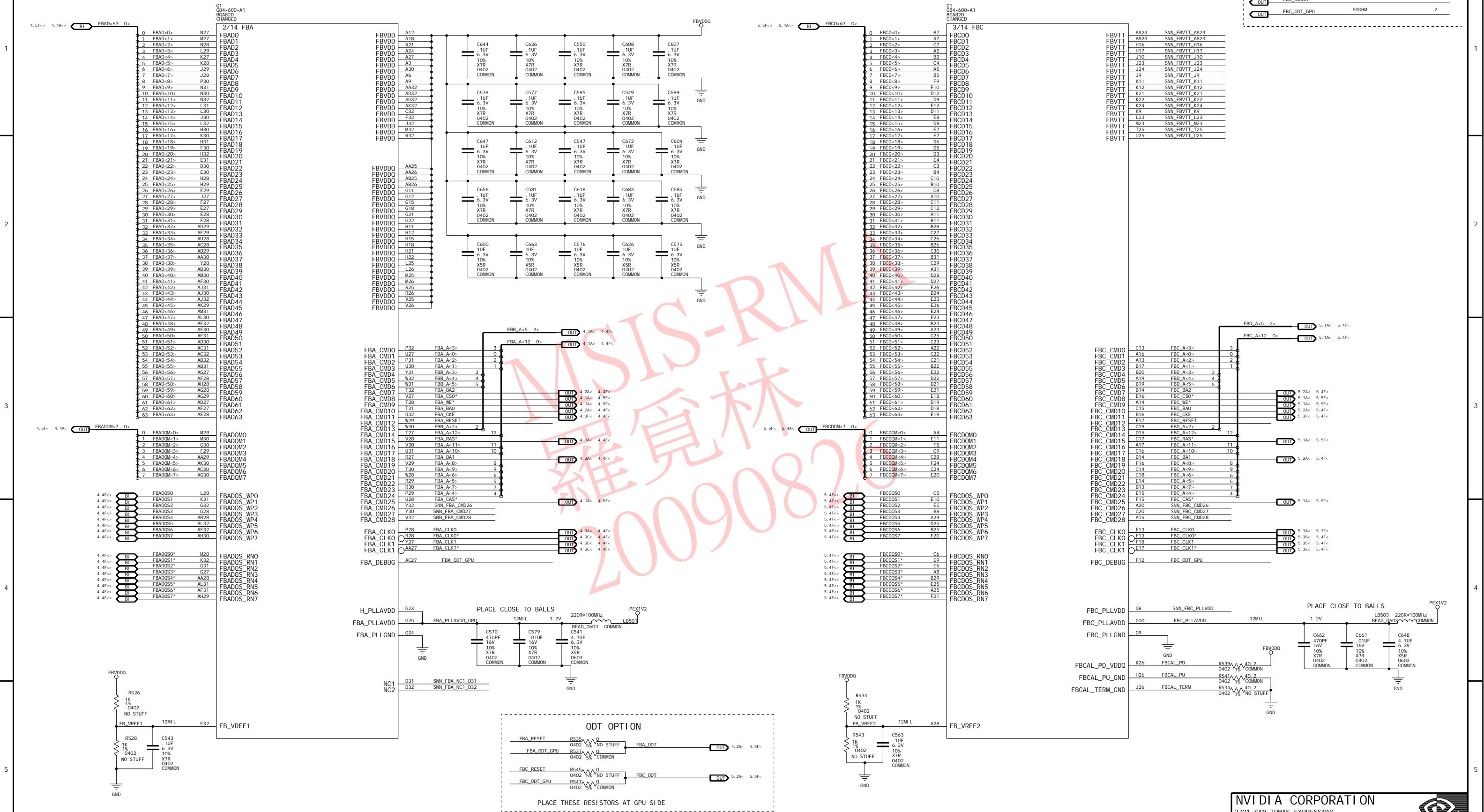
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PAGE 3) GPU MEMORY INTERFACE

	NET	NV_IMPEDANCE	NV_CRTICAL_NET
OUT	FBA_RESET	500HM	2
OUT	FBA_ODT_GPU	500HM	2
OUT	FBC_RESET	500HM	2
OUT	FBC_ODT_GPU	500HM	2



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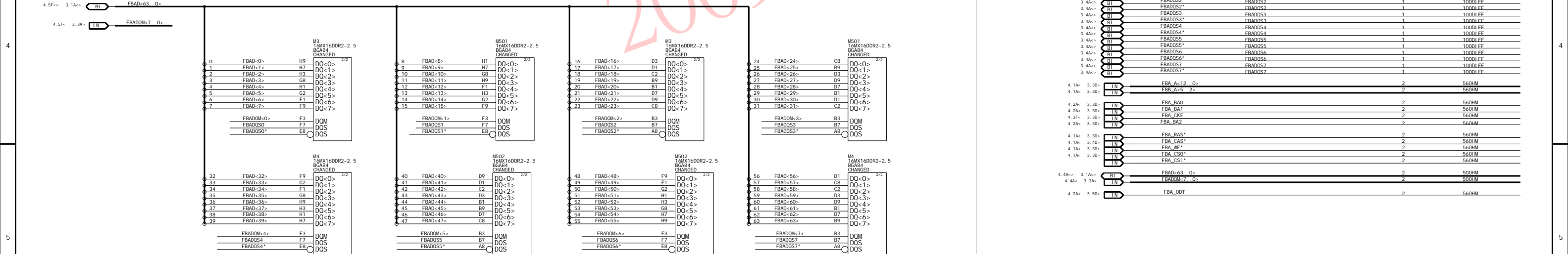
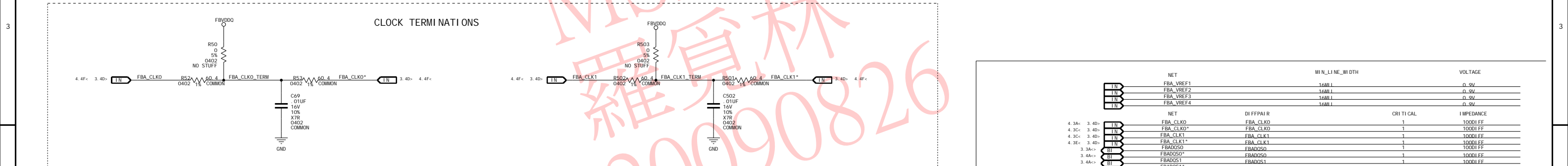
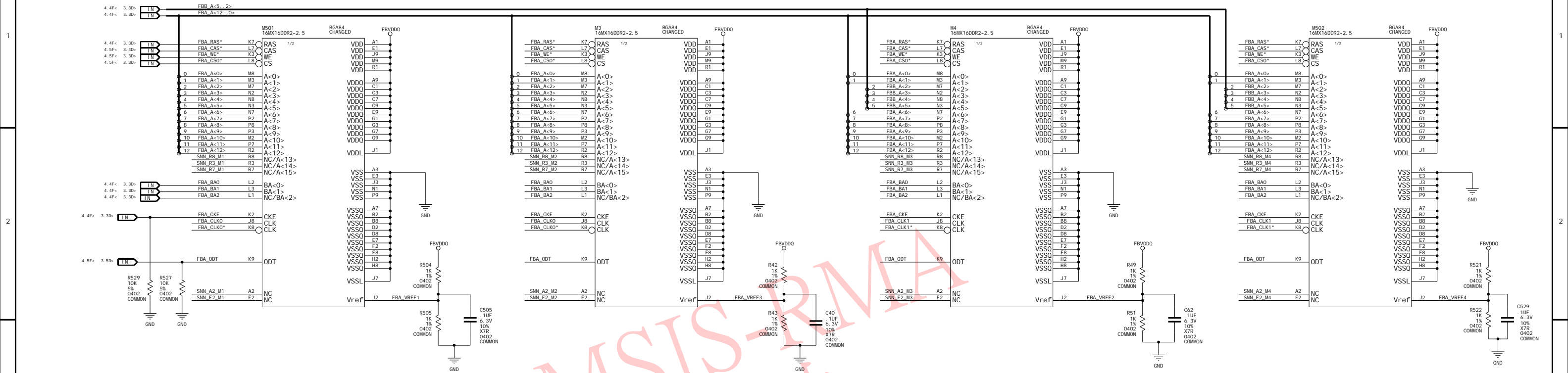
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PAGE 4) MEMORY PARTITION A

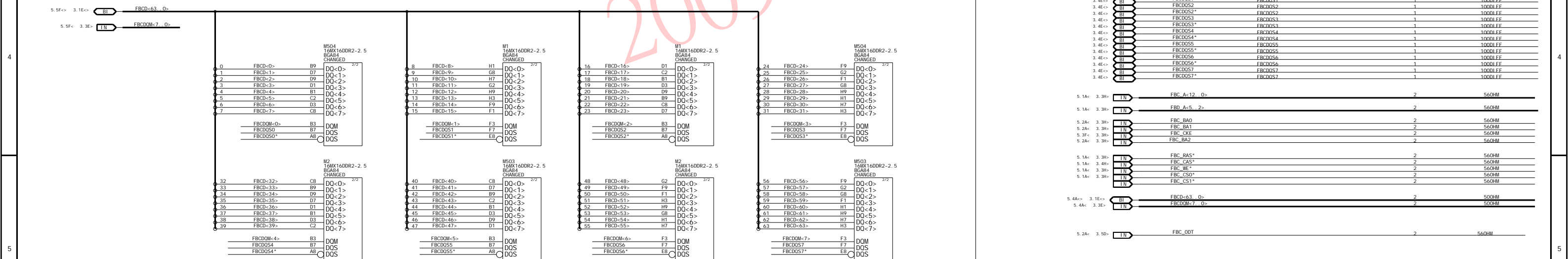
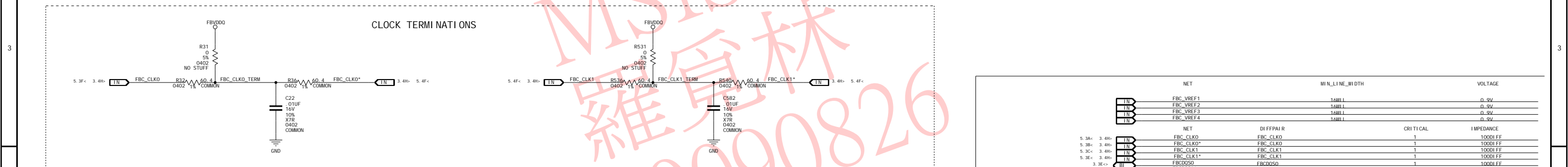
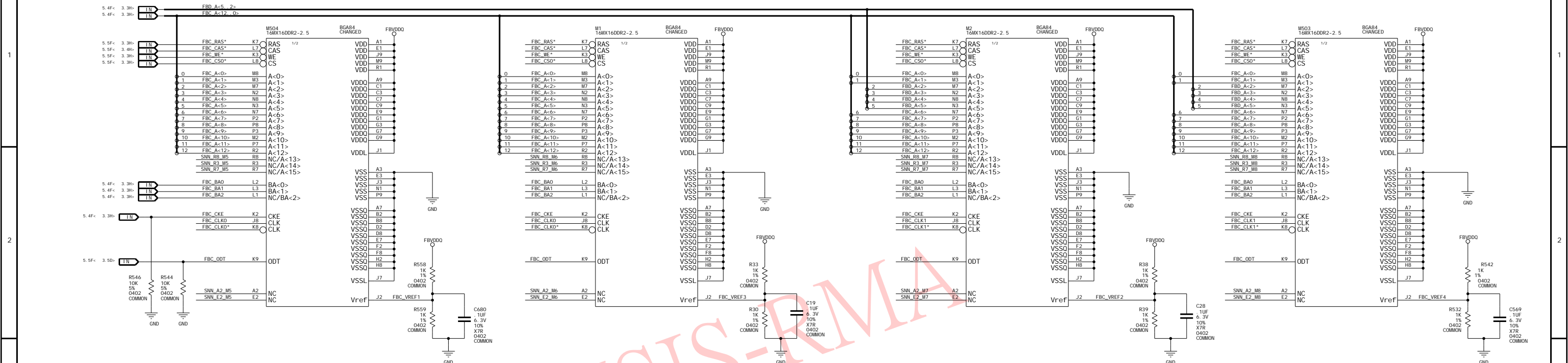


NET	MIN_LI_NE_WI_OTH	VOLTAGE
FBA_VREF1	16MIL	0.9V
FBA_VREF2	16MIL	0.9V
FBA_VREF3	16MIL	0.9V
FBA_VREF4	16MIL	0.9V

NET	DIFFPAIR	Critical	Impedance
FBA_CLK0	FBA_CLK0	1	10001FF
FBA_CLK1	FBA_CLK1	1	10001FF
FBA_CLK1*	FBA_CLK1*	1	10001FF
FBAD0S0	FBAD0S0	1	10001FF
FBAD0S1	FBAD0S1	1	10001FF
FBAD0S1*	FBAD0S1*	1	10001FF
FBAD0S2	FBAD0S2	1	10001FF
FBAD0S2*	FBAD0S2*	1	10001FF
FBAD0S3	FBAD0S3	1	10001FF
FBAD0S3*	FBAD0S3*	1	10001FF
FBAD0S4	FBAD0S4	1	10001FF
FBAD0S4*	FBAD0S4*	1	10001FF
FBAD0S5	FBAD0S5	1	10001FF
FBAD0S5*	FBAD0S5*	1	10001FF
FBAD0S6	FBAD0S6	1	10001FF
FBAD0S6*	FBAD0S6*	1	10001FF
FBAD0S7	FBAD0S7	1	10001FF
FBAD0S7*	FBAD0S7*	1	10001FF

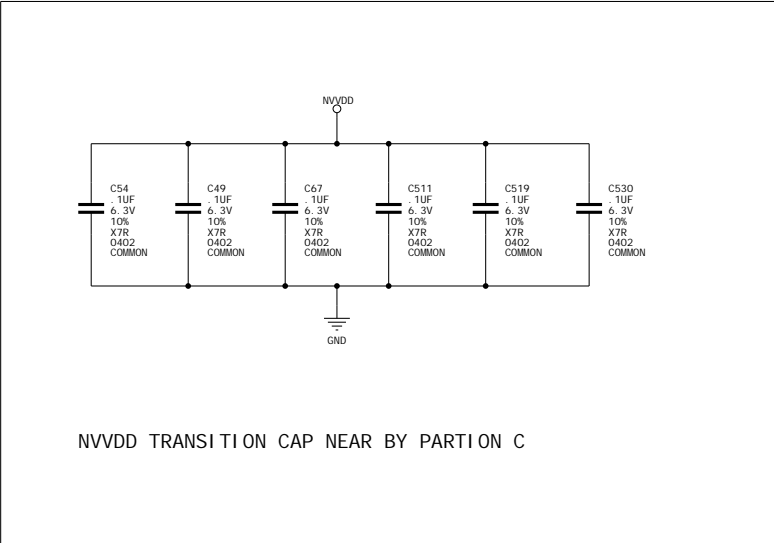
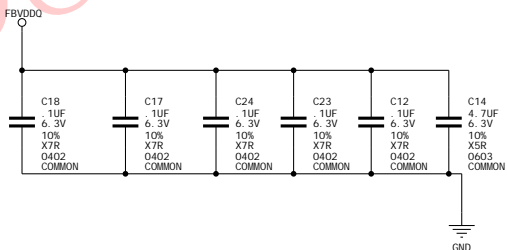
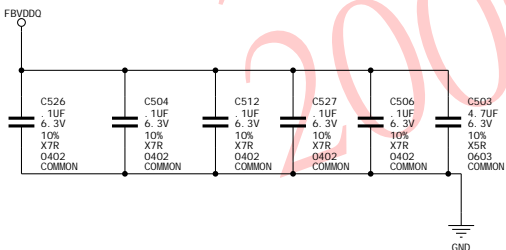
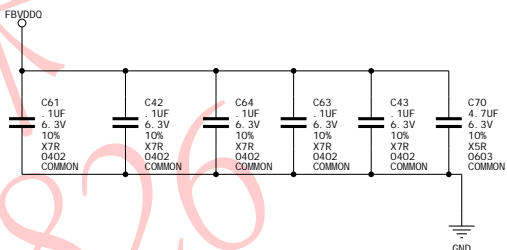
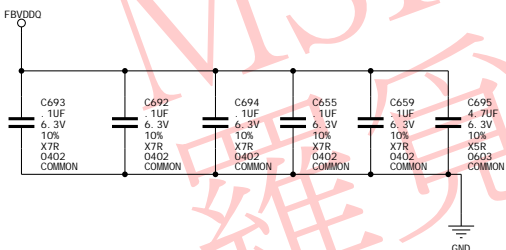
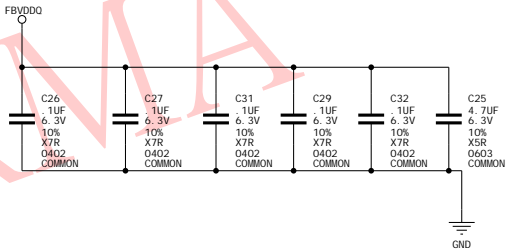
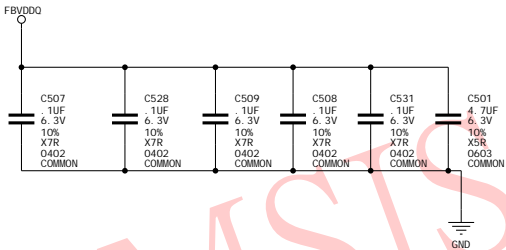
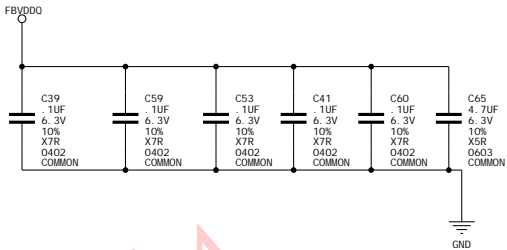
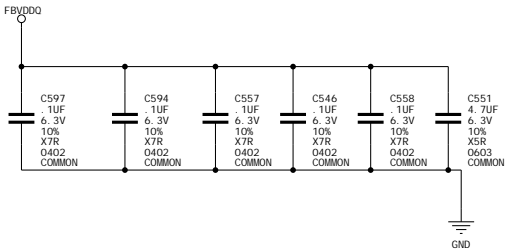
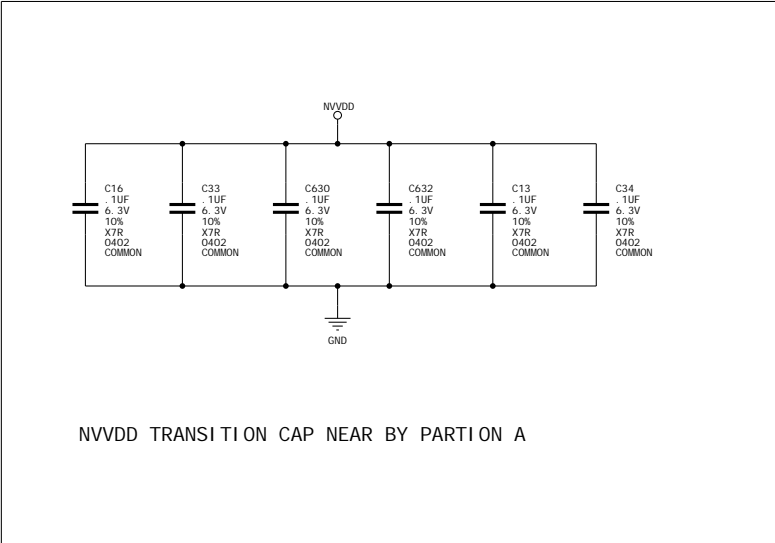
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4.1A< 3.3D>	FBA_A<5_2>	2	560HM
4.2A< 3.3D>	FBA_BA0	2	560HM
4.2A< 3.3D>	FBA_BA1	2	560HM
4.3F< 3.3D>	FBA_CKE	2	560HM
4.2A< 3.3D>	FBA_BA2	2	560HM
4.1A< 3.3D>	FBA_RAS*	2	560HM
4.1A< 3.4D>	FBA_CAS*	2	560HM
4.1A< 3.3D>	FBA_WE*	2	560HM
4.1A< 3.3D>	FBA_CS0*	2	560HM
4.1A< 3.3D>	FBA_CS1*	2	560HM
4.4A< 3.1A<	FBAD<63_0>	2	500HM
4.4A< 3.3A>	FBADOM<7_0>	2	500HM
4.2A< 3.5D>	FBA_ODT	2	560HM

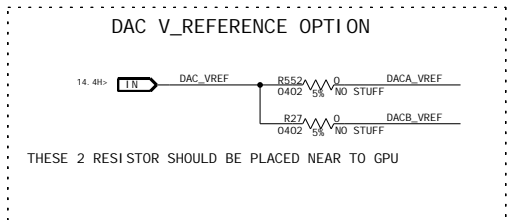
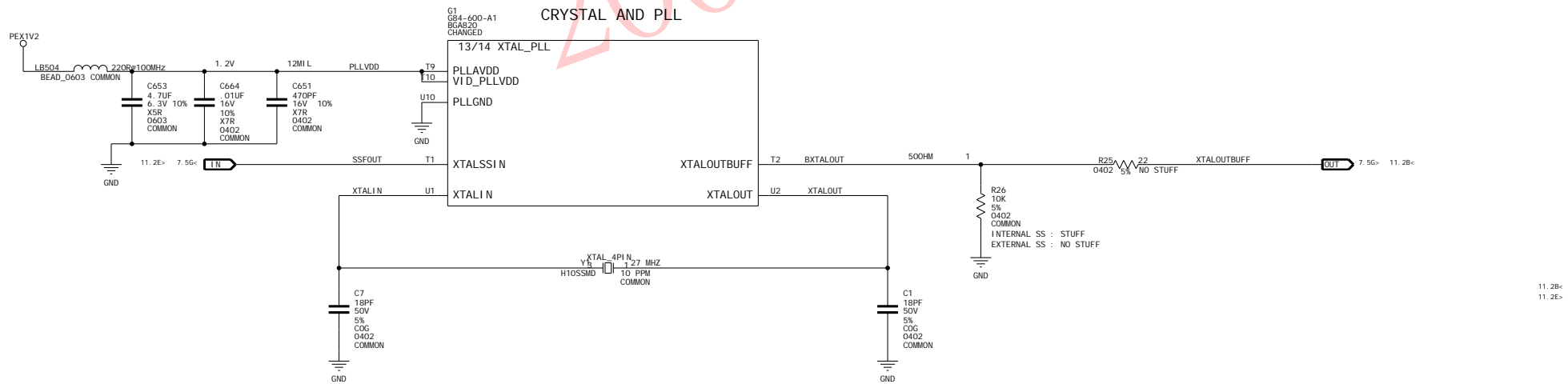
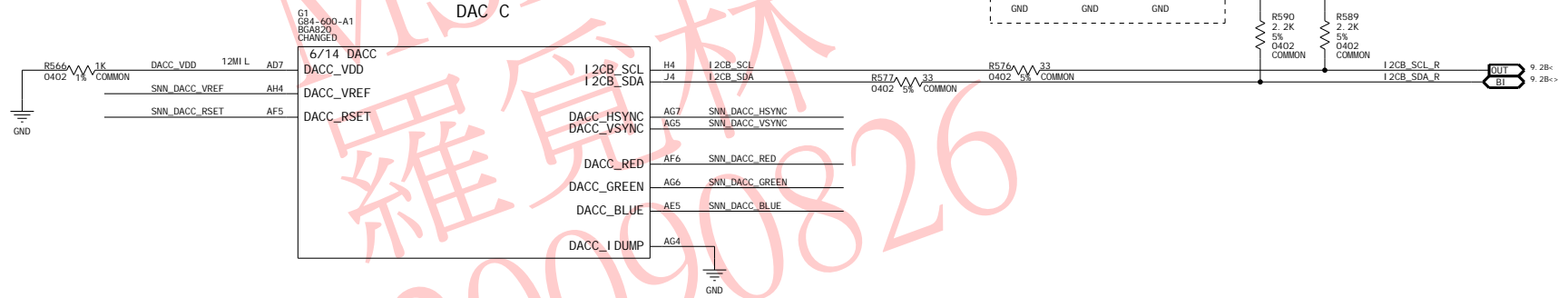
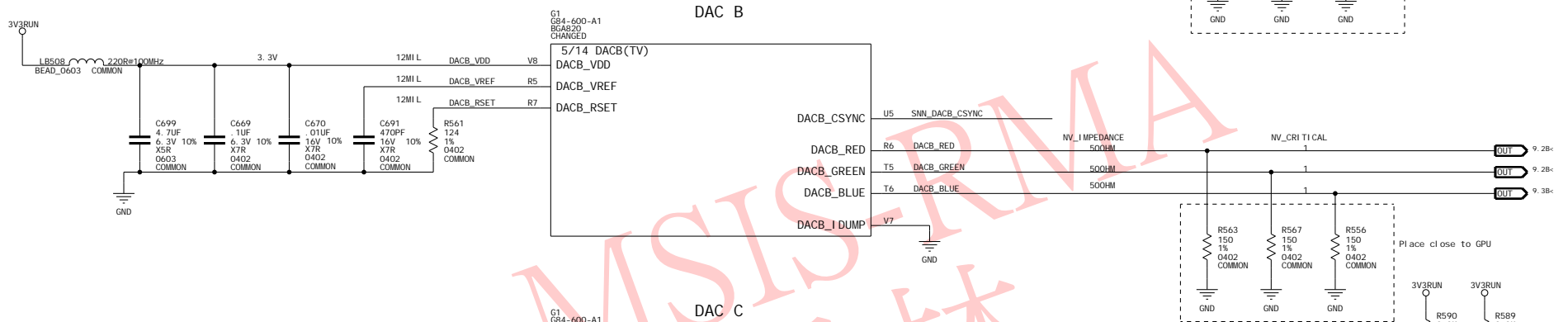
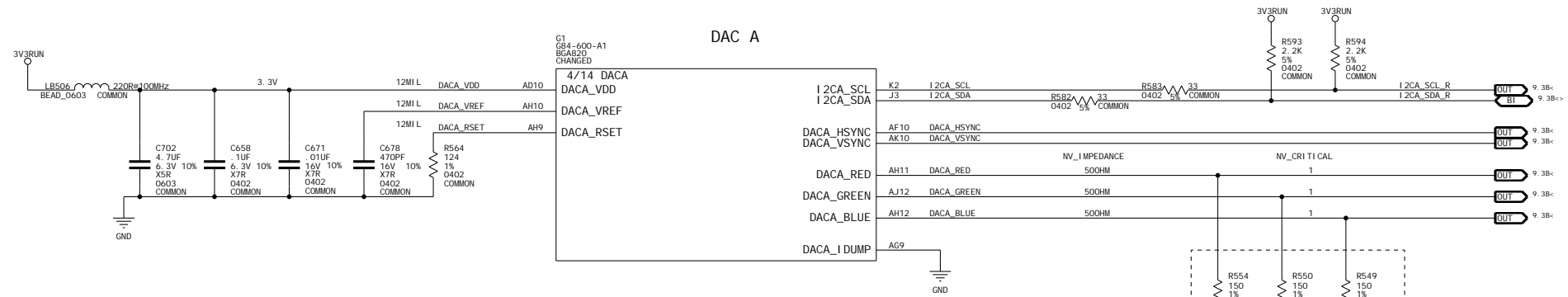
PAGE 5) MEMORY PARTITION C



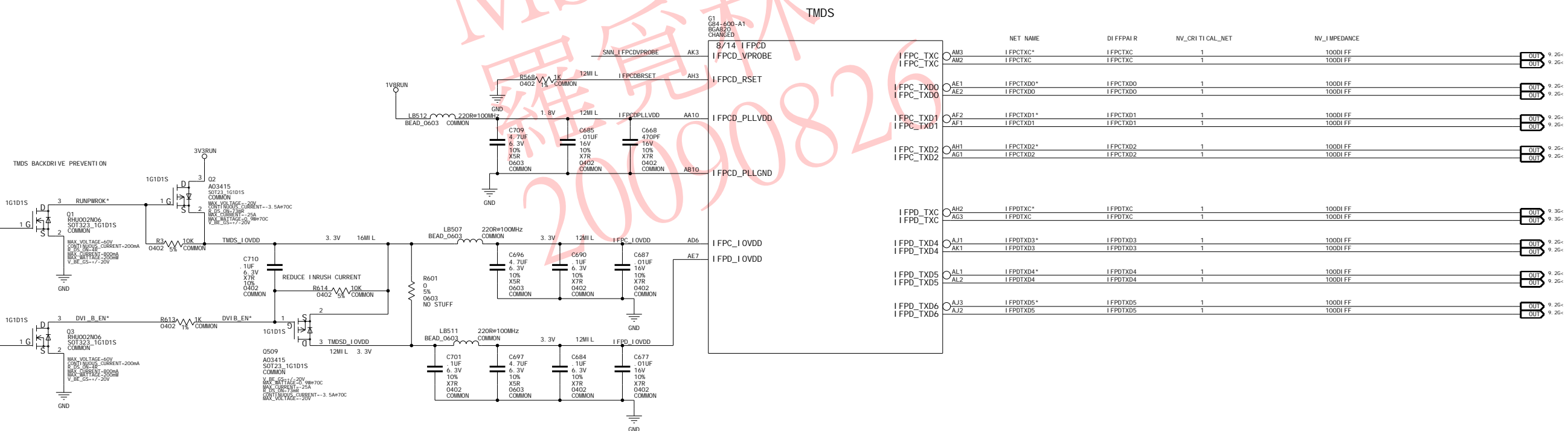
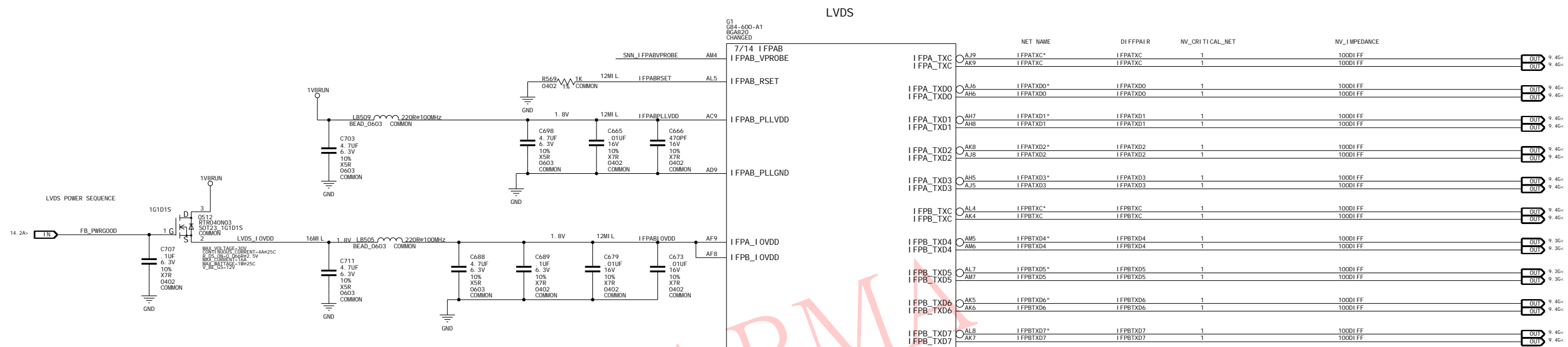
		NET	MN_LIN_E_WDTH	VOLTAGE
		IN	FBC_VREF1	16M I 0.9V
		IN	FBC_VREF2	16M I 0.9V
		IN	FBC_VREF3	16M I 0.9V
		IN	FBC_VREF4	16M I 0.9V
		NET	DI_FFPAIR	
		IN	FBC_CLK0	100DI FF
5.3Ac	3.4Hz	IN	FBC_CLK0*	100DI FF
5.3Bc	3.4Hz	IN	FBC_CLK1	100DI FF
5.3Cc	3.4Hz	IN	FBC_CLK1*	100DI FF
5.3Ec	3.4Hz	IN	FBC_CLK2	100DI FF
3.3Ec		BI	FBCD050	100DI FF
3.4Ec		BI	FBCD050*	100DI FF
3.4Ec		BI	FBCD051	100DI FF
3.4Ec		BI	FBCD051*	100DI FF
3.4Ec		BI	FBCD052	100DI FF
3.4Ec		BI	FBCD052*	100DI FF
3.4Ec		BI	FBCD053	100DI FF
3.4Ec		BI	FBCD053*	100DI FF
3.4Ec		BI	FBCD054	100DI FF
3.4Ec		BI	FBCD054*	100DI FF
3.4Ec		BI	FBCD055	100DI FF
3.4Ec		BI	FBCD055*	100DI FF
3.4Ec		BI	FBCD056	100DI FF
3.4Ec		BI	FBCD056*	100DI FF
3.4Ec		BI	FBCD057	100DI FF
3.4Ec		BI	FBCD057*	100DI FF
5.1Ac	3.3Hz	IN	FBC_A<12..0>	56OHM
5.1Ac	3.3Hz	IN	FBD_A<5..2>	56OHM
5.2Ac	3.3Hz	IN	FBC_BA0	56OHM
5.2Ac	3.3Hz	IN	FBC_BA1	56OHM
5.3Fc	3.3Hz	IN	FBC_CKE	56OHM
5.2Ac	3.3Hz	IN	FBC_BA2	56OHM
5.1Ac	3.3Hz	IN	FBC_RAS*	56OHM
5.1Ac	3.4Hz	IN	FBC_CAS*	56OHM
5.1Ac	3.3Hz	IN	FBC_WE*	56OHM
5.1Ac	3.3Hz	IN	FBC_CSO*	56OHM
		IN	FBC_CS1*	56OHM
5.4Ac	3.1Ec	BI	FBCD<63..0>	50OHM
5.4Ac	3.3Ec	IN	FBCDOM<7..0>	50OHM
5.2Ac	3.5Hz	IN	FBC_ODT	56OHM

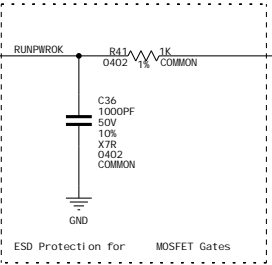
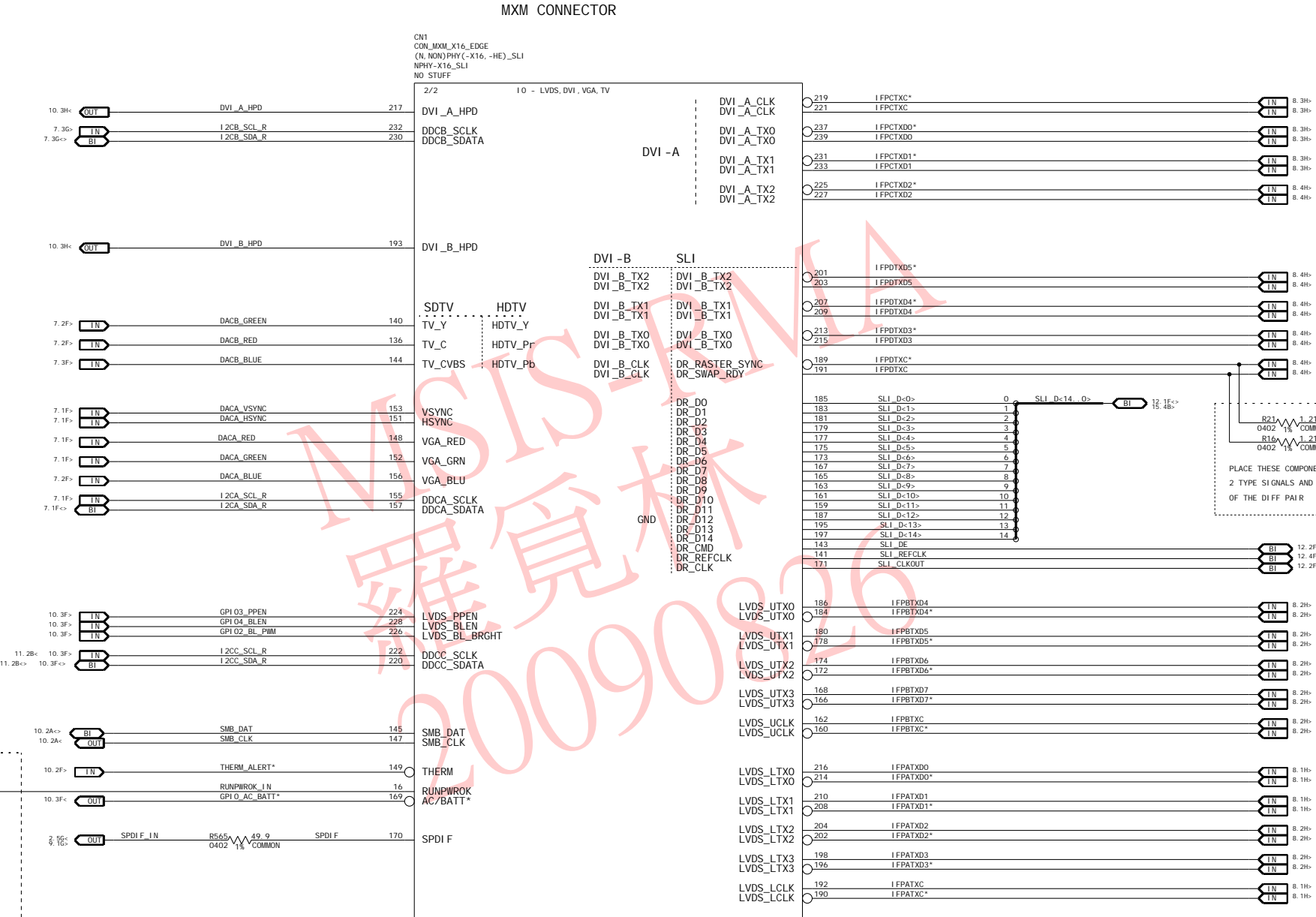
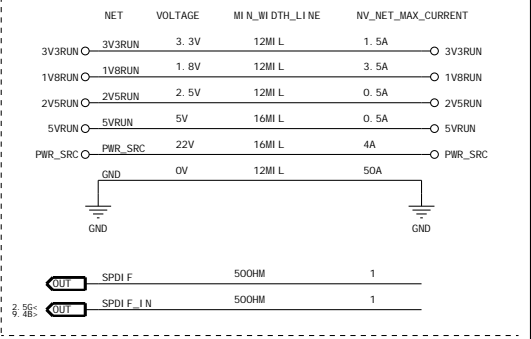
DECOUPLING CAPS FOR MEMORYS (PARTION A AND PARTION C)

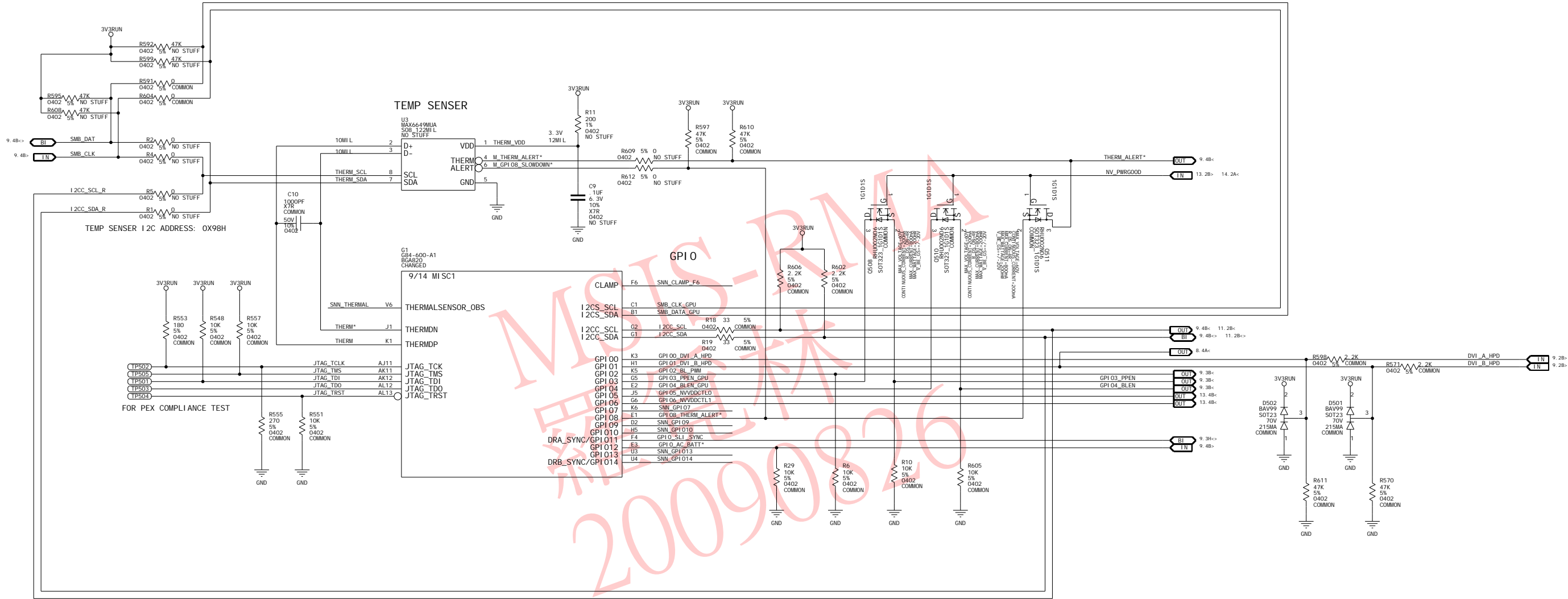




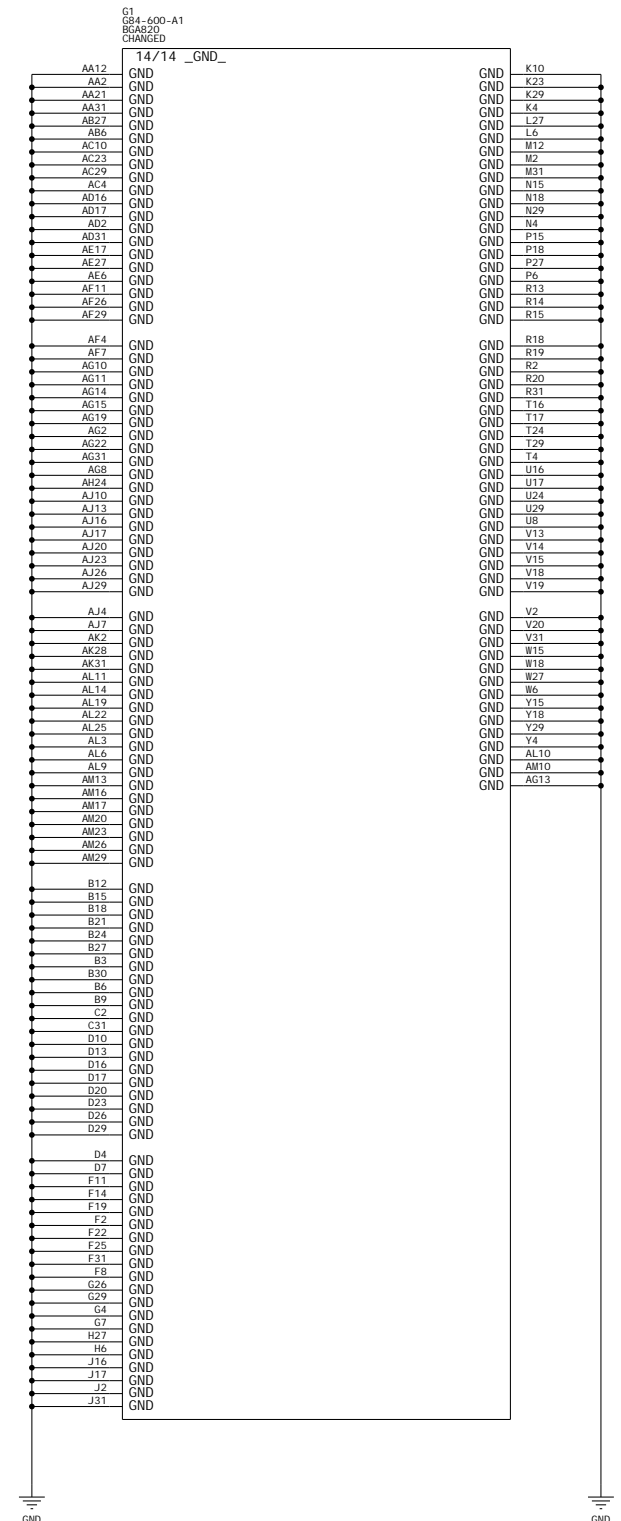
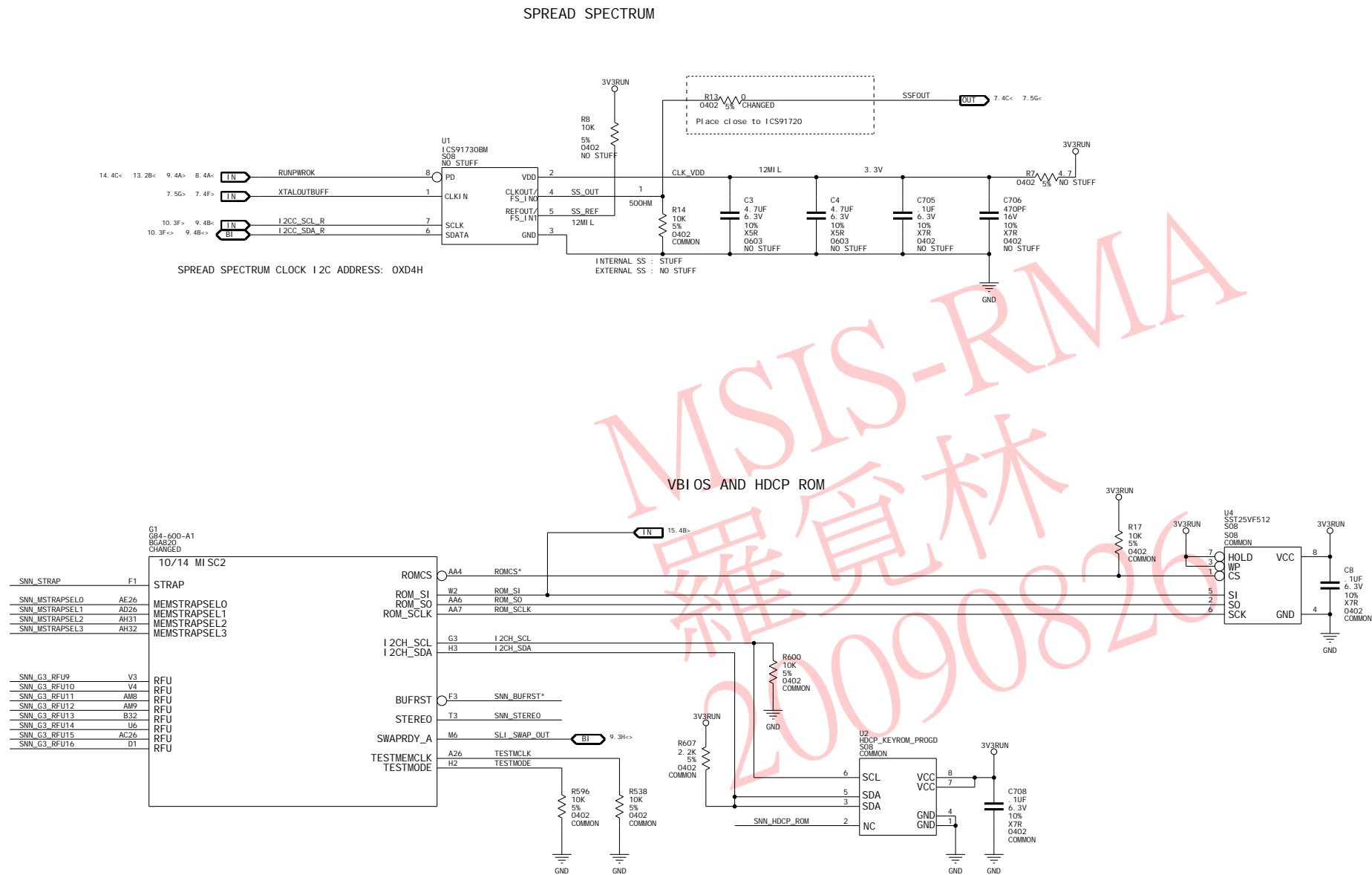
	NV_NET_NAME	NV_I MPEDANCE	NV_CRI TICAL_NET
	XTALOUT	500HM	1
	XTALI N	500HM	1
11. 2B< 7. 4F>	XTALOUTBUFF	500HM	1
11. 2E> 7. 4C<	SSFOUT	500HM	1

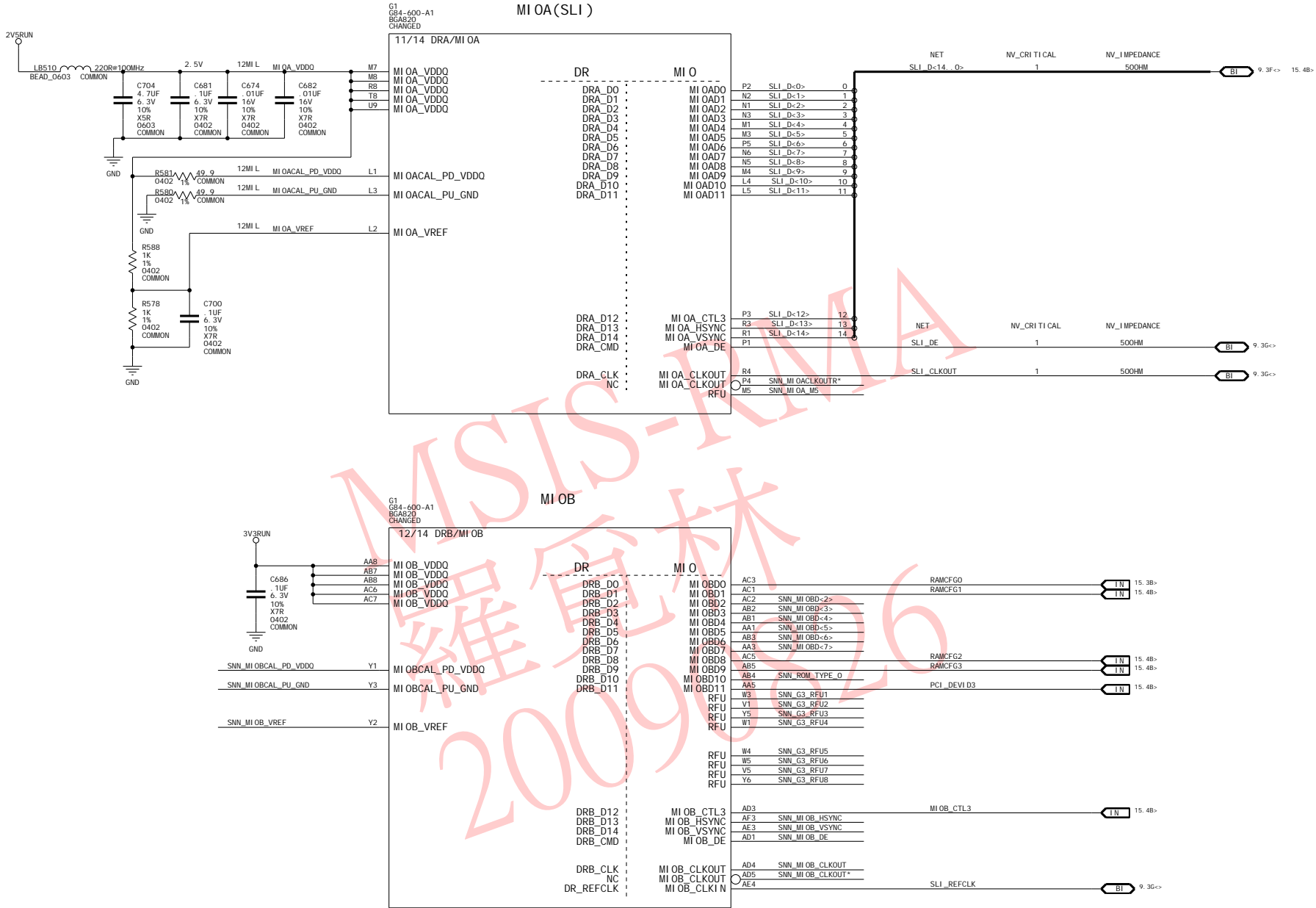






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NVDD=1V
 APPROX. 20A @ 500MHZ
 INPUT CURRENT RMS = 6.8A @ 7.5V INPUT
 OUTPUT PEAK TO PEAK CURRENT = 3A @ 22V INPUT
 SWITCHING FREQ. = 275KHZ

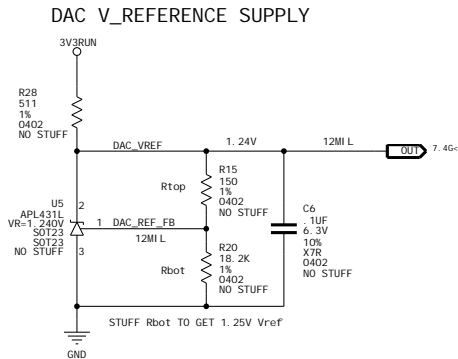
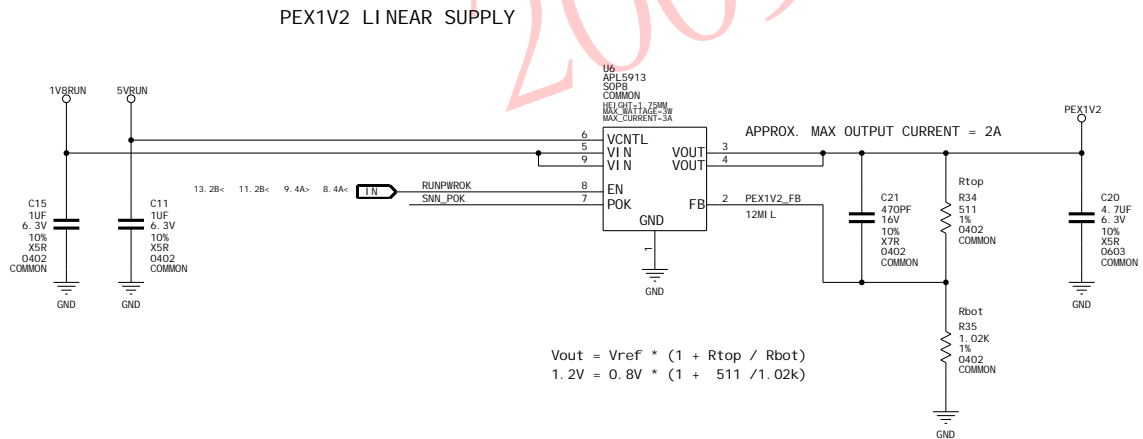
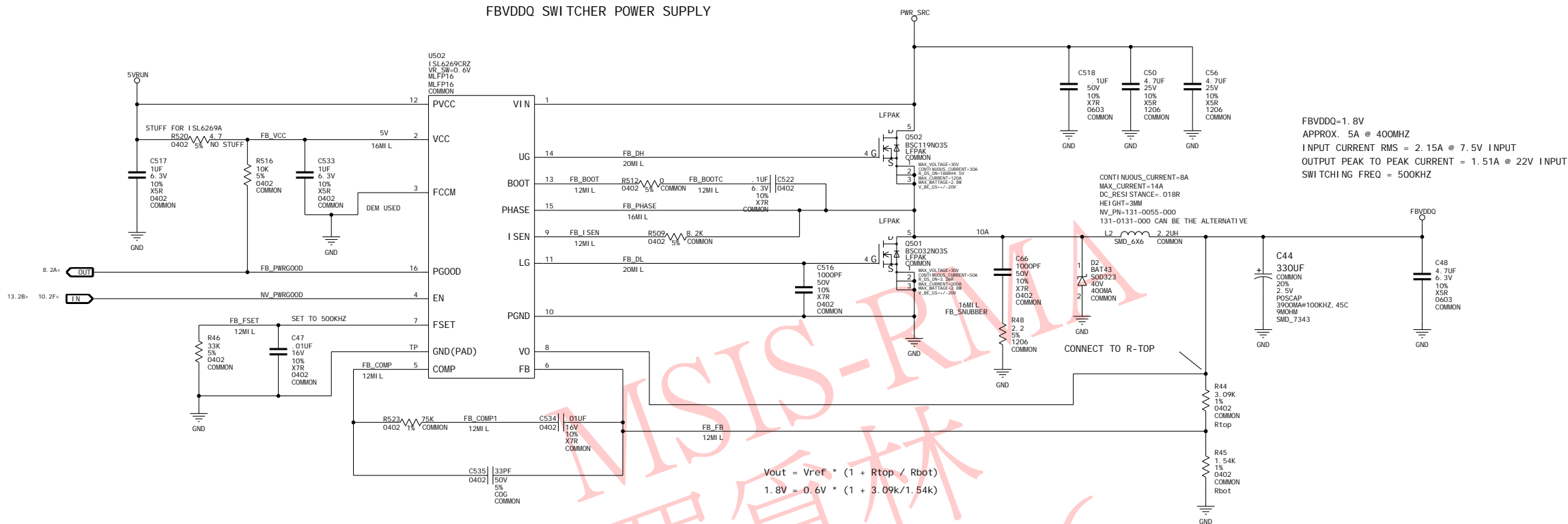


G84M	RTop	RBot	GPI 05
1. 1V 1. 0V	3. 01K 3. 01K	4. 42K 19. 6K 4. 42K	Hi gh Low

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NET	VOLTAGE	MIN_WIOTH_LINE	NV_NET_MAX_CURRENT
PEX1V2	PEX1V2	1.2V	12MIL
FBVDDQ	FBVDDQ	1.8V	12MIL
			2A
			10A



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NAME	myan	DATE	23-OCT-2006




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<table><tr><td colspan="2">ASSEMBLY</td><td colspan="7">G84M-600 450/400 256MB 128bit GDDR2 16Mx16 84FBGA, LVDS + DVI_A/DVI_B + TV_OUT + VGA, MXM-II, HDCP.</td></tr><tr><td colspan="2">PAGE DETAIL</td><td colspan="7"><edit here to insert page detail></td></tr></table> <div>ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, 'MATERIALS') ARE BEING PROVIDED 'AS IS'. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.</div> <table><tr><td colspan="2">NVIDIA CORPORATION</td><td colspan="2" rowspan="2"></td></tr><tr><td colspan="2">2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA</td></tr><tr><td>NV_PN</td><td colspan="3">600-10407-0001-000 A</td><td colspan="2"></td><td colspan="2"></td></tr><tr><td>ID</td><td>p407_a00</td><td>PAGE</td><td colspan="2">17 OF 18</td><td colspan="2"></td><td colspan="2"></td></tr><tr><td>NAME</td><td>myan</td><td>DATE</td><td colspan="2">23-OCT-2006</td><td colspan="2"></td><td colspan="2"></td></tr></table>									ASSEMBLY		G84M-600 450/400 256MB 128bit GDDR2 16Mx16 84FBGA, LVDS + DVI_A/DVI_B + TV_OUT + VGA, MXM-II, HDCP.							PAGE DETAIL		<edit here to insert page detail>							NVIDIA CORPORATION				2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA		NV_PN	600-10407-0001-000 A							ID	p407_a00	PAGE	17 OF 18						NAME	myan	DATE	23-OCT-2006					
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A		B		C		D		E		F		G		H	
<div>Title: Cref Part Report Desi gn: p407_a00 Date: Sep 27 15: 59: 10 2006</div> <div>C1 [7. 5E] C2 [2. 1A] C3 [13. 2E] C4 [8. 4C] C5 [2. 3G] C6 [11. 3F] C7 [14. 2E] C8 [7. 5C] C9 [10. 2D] C10 [2. 3A] C11 [10. 2B] C12 [4. 3B] C13 [8. 4C] C14 [6. 4F] C15 [6. 4F] C16 [6. 2B] C17 [6. 4E] C18 [6. 2A] C19 [6. 4E] C20 [14. 4C] C21 [14. 4E] C22 [14. 4E] C23 [6. 4E] C24 [6. 4E] C25 [6. 2F] C26 [6. 2E] C27 [6. 2E] C28 [5. 2G] C29 [6. 2E] C30 [6. 2E] C31 [6. 2B] C32 [6. 2F] C33 [6. 2A] C34 [13. 2H] C35 [13. 2H] C36 [4. 3D] C37 [14. 3C] C38 [14. 2B] C39 [6. 2E] C40 [6. 2E] C41 [6. 3E] C42 [6. 3F] C43 [14. 2F] C44 [2. 3A] C45 [14. 2G] C46 [2. 2A] C47 [13. 2G] C48 [13. 2G] C49 [14. 2D] C50 [6. 2G] C51 [13. 2F] C52 [14. 2F] C53 [14. 2E] C54 [13. 2B] C55 [6. 2E] C56 [13. 1F] C57 [14. 2E] C58 [6. 2F] C59 [13. 1F] C60 [6. 2E] C61 [6. 2F] C62 [6. 3E] C63 [4. 3G] C64 [6. 3E] C65 [6. 3E] C66 [6. 2G] C67 [13. 1E] C68 [6. 2F] C69 [13. 1F] C70 [6. 3F] C71 [8. 4C] C72 [3. 5E] C73 [4. 3E] C74 [4. 3H] C75 [5. 2E] C76 [5. 2H] C77 [8. 4D] C78 [12. 1B] C79 [12. 1C] C80 [12. 2C] C81 [12. 1C] C82 [12. 1C] C83 [5. 3B] C84 [5. 3D] C85 [8. 4C] C86 [13. 1F] C87 [14. 4C] C88 [3. 2D] C89 [14. 2F]</div>		<div>C90 [14. 4G] C501 [4. 3C] C502 [6. 2D] C503 [6. 4D] C504 [6. 4C] C505 [6. 4D] C506 [6. 2C] C507 [6. 2D] C508 [6. 2D] C509 [13. 2C] C510 [6. 2G] C511 [6. 4D] C512 [13. 1E] C513 [13. 3C] C514 [13. 3C] C515 [2. 2A] C517 [6. 2G] C518 [13. 2B] C519 [13. 2D] C520 [14. 2B] C521 [9. 4B] C522 [14. 2D] C523 [6. 4C] C524 [6. 4D] C525 [6. 2C] C526 [6. 2H] C527 [6. 2D] C528 [13. 4D] C529 [13. 4C] C530 [14. 3B] C531 [14. 3C] C532 [2. 5D] C533 [2. 5C] C534 [13. 2D] C535 [2. 5D] C536 [3. 4D] C537 [2. 5C] C538 [2. 1H] C539 [3. 5A] C540 [2. 5D] C541 [3. 4C] C542 [2. 5C] C543 [6. 2D] C544 [2. 4D] C545 [3. 2D] C546 [3. 1D] C547 [3. 1D] C548 [6. 2D] C549 [2. 4C] C550 [2. 4D] C551 [2. 4C] C552 [2. 4D] C553 [6. 2D] C554 [6. 2D] C555 [2. 4C] C556 [2. 4D] C557 [2. 4C] C558 [2. 4D] C559 [2. 4C] C560 [2. 1G] C561 [2. 3D] C562 [2. 1H] C563 [2. 3C] C564 [2. 3D] C565 [3. 4C] C566 [2. 4G] C567 [2. 2H] C568 [2. 1G] C569 [3. 1C] C570 [3. 1C] C571 [2. 3C] C572 [2. 2G] C573 [3. 2C] C574 [2. 1G] C575 [2. 3D] C576 [3. 2D] C577 [2. 1G] C578 [2. 1H] C579 [2. 3C] C580 [3. 1D] C581 [2. 2G] C582 [2. 3D] C583 [2. 2G] C584 [2. 2G] C585 [3. 1D] C586 [6. 2C] C587 [6. 2C] C588 [2. 1H] C589 [2. 1G] C590 [3. 2C] C591 [2. 2G] C592 [2. 3C] C593 [2. 1G] C594 [2. 2G] C595 [2. 3D] C596 [2. 1G]</div>		<div>C597 [3. 1D] C598 [3. 1D] C599 [2. 2G] C600 [2. 2G] C601 [2. 3C] C602 [3. 2C] C603 [2. 2D] C604 [2. 1G] C605 [2. 2G] C606 [2. 2G] C607 [3. 2D] C608 [2. 4G] C609 [2. 2C] C610 [2. 2H] C611 [2. 4G] C612 [2. 2D] C613 [2. 2G] C614 [2. 2G] C615 [2. 2H] C616 [3. 2D] C617 [3. 2D] C618 [2. 4G] C619 [2. 1G] C620 [2. 2C] C621 [6. 2B] C622 [2. 2D] C623 [2. 2G] C624 [6. 2B] C625 [2. 4G] C626 [2. 2G] C627 [2. 2C] C629 [2. 2G] C630 [3. 1C] C631 [2. 2G] C632 [2. 2G] C633 [2. 4G] C634 [2. 1G] C635 [3. 2C] C636 [3. 1C] C637 [3. 4H] C638 [2. 4G] C639 [2. 2G] C641 [2. 2G] C642 [2. 2G] C643 [6. 3D] C644 [2. 2H] C645 [3. 2C] C646 [7. 1B] C647 [2. 4G] C648 [6. 3D] C649 [2. 3G] C650 [3. 4H] C651 [3. 4H] C652 [3. 2C] C653 [7. 1C] C654 [8. 2D] C655 [8. 2D] C656 [2. 3G] C657 [7. 4C] C658 [7. 4C] C659 [8. 4D] C660 [7. 2B] C661 [3. 2D] C662 [7. 1C] C663 [8. 2D] C664 [2. 4G] C665 [7. 2C] C667 [8. 2D] C668 [5. 2C] C669 [8. 4D] C670 [3. 2D] C671 [8. 4D] C672 [12. 3C] C673 [8. 2C] C674 [8. 4D] C676 [8. 4D] C677 [7. 2C] C678 [6. 3C] C679 [6. 3C] C680 [6. 3D] C681 [8. 2C] C682 [7. 4B] C683 [7. 2B] C684 [7. 1B] C685 [6. 3D] C686 [8. 2C] C687 [8. 4C] C688 [3. 2D] C689 [8. 2D] C690 [8. 2B] C691 [11. 2D] C692 [11. 2E] C693 [11. 2D] C694 [8. 2C] C695 [11. 2D] C697 [11. 4E]</div>		<div>C698 [2. 3G] C598 [2. 3B] CN1 [9. 3D] D1 [13. 2E] D2 [14. 2E] D501 [10. 3G] D502 [10. 3G] G1 [2. 3F] G1 [3. 3B 3. 3F] G1 [7. 4D 7. 3D 7. 2D] G1 [7. 1D] G1 [8. 4E 8. 2E] G1 [10. 3C] G1 [11. 4B 11. 3G] G1 [12. 4D 12. 2D] L1 [13. 2F] L2 [14. 2E] LB1 [8. 4C] LB2 [12. 1B] LB3 [8. 4C] LB501 [3. 4D] LB502 [2. 4H] LB504 [3. 4H] LB506 [7. 2B] LB507 [8. 3C] LB508 [7. 1B] LB509 [7. 4B] LB510 [8. 2C] LB511 [8. 1C] M1 [5. 4C 5. 4D] M1 [5. 2D] M2 [5. 5B 5. 5D] M2 [5. 2F] M3 [4. 4B 4. 4D] M3 [4. 2D] M4 [4. 5E 4. 5B] M4 [4. 2F] M501 [4. 2B 4. 4E] M501 [4. 4C] M502 [4. 5D 4. 5C] M502 [4. 2H] M503 [5. 5C 5. 2G] M503 [5. 5E] M504 [5. 4E 5. 4B] M504 [5. 2B] MEC1 [15. 4F 15. 4F] MEC2 [15. 4F 15. 4F] MEC2 [15. 4F 15. 3F] O1 [10. 2E] O2 [8. 4C] O3 [8. 4B] O4 [8. 4B] O5 [8. 4B] O501 [14. 2D] O502 [13. 2E] O503 [14. 2D] O504 [13. 2E] O505 [13. 4D] O506 [13. 2D] O507 [13. 4D] O509 [8. 2B] O510 [10. 2F] O511 [10. 2E] R1 [11. 2C] R2 [11. 2D] R3 [10. 2B] R4 [8. 4C] R5 [10. 2B] R6 [10. 2B] R7 [4. 3B] R8 [4. 3D] R9 [10. 2B] R10 [10. 2D] R11 [3. 5E] R12 [10. 2D] R13 [10. 2D] R14 [11. 2C] R15 [7. 4E] R16 [15. 3C] R17 [10. 3E] R18 [10. 3E] R19 [10. 3D] R20 [10. 3D] R21 [11. 3E] R22 [3. 5E] R23 [15. 2C] R24 [15. 2C] R25 [15. 2D] R26 [7. 4F] R27 [5. 2D] R28 [5. 2D] R29 [5. 3A] R30 [14. 5E] R31 [14. 4E] R32 [2. 2D] R33 [5. 3D]</div>		<div>R34 [5. 2F] R35 [5. 2F] R36 [5. 3B] R37 [7. 4H] R38 [14. 2B] R39 [14. 3F] R40 [4. 3E] R41 [4. 2E] R42 [4. 3D] R43 [14. 3F] R44 [13. 2F] R45 [13. 2B] R46 [14. 2D] R47 [4. 3A] R48 [4. 2F] R49 [4. 3F] R50 [9. 3G] R51 [9. 3G] R52 [9. 3G] R53 [9. 3G] R54 [10. 2B] R55 [5. 3D] R56 [15. 2D] R57 [8. 4B] R58 [8. 4B] R59 [10. 2A] R60 [10. 2B] R61 [10. 2B] R62 [12. 2C] R63 [12. 2C] R64 [12. 2C] R65 [12. 2C] R66 [7. 4H] R67 [10. 2A] R68 [14. 4C] R69 [14. 4C] R70 [15. 2C] R72 [10. 2B] R73 [14. 4G] R74 [3. 5D] R75 [3. 5D] R76 [3. 5D] R77 [3. 5D] R501 [13. 2B] R502 [4. 2C] R503 [4. 3D] R504 [4. 3C] R505 [13. 2B] R508 [13. 3C] R509 [14. 2D] R510 [14. 2E] R512 [13. 2D] R513 [13. 2D] R514 [9. 4B] R515 [13. 4C] R516 [13. 4D] R517 [13. 3E] R519 [4. 3B] R520 [4. 2H] R521 [14. 2B] R522 [13. 3E] R523 [13. 4D] R524 [14. 3C] R525 [13. 2F] R526 [4. 3H] R528 [13. 4C] R529 [14. 3B] R530 [13. 4C] R531 [3. 5A] R532 [4. 2A] R533 [3. 5A] R534 [4. 3F] R535 [13. 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[10. 2C] U4 [11. 2C] U5 [11. 3F] U6 [14. 4D] U501 [13. 2C] U502 [14. 2C] Y1 [7. 5D]</div>					

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