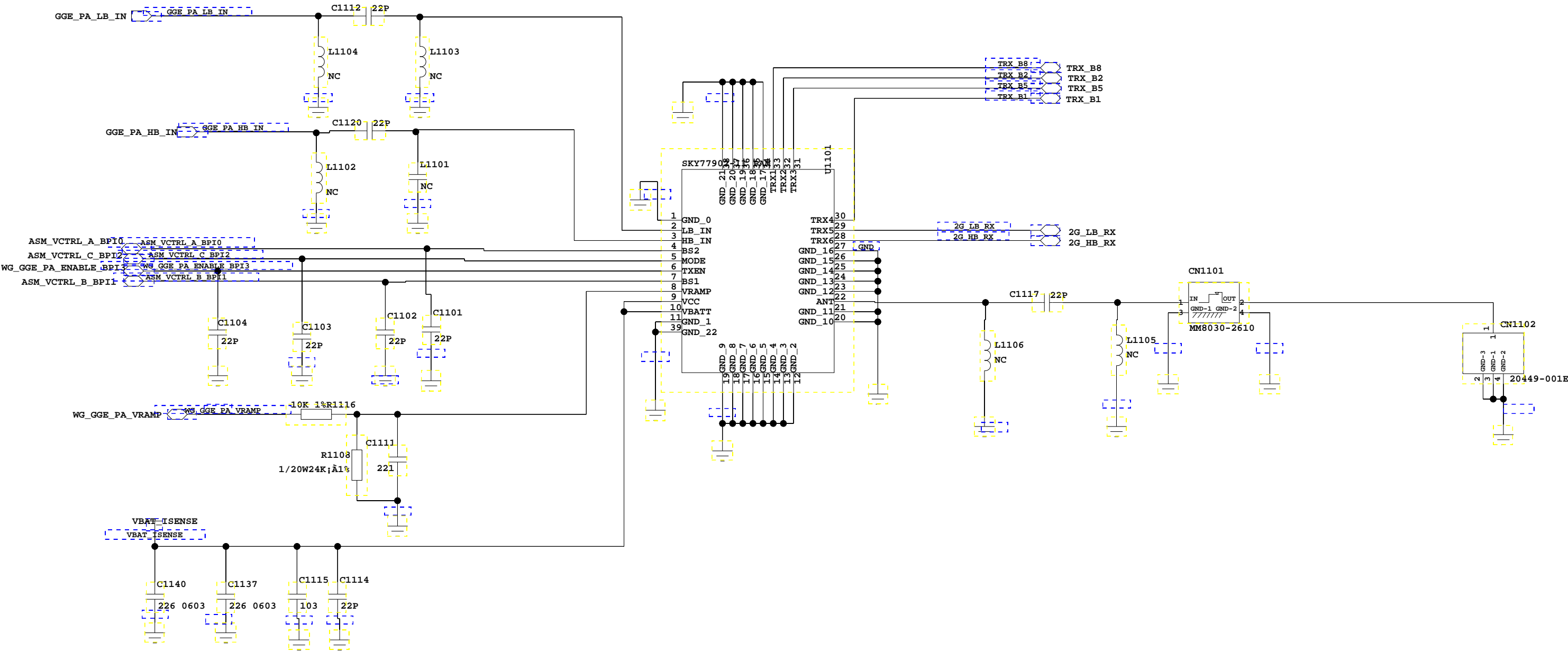
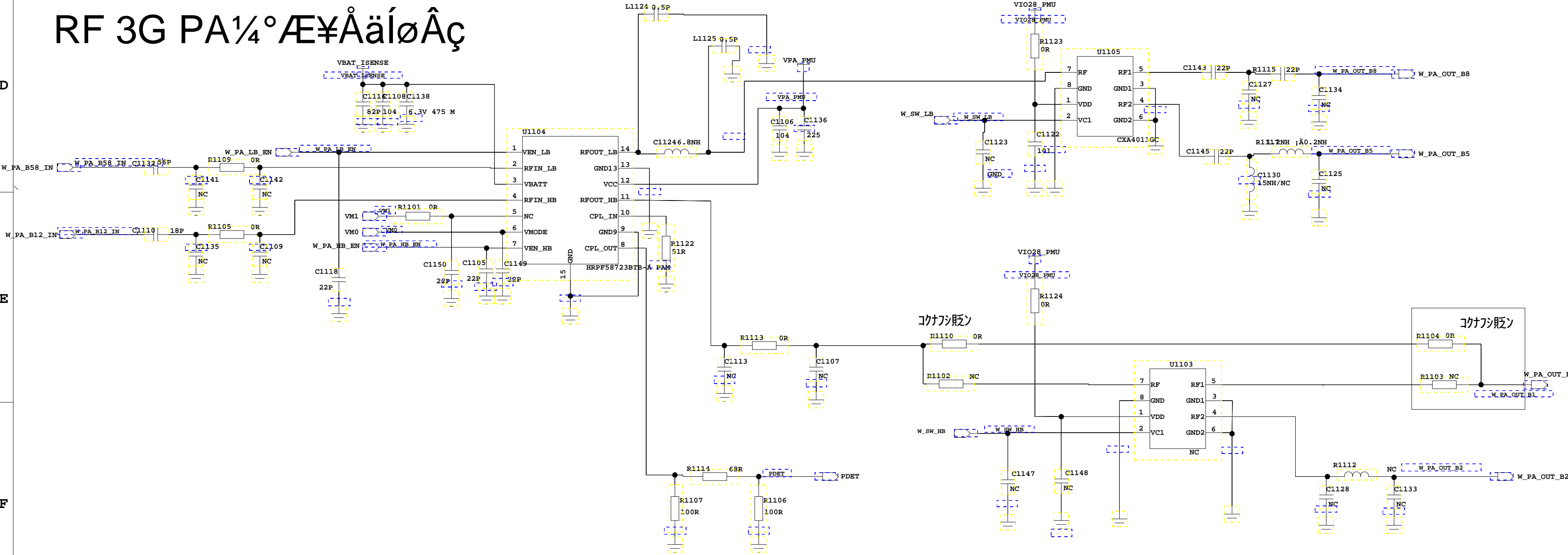
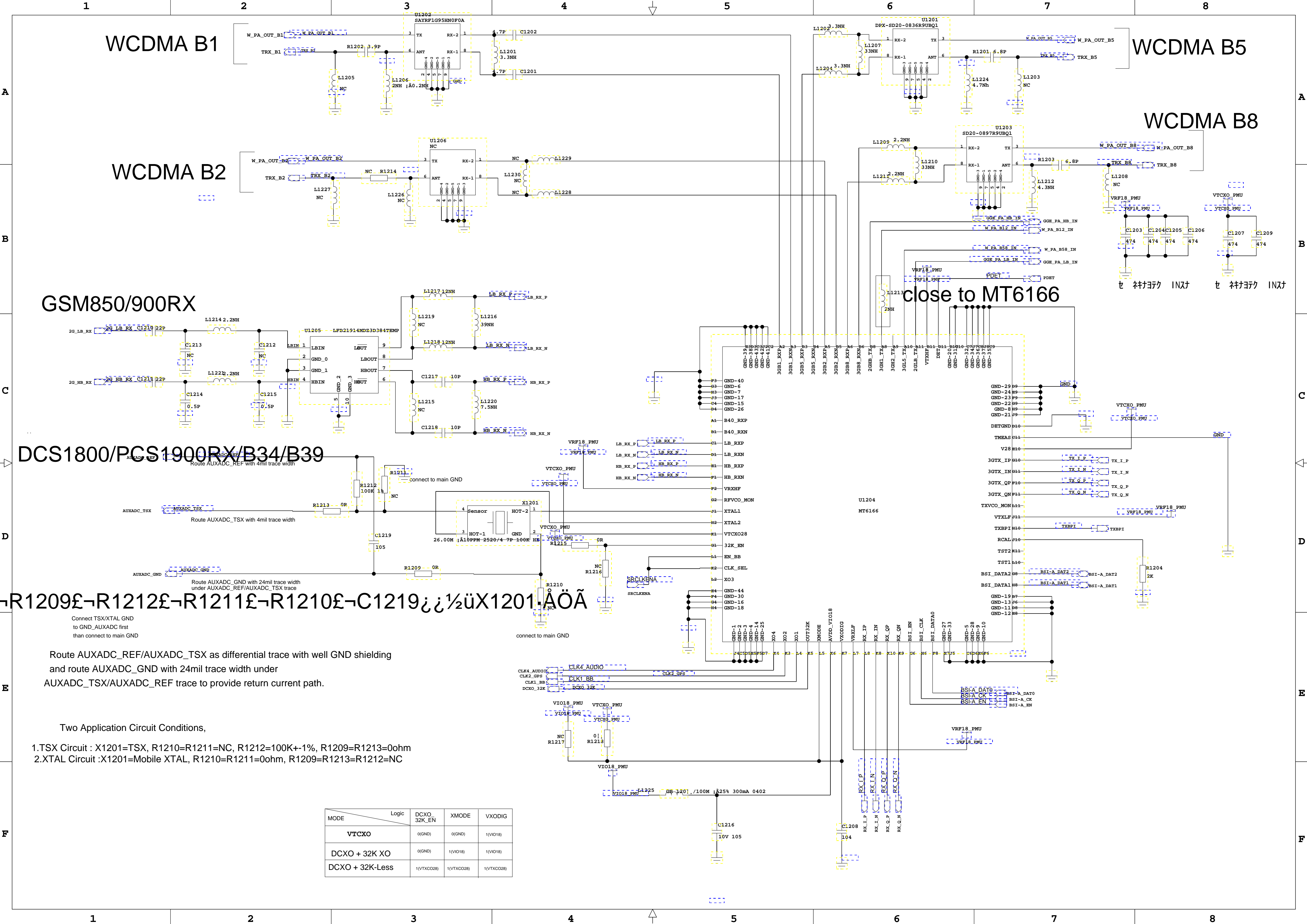


RF 2G PA



RF 3G PA





WCDMA B1

WCDMA B2

WCDMA B5

WCDMA B8

GSM850/900RX

DCS1800/PCS1900RX/B34/B39

close to MT6166

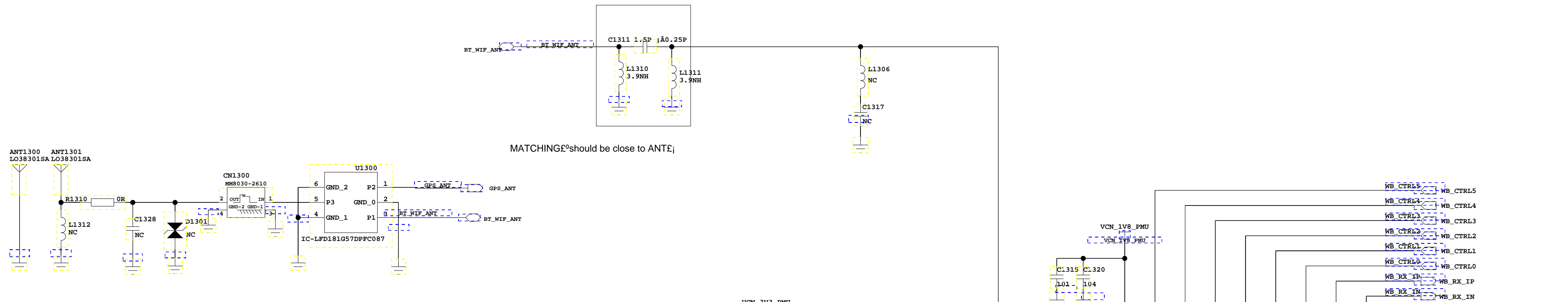
Route AUXADC_REF/AUXADC_TSX as differential trace with well GND shielding and route AUXADC_GND with 24mil trace width under AUXADC_REF/AUXADC_TSX trace to provide return current path.

Two Application Circuit Conditions,

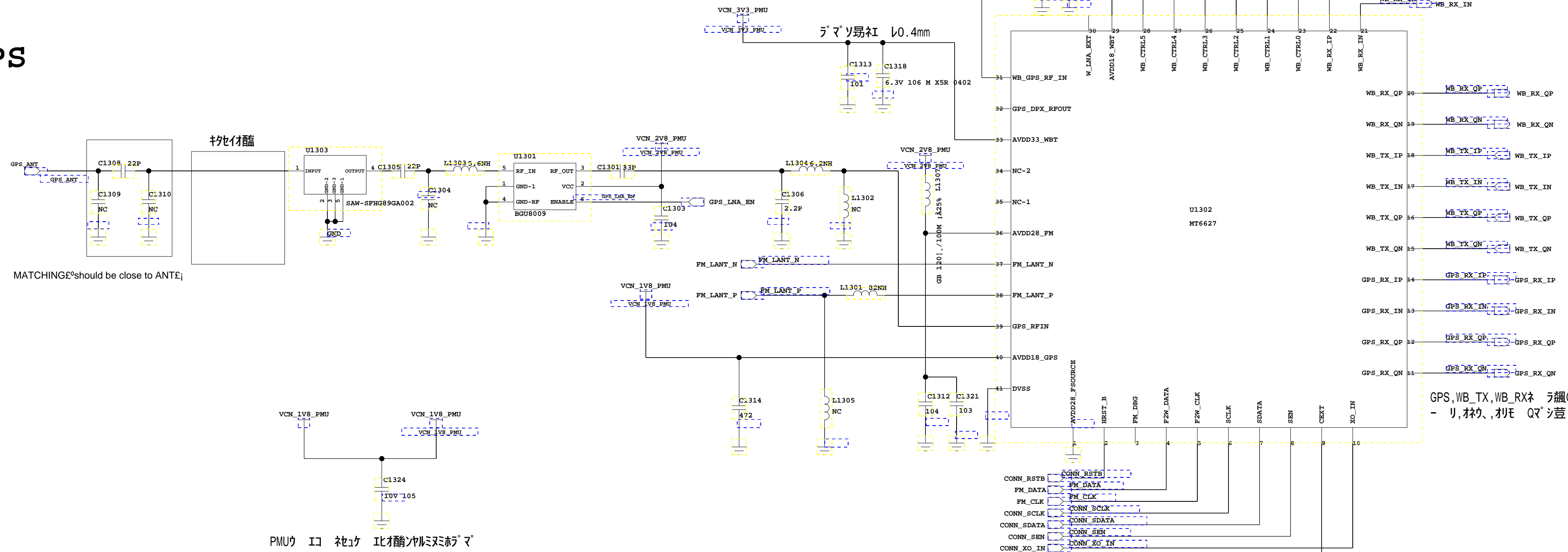
- 1.TSX Circuit : X1201=TSX, R1210=R1211=NC, R1212=100K+-1%, R1209=R1213=0ohm
- 2.XTAL Circuit :X1201=Mobile XTAL, R1210=R1211=0ohm, R1209=R1213=R1212=NC

MODE	Logic	DCXO 32K_EN	XMODE	VXODIG
VTCXO		0(GND)	0(GND)	1(VIO18)
DCXO + 32K XO		0(GND)	1(VIO18)	1(VIO18)
DCXO + 32K-Less		1(VTXCO28)	1(VTXCO28)	1(VTXCO28)

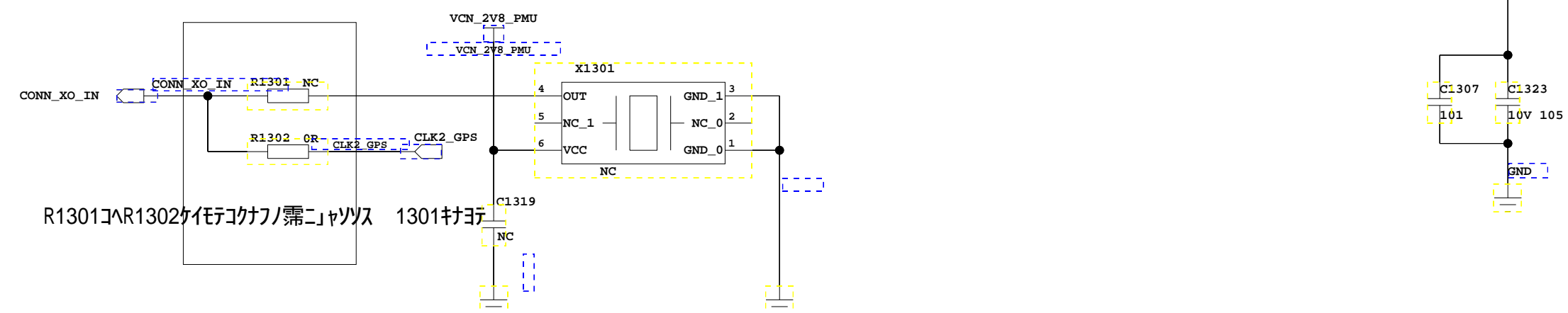
WIFI/Bluetooth



GPS



GPS, WB_TX, WB_RXネ ラ飜QメIノマツラ メ
- リ,オウ、オモ Qマ°シ荳 E2アカ°ソ



PM6323

A

B

C

D

E

F

A

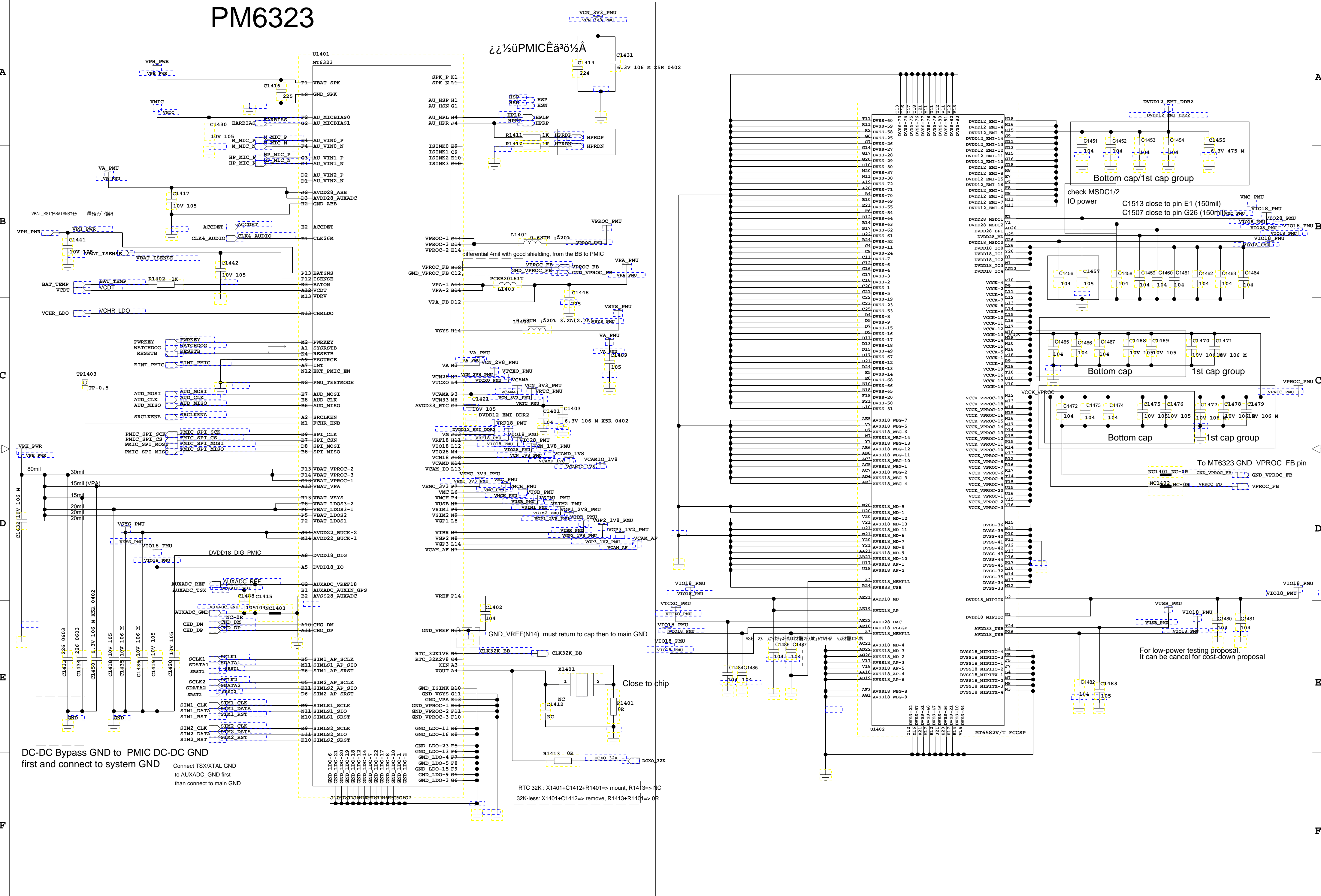
B

C

D

E

F



A

B

C

D

E

F

A

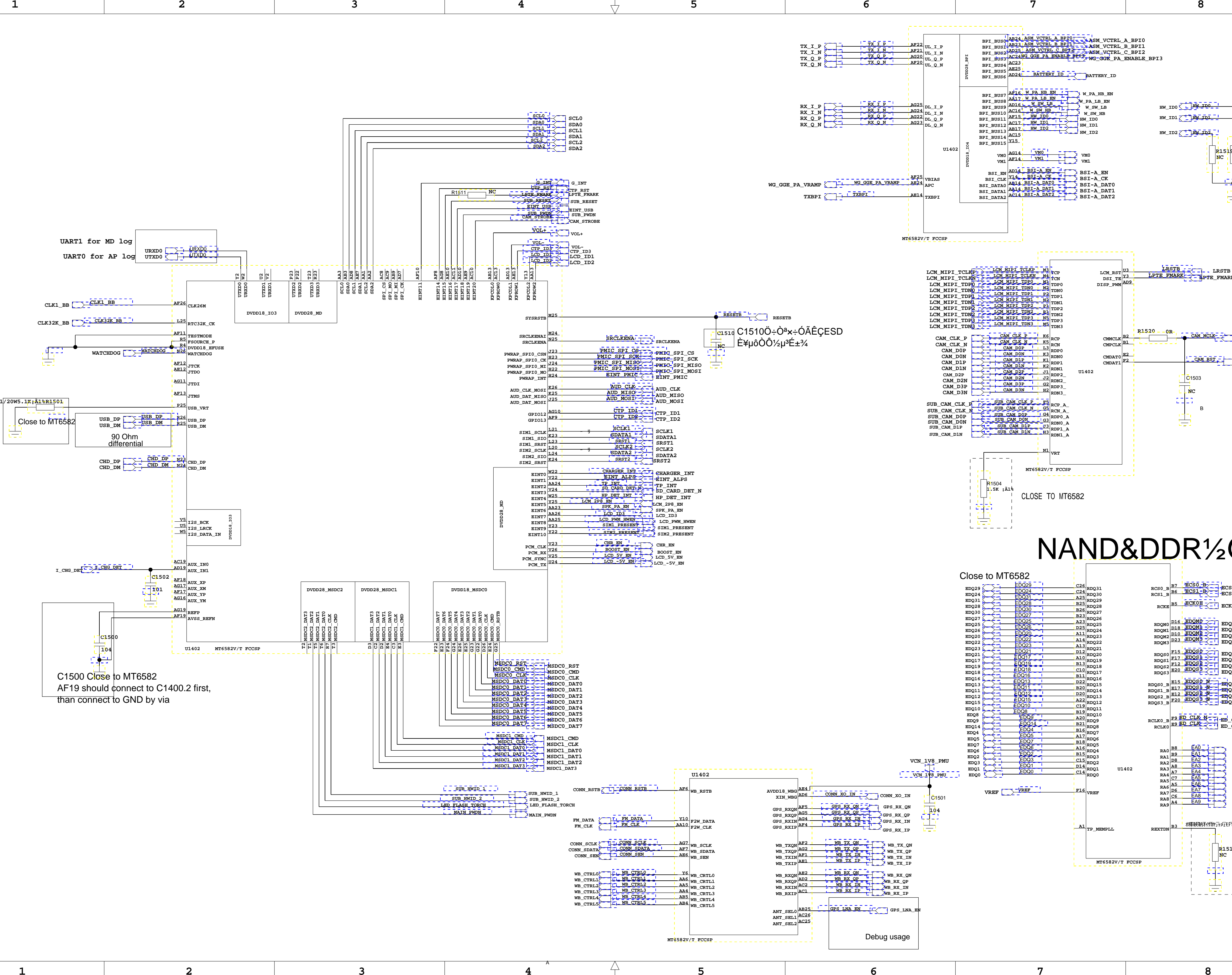
B

C

D

E

F



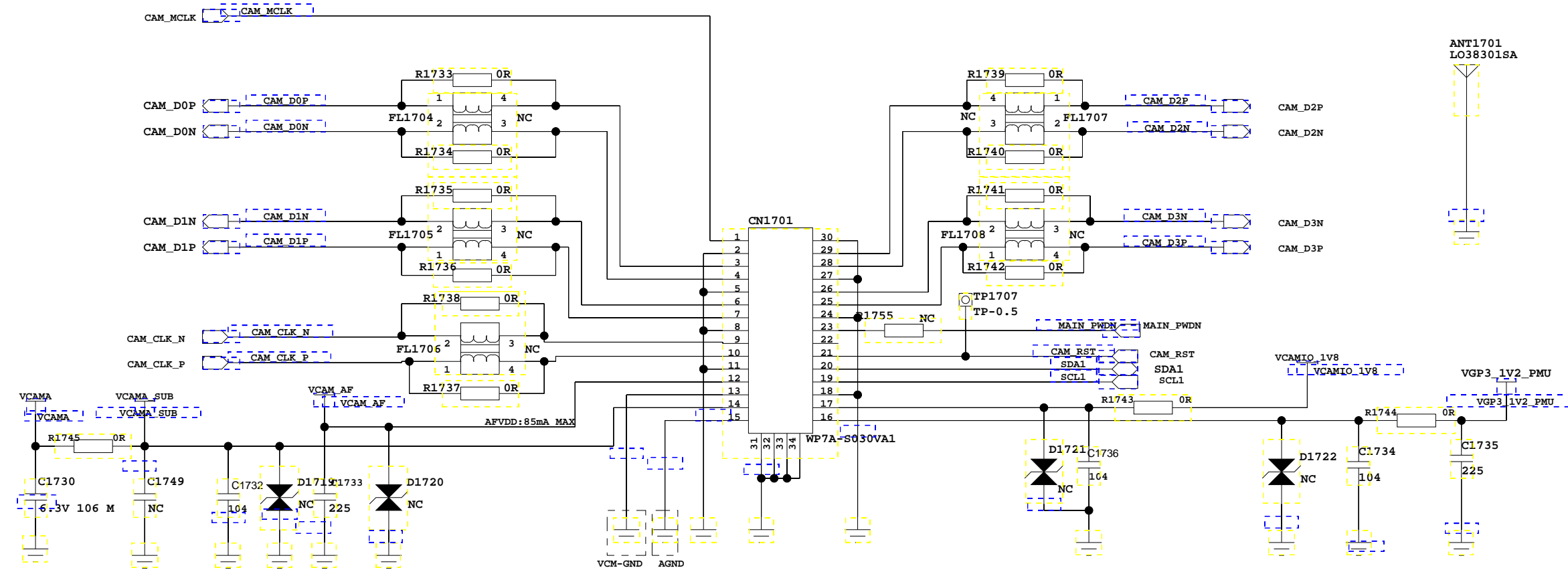
A



CAMERA Cam 8M

I2C Address

0x2D (Write:0x5A, Read:0x5B)



Flash LED Driver

I2C Address

```
0x63 (Write:0xC6, Read:0xC7)
```

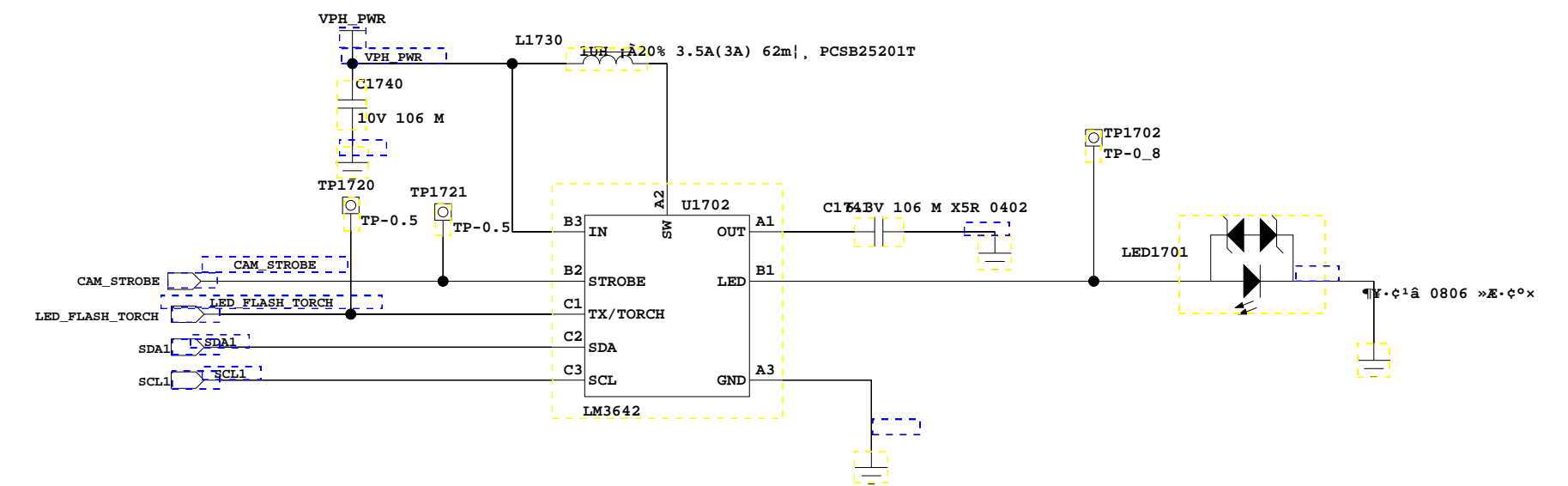
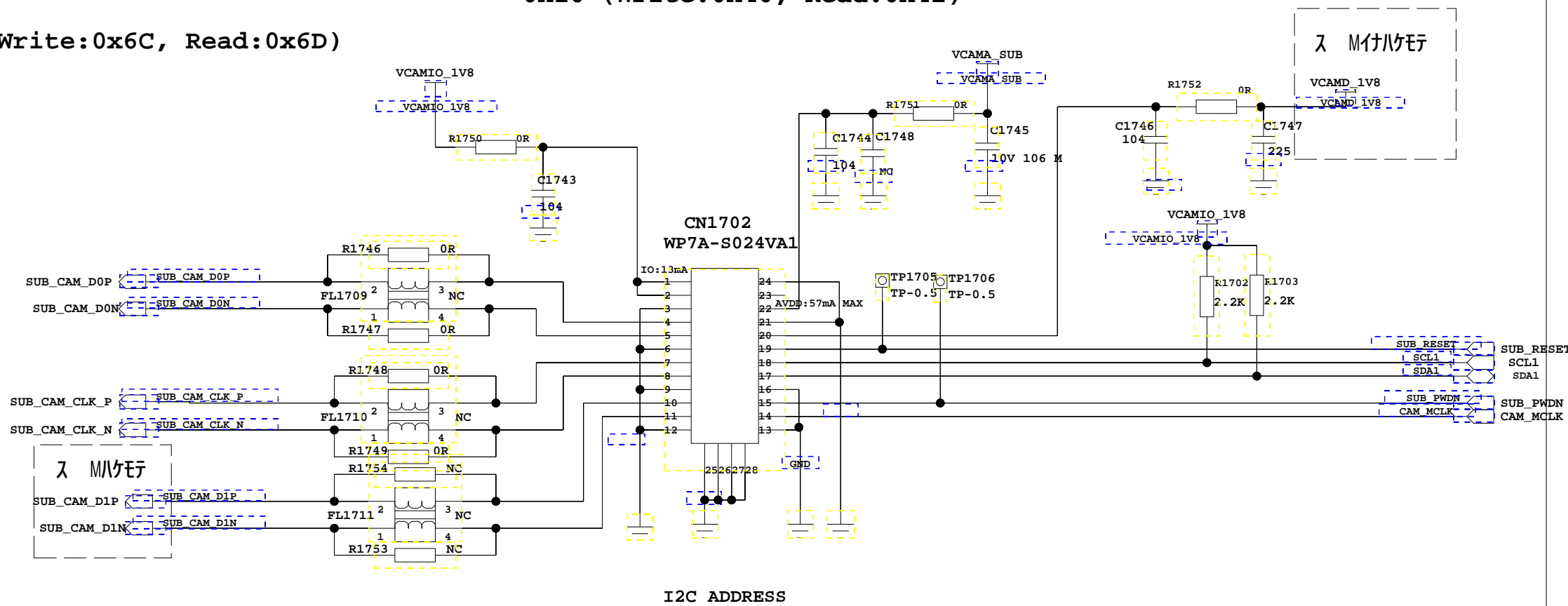
SUB Cam 2M $\frac{1}{4}$ æÈÝ5M

2M-I2C Address

0x20 (Write:0x40, Read:0x41)

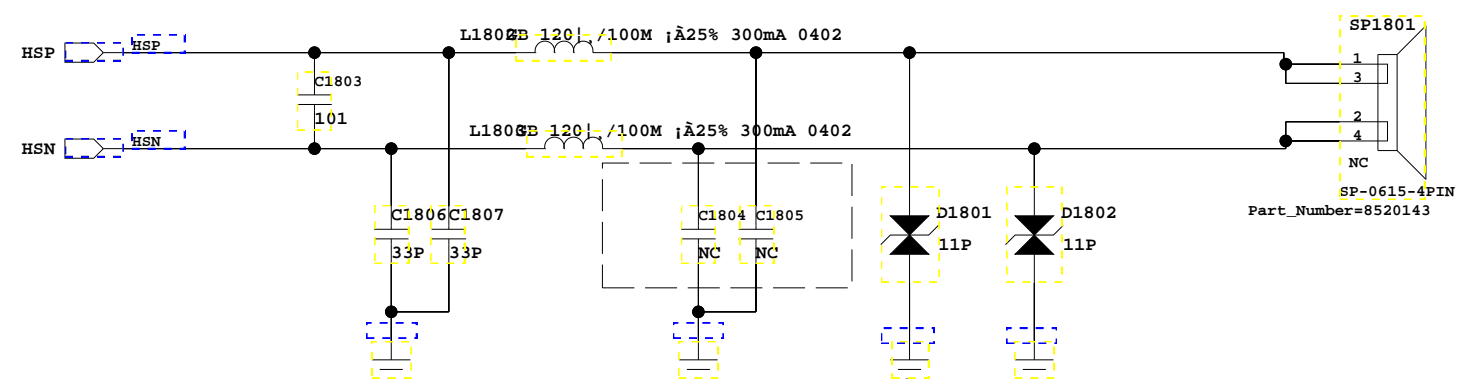
5M-I2C Address

0x36 (Write:0x6C, Read:0x6D)

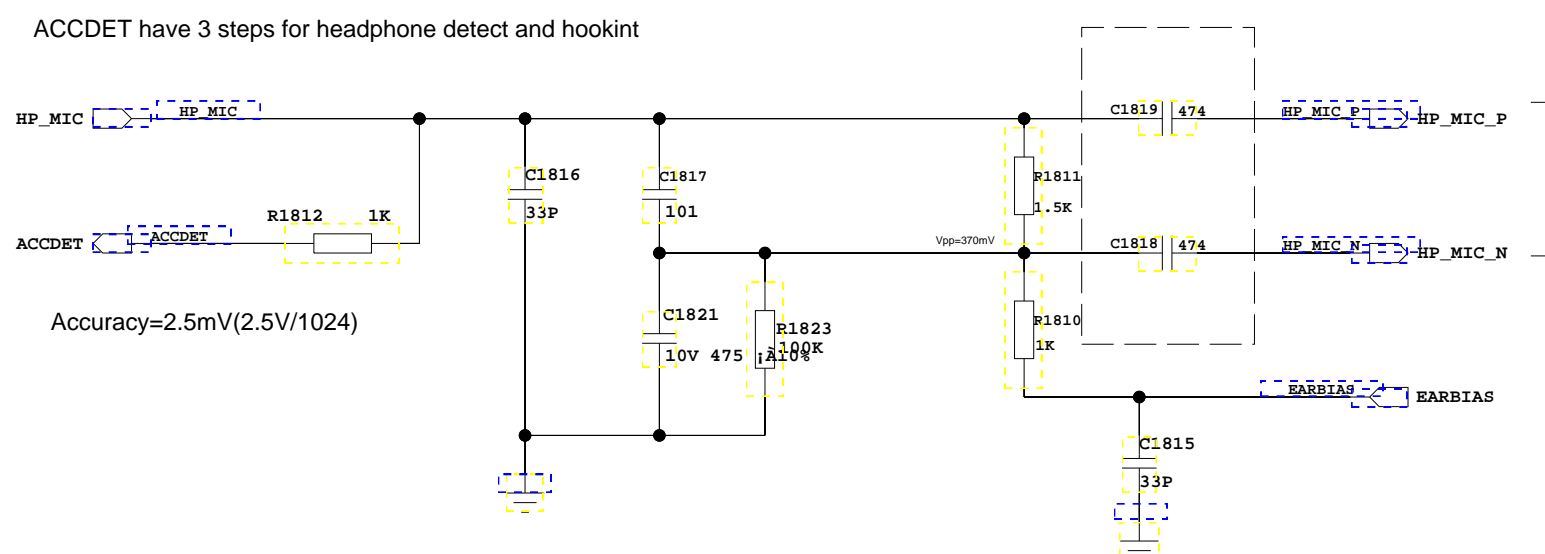


I2C 7bit address is 0x63

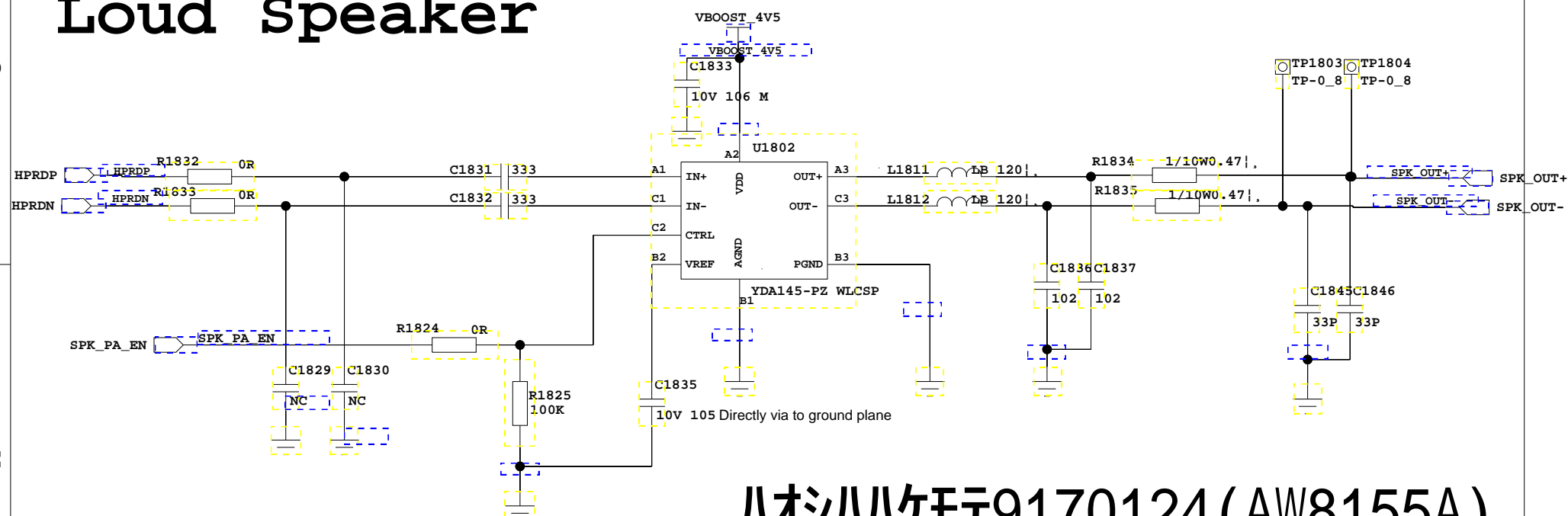
Receiver



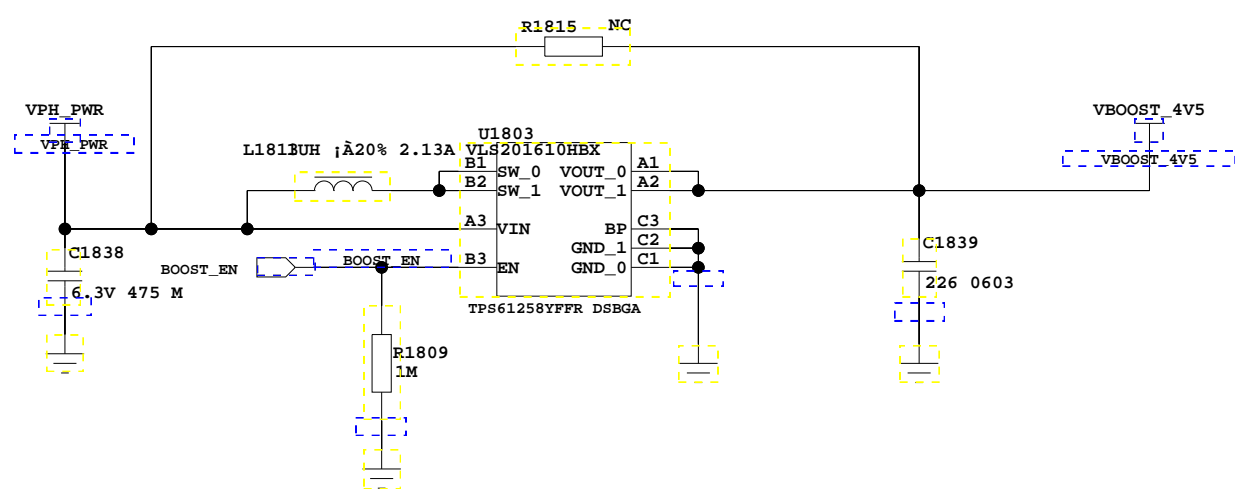
HP MIC



Loud Speaker



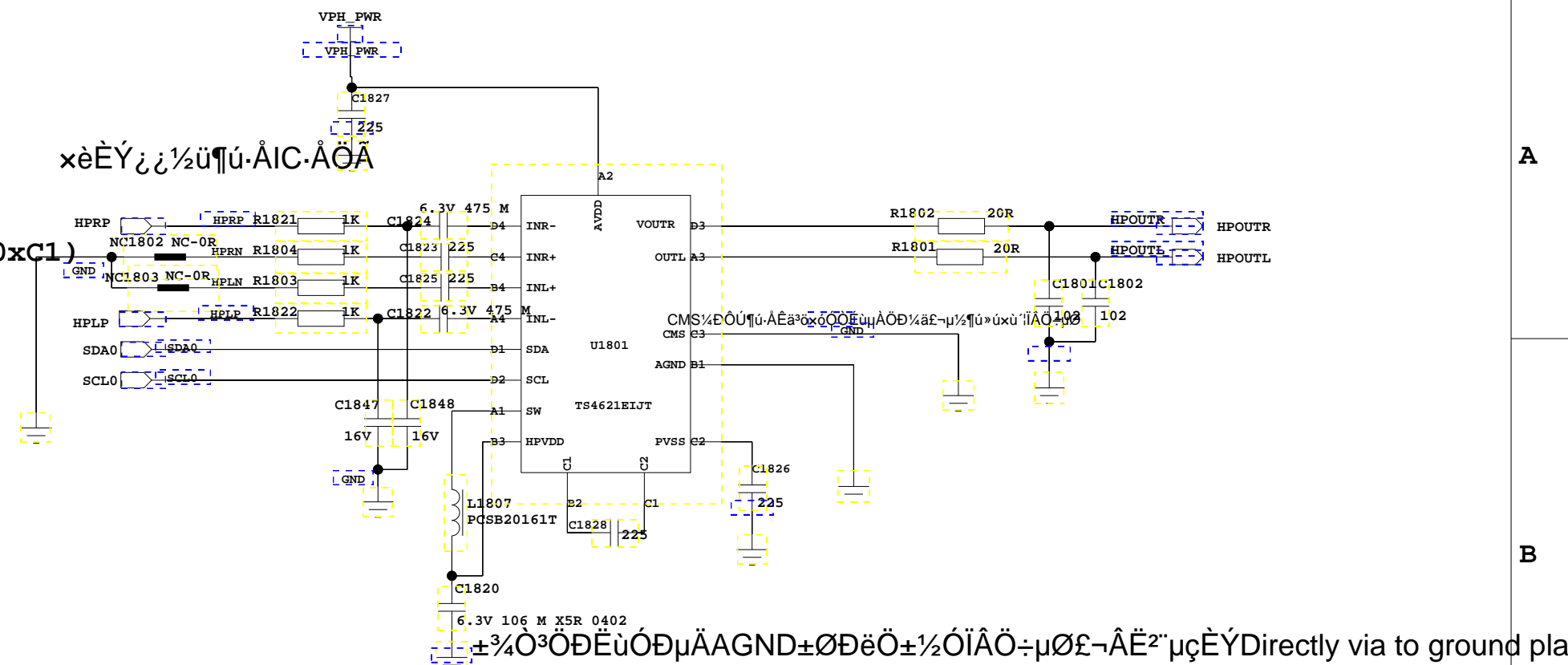
5V BOOST



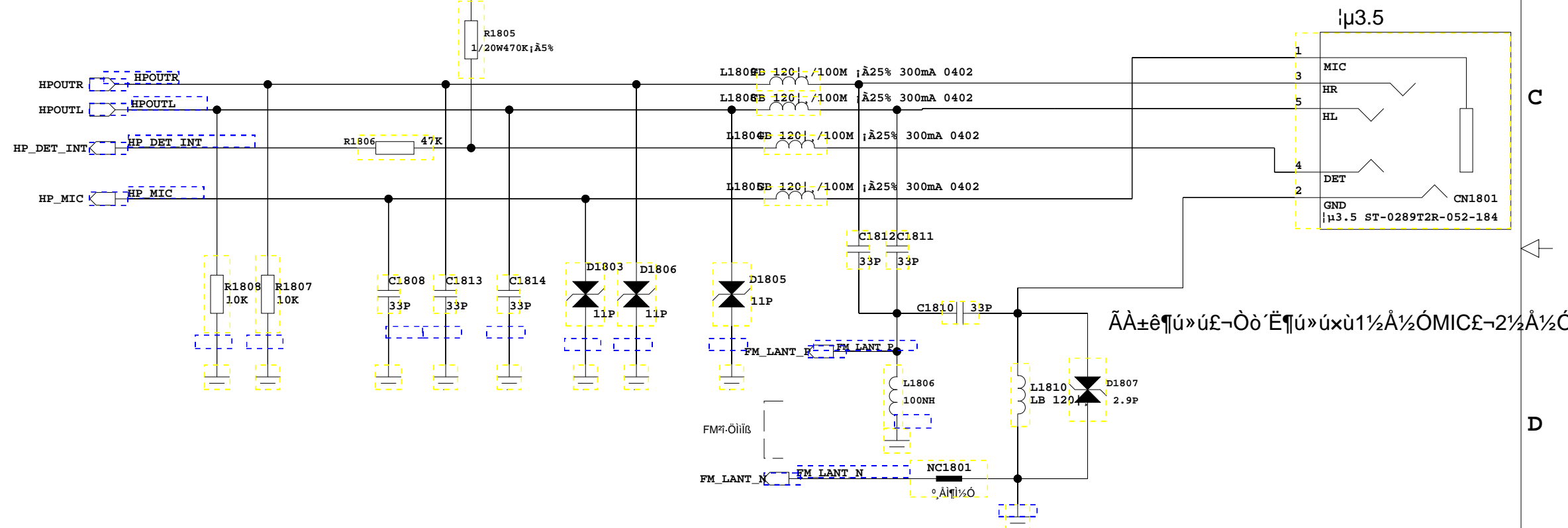
¶ú. Å

I2C Address

0x60 (Write:0xC0, Read:0xC1)



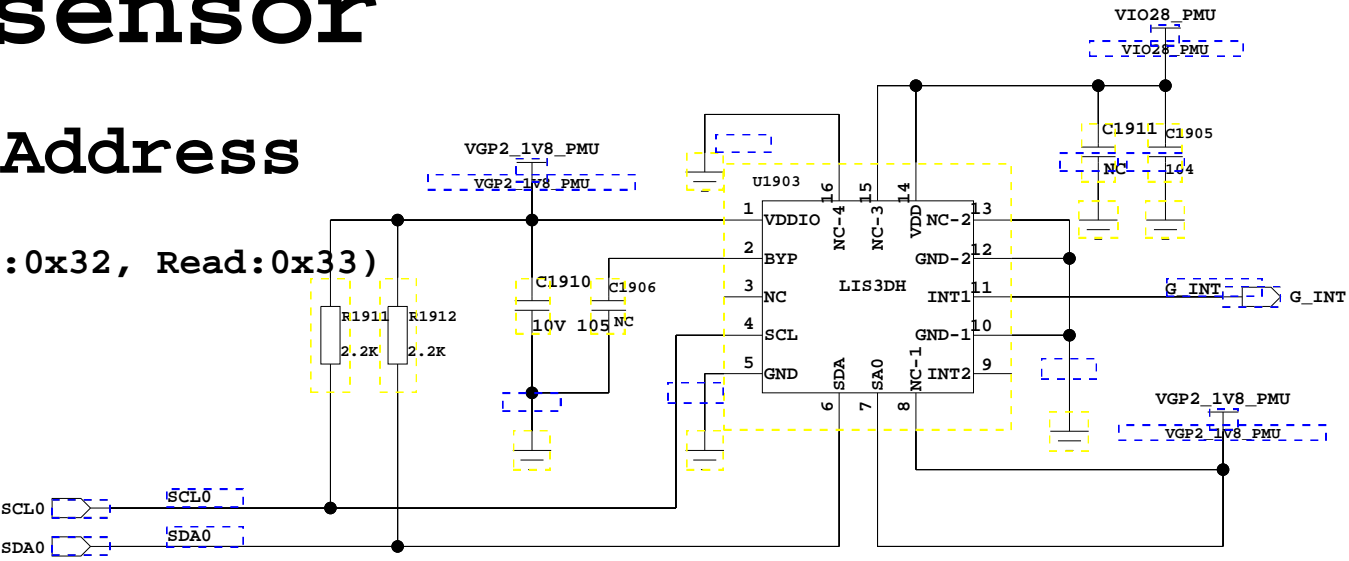
¶ú»ú×û

 $\pm \text{MIC}$

G-sensor

I2C Address

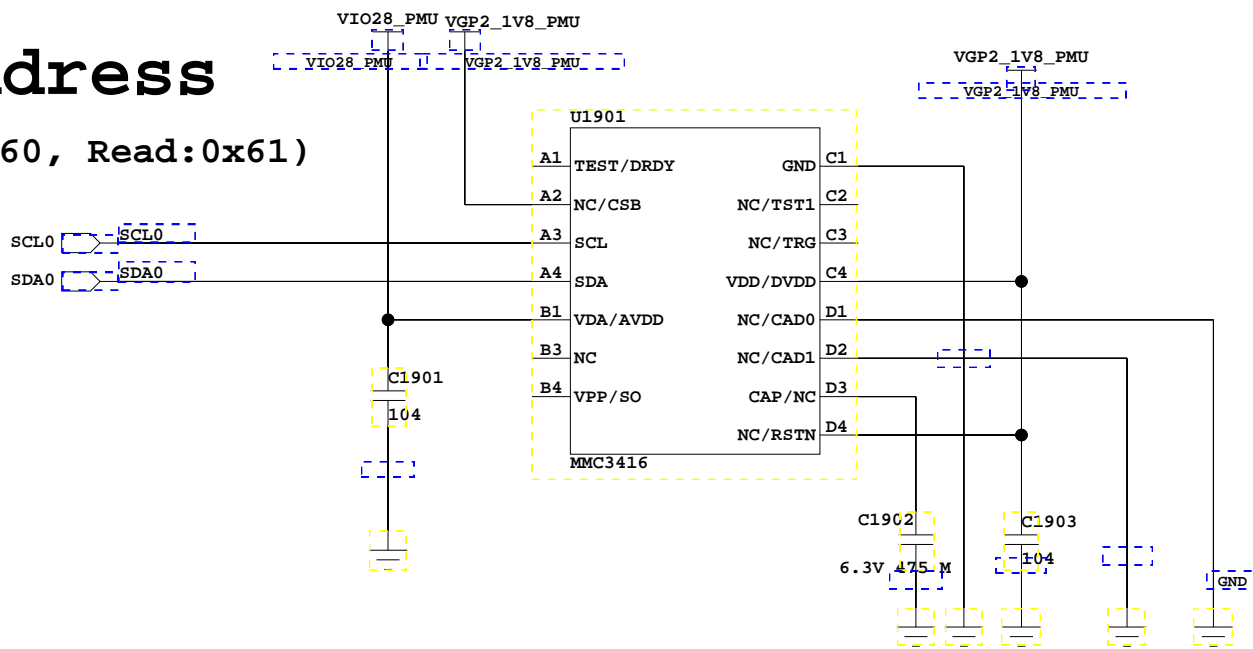
0x19 (Write:0x32, Read:0x33)



M-sensor

I2C Address

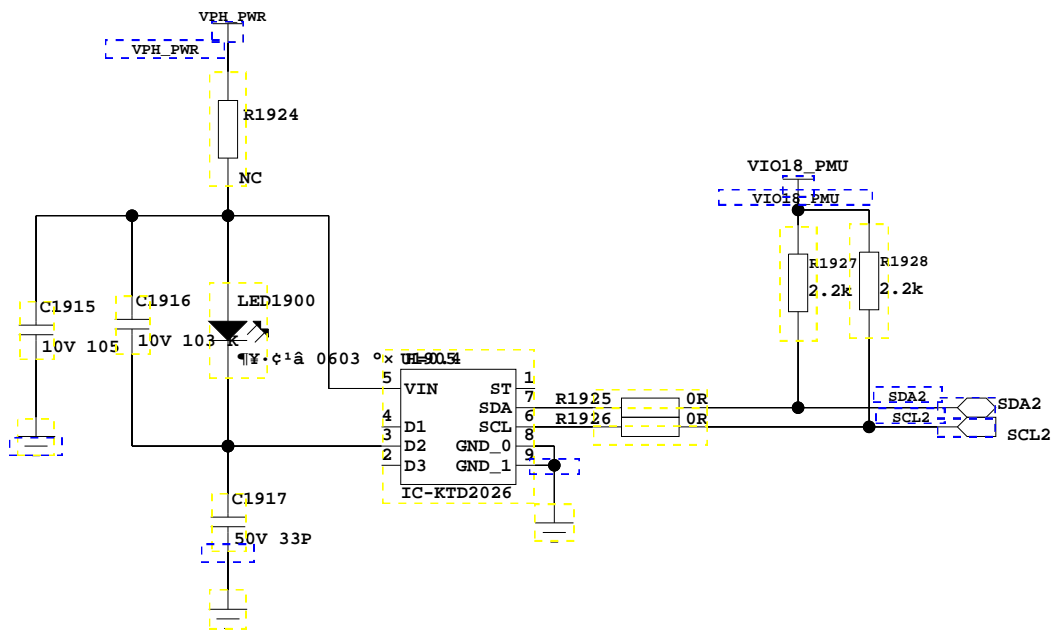
0x30 (Write:0x60, Read:0x61)



Remote Control

I2C Address

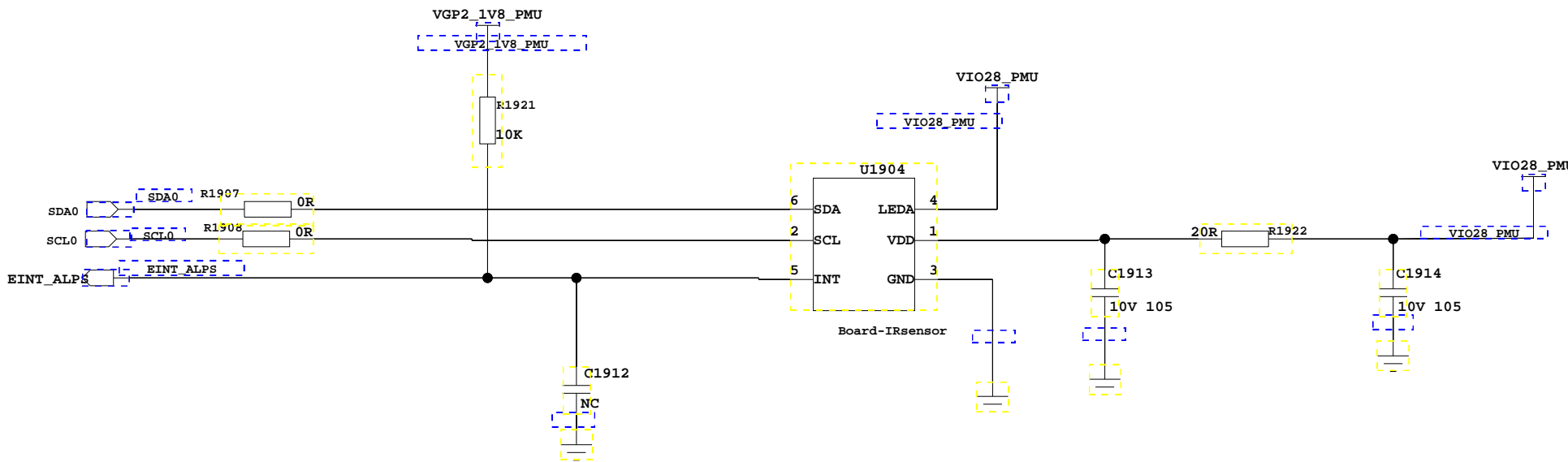
0x30 (Write:0x60, Read:0x61)



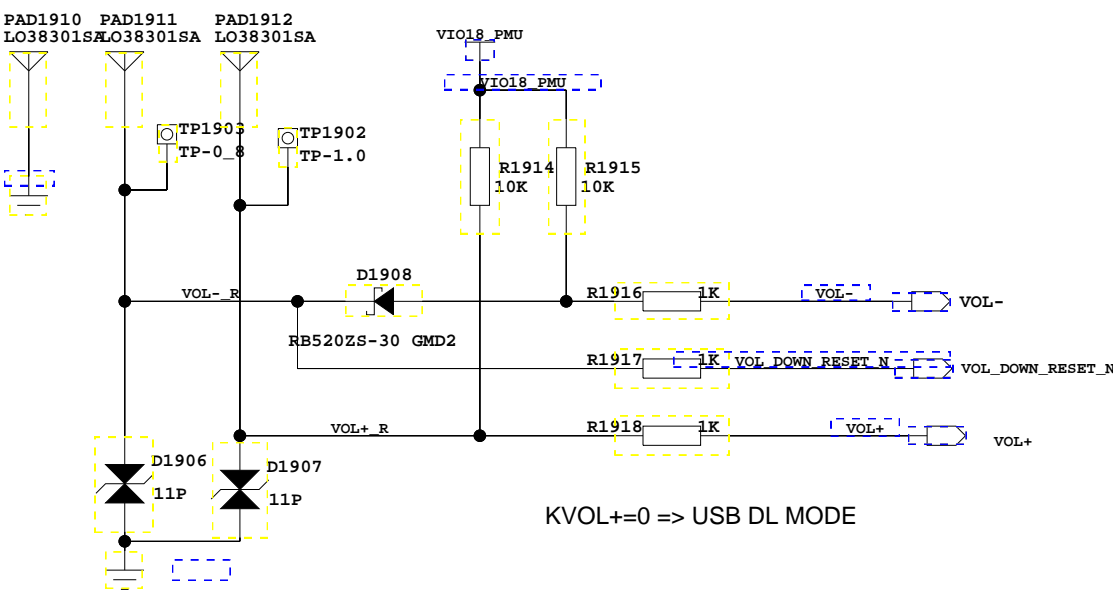
A.L.S.+P.S sensor module & indicator LED

I2C Address

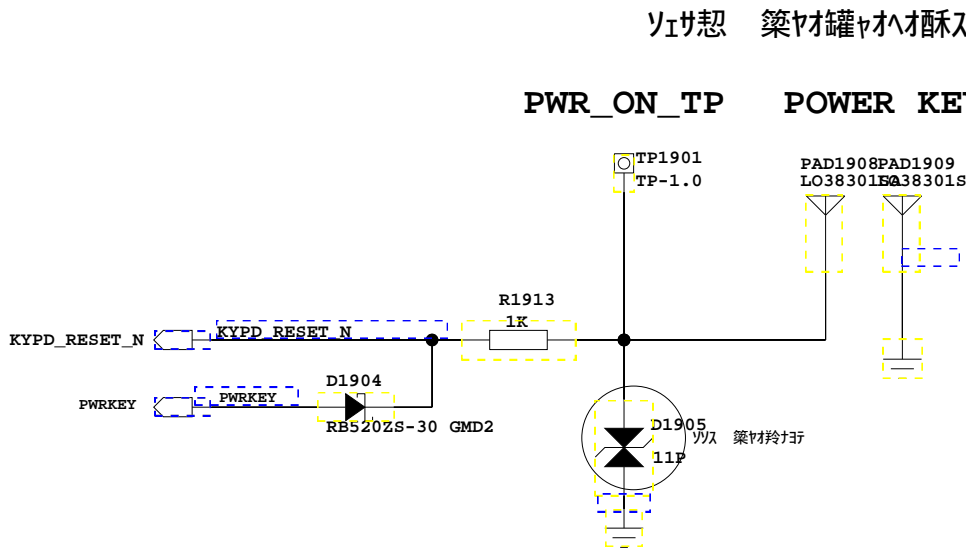
0x39 (Write:0x72, Read:0x73)



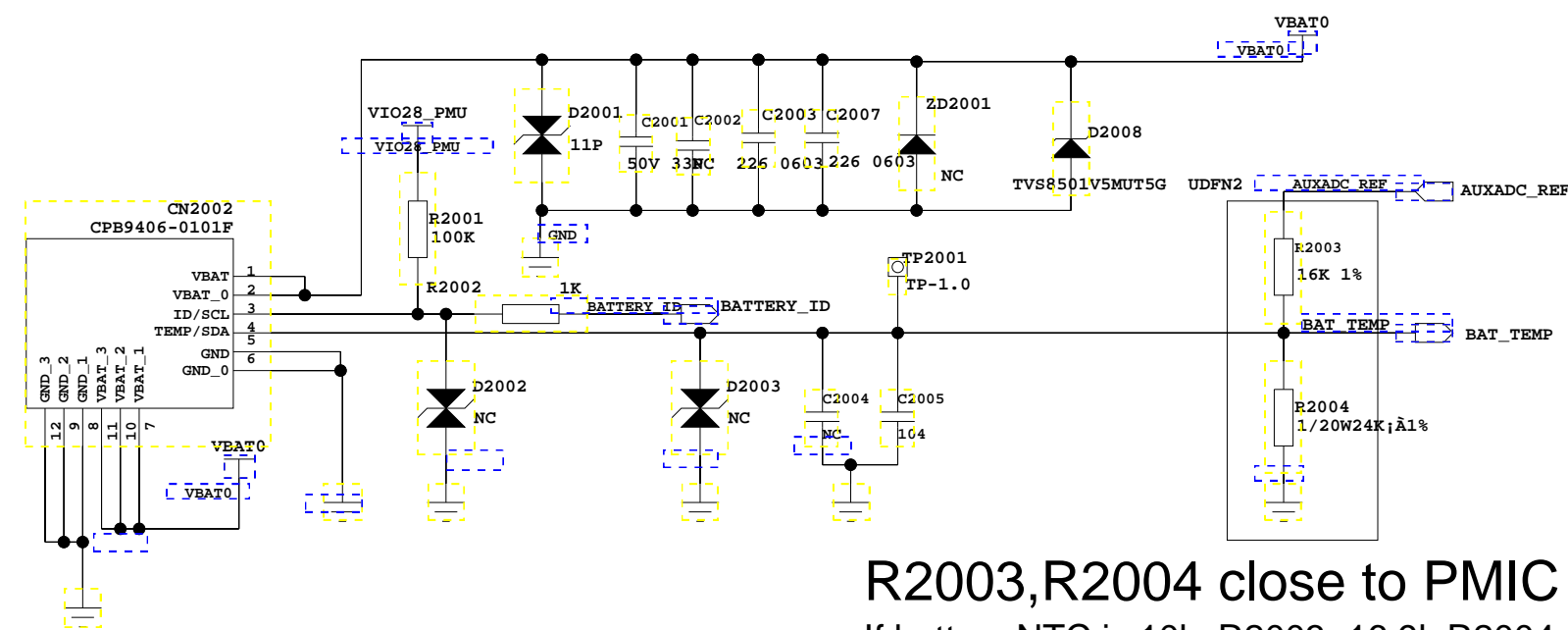
VOL+/- KEY



POWER KEY



BT_CN



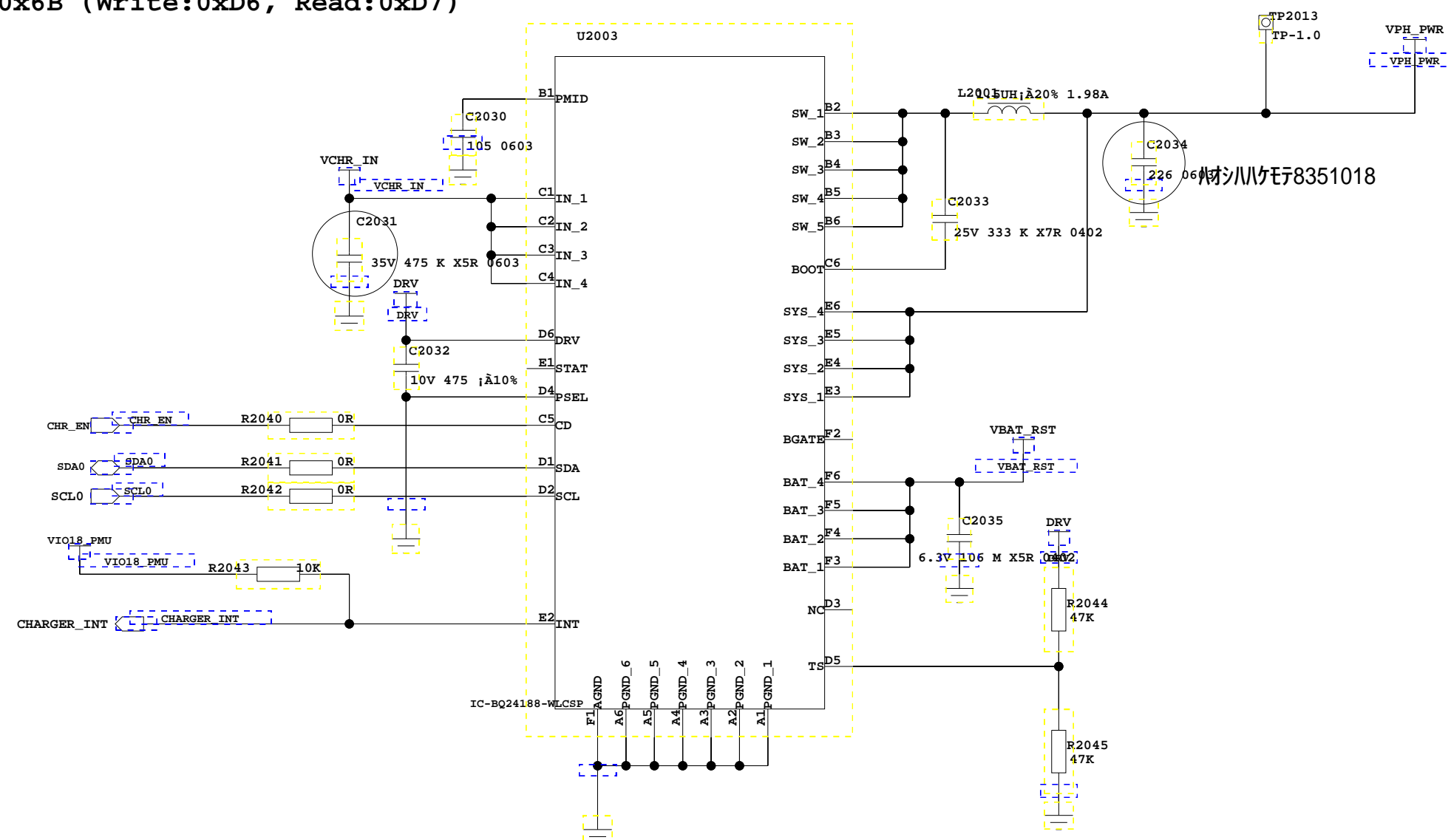
R2003,R2004 close to PMIC
If battery NTC is 10k; R2003=16.9k,R2004=27k
If battery NTC is 47k; R2003=61.9k,R2004=100k

Circuit of Charge

DC-DCヤニ -レキナホサヨテラ ュサキツキラ隔。ヤユヤ

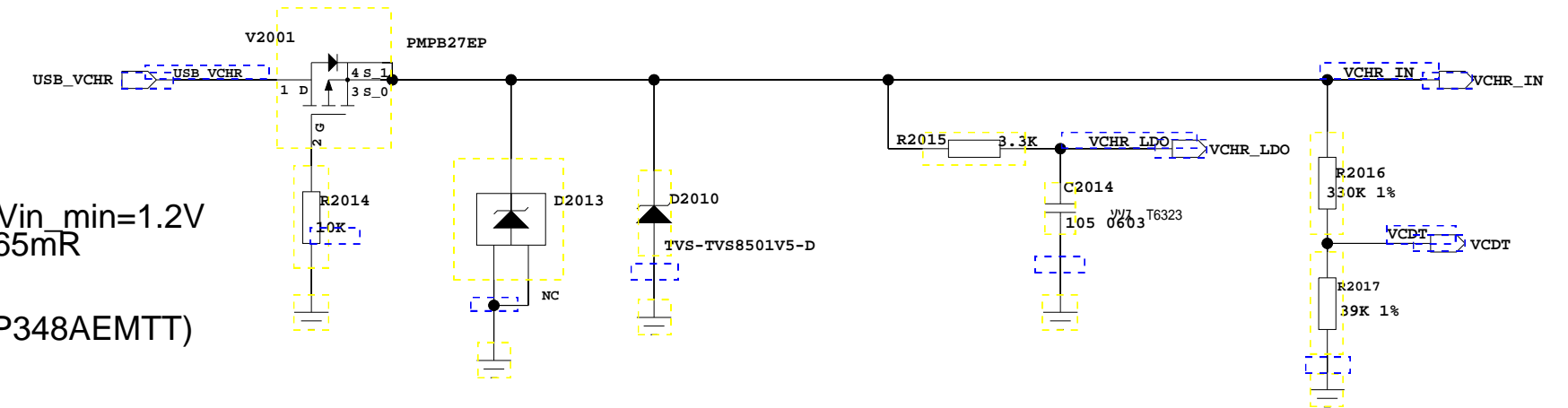
I2C Address

0x6B (Write:0xD6, Read:0xD7)



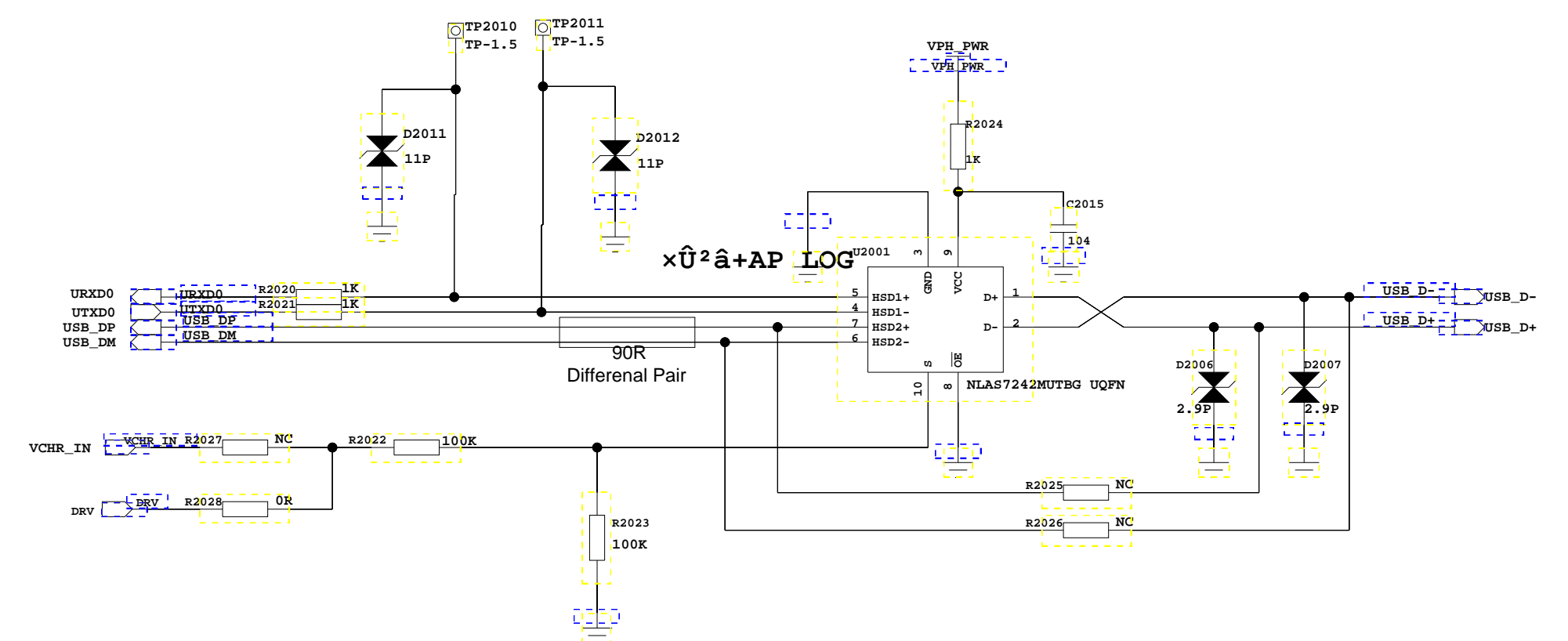
OVP

ハオシハハケモテ8080286

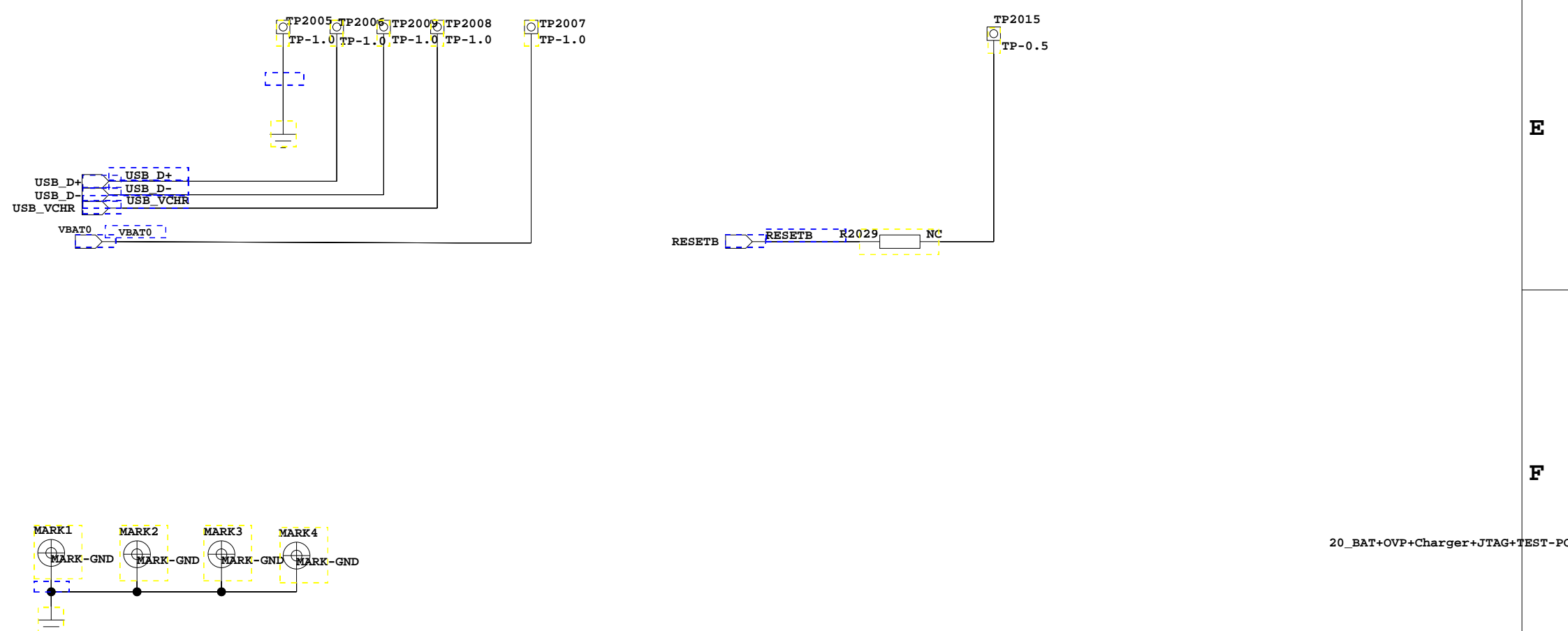


Vin_max=+28V Vin_min=1.2V
Rds_on_nmos=65mR
Imax=2A
UVP:3.0-3.5V
OVP:5.7-6.4(NCP348AEMTT)

USB a 1 Ø

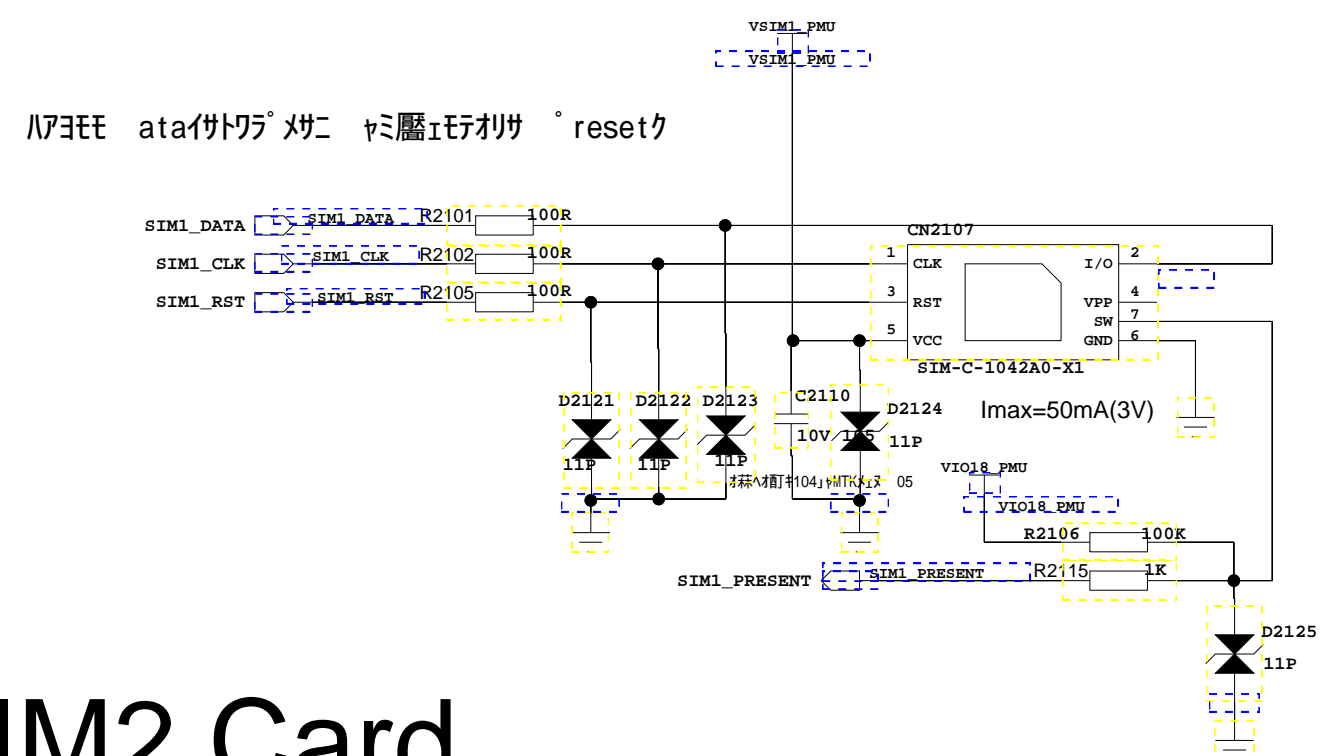


TEST POINT

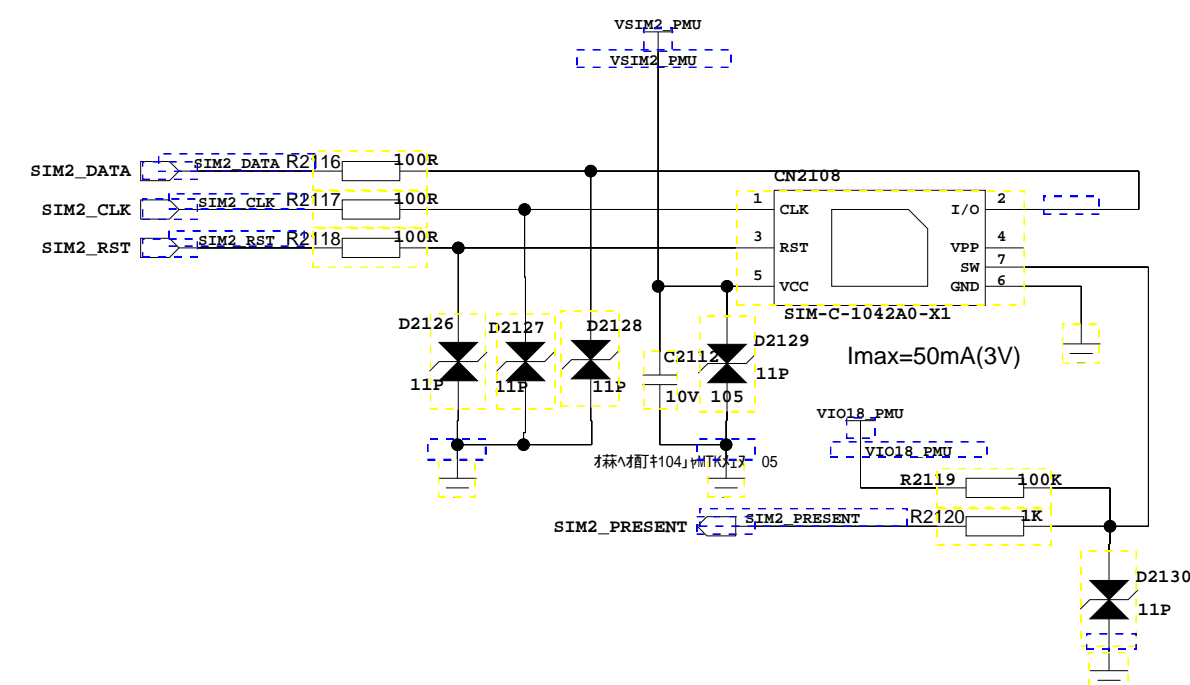


0_BAT+OVP+Charger+JTAG+TEST-PC

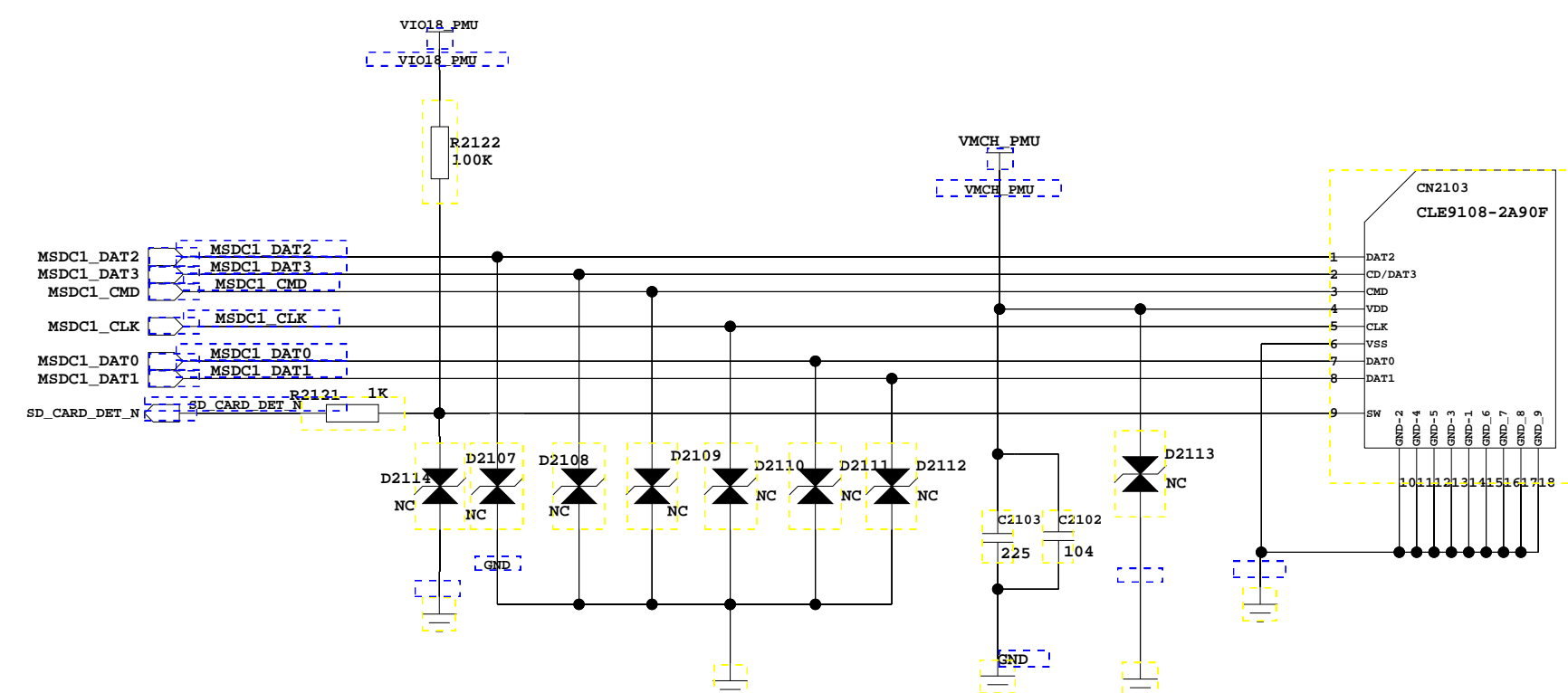
SIM1 Card



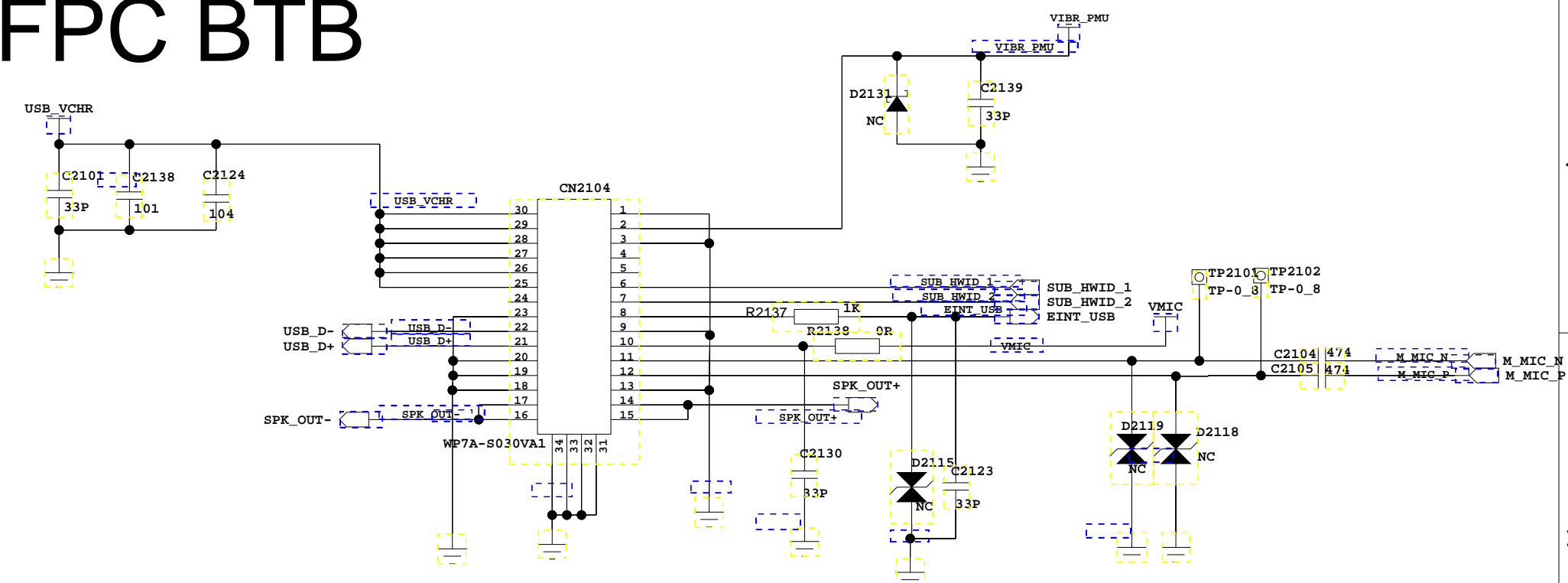
SIM2 Card



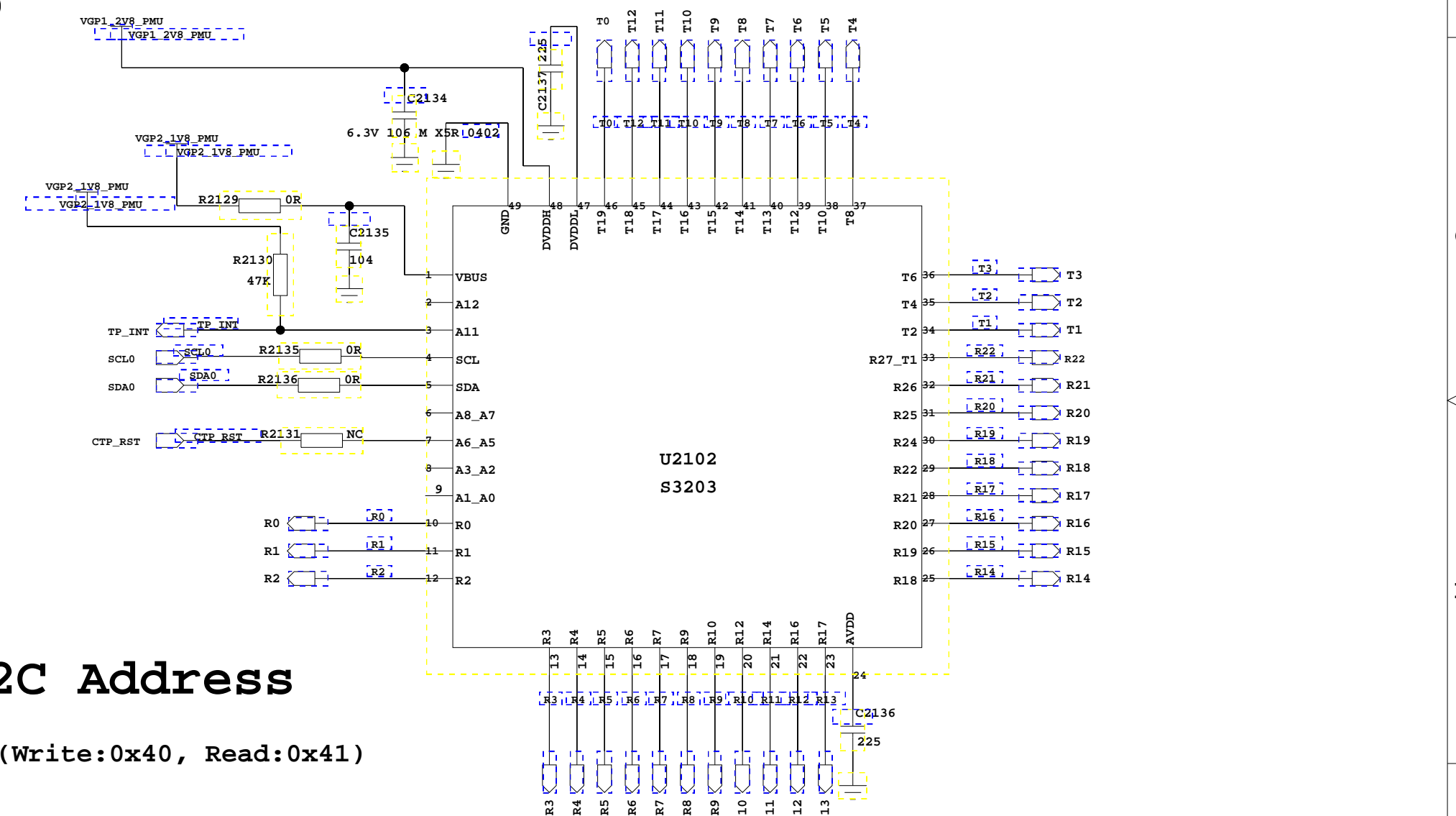
TF CARD



FPC BTB

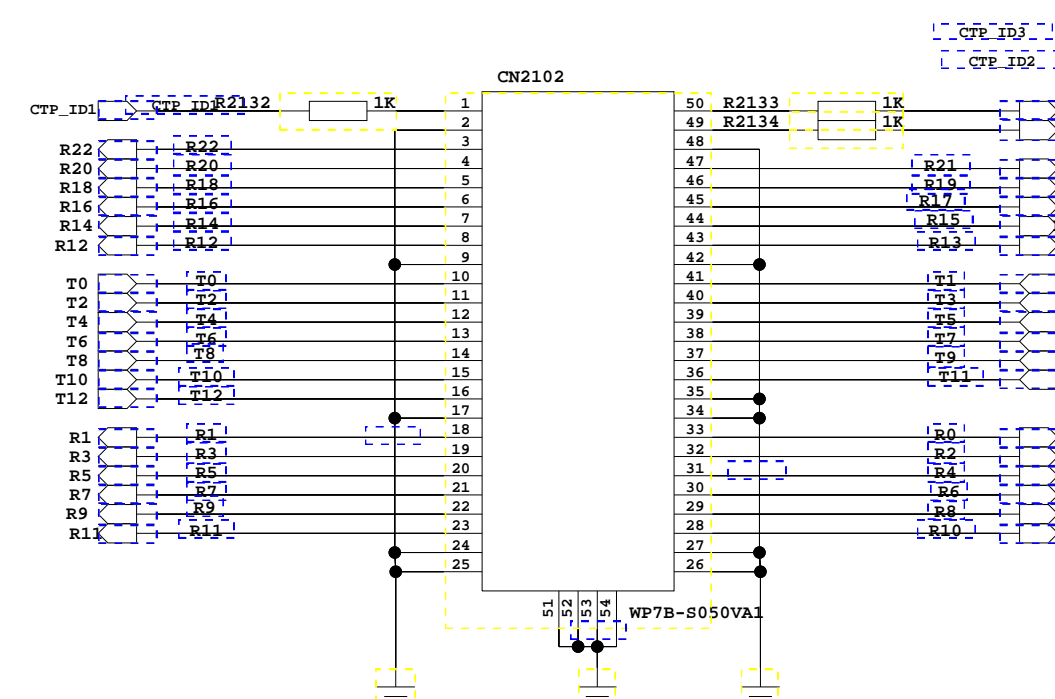


TP



I2C Address

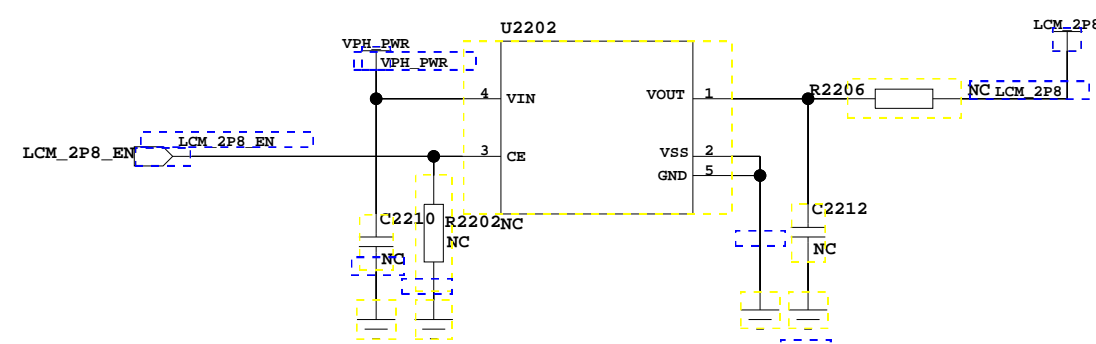
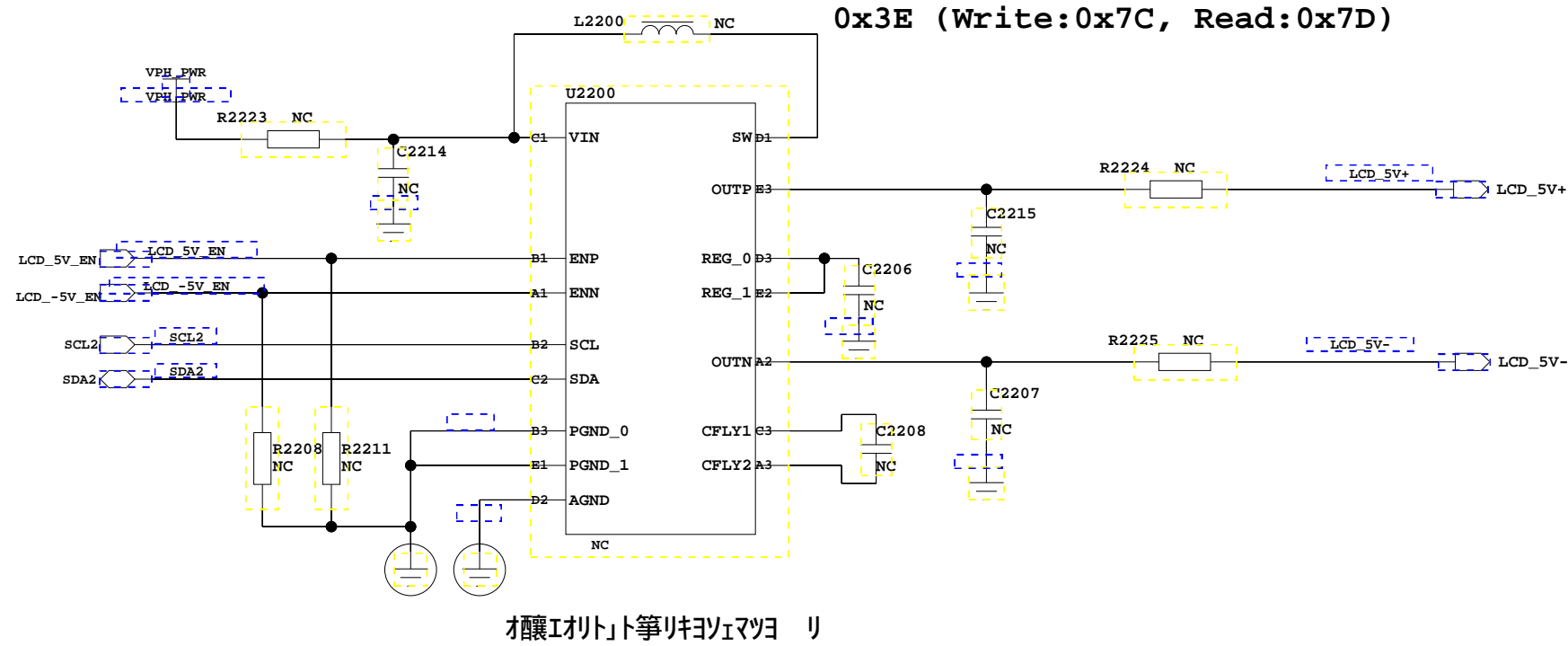
0x20 (Write:0x40, Read:0x41)



LCD 。 5V drive

I2C Address

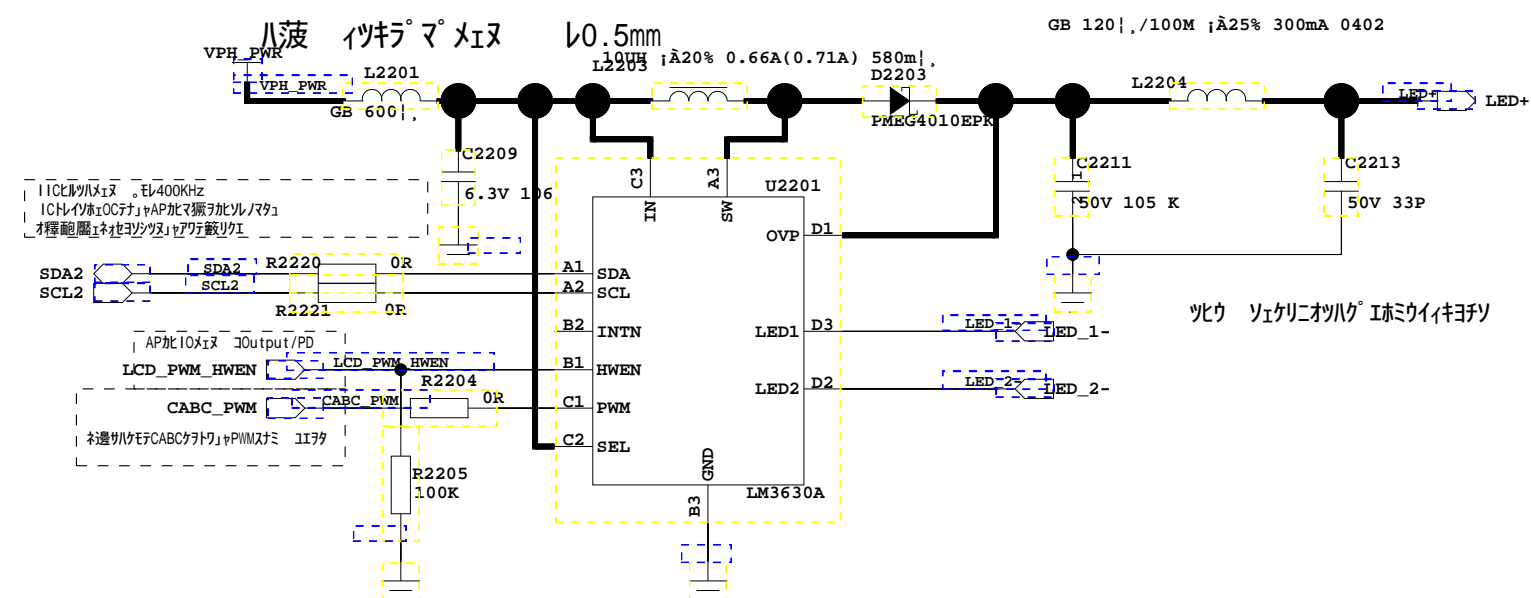
0x3E (Write:0x7C, Read:0x7D)



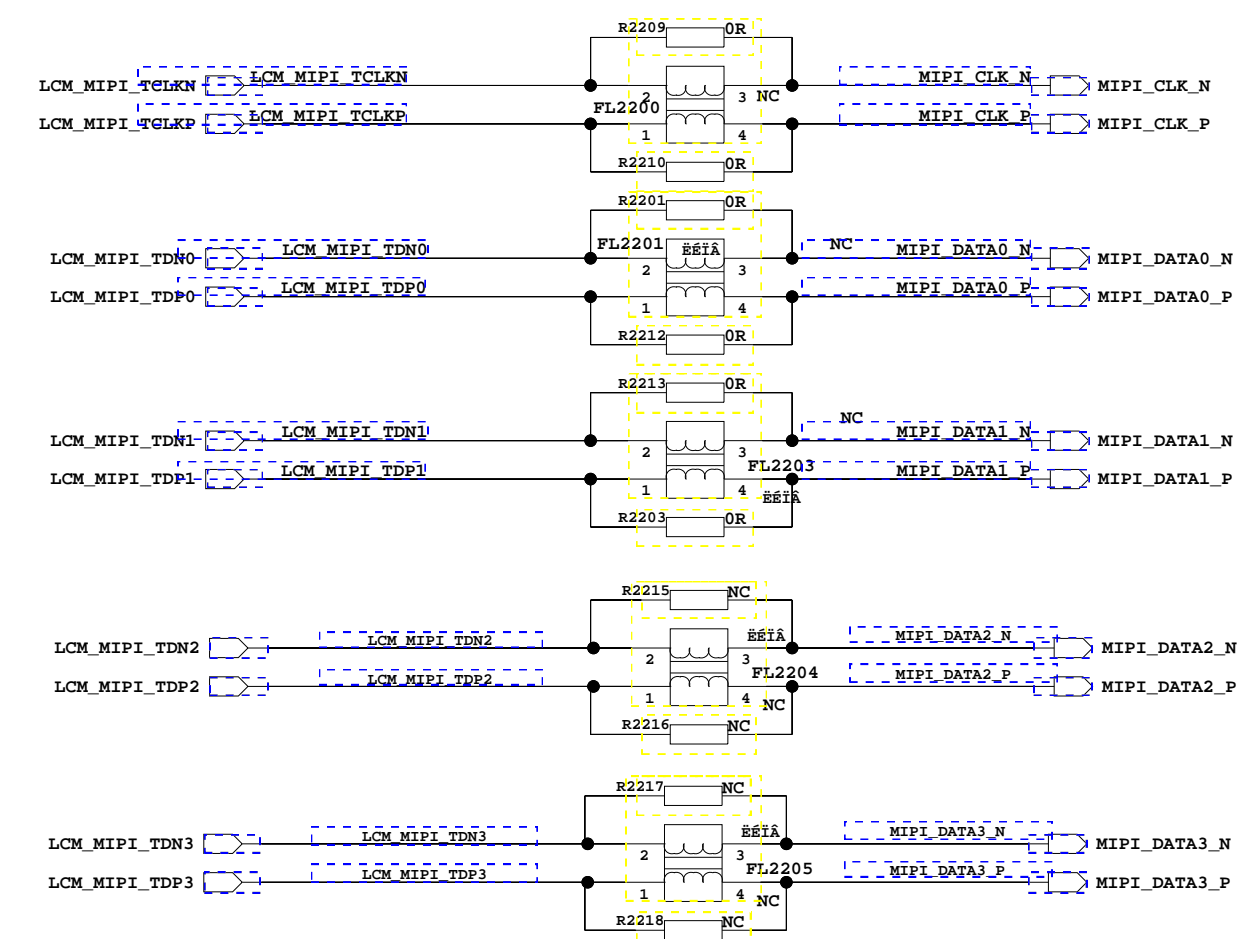
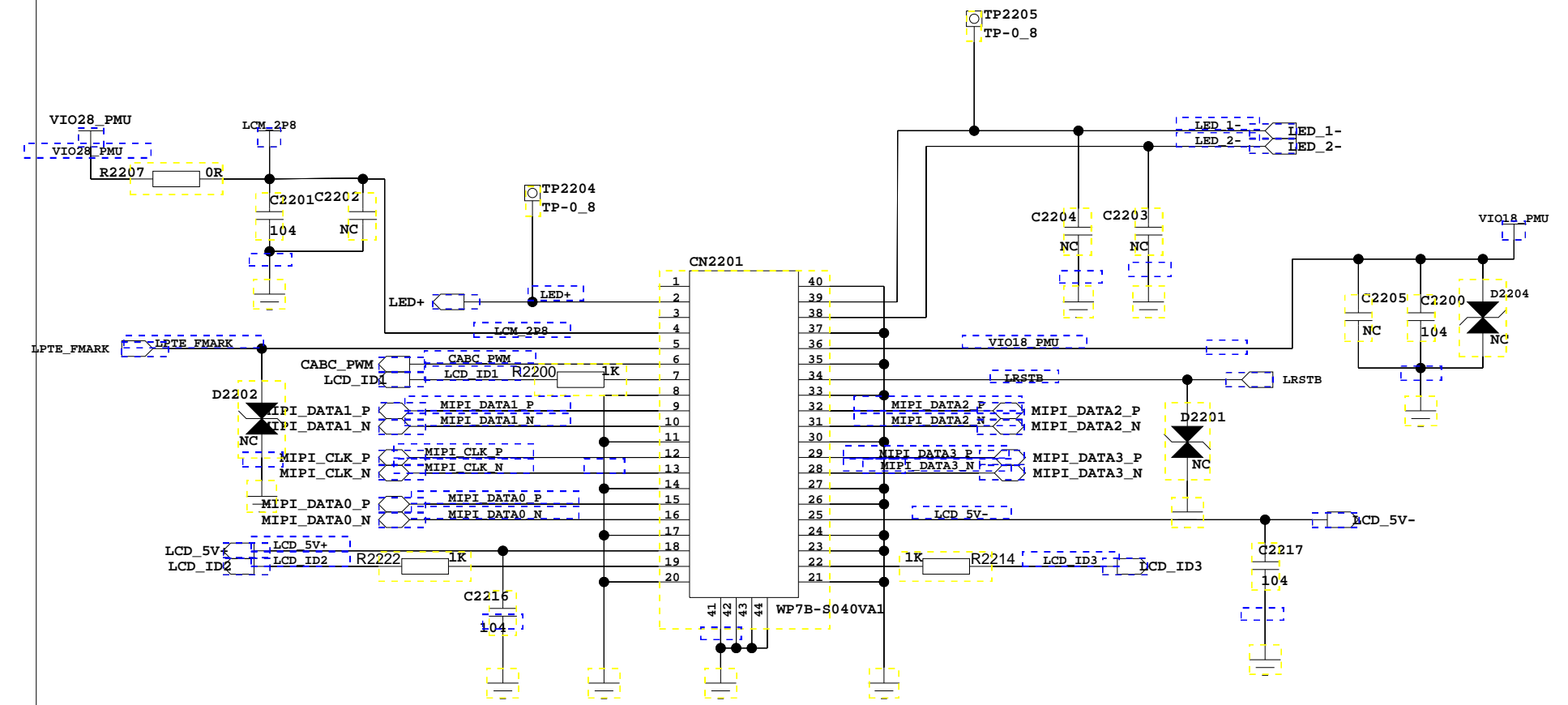
LCD BACKLIGHT

I2C Address

0x38 (Write:0x78, Read:0x79)



LCD Interface



RESET

