

PG410 A01

GP106 8GB/4GB GDDR5, 256b, 256Mx32/128MX32
Tall DVI-D + DP + DP + HDMI/DP + DP


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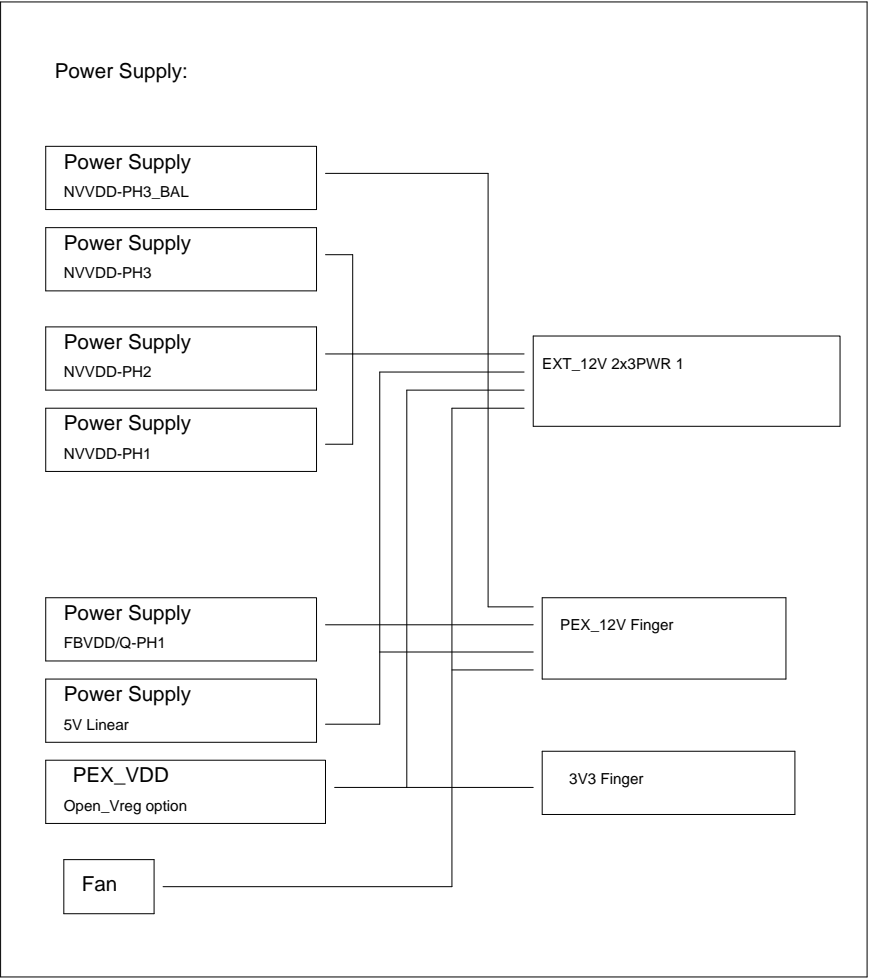
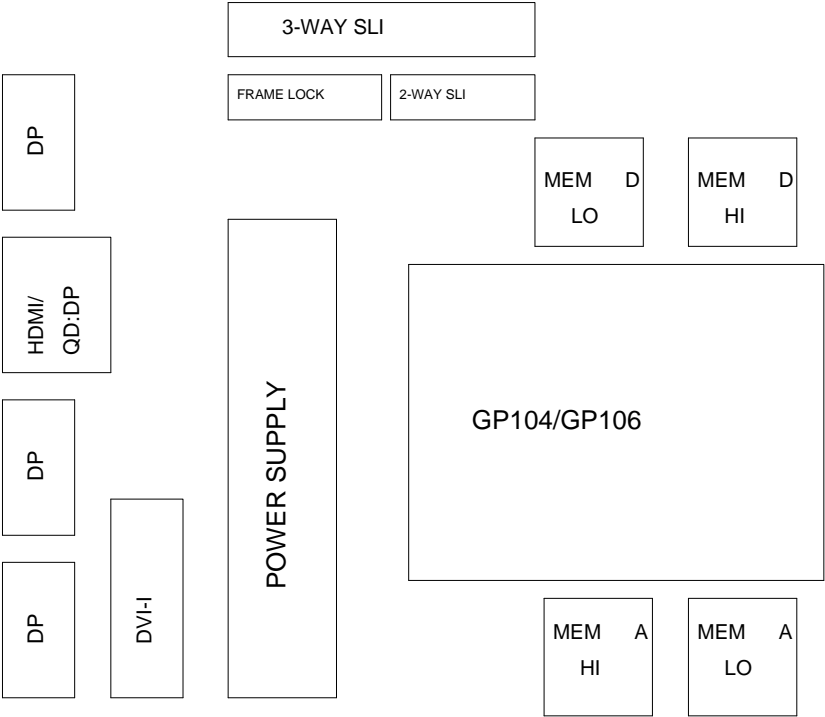
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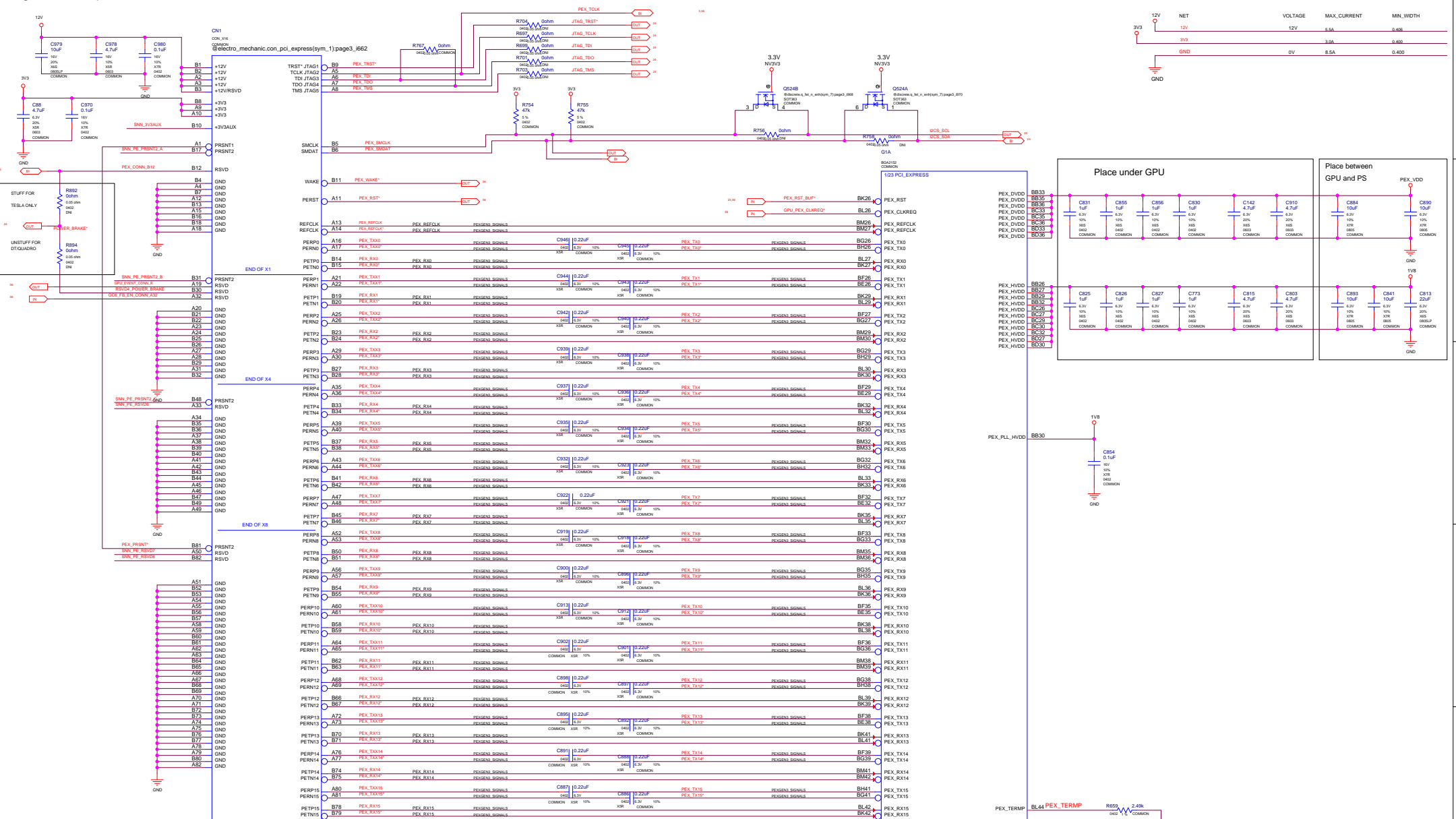
NV_PN	600-1G410-BASE-100		
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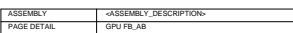
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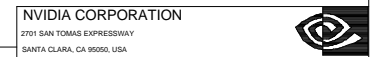
ASSEMBLY	<ASSEMBLY DESCRIPTION>
PAGE DETAIL	Block Diagram

NVIDIA CORPORATION			
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NV_PN	600-1G410-BASE-100	PAGE	2 OF 37
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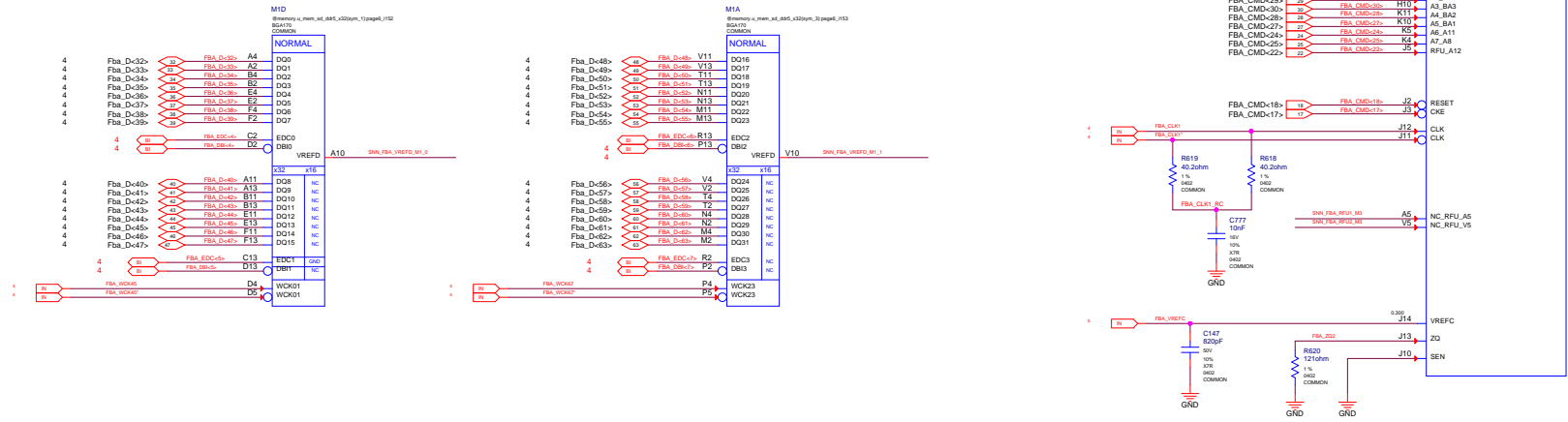


ASSEMBLY		ASSEMBLY DESCRIPTION	
PAGE DETAIL		PCI Express	
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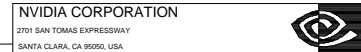


NV_PN	600-1G410-BASE-100		
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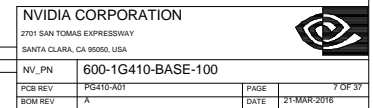


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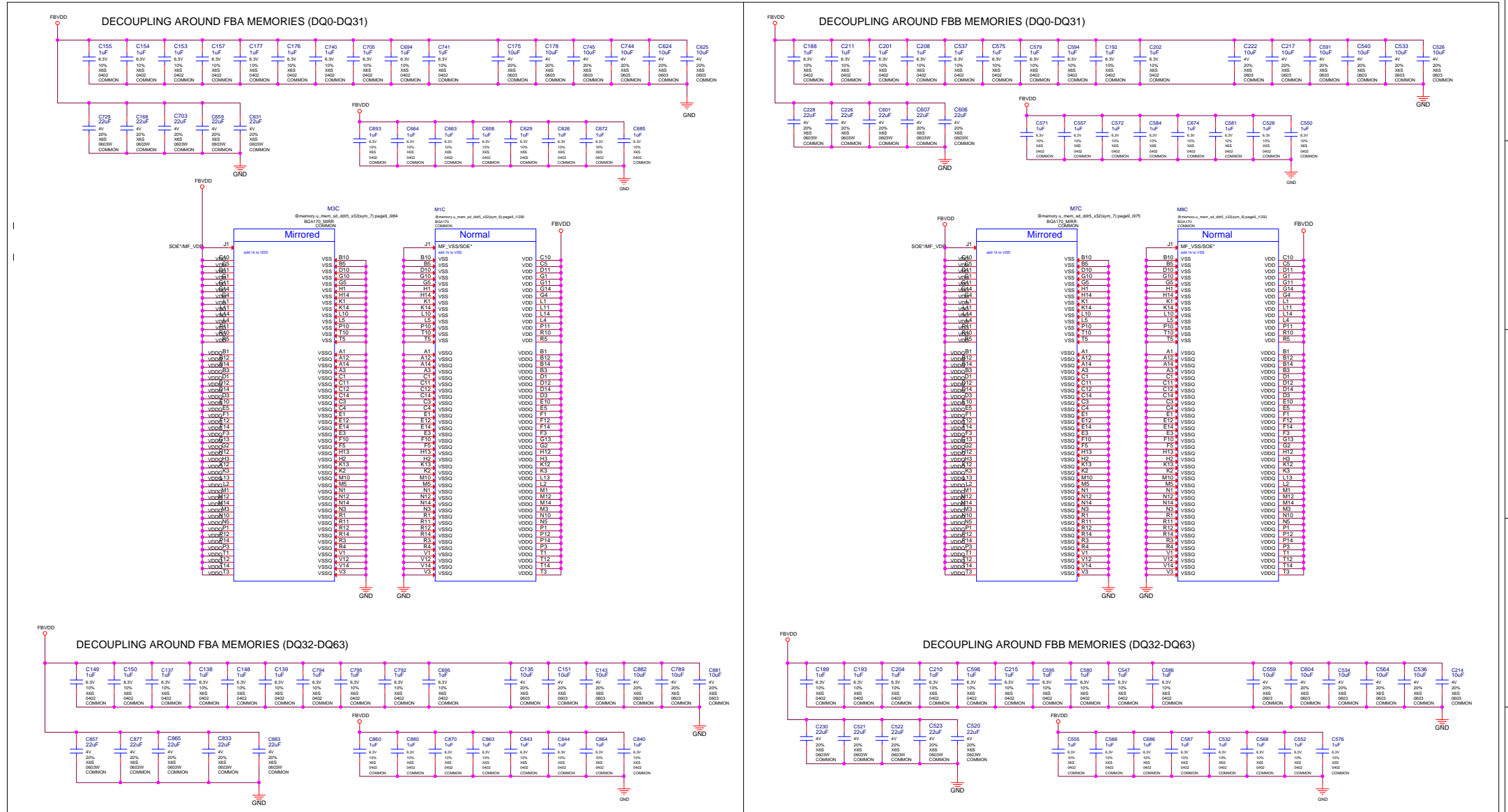
ASSEMBLY	ASSEMBLY DESCRIPTION
PAGE DETAIL	MEMORY: FB[A63:32]



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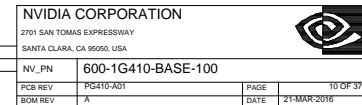
ASSEMBLY
PAGE DETAIL
MEM_FB_AB_PWR

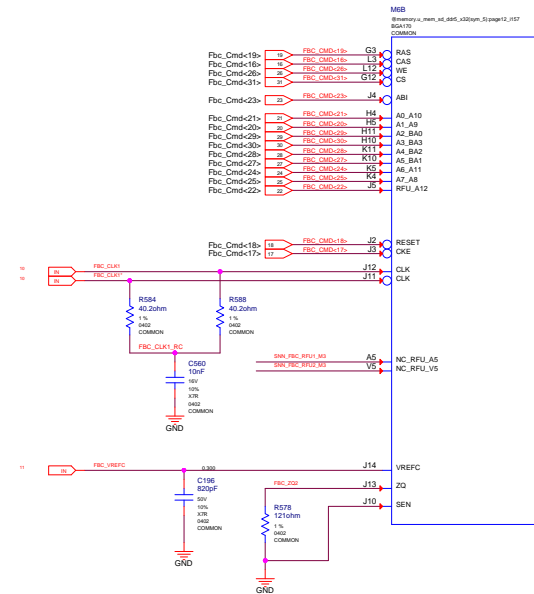
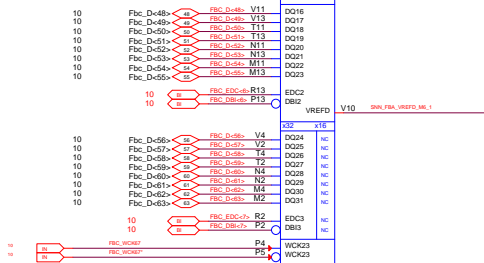
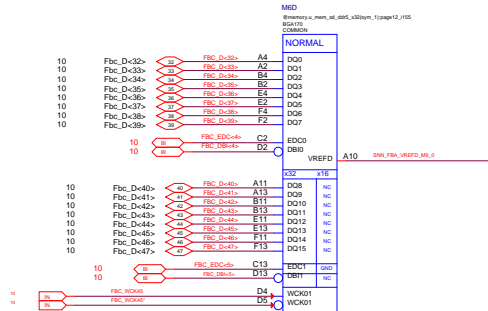
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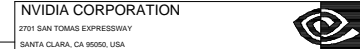




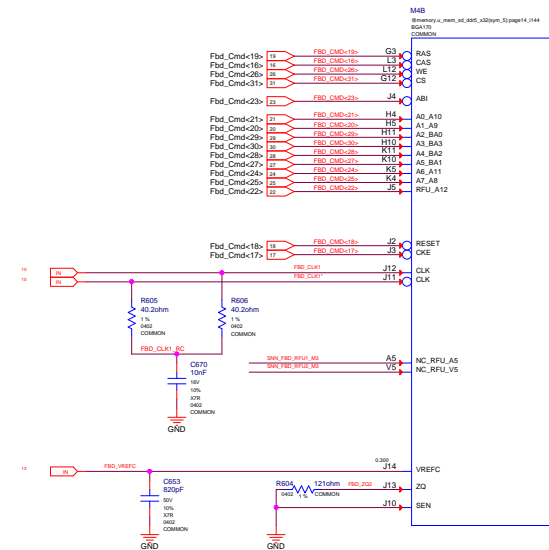
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ASSEMBLY	ASSEMBLY DESCRIPTION
PAGE DETAIL	MEMORY: FBQ[63:32]

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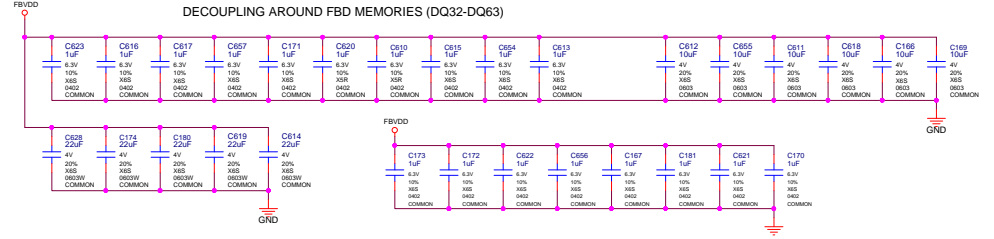
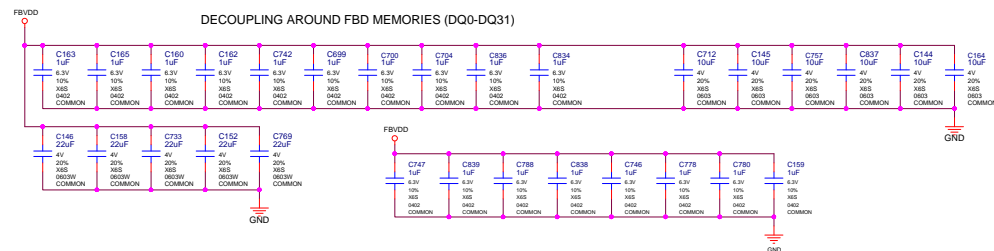


ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	MEMORY: FBD[63:32]

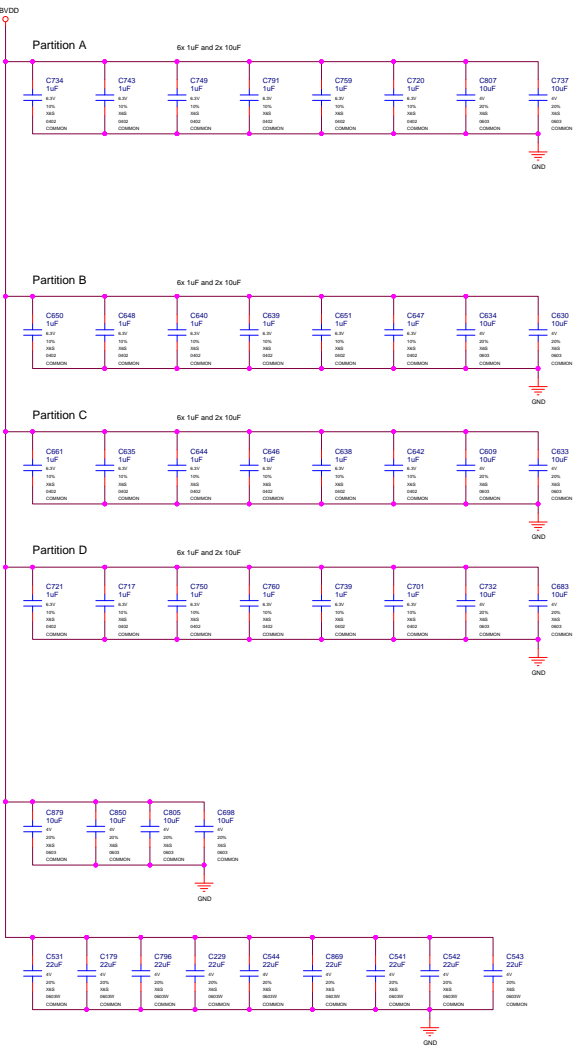
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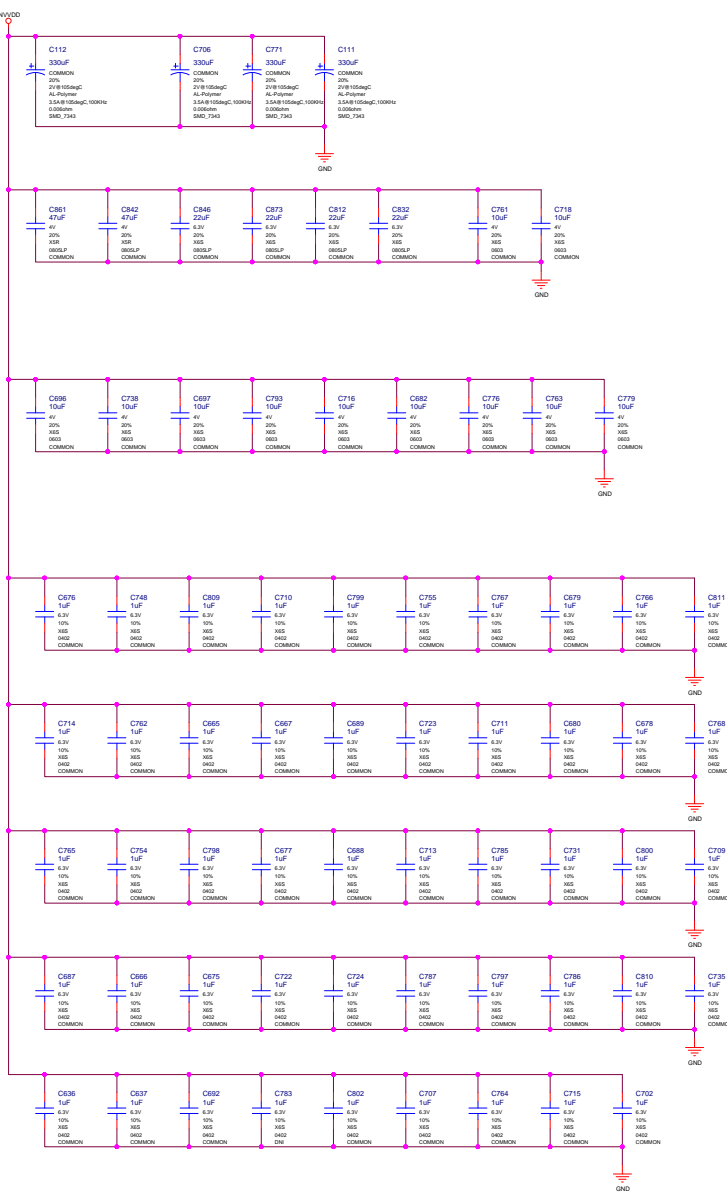
NV_PN	600-1G410-BASE-100		
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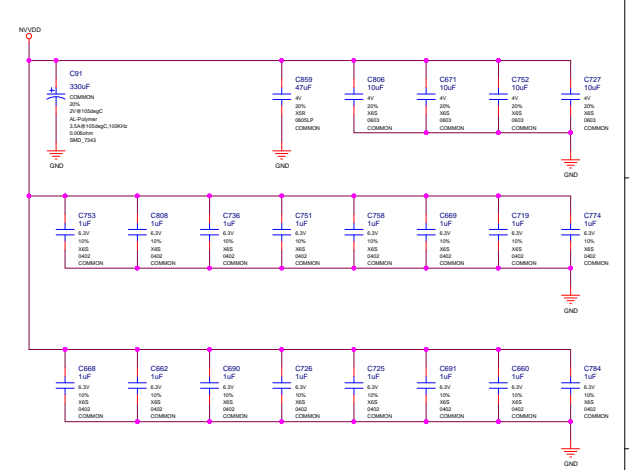
FBVDD



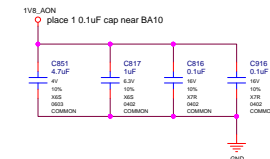
NVDD



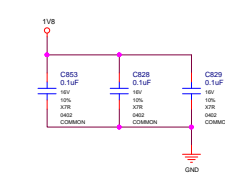
NVDD



1V8_AON



1V8_MAIN



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ASSEMBLY	ASSEMBLY DESCRIPTION
PAGE DETAIL	GPU Decoupling

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NV_PN	600-IG410-BASE-100		
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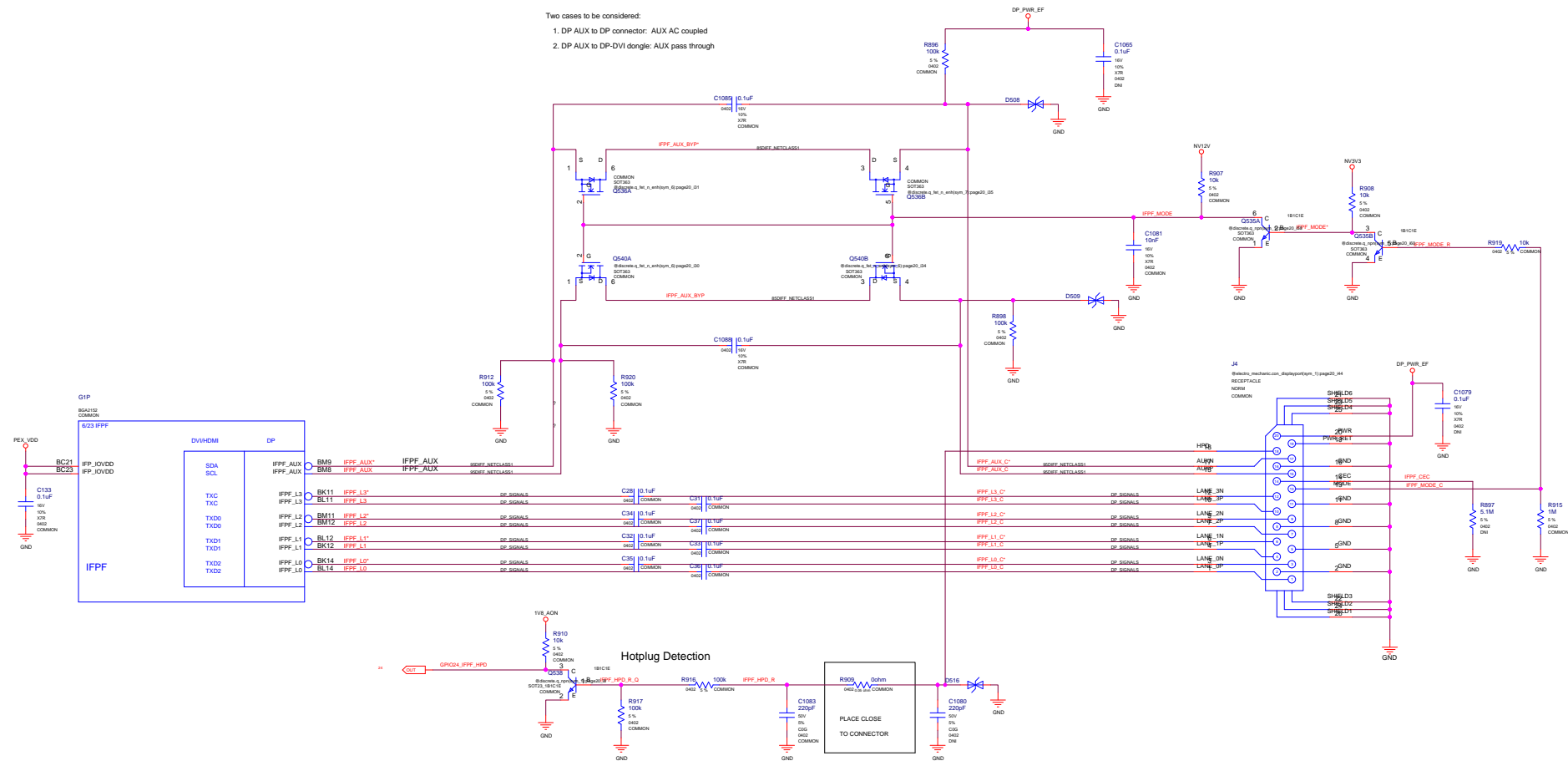


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BOM REV	A


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ASSEMBLY	«ASSEMBLY_DESCRIPTION»
PAGE DETAIL	IFPAB DVI-D-DL

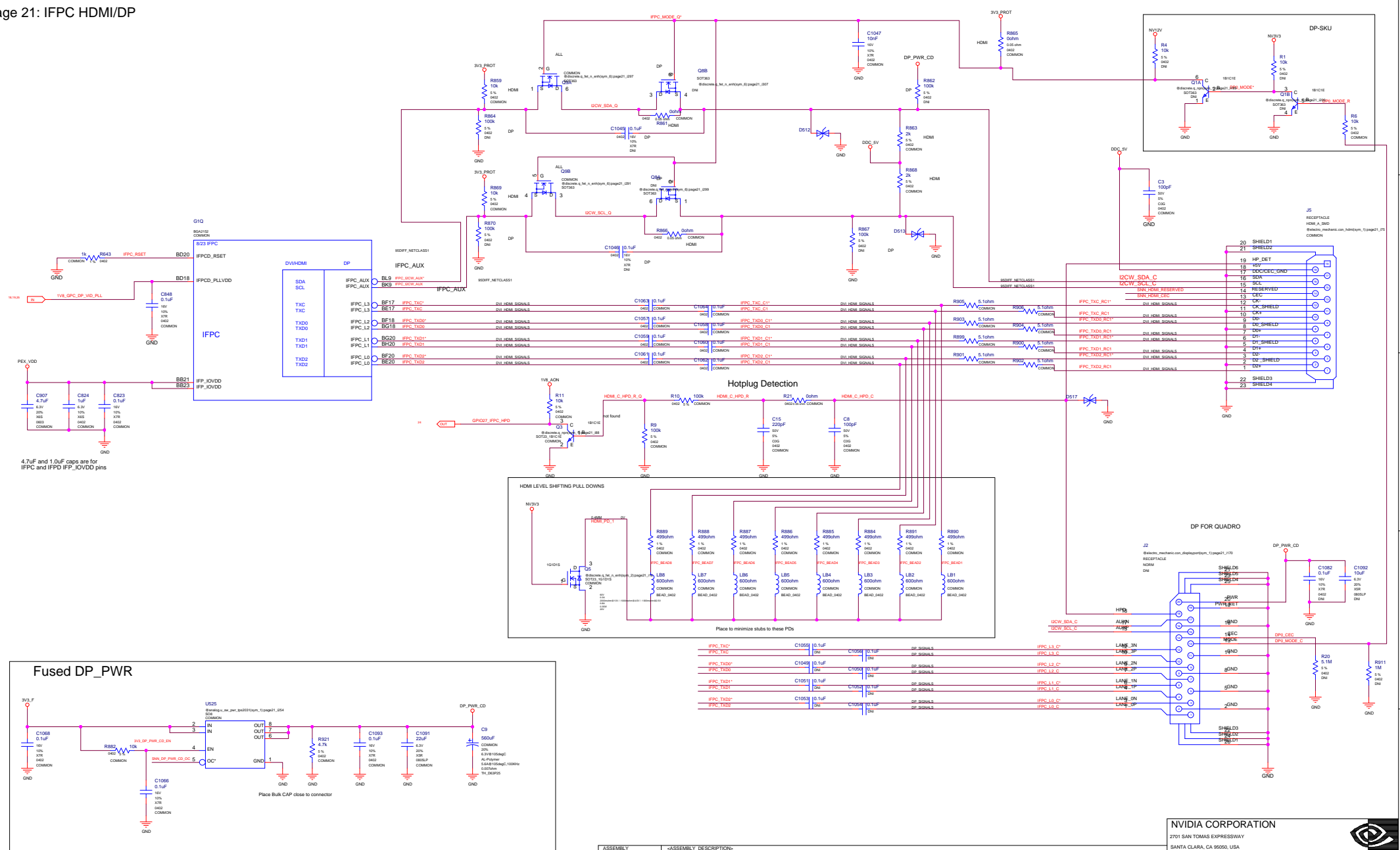


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ASSEMBLY	«ASSEMBLY_DESCRIPTION»
PAGE DETAIL	IFPF DP

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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	IFPC HDMI/DP

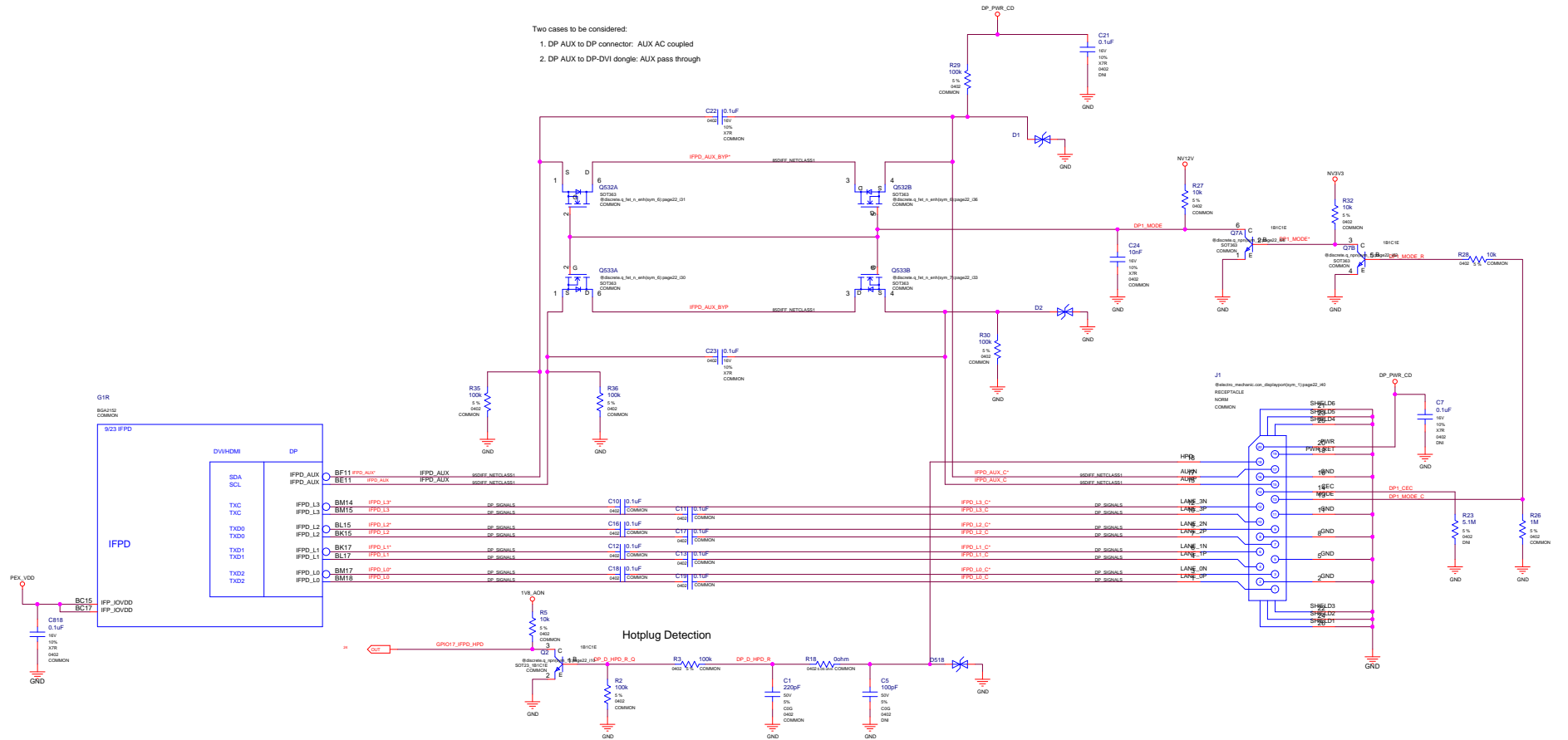
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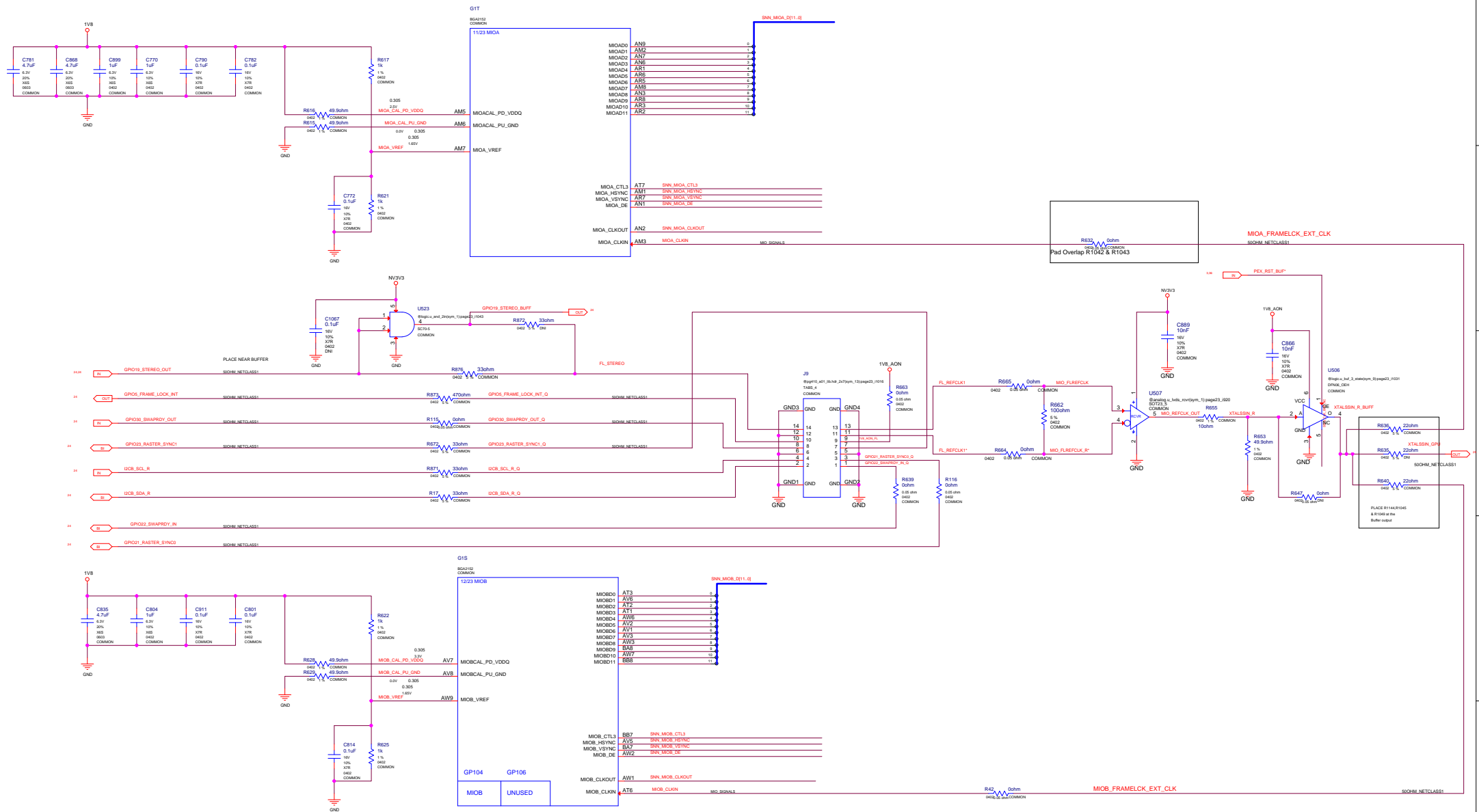
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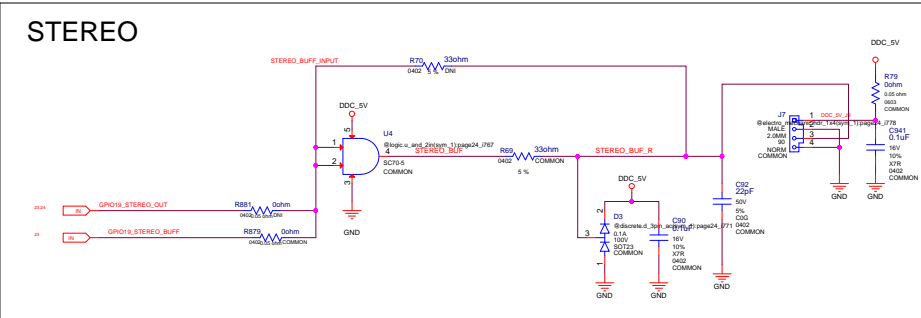
NV_PN	600-1G410-BASE-100
-------	--------------------

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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	MISC1: Fan, Thermal, JTAG, GPIO, STEREO

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STRAP2	STRAP1	STRAP0	RAMCFG[4:0]
L	L	L	00000
L	L	H	00001
L	H	L	00010
L	H	H	00011
H	H	L	00110
H	H	H	00111
L	L	M	01000

ROM_SO	ROM_SI	ROM_SCLK	SOR_EXPOSED[3:0]	1:ENABLE 0:DISABLE
L	L	L	1111 DEFAULT	SOR0/1/2/3 ENABLE
L	L	H	1110	
L	H	L	1101	
L	H	H	1100	
H	L	L	1011	
H	L	H	1010	
H	H	L	1001	
H	H	H	1000	
L	L	M	0111	
L	M	L	0110	
L	M	H	0101	
L	H	M	0100	
H	L	M	0011	
H	M	L	0010	
H	M	H	0001	
H	H	M	0000	

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1 DEFAULT
L	L	L	0	0	0	0

H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V

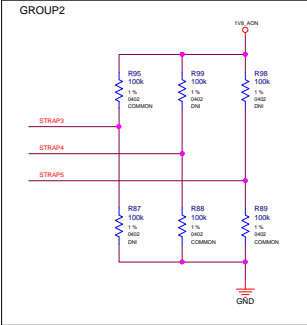
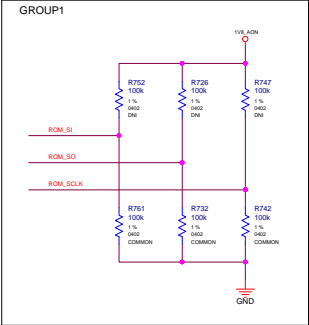
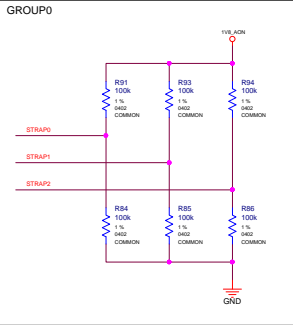
1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE

1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGINAL

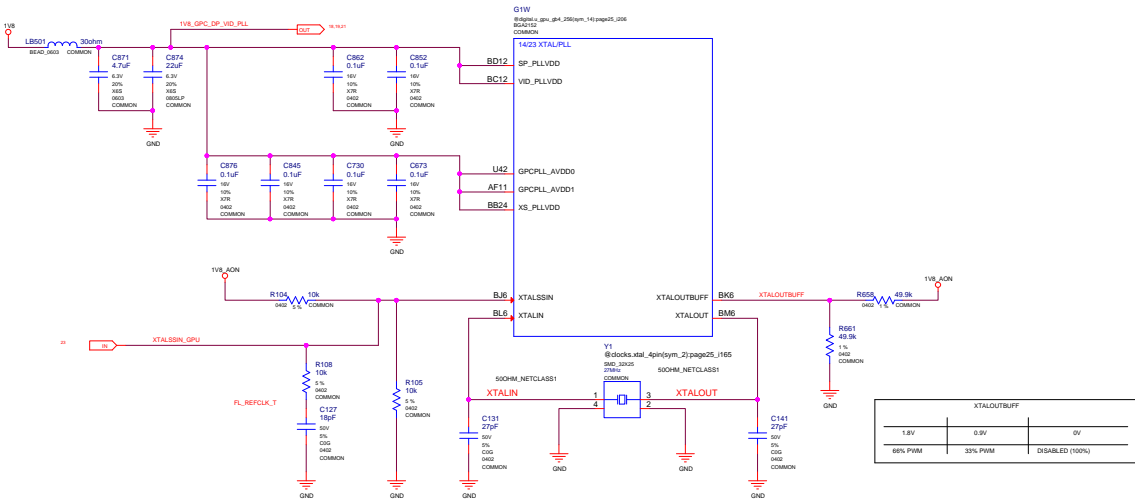
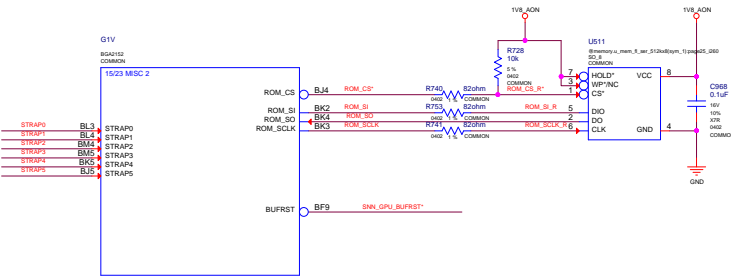
1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER

1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE

ASSEMBLY	ASSEMBLY DESCRIPTION
PAGE DETAIL	MISC2: ROM, XTALSTRAPS



CFG(3:0)	Config	Width	Vendor
0000	256Mx32	256-bit	Samsung
0001	256Mx32	256-bit	Micron
0010	256Mx32	256-bit	Hynix
0011	Reserved		
0100	Reserved		
0101	Reserved		
0110	Reserved		
0111	Reserved		



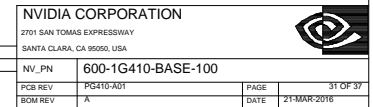
XTALOUTBUFF		
1.8V	0.9V	0V
88% PWM	33% PWM	DISABLED (100%)

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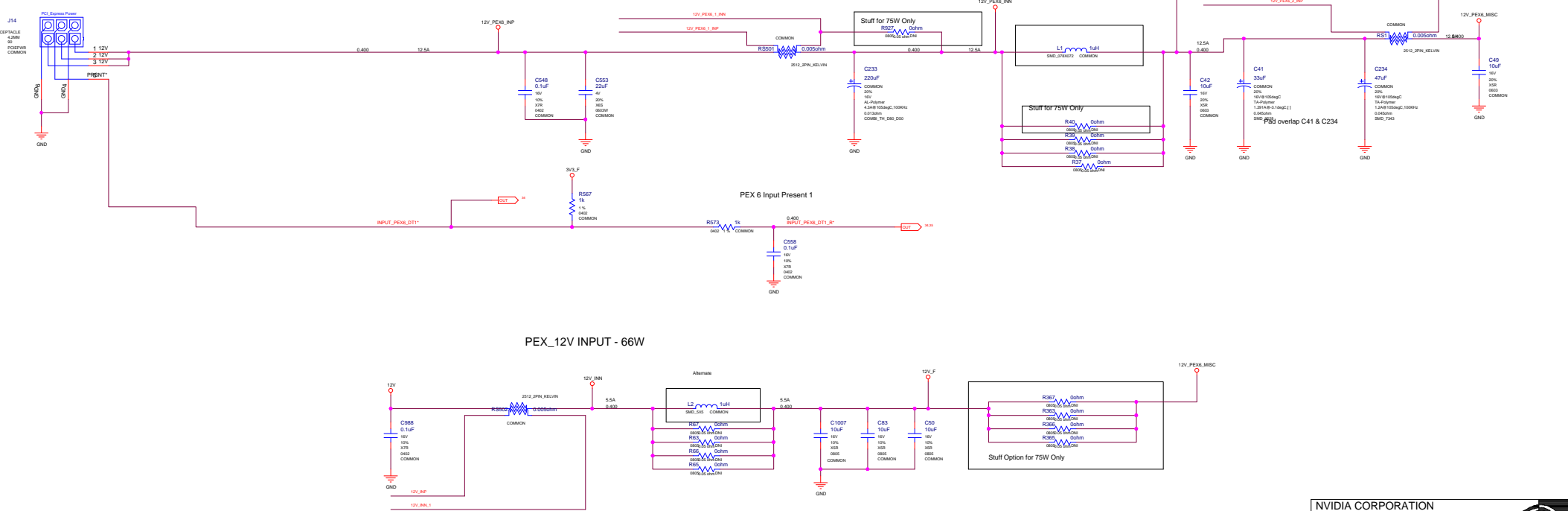
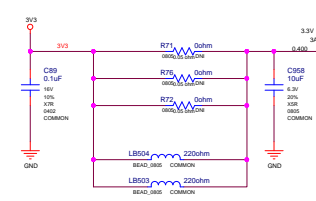
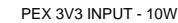
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ASSEMBLY	«ASSEMBLY_DESCRIPTION»
PAGE DETAIL	PS: Input, Filtering, and Monitoring

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2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA

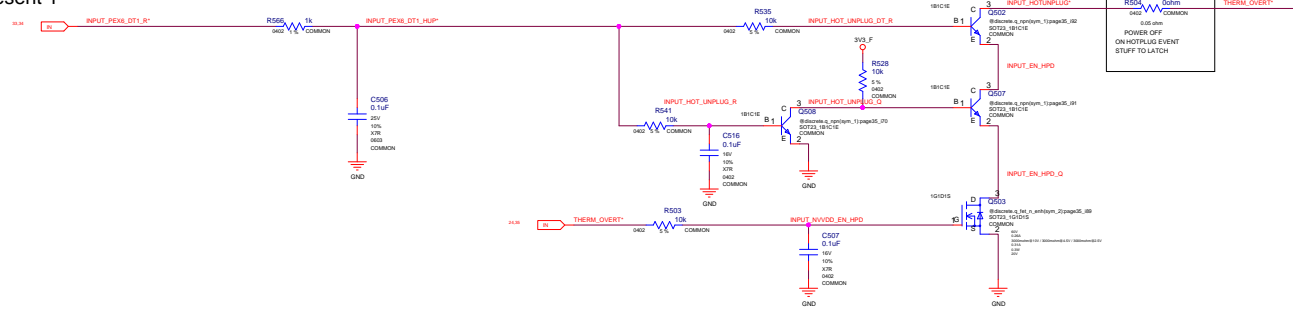


NV_PN	600-1G410-BASE-100
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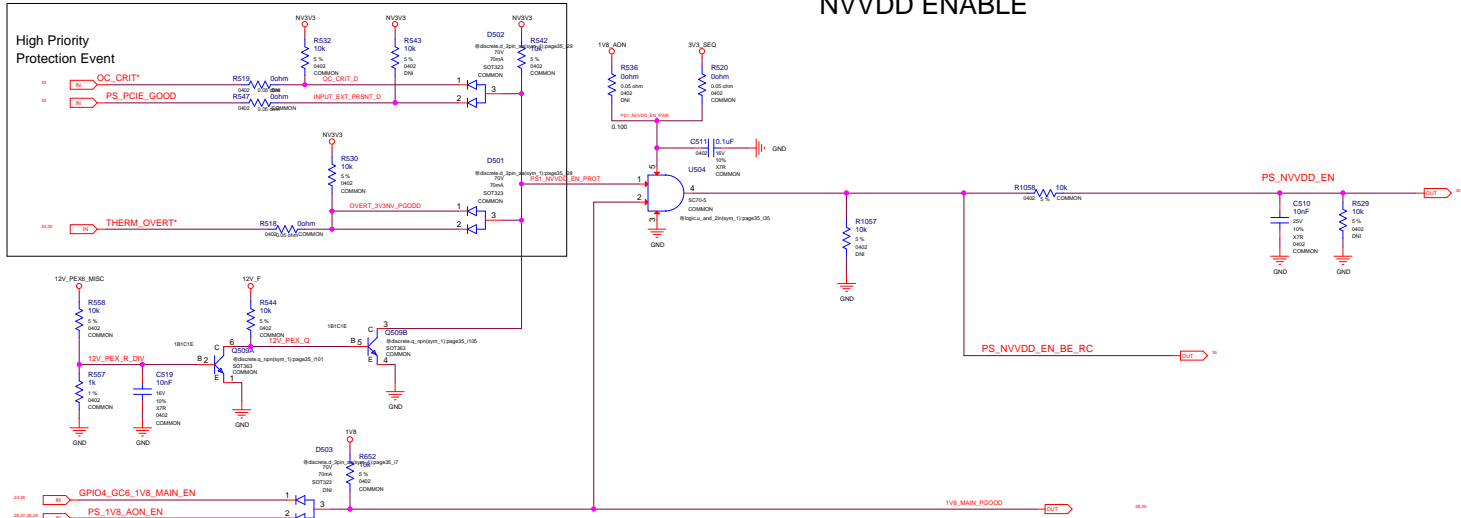
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PEX Input Present 1

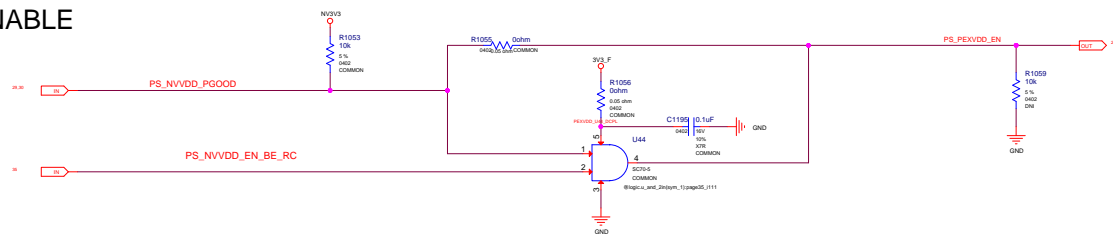
POWER CONNECTOR HOT-UNPLUG DETECT



NVVDD ENABLE



PEX ENABLE



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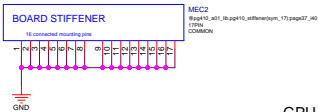
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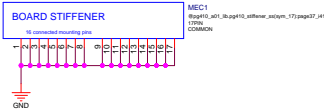
Brackets:



MOUNTING HOLES FOR HS DS:



MOUNTING HOLES FOR HS SS:

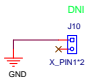


GPU Stiffener:

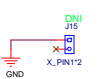


$V_{out} = 0.8V \cdot (1 + R_{UR}/R_1)$
 $1.05V = 0.8V \cdot (1 + 3.57/11.5k)$

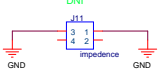
Reserve J8 - J12 for Impedance check



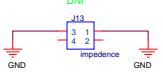
MEM Data
FBA_D<9>
L3
0.143MM / 40ohm +/- 10%
Reference L2 GND



MEM Data
FBA_D<9>
L6
0.144MM / 40ohm +/- 10%
Reference L7 GND



MEM CLK
FBD_CLK0 / FBD_CLK0*
L6
0.088MM / 0.1MM / 80ohm +/- 10%
Reference L7 GND



HDMI/DVI TMDS signals
IFPC_TXD0 / IFPC_TXD0*
L6
0.088MM / 0.209MM / 95ohm +/- 10%
Reference L7 GND

FM1
SW_FB

F_PAD_X

FM2
SW_FB

F_PAD_X

FM3
SW_FB

F_PAD_X

FM4
SW_FB

F_PAD_X

FM5
SW_FB

F_PAD_X

FM6
SW_FB

F_PAD_X

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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	MECH

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