

P381: G72, DDR2 MEMORY 16Mx16

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Page 3: Frame Buffer Interface
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Page 7: DACB, VGA Output, TV Out
Page8: S-Video Minidin Connector
Page 9: TMDs Interface
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Page 11: STRAPS, Mechanical Parts
Page 12: XTAL, GPIO, BIOS, FAN, JTAG, THERMAL
Page 13: PowerSupplyI: NVVDD, A3V3
Page 14: PowerSupplyII: 5V, DDC5V, F3V3, TMDs_PLLVDD

Base on P381_ A04 (V056-V3.0)modify

- Page8, removed mini-din 4pin. add mini-din 7pin support HDTV
Page 13: PowerSupplyI: NVVDD, A3V3 add mlcc 10U/16V 1206 5pcs
remove L5 1R0
Page 9: ADD 預留R589,R590,R591,R592,R593,R594,R595,R596 FOR EMI
Page 12: XTAL 雙LOAYOUT SMD 49US

REV	VARIANT	NVPN	ASSEMBLY
B	BASE	600-10381-xxxx-400	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU0010	600-10381-0010-400	P381: G3-64.7300LE, 64 BIT DDR2 18Mx16 MEMORY, VGA+DVI+TVout
2	SKU0011	600-10381-0011-400	P381: G3-64.7300LE, 64 BIT DDR2 32Mx16 MEMORY, VGA+DVI+TVout
3	SKU0012	600-10381-0012-400	P381: G3-64.7300QS, 64 BIT DDR2 32Mx16 MEMORY, VGA+DVI+TVout
4	SKU0013	600-10381-0013-400	P381: G3-64.7300QS, 64 BIT DDR2 18Mx16 MEMORY, VGA+DVI+TVout
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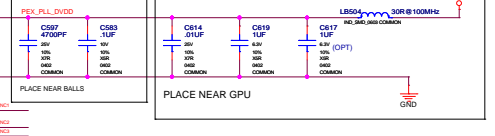
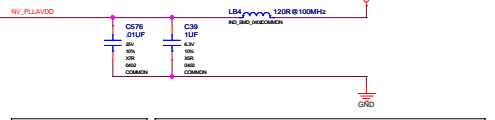
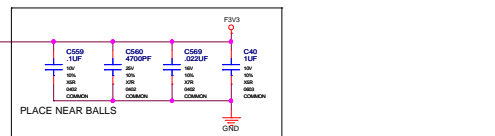
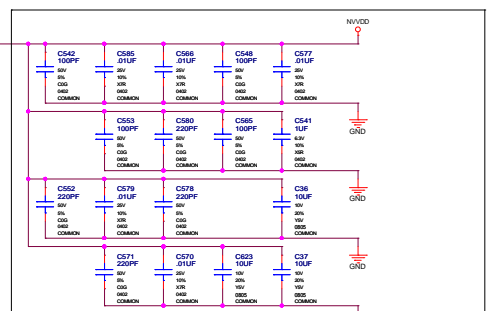
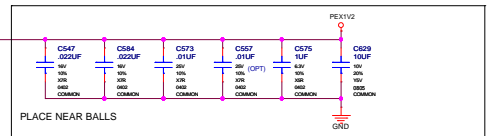
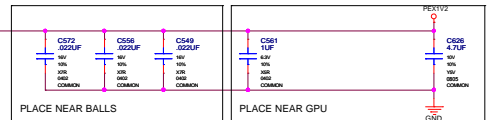
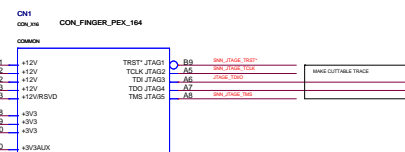
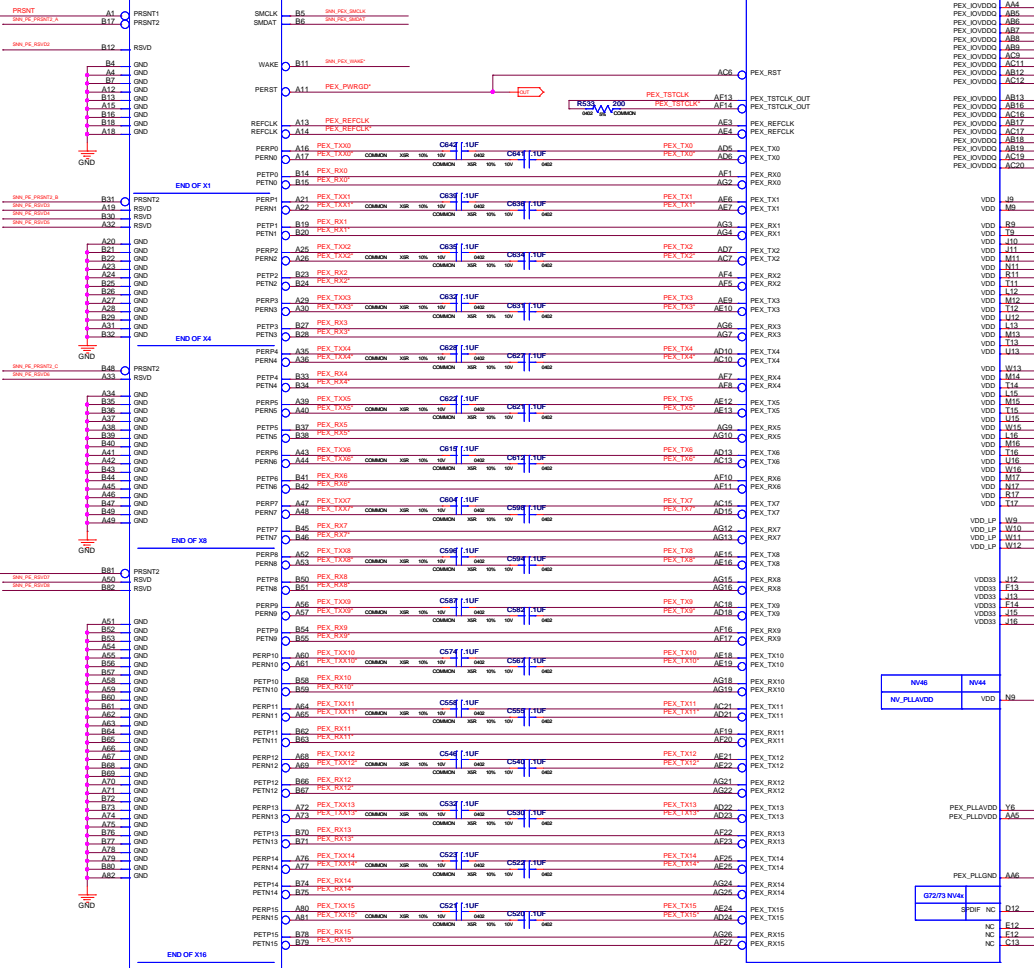
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TITLE		DATE	11-MAR-2005

Figure 1 shows two schematic diagrams of filter stages. The left diagram, titled 'PLACE NEAR FINGERS', shows a 3V3 regulated supply connected to three capacitors in parallel: C24 (10µF, 10V), C22 (10µF, 10V), and C23 (0.1µF, 10V). All capacitors are connected to a common ground. The right diagram shows a 12V regulated supply connected to three capacitors in parallel: C11 (4.7µF, 20V), C658 (4.7µF, 20V), and C647 (0.1µF, 10V). All capacitors are connected to a common ground. A 3V3 output is also indicated at the bottom right.



	Net Name	OFF_PAIR	CRITICAL	IMPEDANCE
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16	PCA10010A	PCA102	+	0.000T
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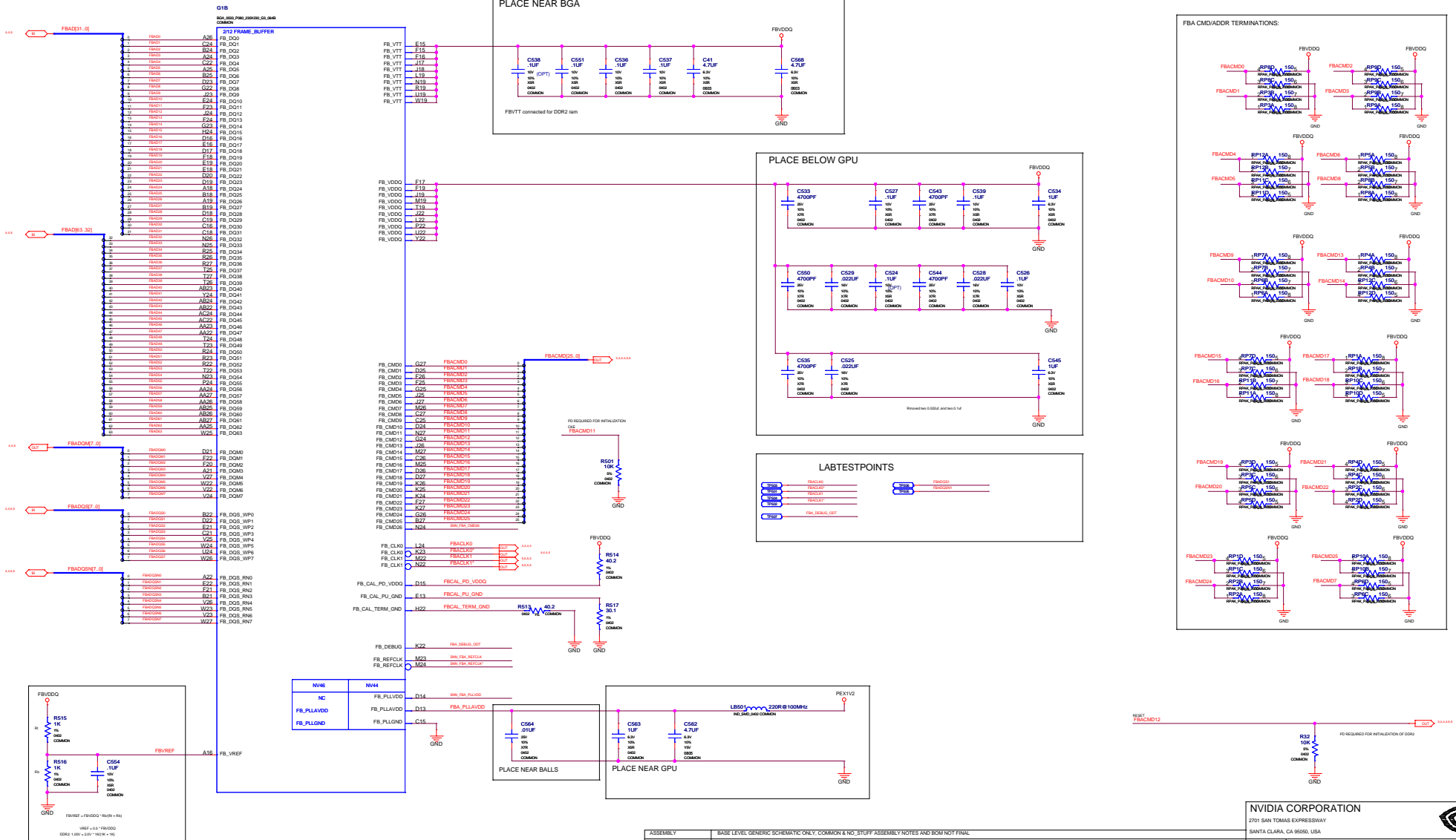
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PAGE DETAIL	PCI Express Interface

ASSEMBLY	BASE LEVEL GENERIC
PAGE DETAIL	PCI Express Interface

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NAME	

GPU: FB-Interface



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PAGE DETAIL	Frame Buffer Interface

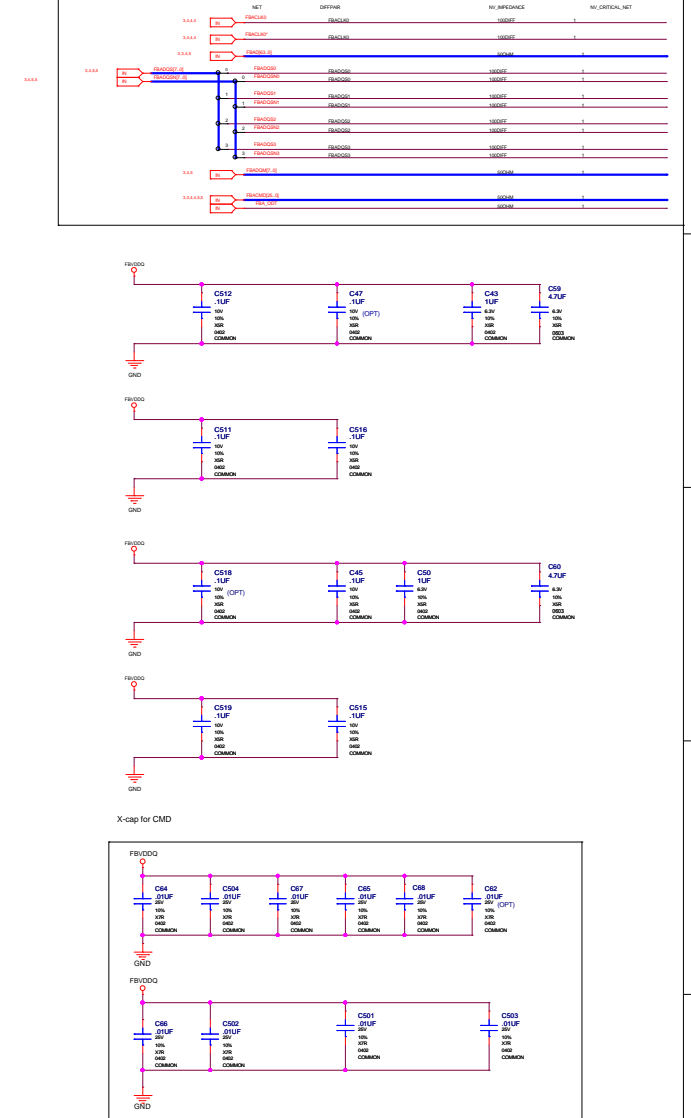
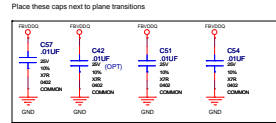
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2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA



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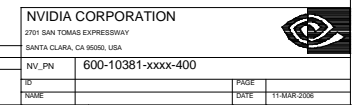
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PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY



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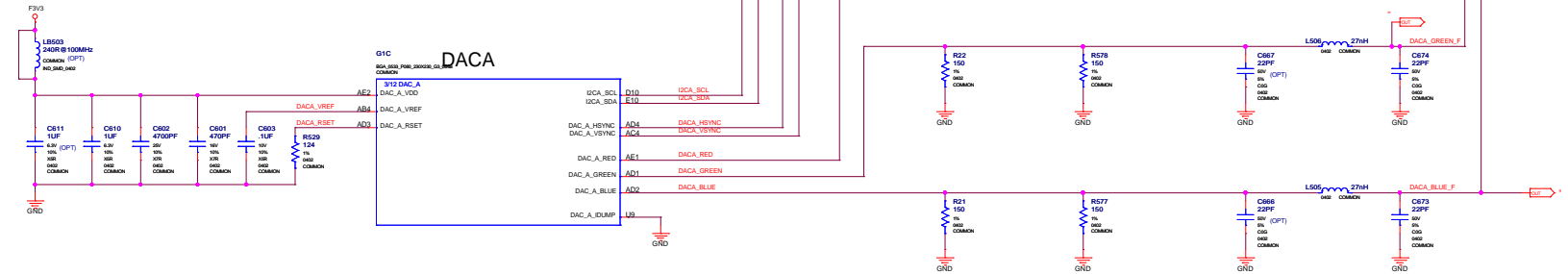
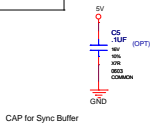


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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Memory 1st bank 32..63

DAC-A, DB15 Connector

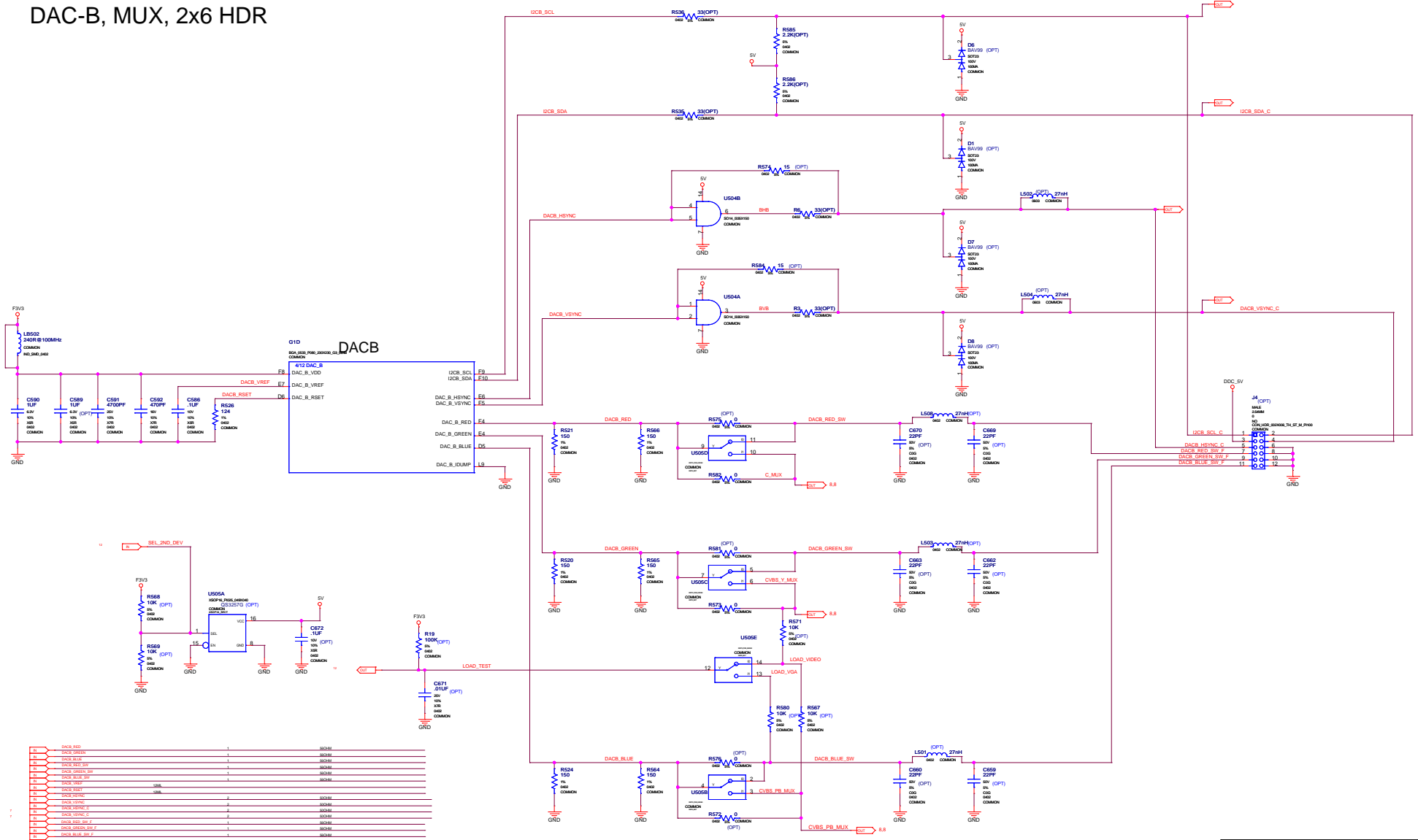
Net Name	MIN_LINE_WIDTH	CRITICAL_NET	IMPEDANCE
DACA_VSYNC	2		100OHM
DACA_VREF	2		100OHM
DACA_HSYNC_C	2		100OHM
DACA_HSYNC_F	2		100OHM
DACA_RED	1		100OHM
DACA_GREEN	1		100OHM
DACA_BLUE	1		100OHM
DACA_RED_F	1		100OHM
DACA_GREEN_F	1		100OHM
DACA_BLUE_F	1		100OHM



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DAC-B, MUX, 2x6 HDR



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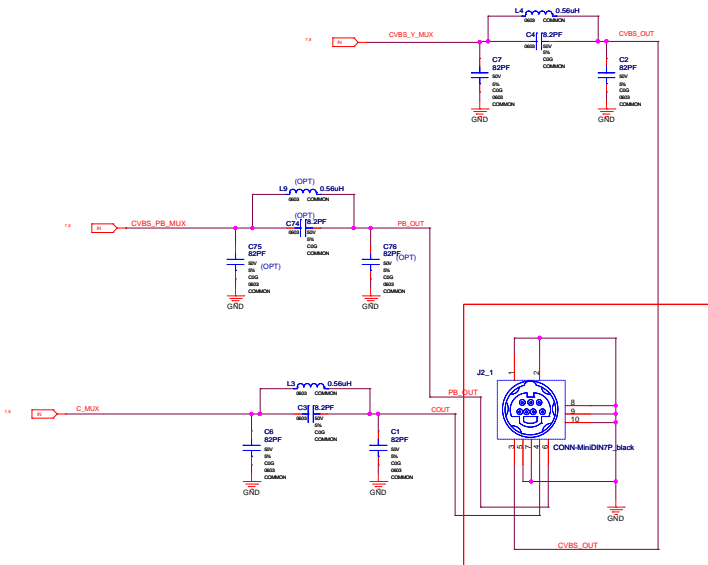
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACB, VGA Output, TV Out

VIDEO CONNECTORS: MiniDIN

	Name	LINE_WIDTH	CRITICAL	IMPEDANCE
14	C_MUX			50OHM
14	CVBS_Y_MUX			50OHM
14	CVBS_Y_MUX			50OHM
14	CVBS_Y_MUX			50OHM
14	CVBS_Y_MUX			50OHM



Replace MiniDin with Compupack Part

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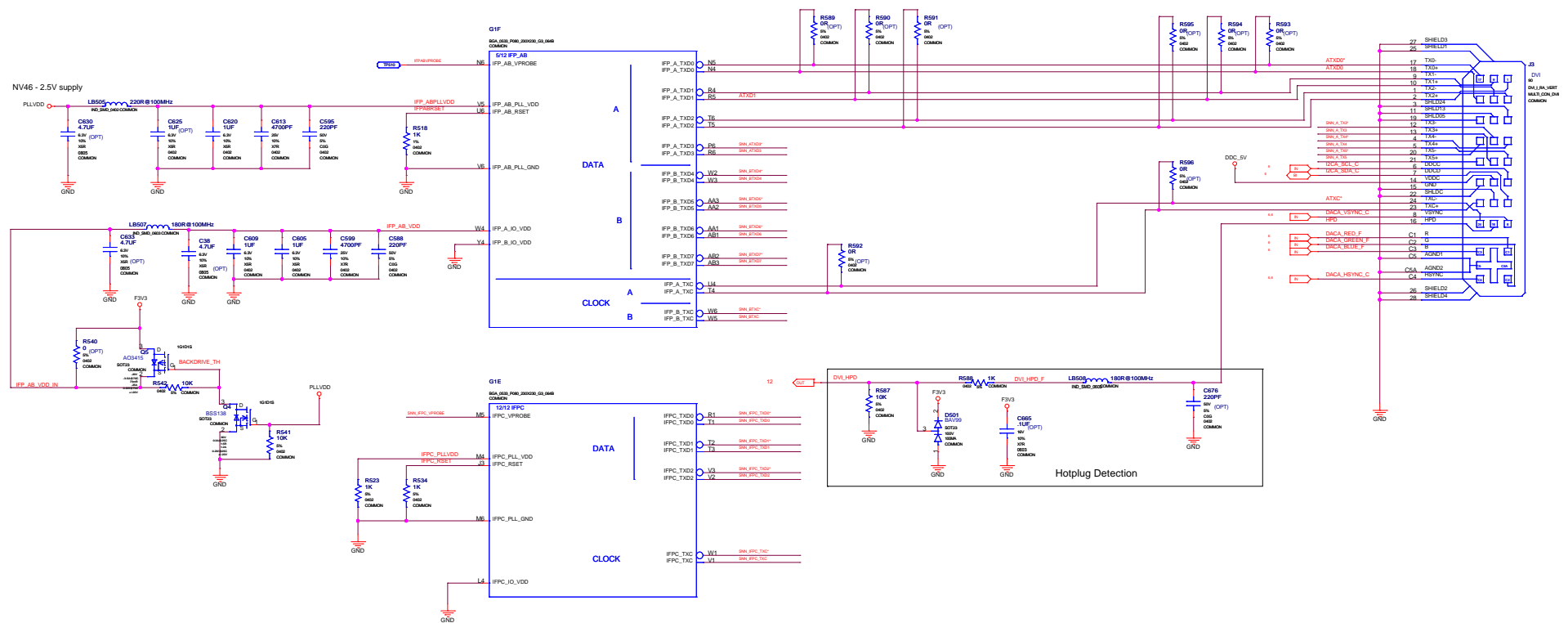
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	VIDEO CONNECTORS: MVDIN, 246 HDR

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NAME			

Internal TMDS, DVI-Connector

Net Name		Diff	CRITICAL	IMPEDANCE
1	AT2C	AT2C	1	100000F
2	AT2C	AT2C	1	100000F
3	AT2D	AT2D	1	100000F
4	AT2D	AT2D	1	100000F
5	AT2E	AT2E	1	100000F
6	AT2E	AT2E	1	100000F
7	AT2F	AT2F	1	100000F
8	AT2F	AT2F	1	100000F
9	AT2G	AT2G	1	100000F
10	AT2G	AT2G	1	100000F

Net Name		Min Line Width	Voltage
1	BP_001VCC100	0.004	0.0V
2	BP_001VCC	0.004	0.0V
3	BP_001VCC_01	0.004	0.0V



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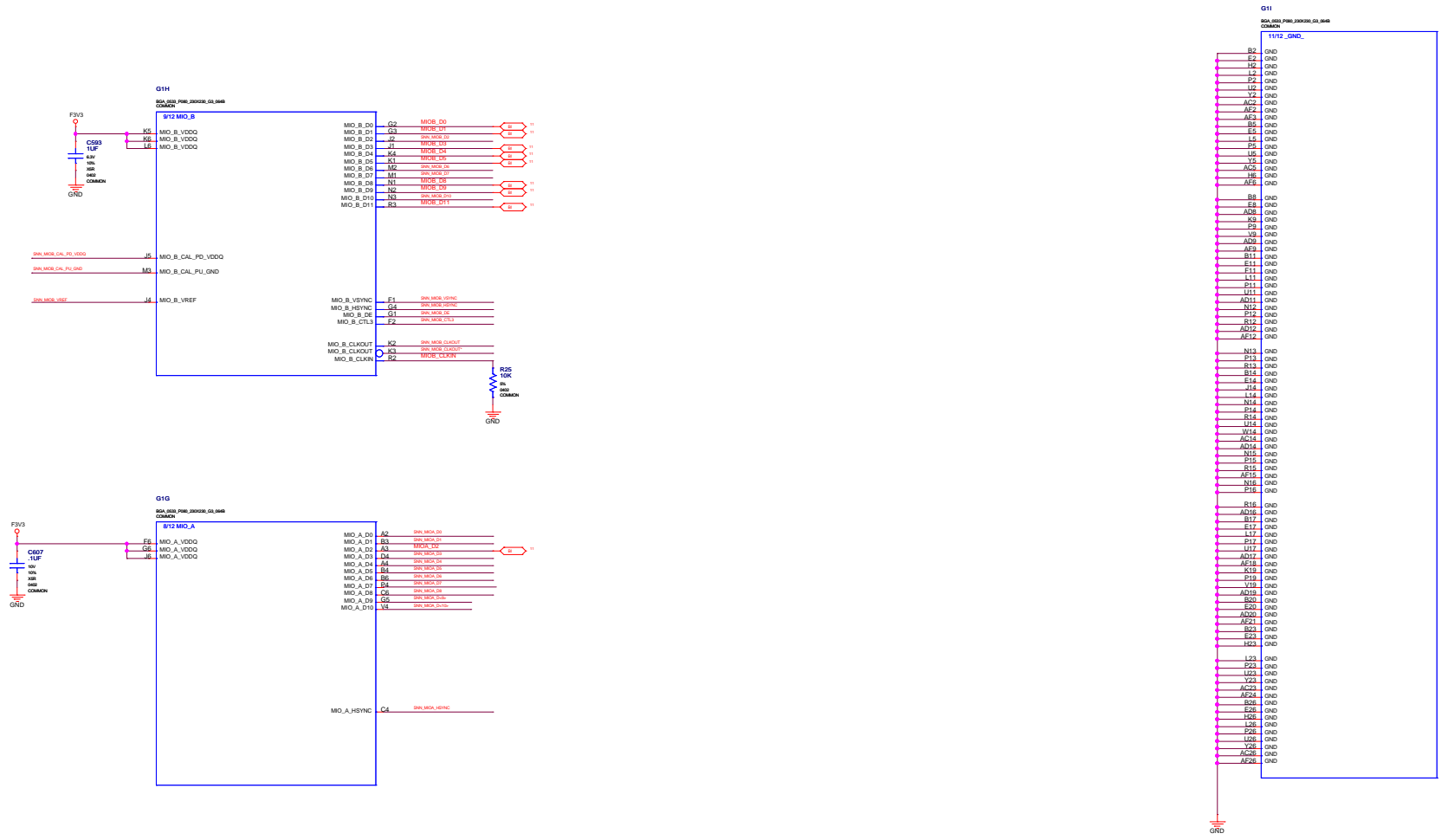


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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	TMDS Interface

MIOA, MIOB Interface



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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	MIOA, MIOB Interface, LPC-ROM

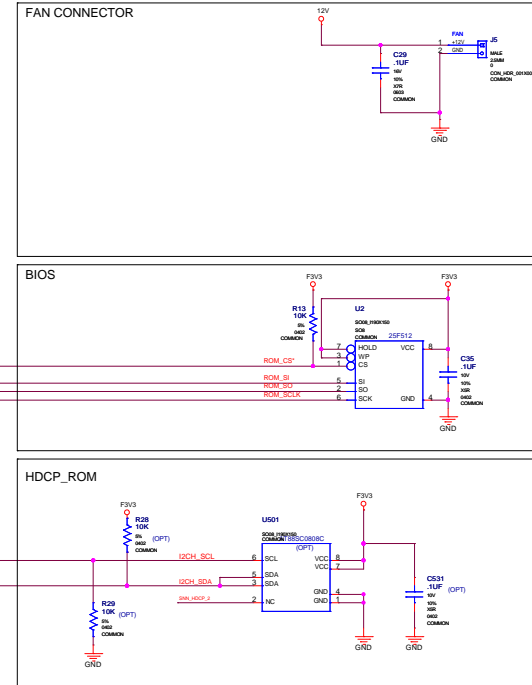
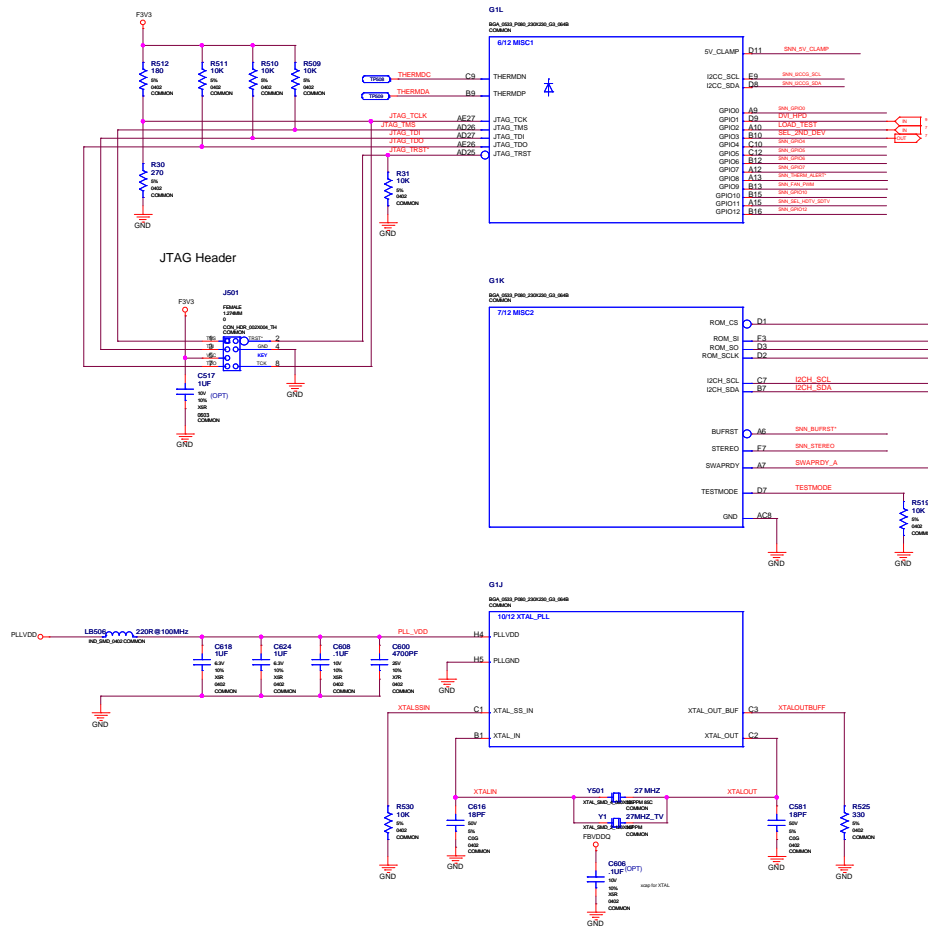
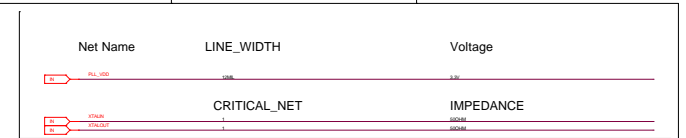
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	STRAPS, Mechanical Parts

ID		PAGE	
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XTAL, GPIO, BIOS, FAN, JTAG, THERMAL



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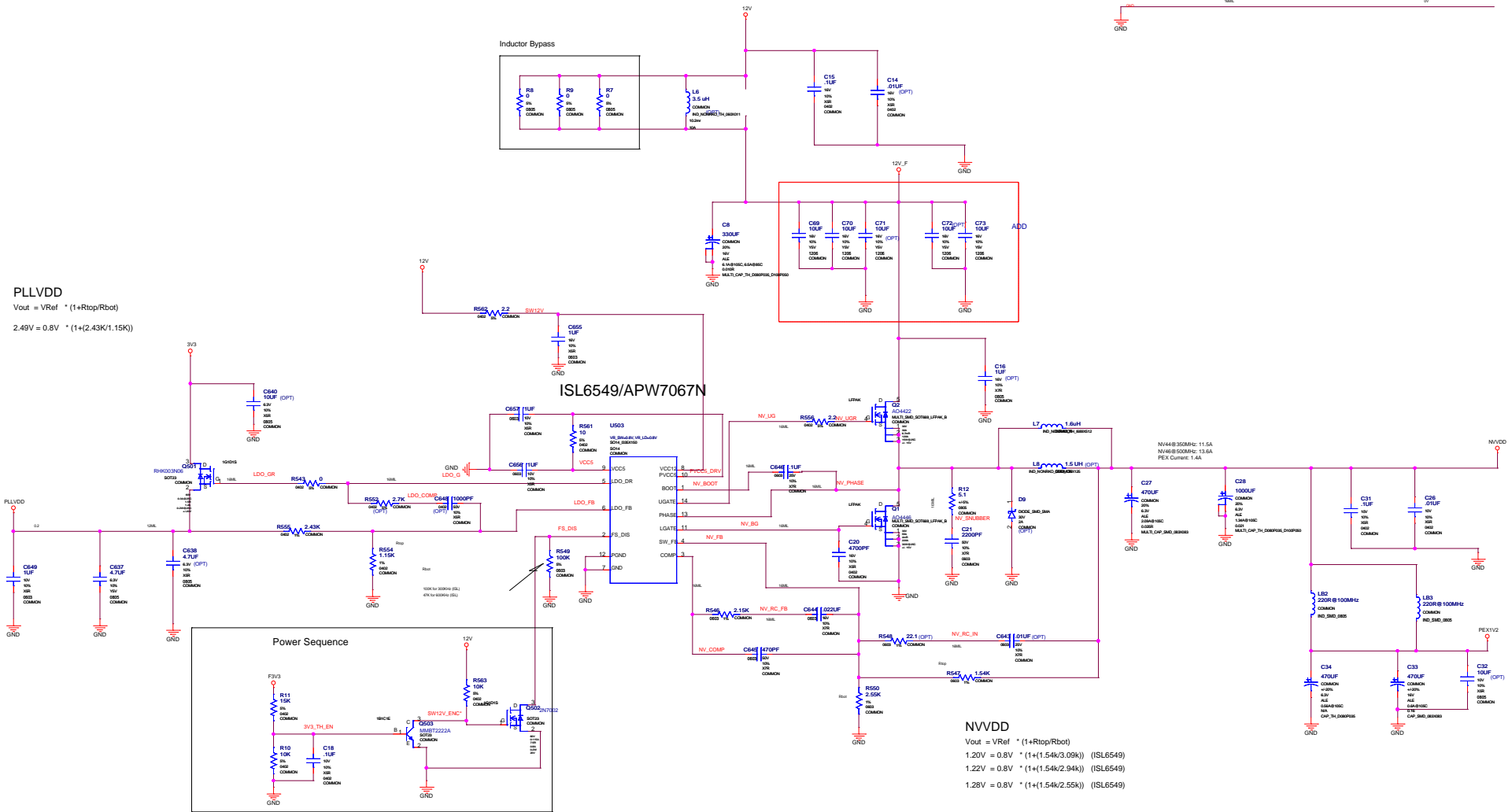
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	XTAL, GPIO, BIOS, FAN, JTAG, THERMAL

PowerSupply1: NVVDD, PLLVDD

Net Name	LINE_WIDTH	Current	Voltage
12V	30MIL	4	12V
12V_F0	30MIL	4	12V
N+VDD	30MIL	15	1.2V
PEX1V2	30MIL	4	1.2V
GND	30MIL		0V



Replace SO-8 FETs with AO4422

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PowerSupply: NVDD, A3V3

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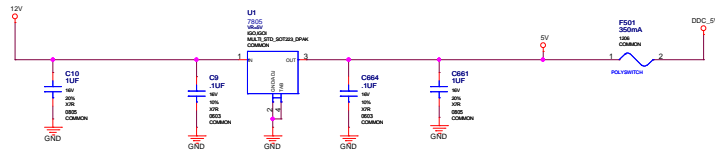
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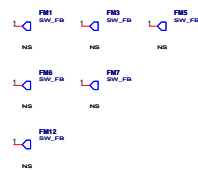
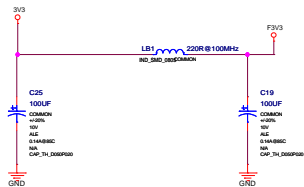
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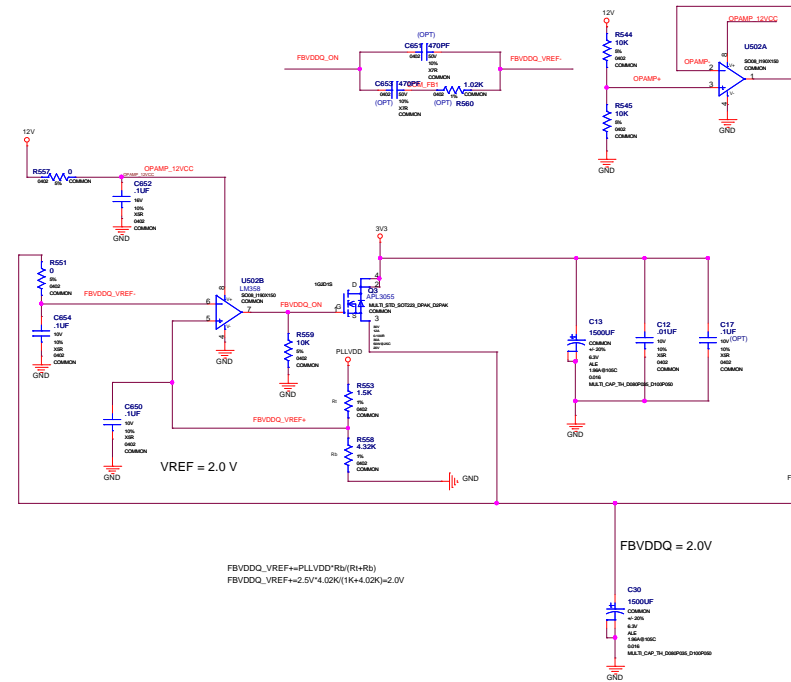
PowerSupplyII: 5V, DDC5V, F3V3, FBVDDQ



F3V3



FBVDDQ



Net Name	LINE_WIDTH	Current	Voltage
5V	1500	200	5V
DDC_5V	1500	50	5V
F3V3	1500	1	3.3V
F3V3	8000	3	3.3V
F5V000	3000	3	5.00V

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