## NV11, 2MX32 SDR, RGB, INTERNAL DVI-I, AGP. PCI DEVICE ID 0X0=0X110 FOR NV11.

STRAPS: AGP4X, SIDEBAND DIS, FAST WRITE ENA, ADP BIOS, NORMAL PCIAD, 14.318 MHZ STRAP\_RAM\_TYPE [5:2]=0000-2MX32 SDR SDRAM

## HISTORY REV. COO:

- 1) C00: Implemented Separate DFP\_VREF and DFP\_RSET for each TMDS channel.
- 2) COO: Added RC networks for FBDQS and FBCLK.
- 3) C00: Fixed 64 bit memory interface.
- 4) COO: Added more bulk decoupling caps to 3.3V rail. 5) COO: Added alternative footprint for D800 and C808.
- 6) C00: Changed HSYNC/VSYNC caps from 220pf to 22pf.
- 7) C00: Recalculated DFP hi frequency caps to have RSF from  $65\,\mathrm{mhz}$  to  $108\,\mathrm{mhz}$ .

602-P0039-0000-C00

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