

NV11, 4MX16 SDR SDRAM-6ns, RGB, AGP4X, TV
DOWN,DVI.

PCI DEVICE ID 0X0=0X110 FOR NV11.

STRAPS: AGP4X, FAST WRITE ENA, ADP BIOS, NORMAL PCIAD, 14.318MHZ
STRAP_RAM_TYPE [5:2]=0001-4MX16 SDR SDRAM

HISTORY REV. A00 BASED ON P39-C00:

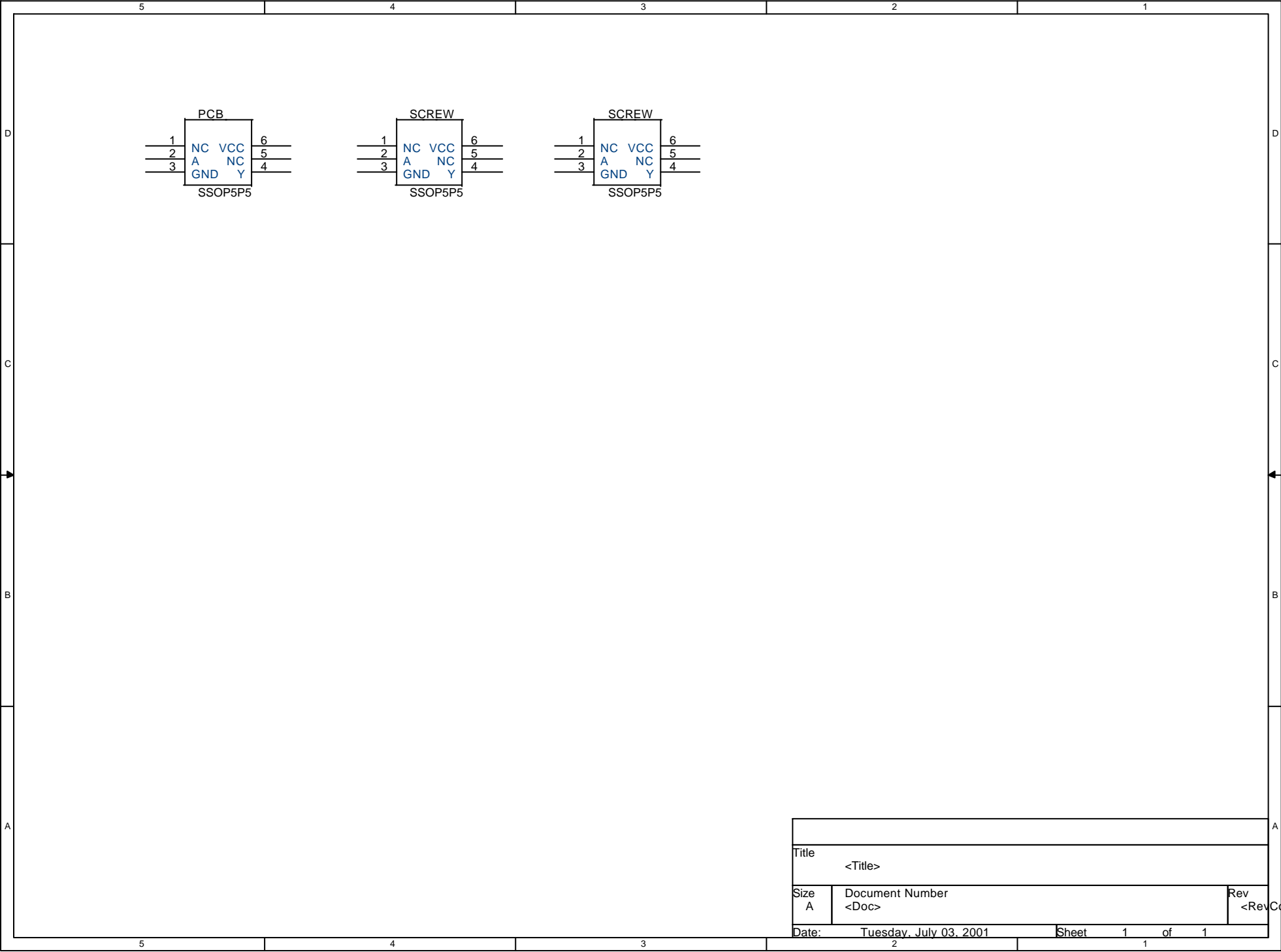
HISTORY REV. A01: NO netlist change:redo delay for
FB*CLK*

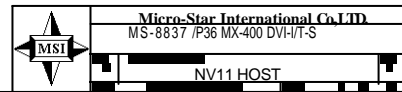
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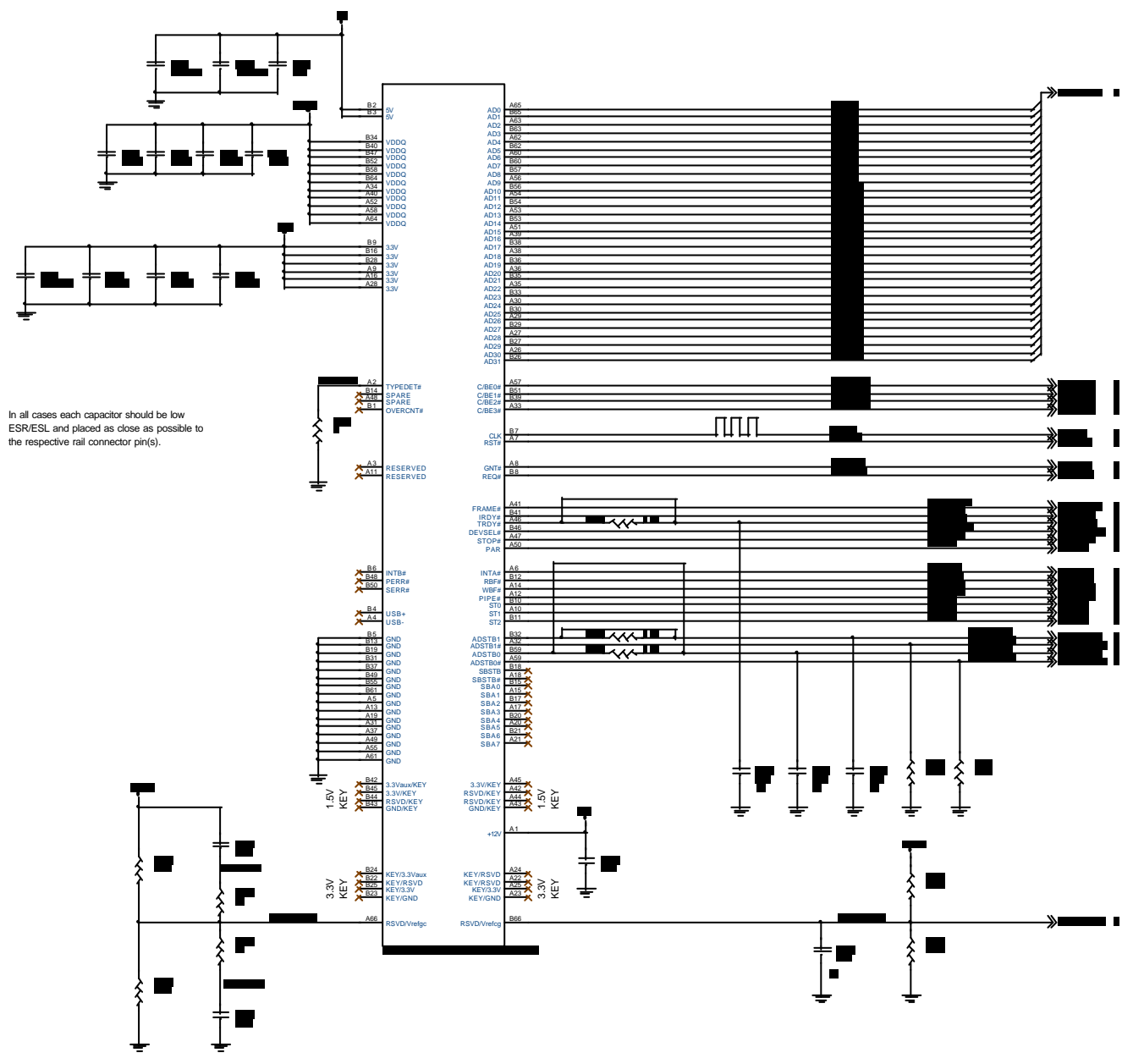


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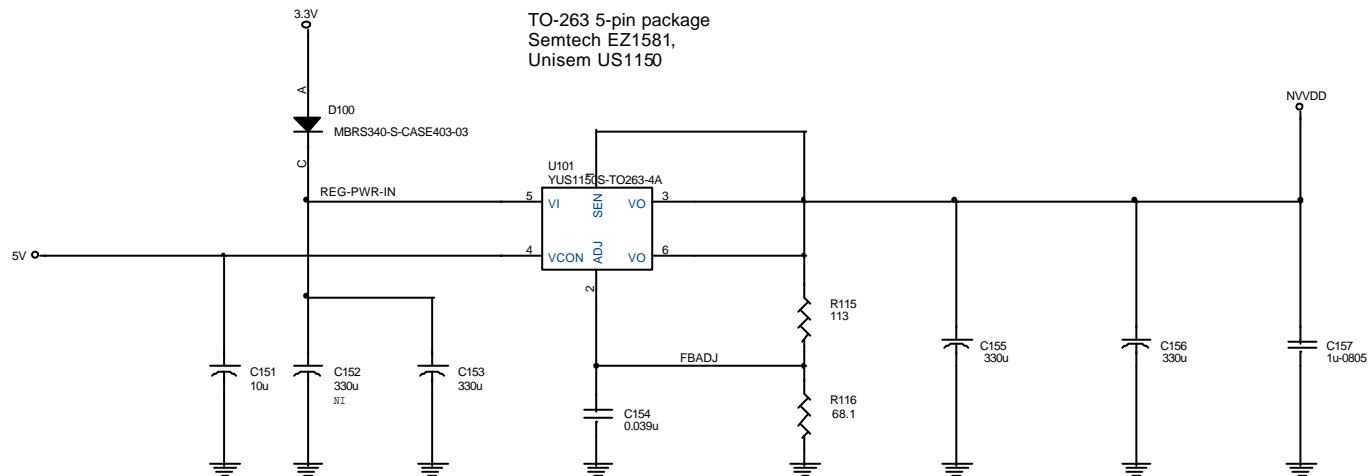
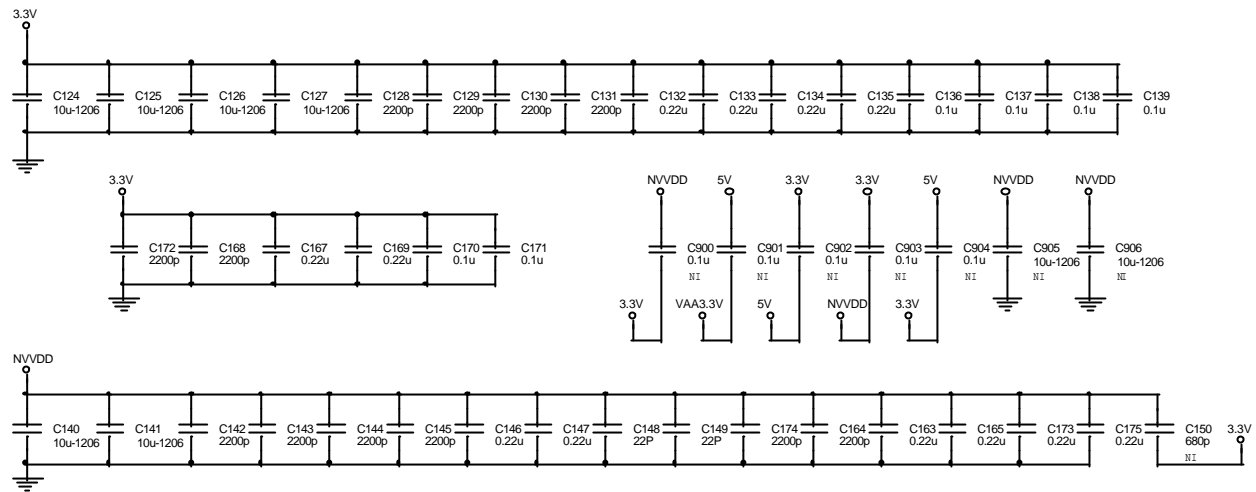
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In all cases each capacitor should be low ESR/ESL and placed as close as possible to the respective rail connector pin(s).

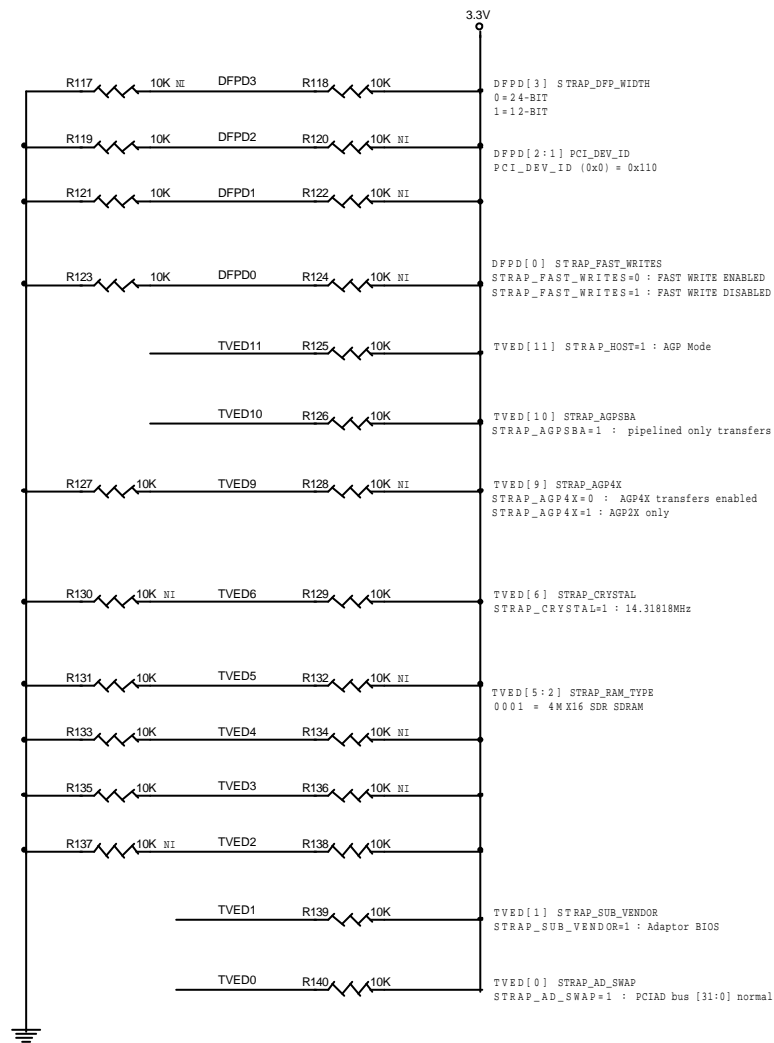


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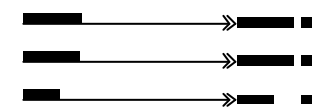
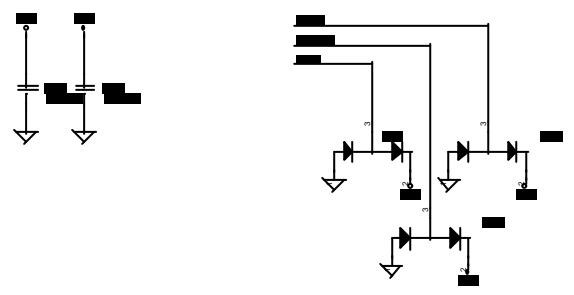
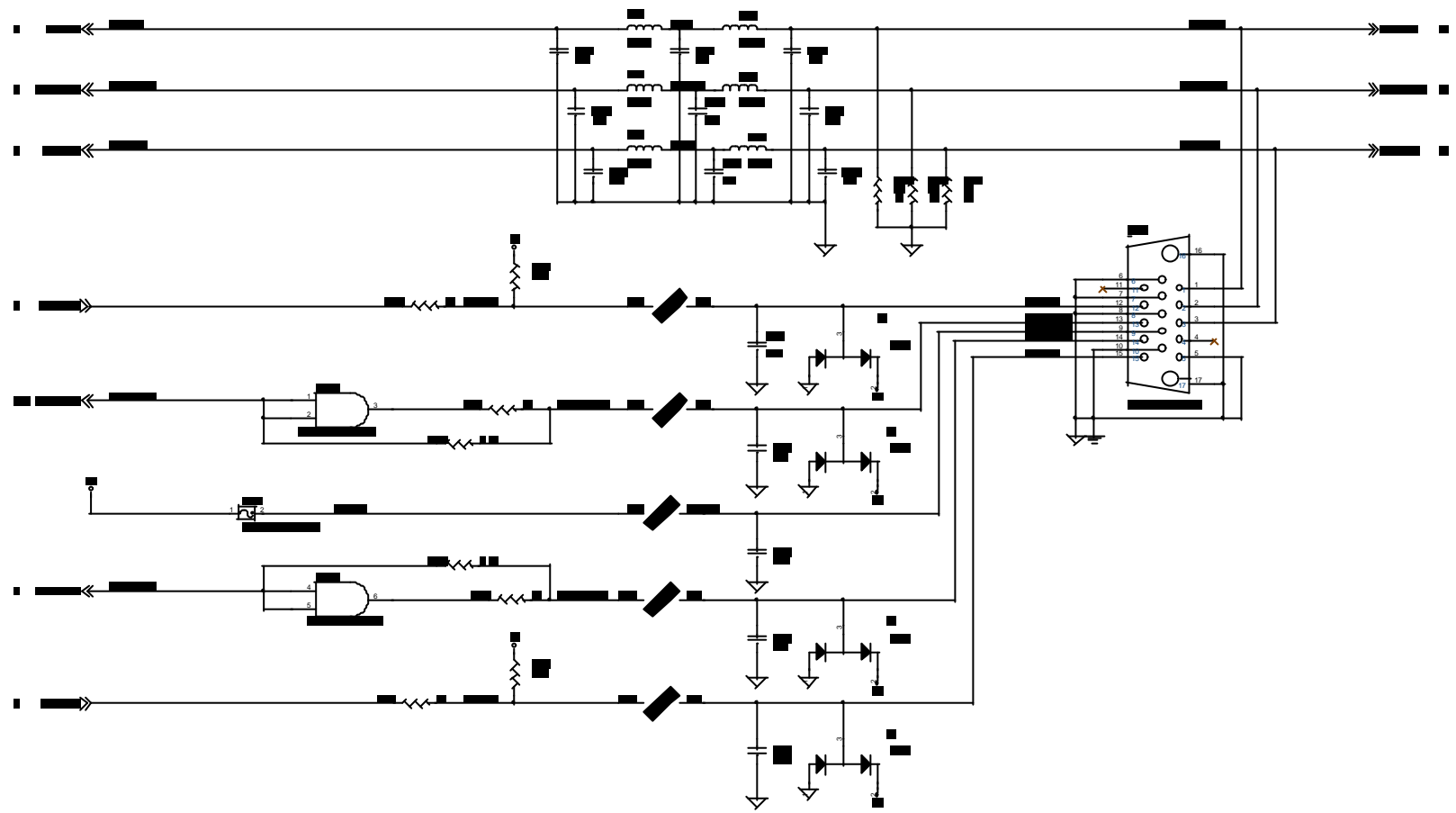
13,15 DFPD[11:0] >>
7,12 VIPD[7:0] >>
7,12,14,15 TVED[11:0] >>

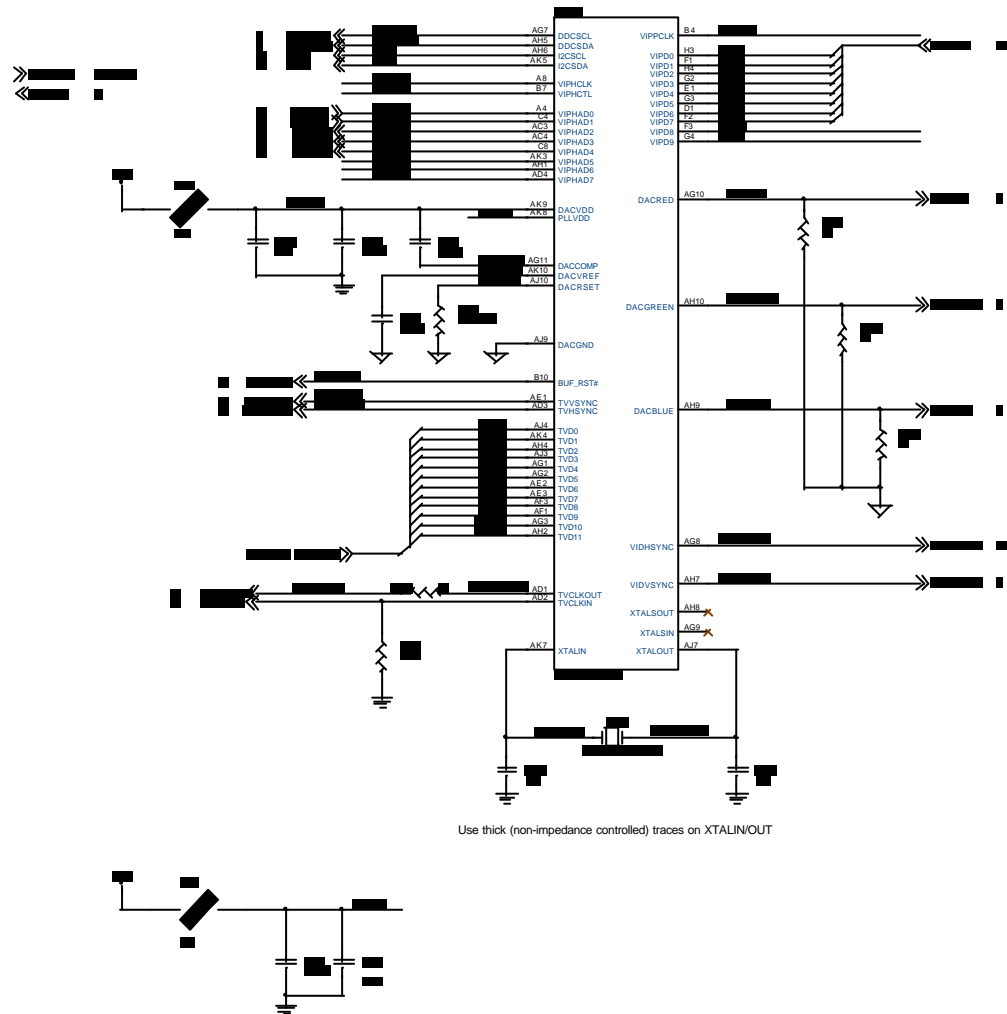


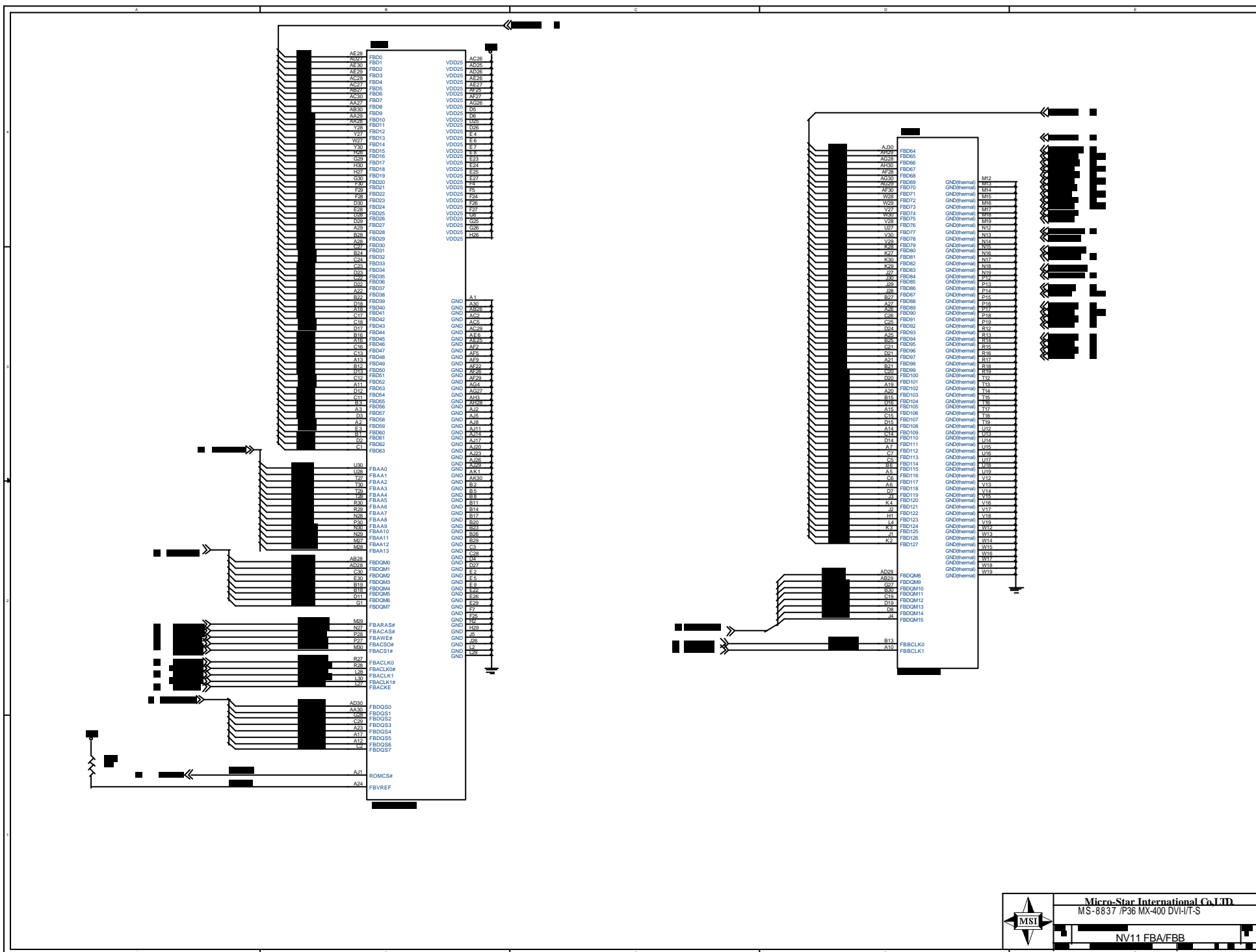
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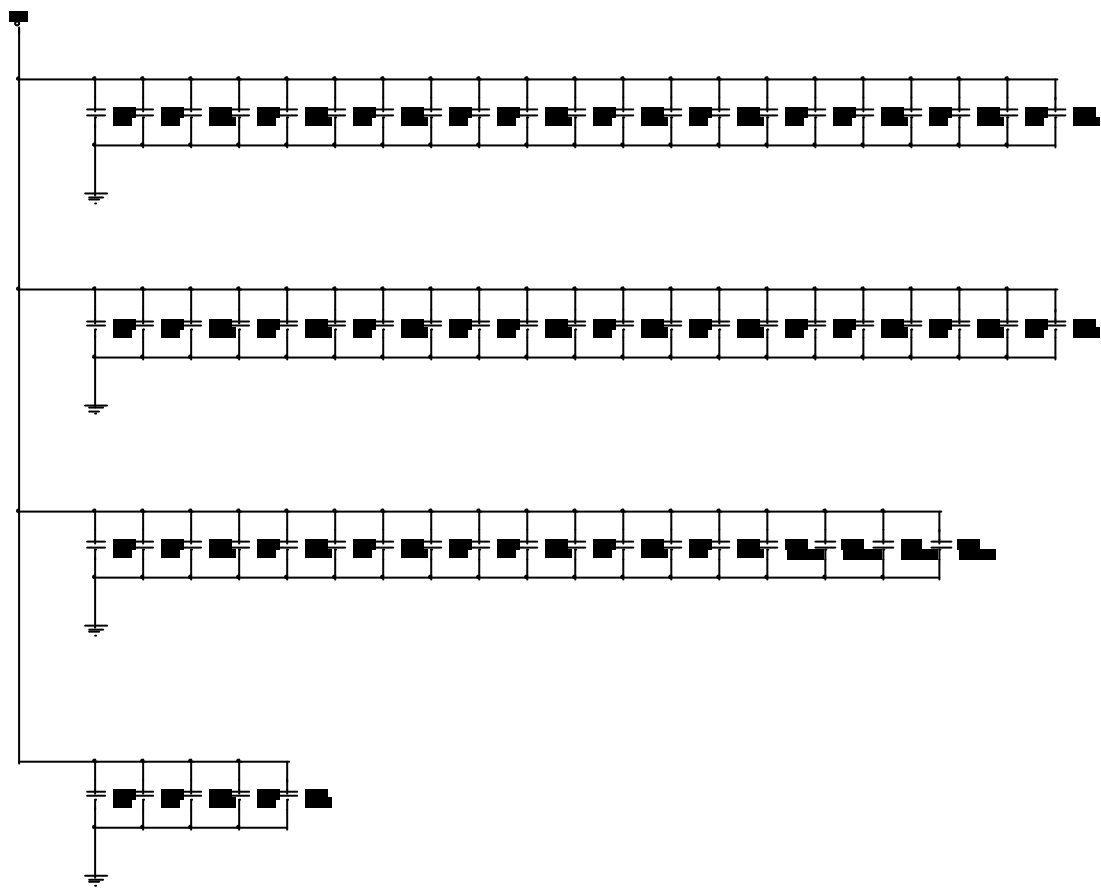
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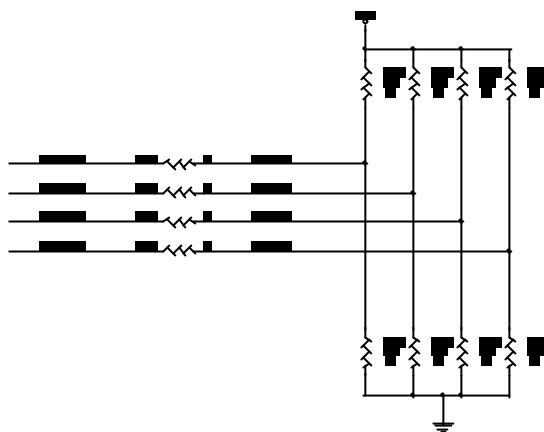
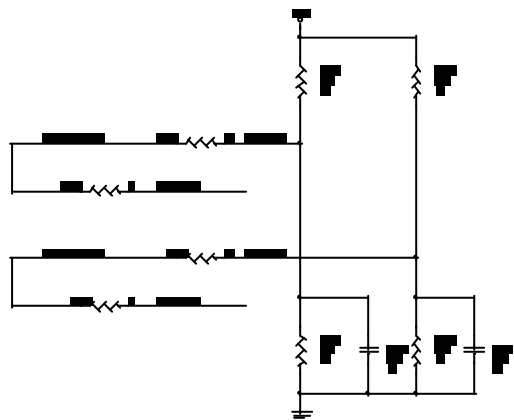
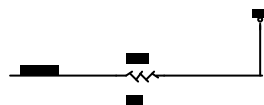
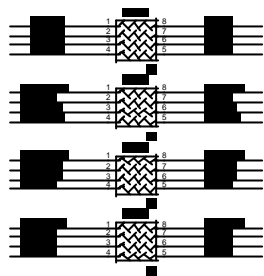
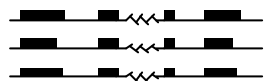
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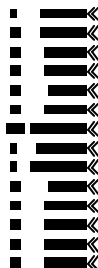
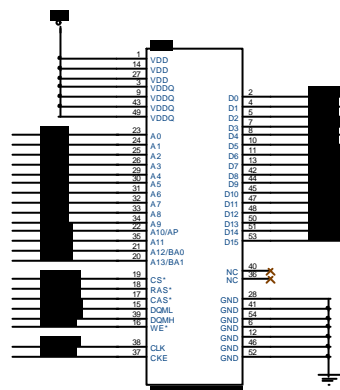
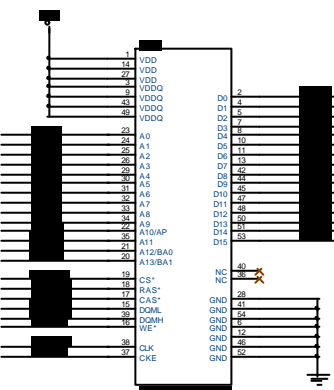
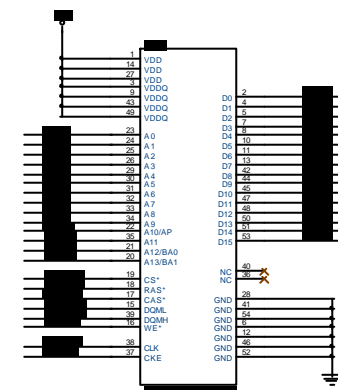
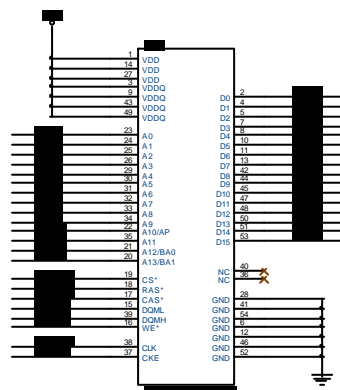
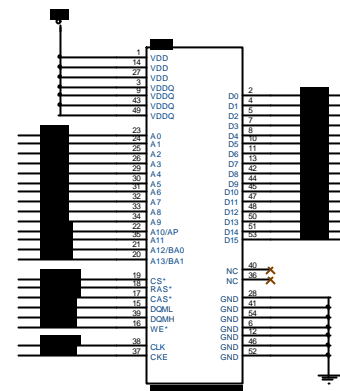
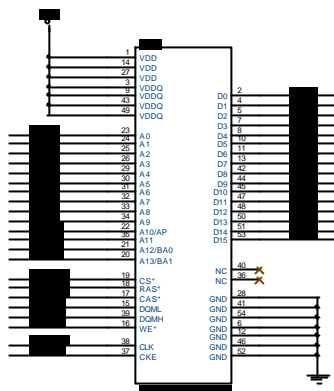
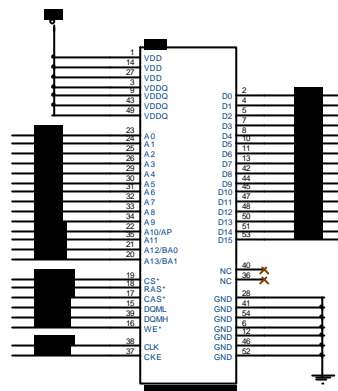
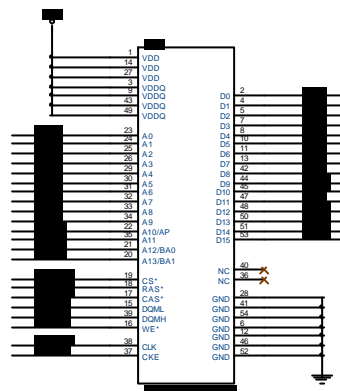


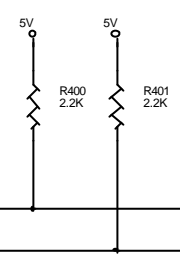
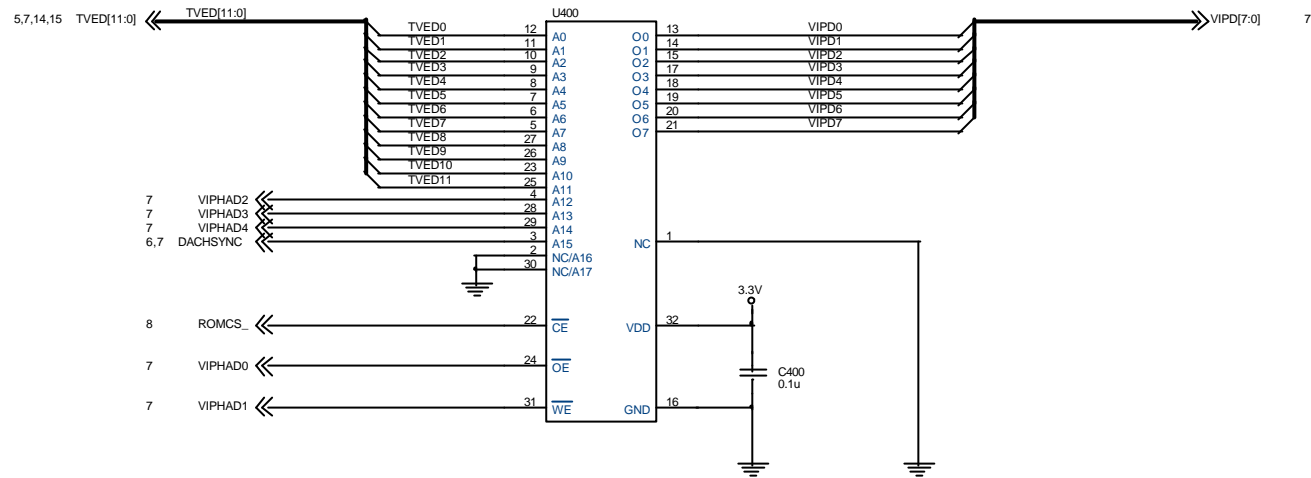








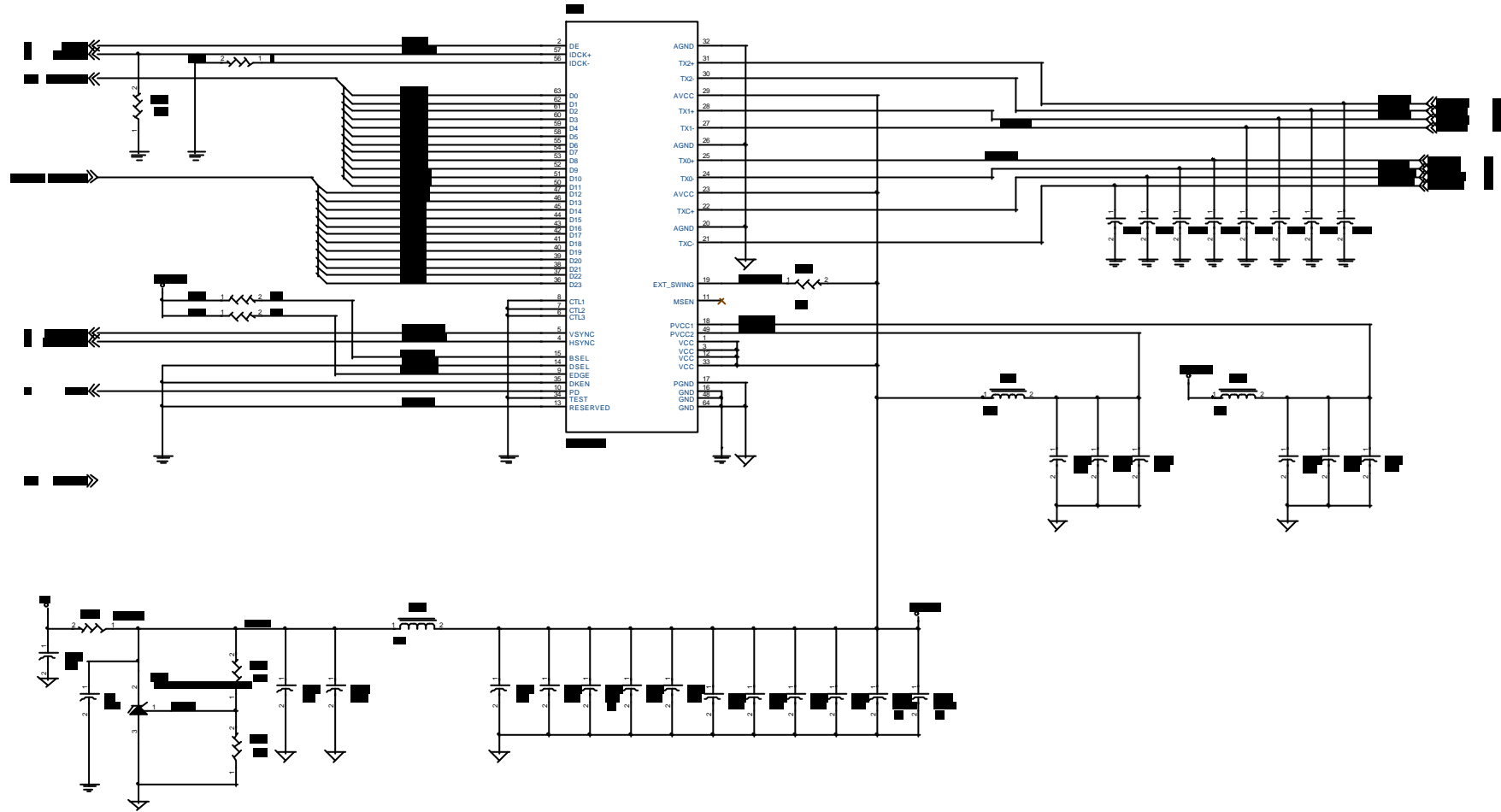




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