

## Model List

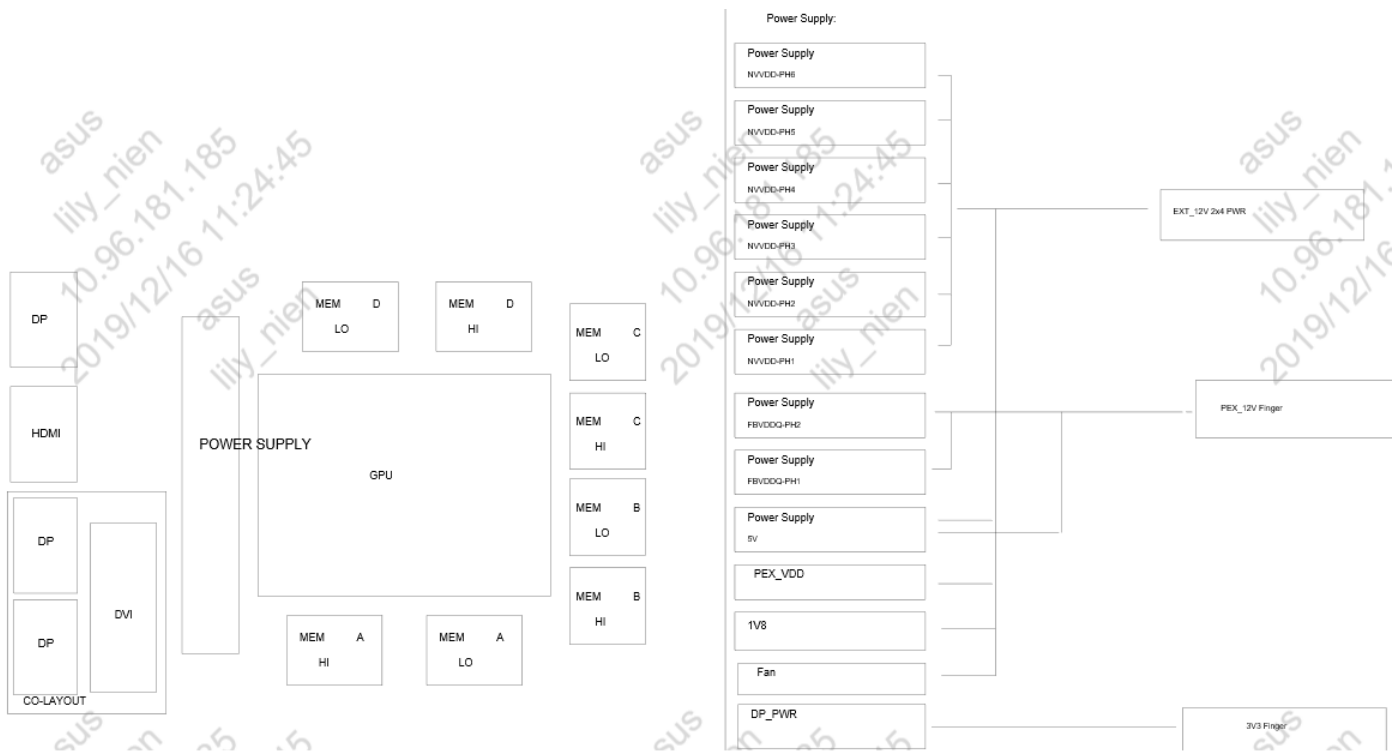
Model name	60PN	GPU	Date
DUAL-RTX2060S-8G-EVO-V2	YV0DZ2	CG161P	2019/12/16

## STANDARD APPEARANCE





## BLOCK DIAGRAM



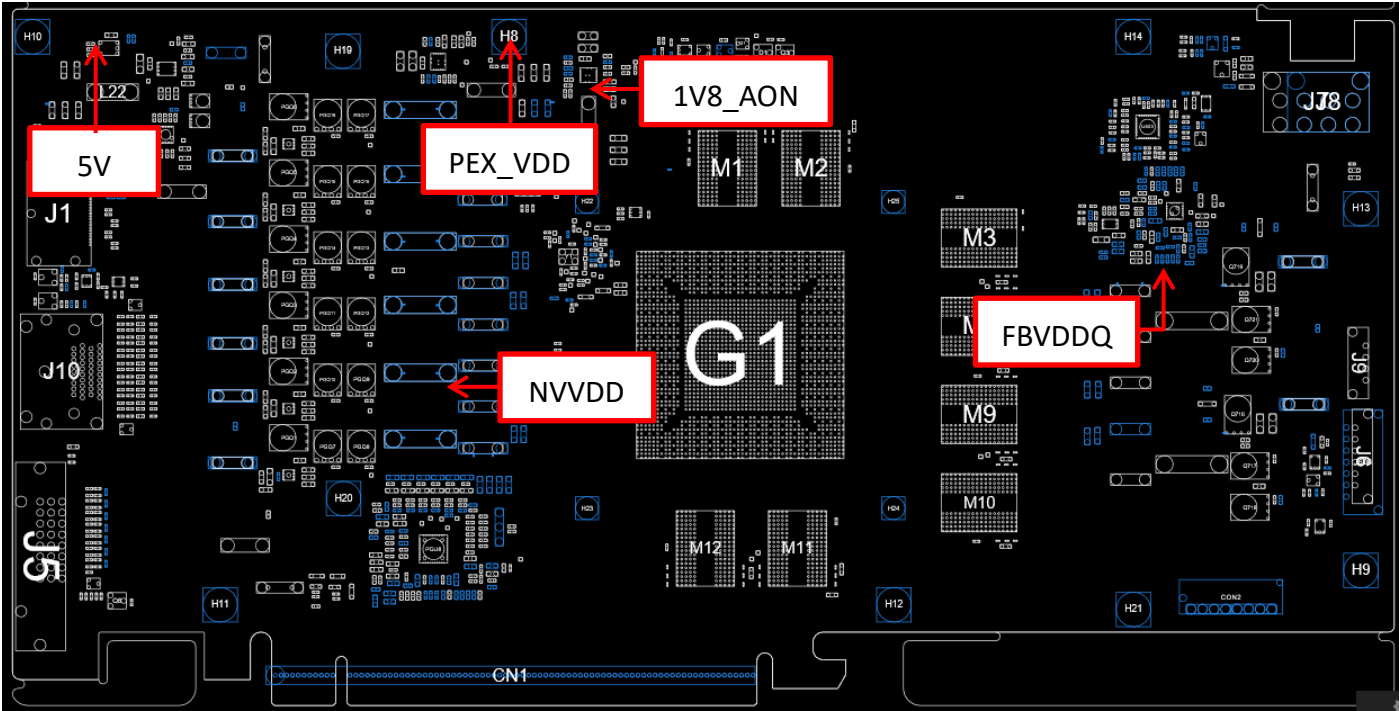
The diagram illustrates the power supply system for the PS module, showing four main power rails and their connections to the MP2888+CSD95480 converter.

- NVVDD phase2-3:** Connected to the NVVDD rail. Current: 3.9A. Voltage: 1V/20\*2=40A. Efficiency: Eff 85%.
- PEX\_VDD:** Connected to the PEX\_VDD rail. Current: 1V/5A. Efficiency: Eff 85%.
- FBVDDQ phase1:** Connected to the FBVDDQ rail. Current: 1.98A. Voltage: 1.35V/15A. Efficiency: Eff 85%.
- NVVDD phase1:** Connected to the NVVDD rail. Current: 1.96A. Voltage: 1V/20\*1=20A. Efficiency: Eff 85%.

Additional components and connections shown include:

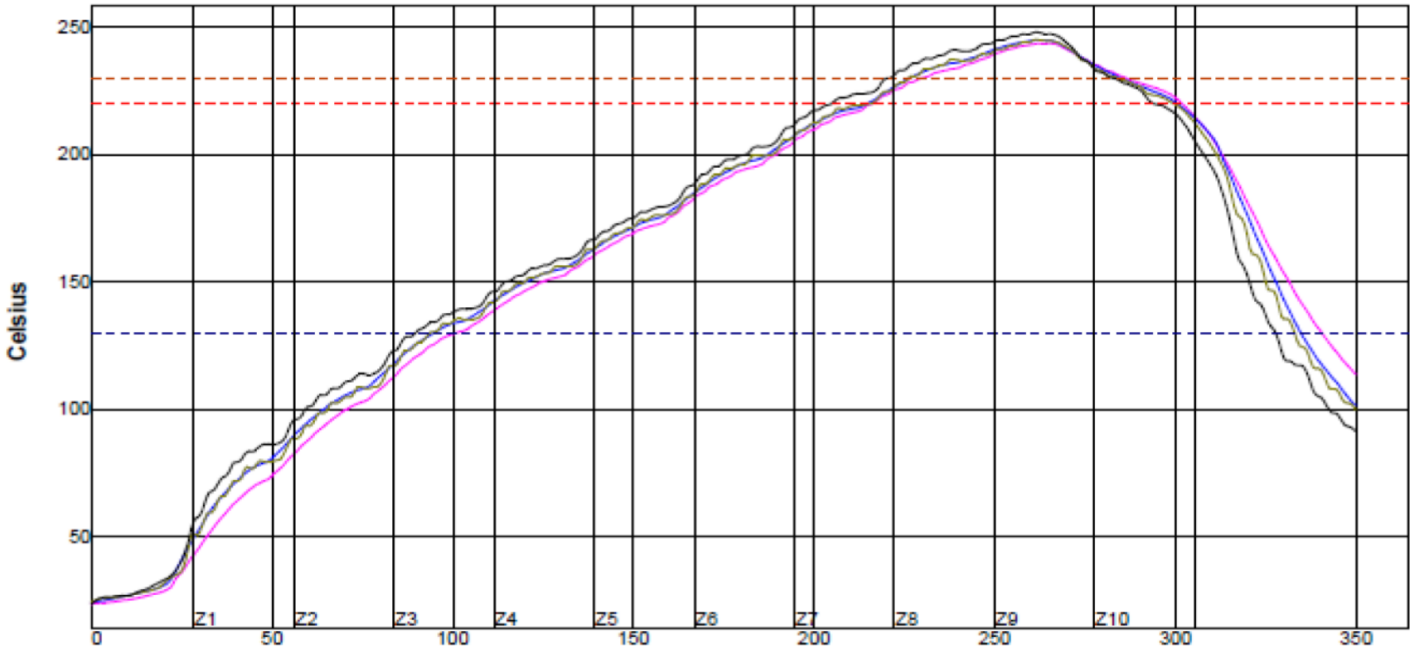
- MP2888+CSD95480:** The main power converter, shown in a yellow oval.
- PS NVVDD EN RD:** A signal line connected to the PS NVVDD PG00D (与?) pin.
- SLI\_BRG\_PRST+ #B?PS\_SLI\_FGC6\_OR:** A signal line connected to the SLI\_BRG\_PRST+ pin.

# VOLTAGE MEASURE POINT



# BGA REFLOW PROFILE

Setpoints (Celsius)										
Zone	1	2	3	4	5	6	7	8	9	10
Top	100	125	150	170	190	215	235	250	255	200
Bottom	100	125	150	170	190	215	235	250	255	200
Conveyor Speed ( inch/min ) : 26.00										



PWI= 86%	Max Rising Slope		Max Falling Slope		Preheat 130-220C		Reflow Time /220C		Peak Temp		Tot Time /230C	
U4 CEN - 1	1.80	-28%	-2.88	16%	115.95	71%	85.97	84%	243.84	38%	67.38	43%
M2 - 1	2.07	-17%	-3.28	-13%	120.00	78%	85.84	83%	245.08	51%	58.78	47%
J7 - 2	2.15	-14%	-3.41	-21%	121.51	80%	84.73	78%	245.34	53%	58.12	45%
L7 - 2	2.35	-8%	-3.75	-38%	115.18	71%	88.37	85%	248.28	83%	83.88	58%
Delta	0.55		1.06		5.35		4.85		4.44		8.00	