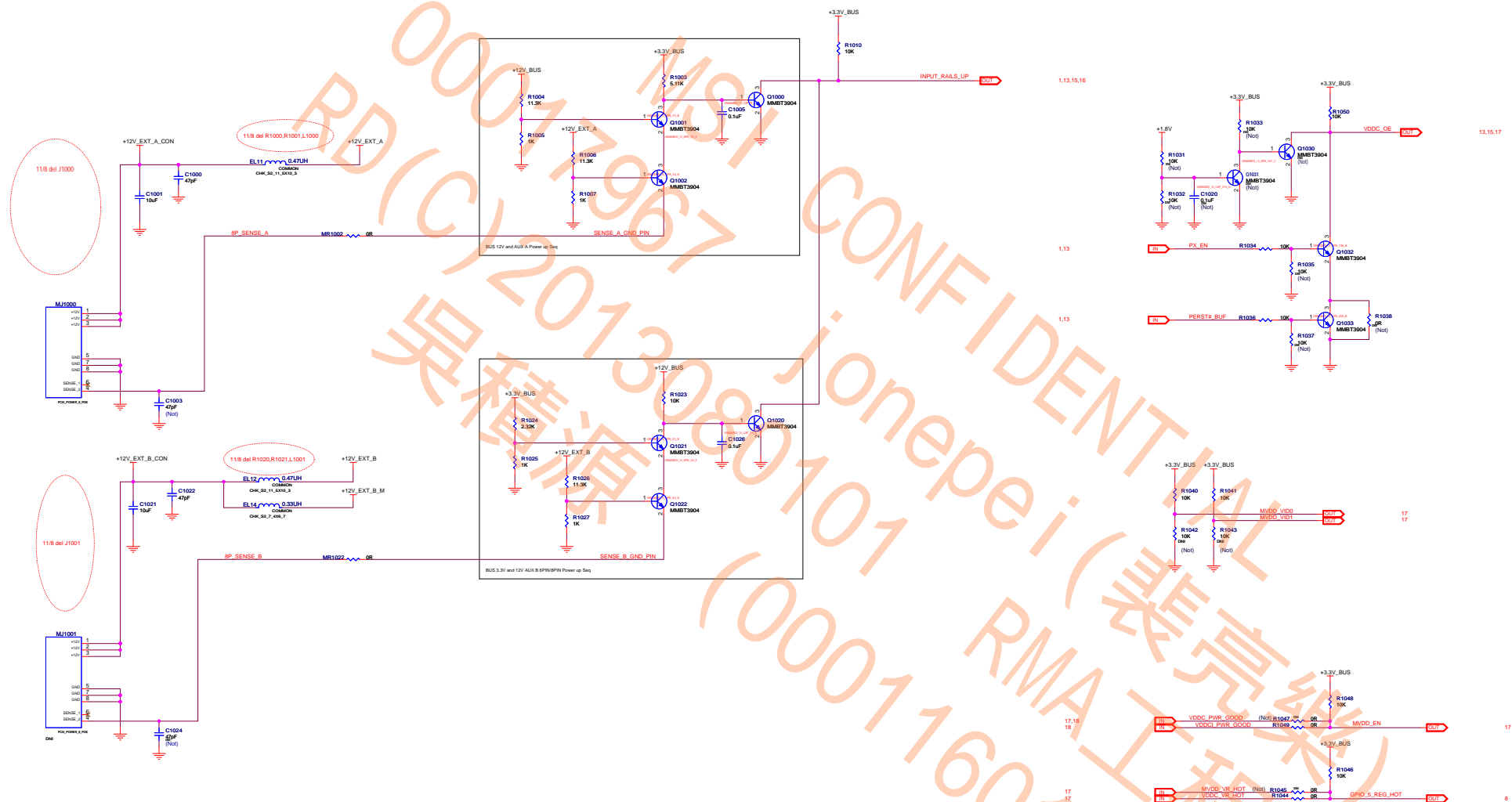
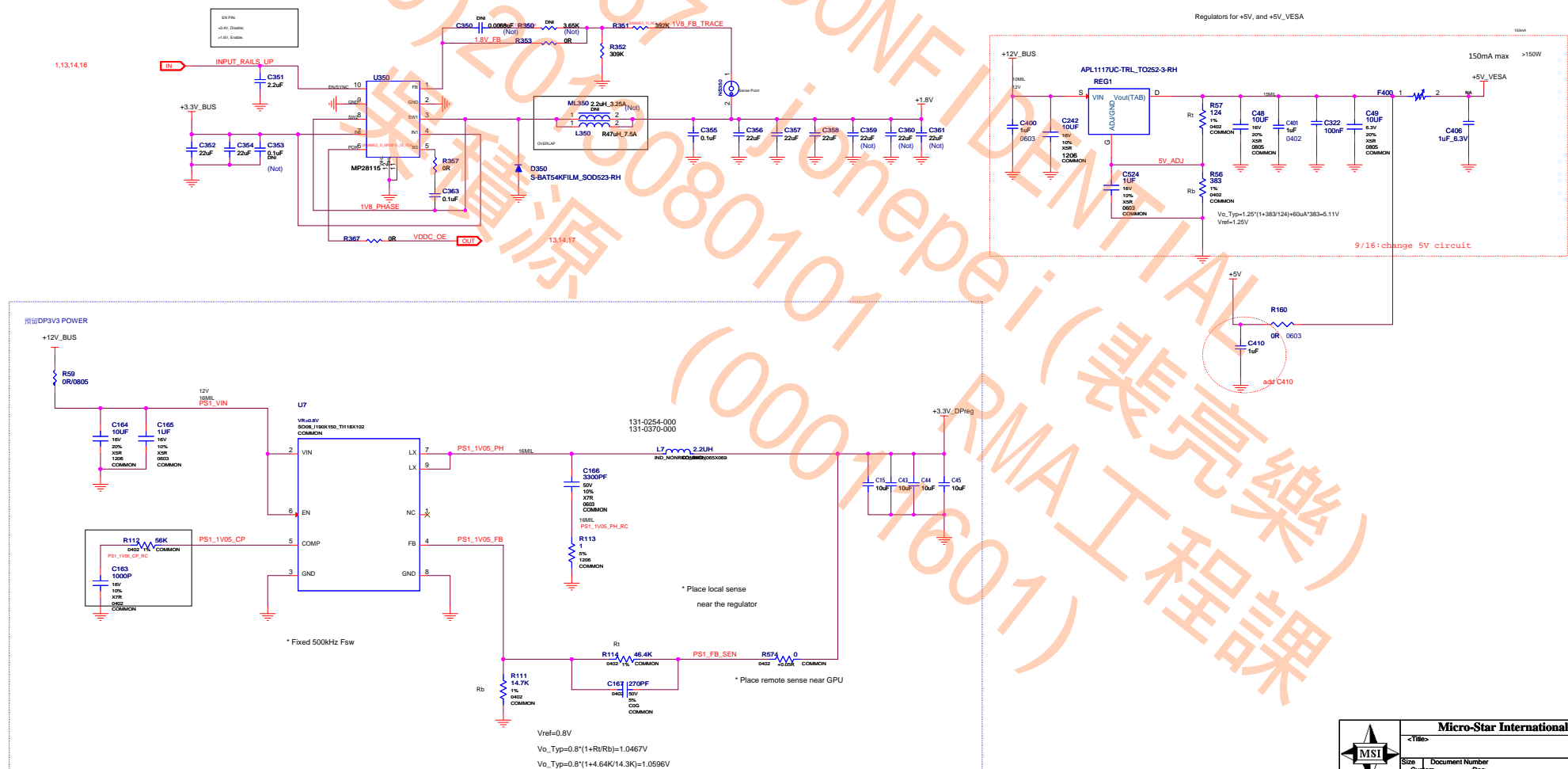
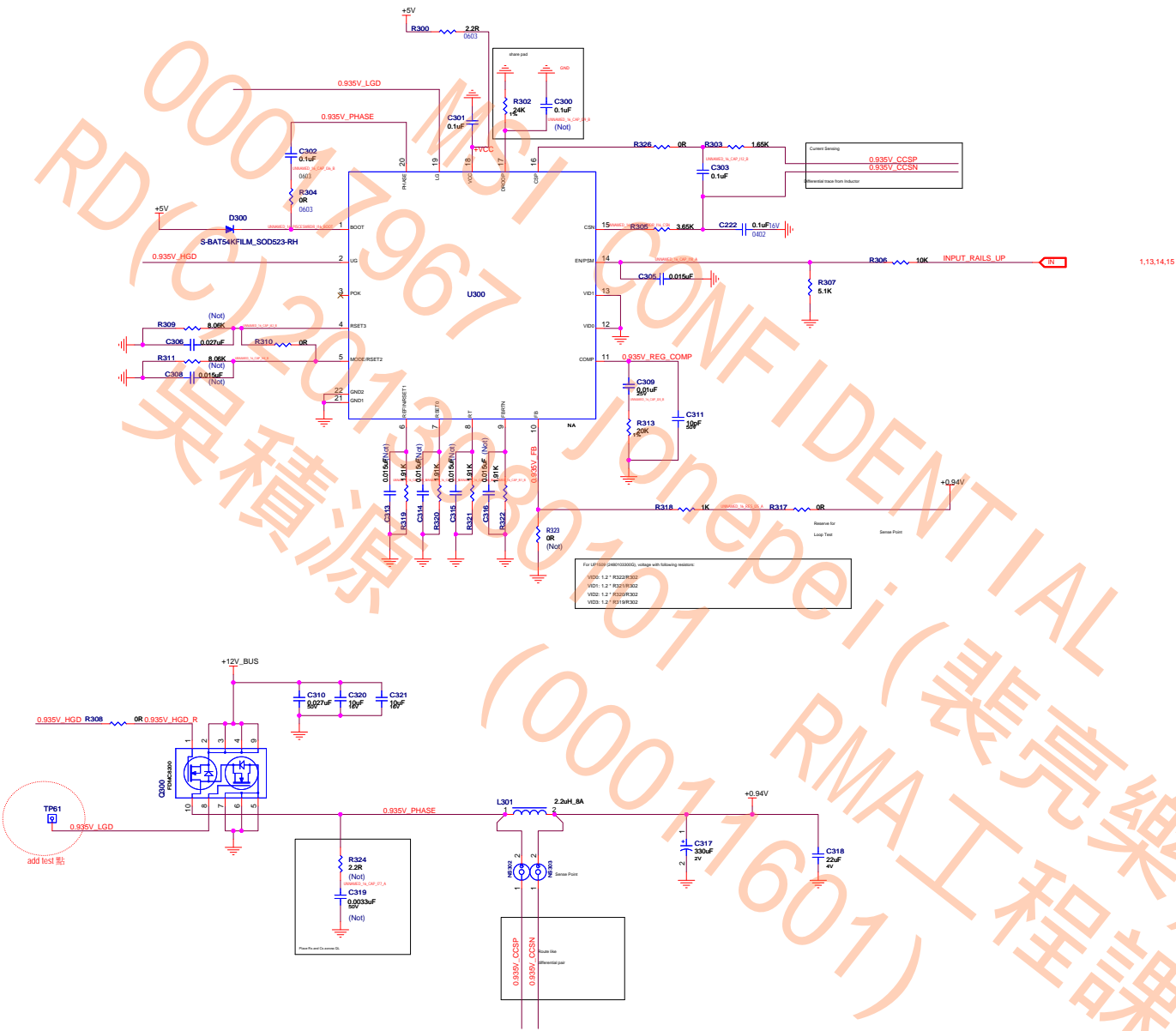


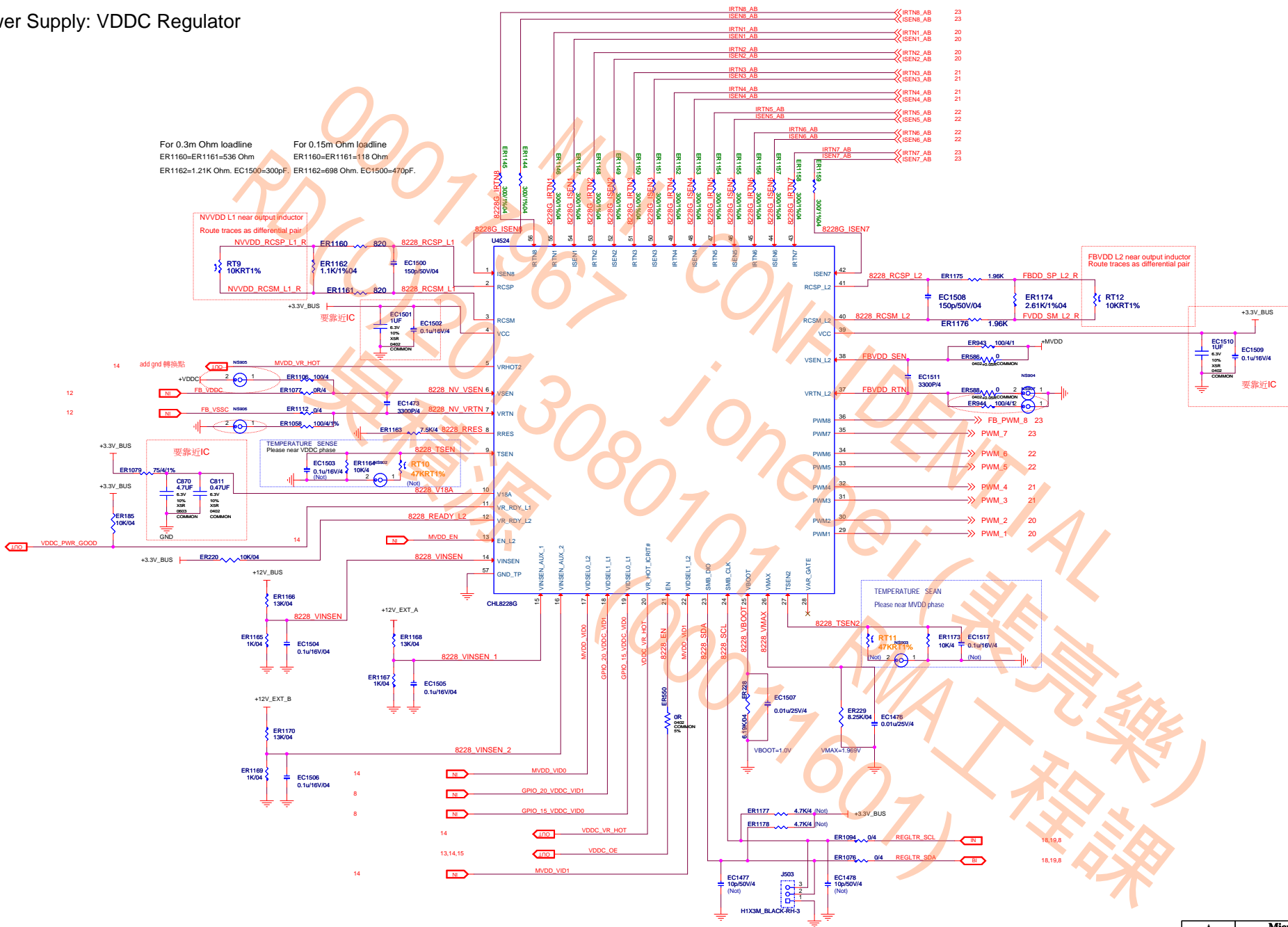
(14) POWER MGMNT







Power Supply: VDDC Regulator



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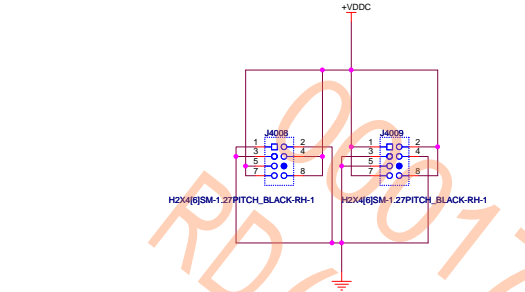
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Large	Doc. No. 3

Date: Thursday, January 1, 2003

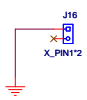
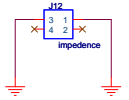
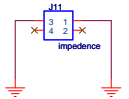
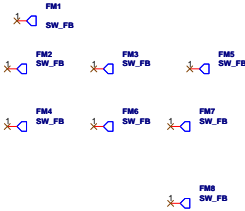
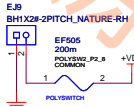
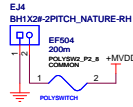
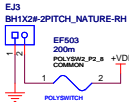
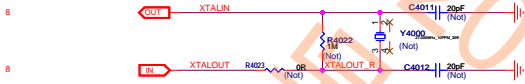
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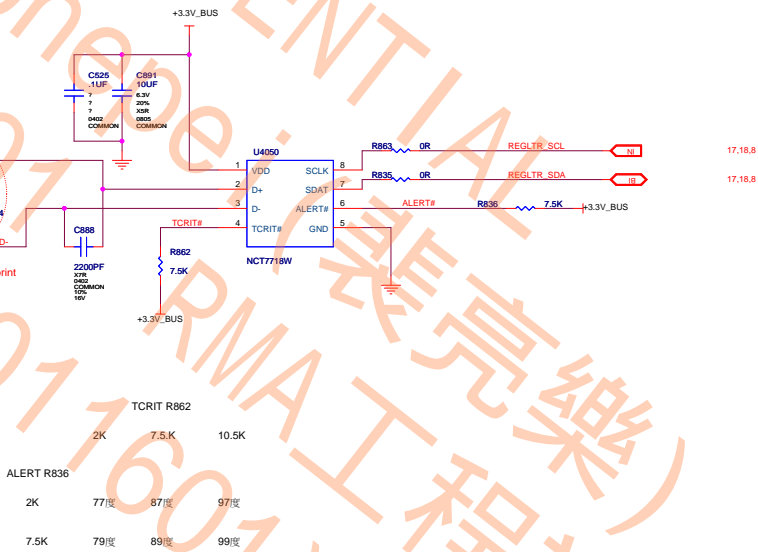


SWITCH CONNECTIONS TO PINSTRAPS

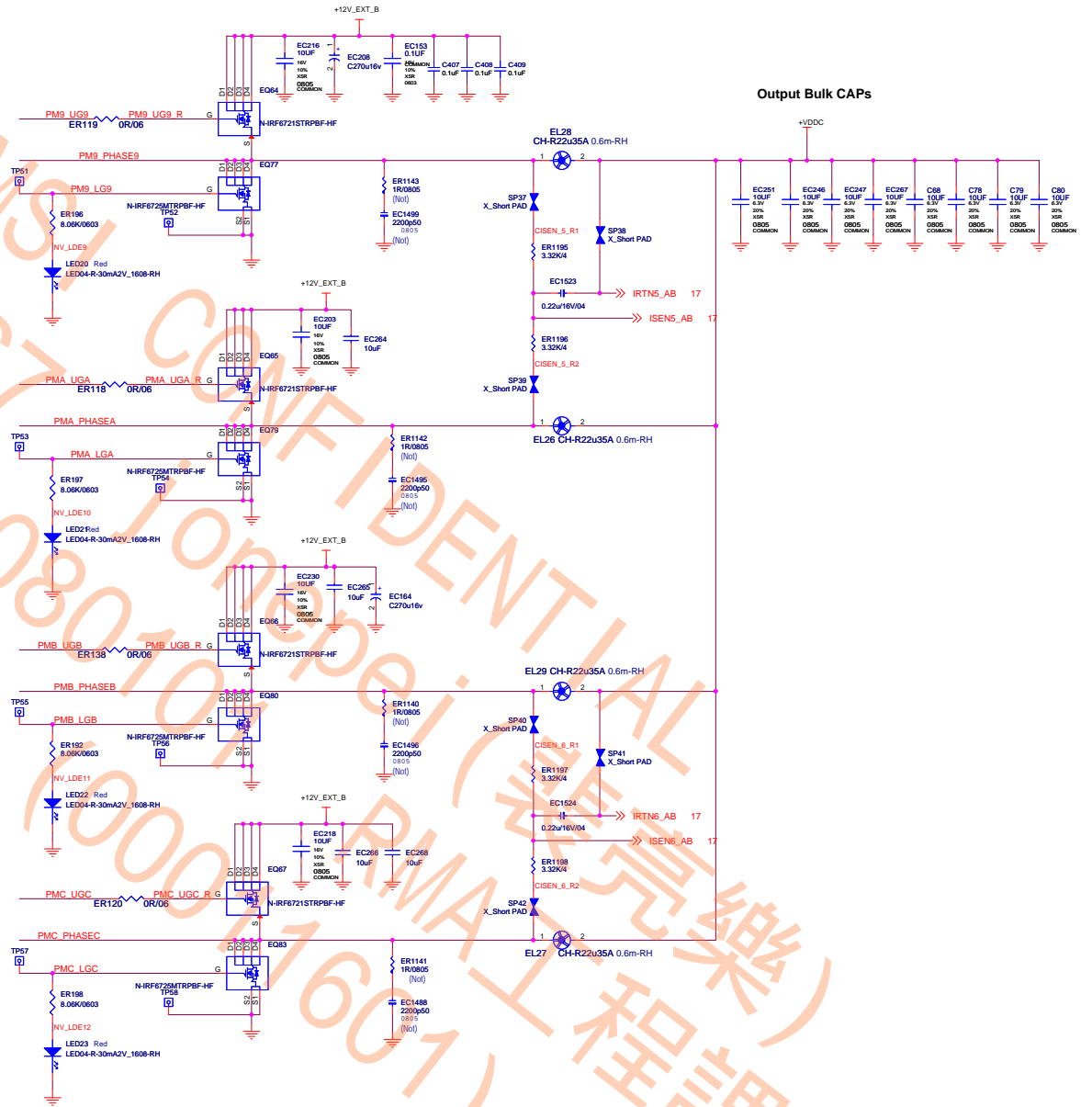
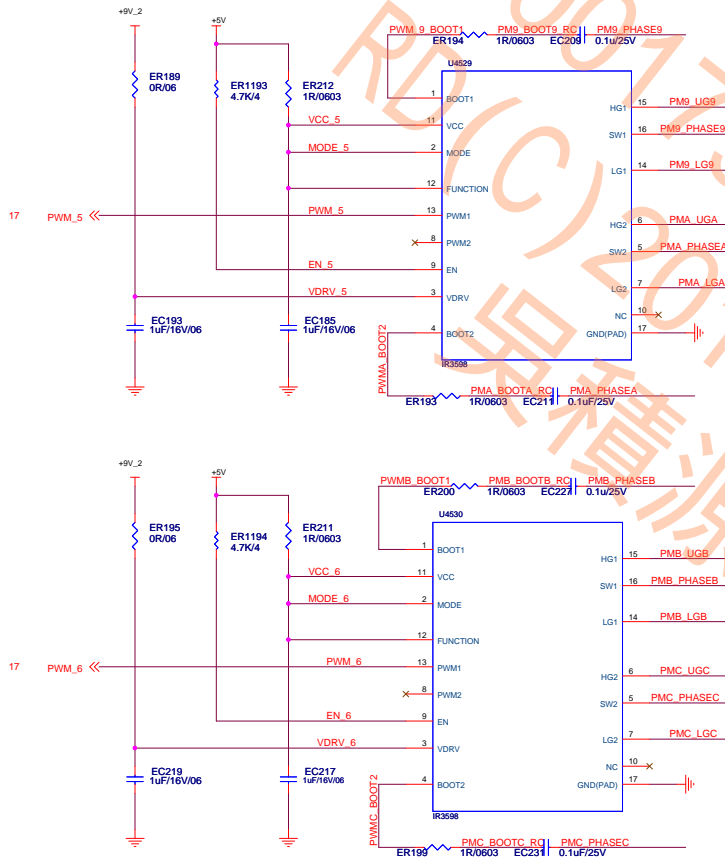
GP000 - TX_GP02_000V_000V_000V (Transmitter Power Savings Enable)
0: 00% Tx output saving for mobile mode
1: 50% Tx output saving (Default setting for Desktop)
2: 100% Tx output saving (Default setting for Desktop)
GP001 - TX_GP02_000V_000V_000V (Transmitter Power Savings Enable)
0: 00% Tx output saving for mobile mode
1: 50% Tx output saving (Default setting for Desktop)
2: 100% Tx output saving (Default setting for Desktop)
GP002 - BP_GP02_000V_000V_000V (Sleep Controller Switch)
0: Sleep Controller Switch
1: Sleep Controller Switch



ADDR0	ADDR1	SEC-BLANK ADDRESS
FLOATING	FLOATING	0000
FLOATING	GND	0001
GND	FLOATING	0004
GND	GND	0005



ALERT R836	2K	77度	87度	97度
7.5K	79度	89度	99度	



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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Power Supply: 2V5.5V, PEX_VDD

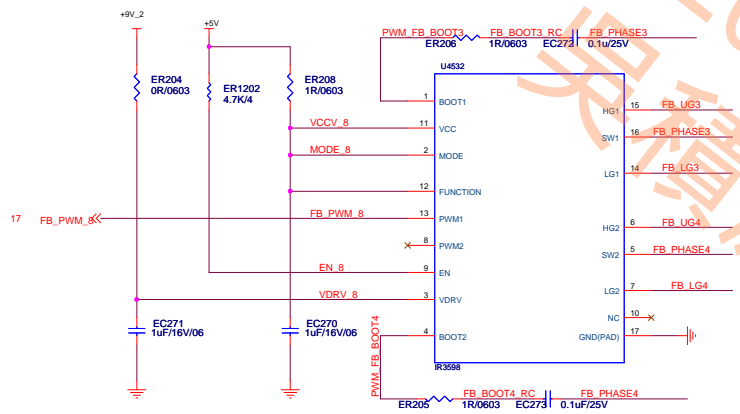
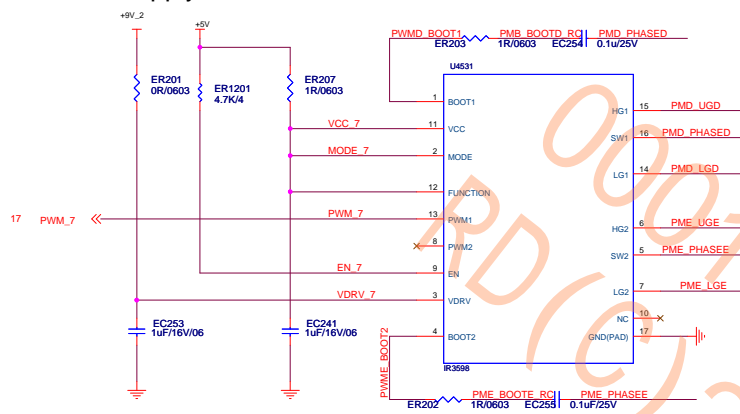


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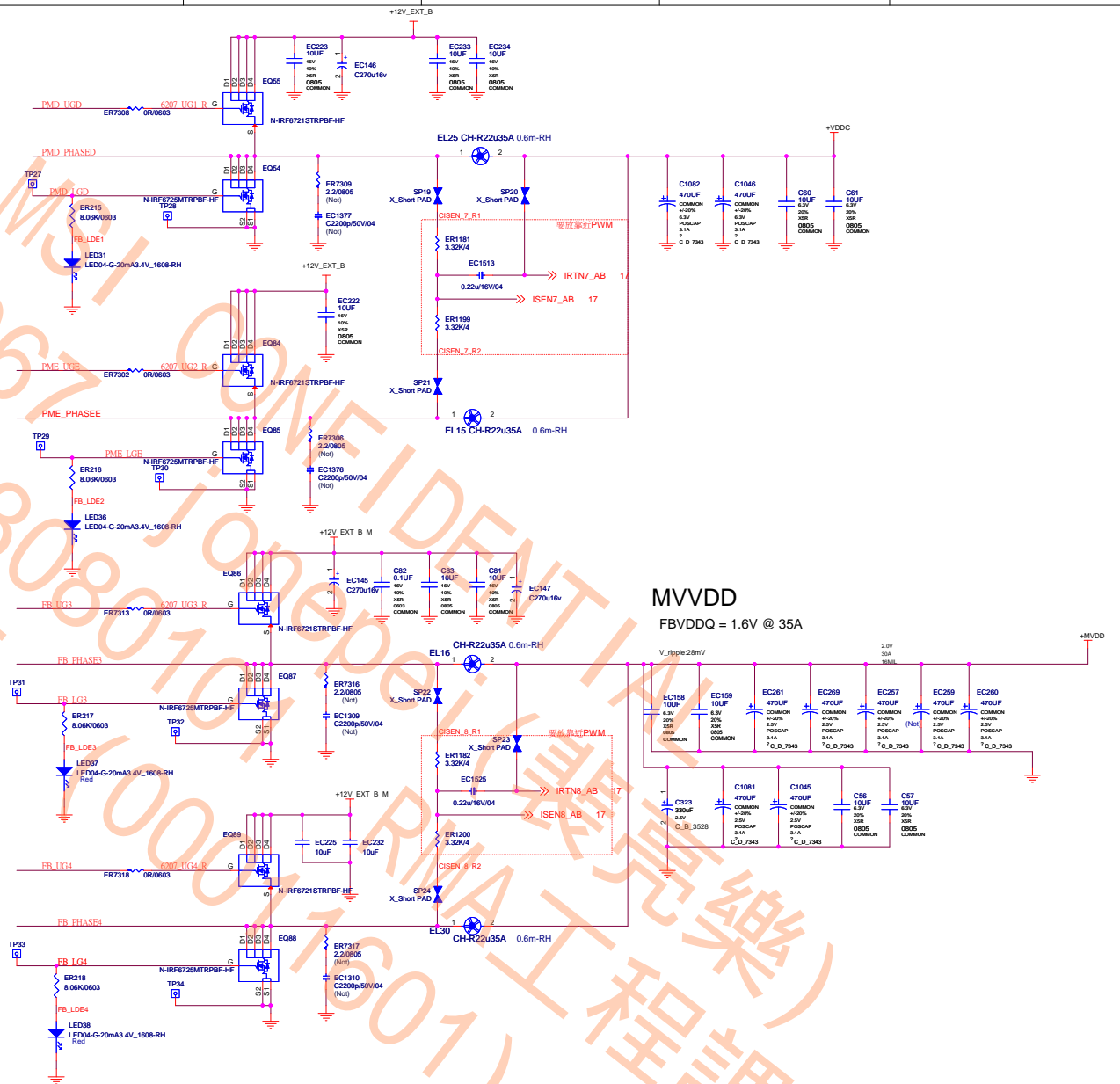
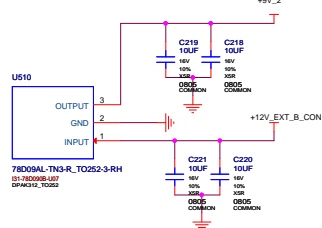
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Custom	Doc	Rev

Power Supply: VDDC 13 ~14 Phase ,MVVDD



9V POWER SUPPLY FOR DRIVER



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Power Supply: FBVDD/Q



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Title	

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1	000	11/08/21	Base on :G84 1. Change the power delivery time for support Pro AGC 2. Change the VDDC input bulk capacitor to height validation the space
2	00	11/11/01	Base on :G86 1. Change the circuit for the control to BIOS mode

Base on C386 modify V278-1.0

- page 8 add sw-dip2 , modify dual VBIOS circuit
- page 14. del R1000,R1001,R1020,R1021,L1000,L1001, add EL11,EL12
- page 19 remove deug circuit
- page 17,21,22,23 modift power VDDC+MVDD
- page 13.remove NB200
- page 15.modify 5V circuit , 預留3V3_DP power
- page 16.0.94V modify circuit ,remove R301,R325, R312
- page 18 change pwm ,add 6262 circuit
- page 11 dual link DVI change two sing link DVI
- page 9 remove hdmi circuit ,change min DP circuit
- page 19 add NCT7718 circuit

Base on V278-1.1

- page 13 add 1x2pin Header
- page 18 add sunbber RC

