

8885 (C69), 4/8Mx16 DDR, 32/64MB, LFH and RGB-TV-out, PCI

PCI DEVICE ID = 0X172 FOR GF4-MX420-XX.



MS-8878 BASE ON P73-A04 MODIFY

- Page 1. Add CE1 and RE1 for EMI solution.
Add compability with M/B.
rename , C22 to C144,R4 to R111,R5 to R112,R6 to R113,U1 to U101
change NET name, U100.AH17 to BUF_PCIRST_ also at the R112 and C144.
- Page 4. U300.9,U300.10,U300.1,U300.2 and U300.7 pin connect to GND.
- Page 5. remove VGA2 function.
- Page 9. Remove SAA7114H function and ADD AV-OUT & S_OUT conector.
remove L500,C500,C505,C506,C538,C539,C507,C508
- Page 10. C613,C604,C605,C614,L600 and L601 change to DIP type,ADD C620 , Q1 change to AOS4800
- Page 11. R704~R719,R726~R733,R745,R746 PAD size change to 0603 type and Add R747 10k ohm.
Add parallel bios function.
R746,R747 stuff (00)
- Page 6 . Add Fan power function.


MS-8878 VER:310 MODIFY

- Page 2. Add 74XCT74 buffer for PCI_RST function.
- Page 5. Add 75ohm and bypass Cap. for TV-OUT function.
- Page 9. Swap S-OUT.3 and S-OUT.4 net name.
- Page 10. Change DIP type CAP. footprint.
- Page 11. Change U1000.22 net name.

MS-8885 BASE ON MS-8878 VER:310

- Page 2. Remove AGP interface.
- Page 3. Add PCI interface & level shift IC (Insert page).
- Page 7. Add LFH interface.
- Page 10. Change AV_OUT & S_OUT connector to S-TV-out(9 pin).
- Page 13. Add H/W monitor.
- Page 14. Add Linear Power Supplies.

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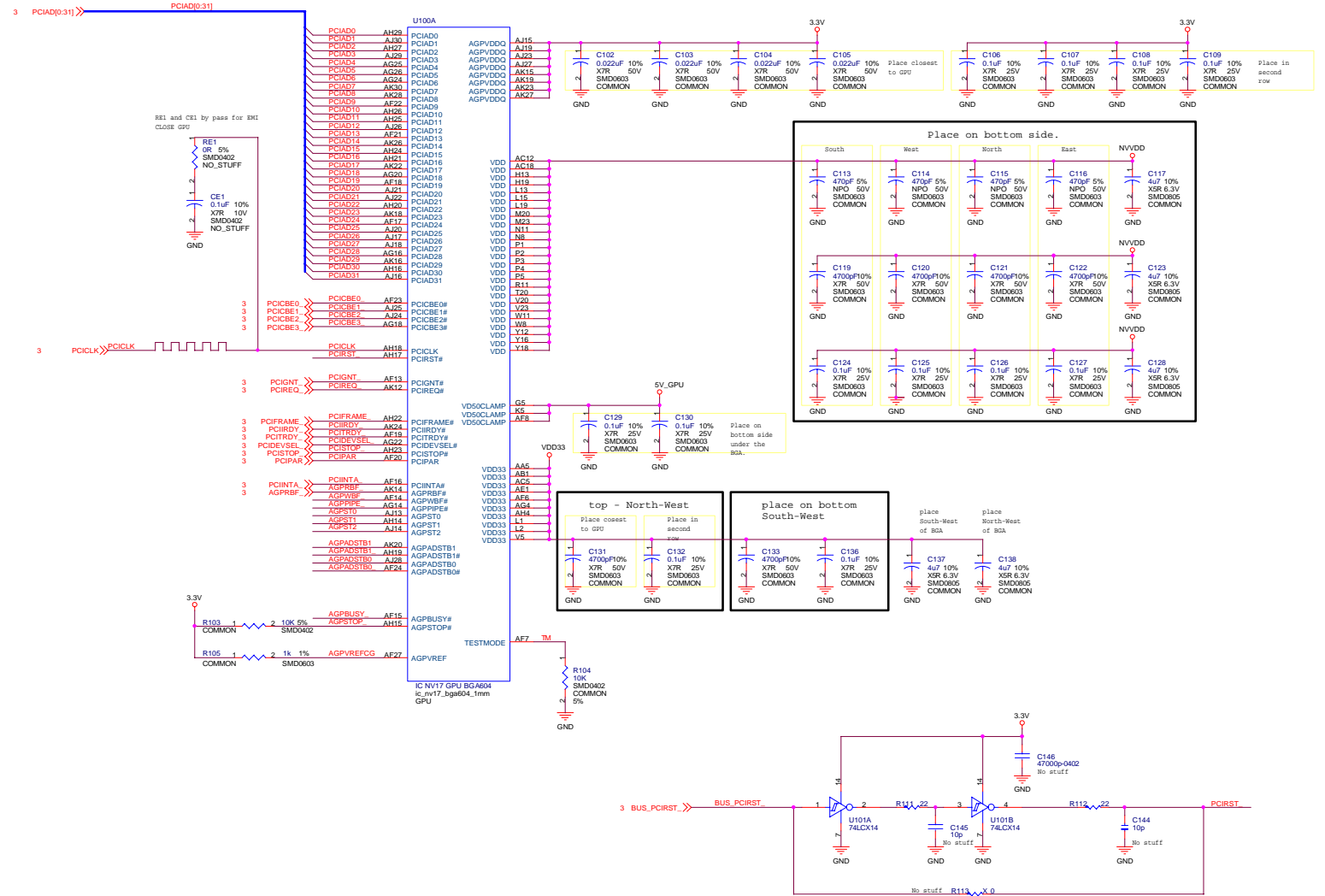
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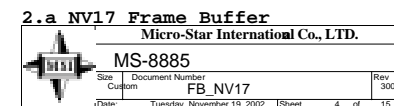
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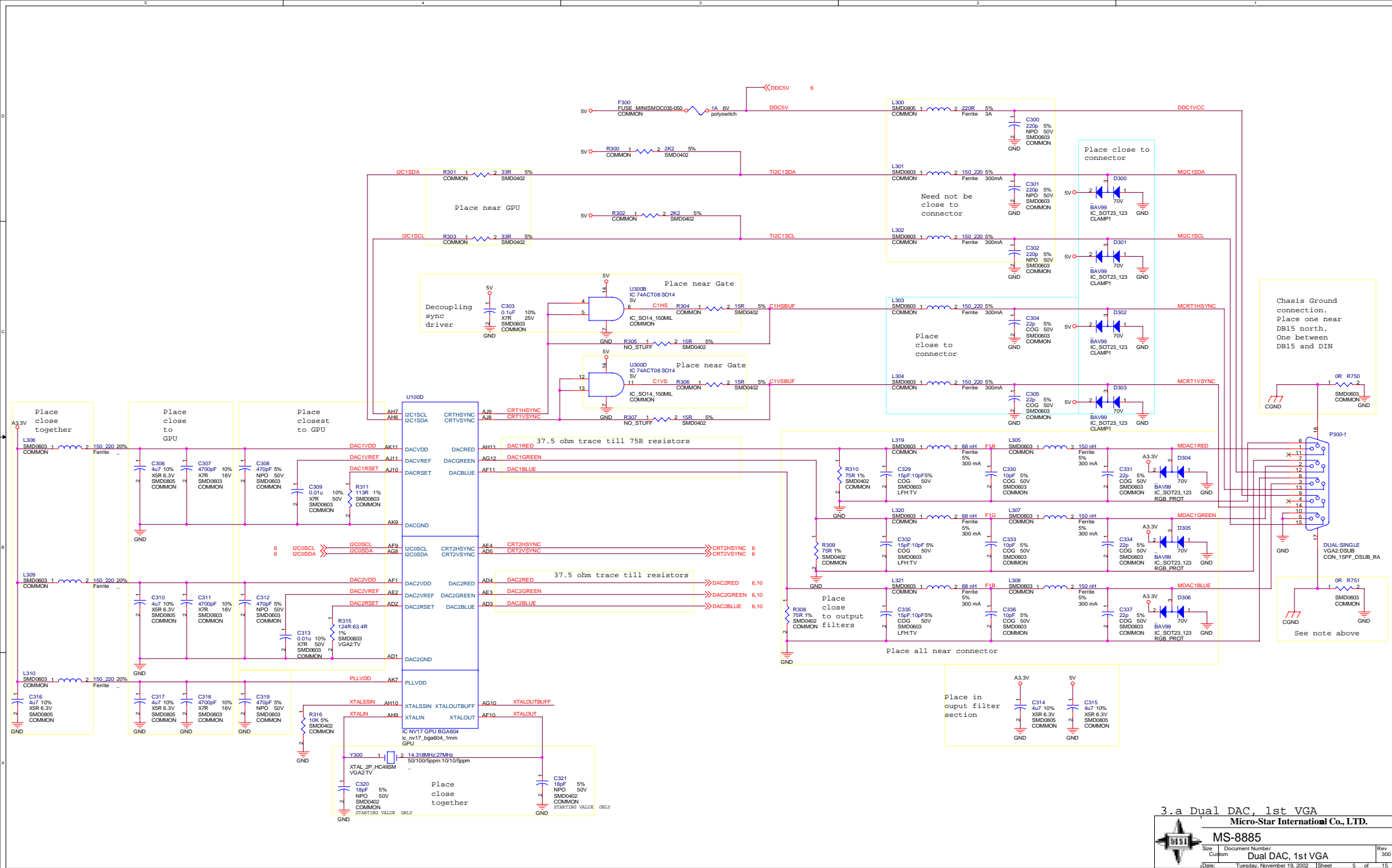
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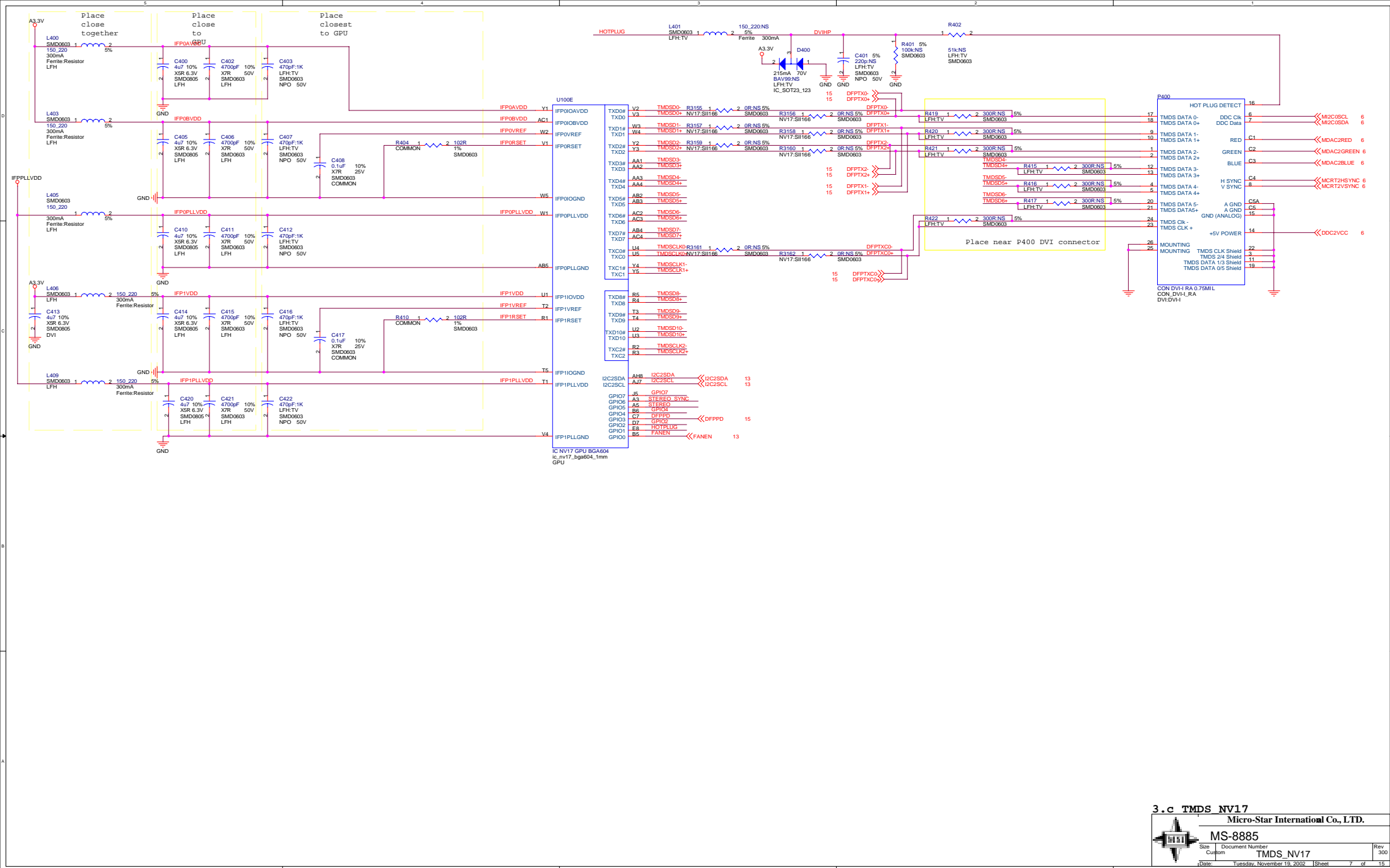


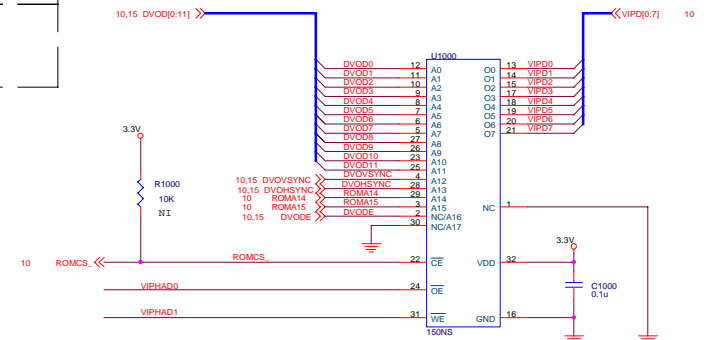
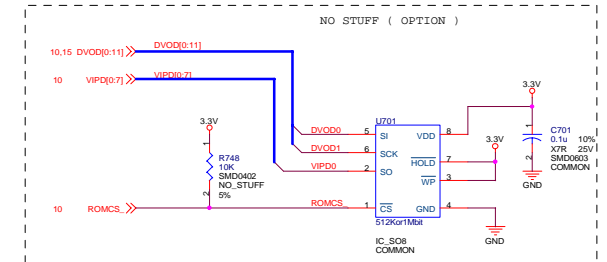
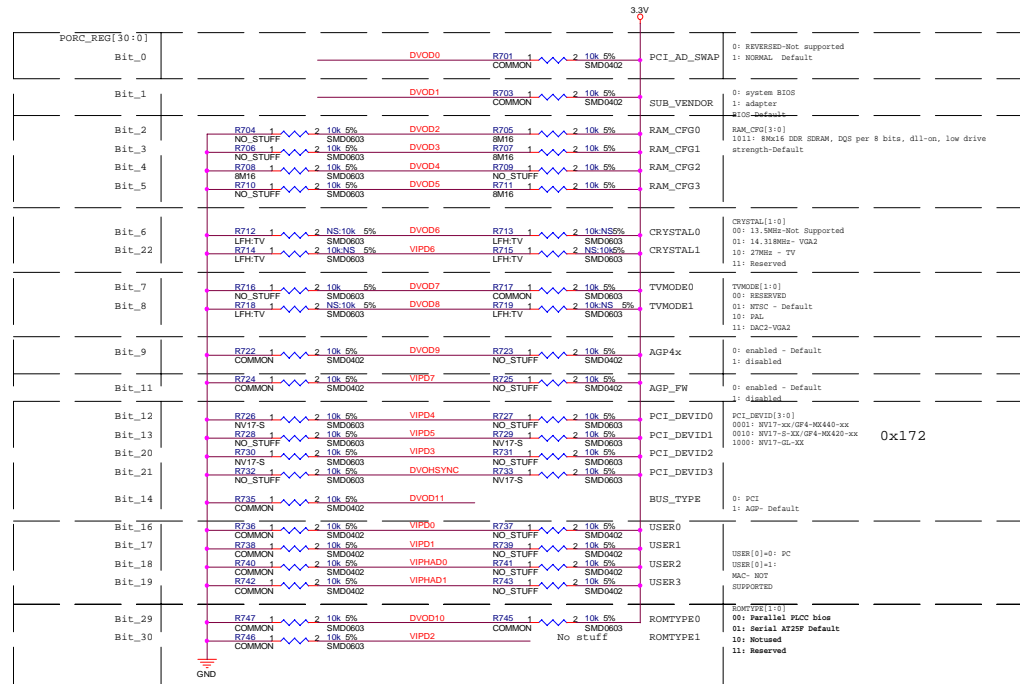
1. AGP interface, core decoupling

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NV17 HOST			
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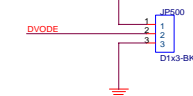


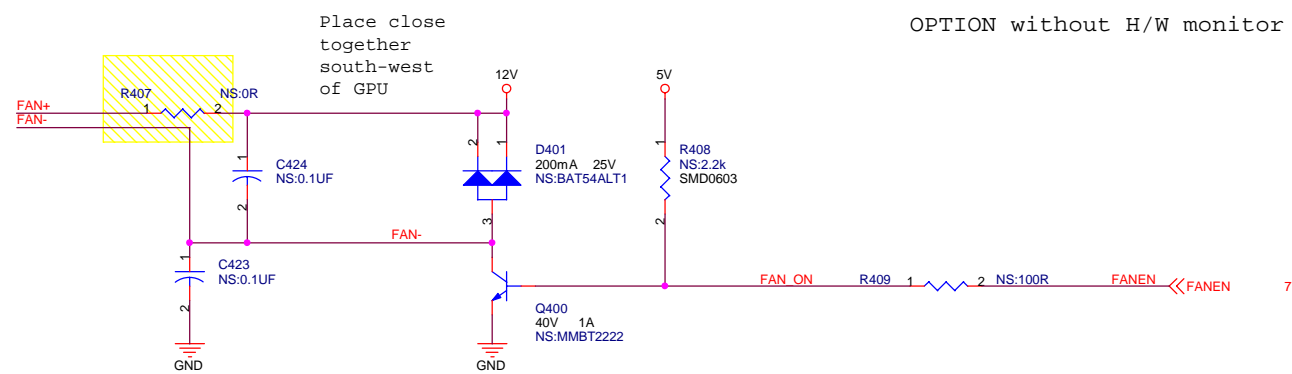
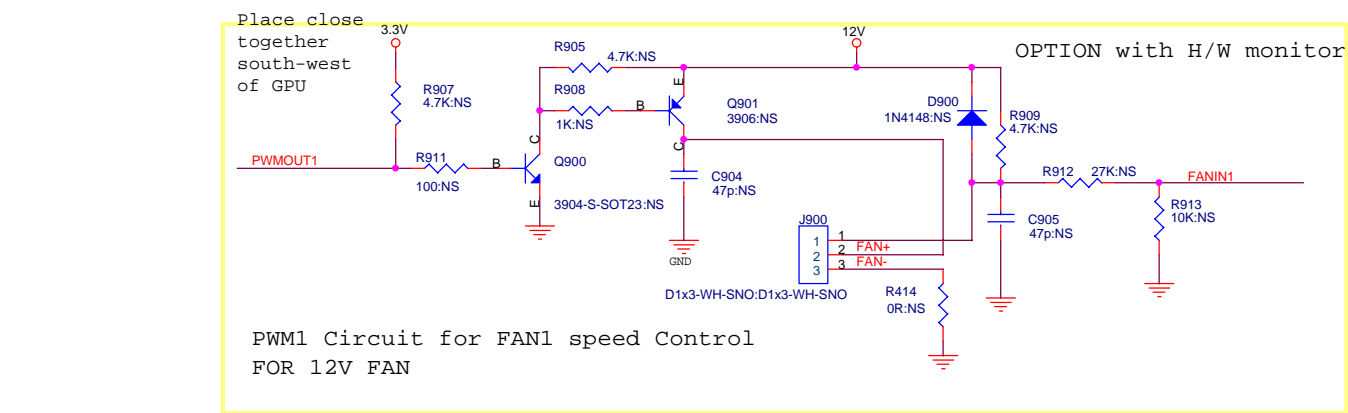
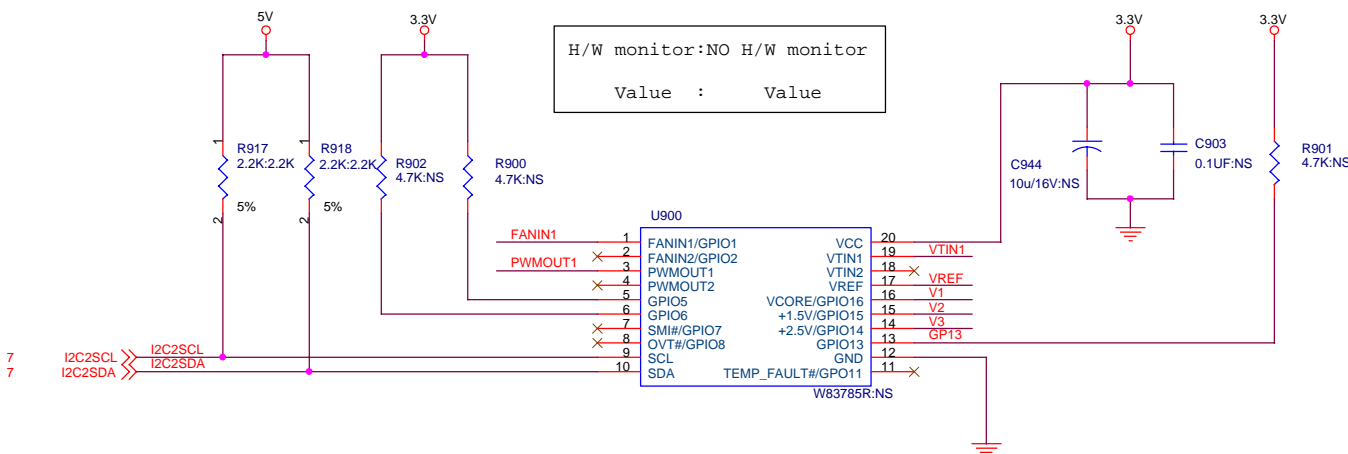




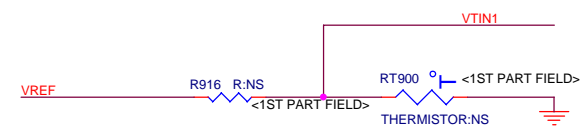
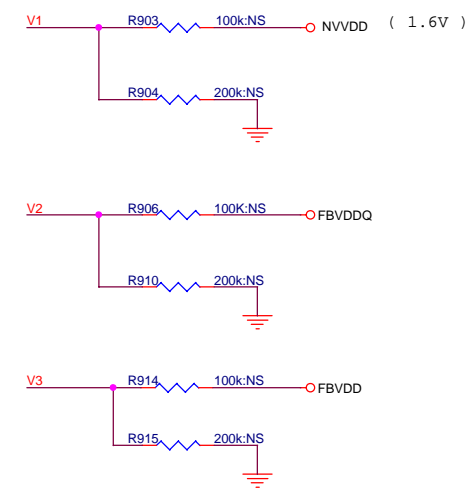


Twin BIOS





VOLTAGE SENSING CIRCUIT



TEMPERATURE SENSING CIRCUIT

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