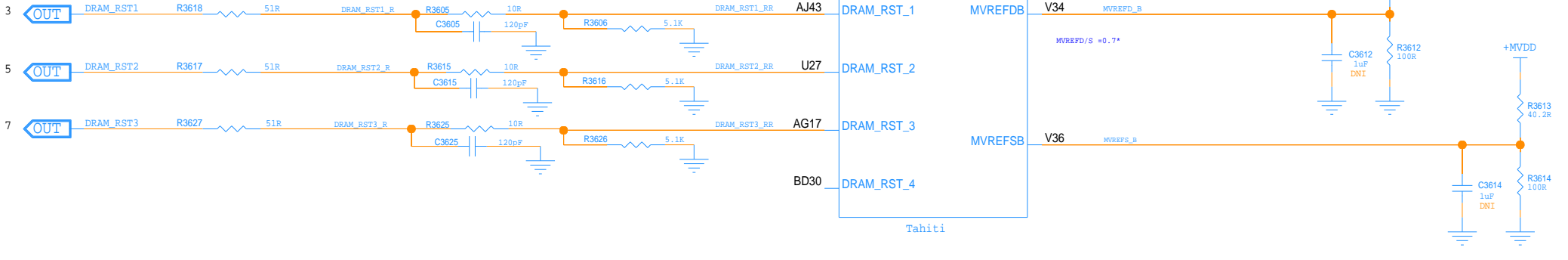
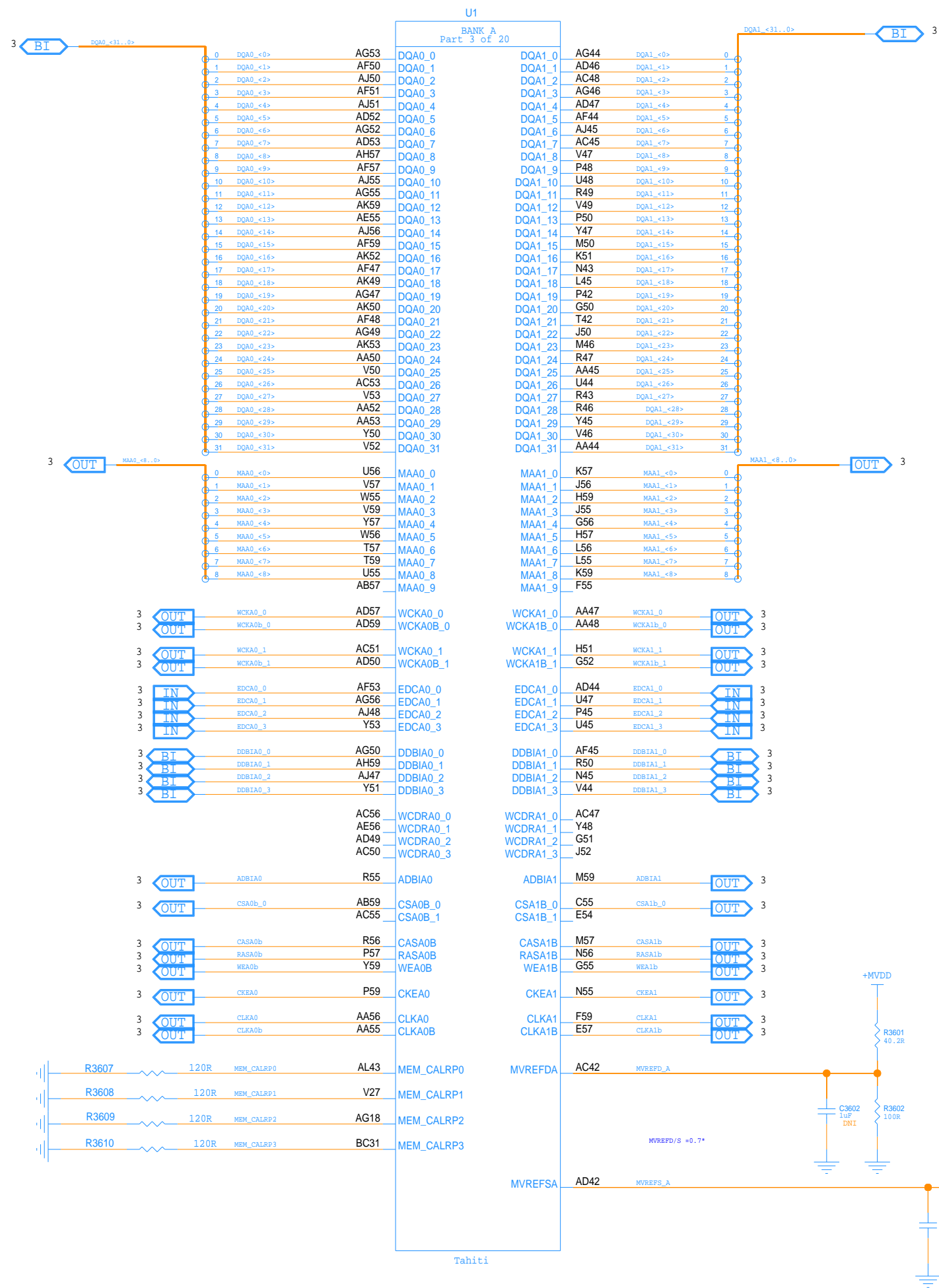


(2) TAHITI Memory Channel A&B



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SHEET: TAHITI MEM AB

DATE: Wed Nov 02 15:28:40 2011

REV: 1.0

SHEET NUMBER: 2 OF 21

DOCUMENT NUMBER: 105-C38600-00

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TITLE:

[illegible]

NOTES: NOTE

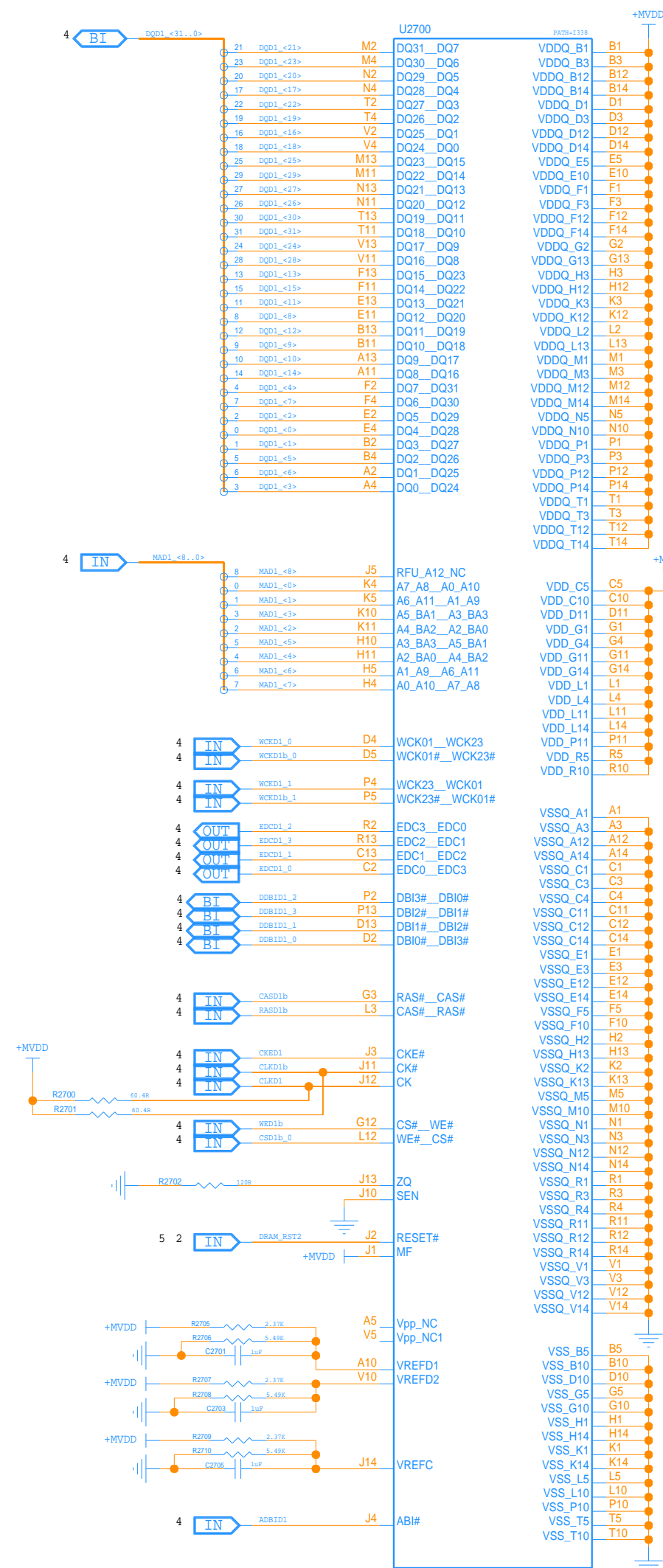
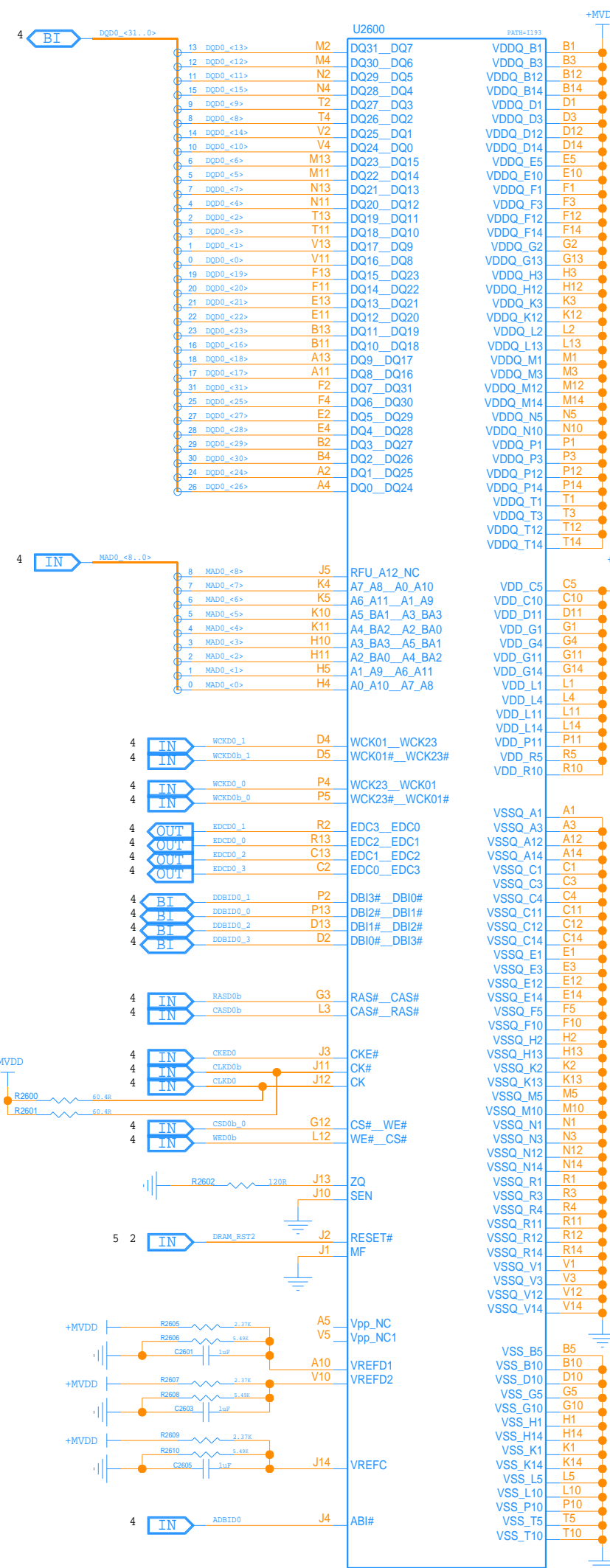
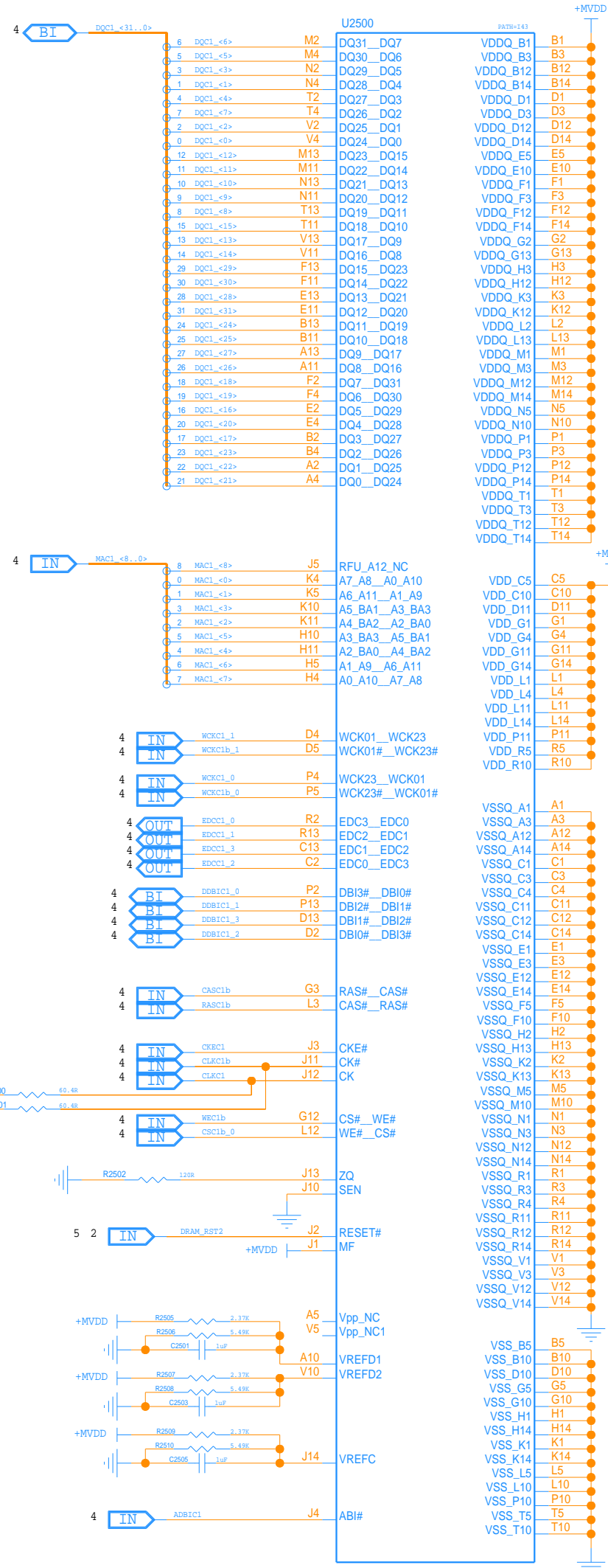
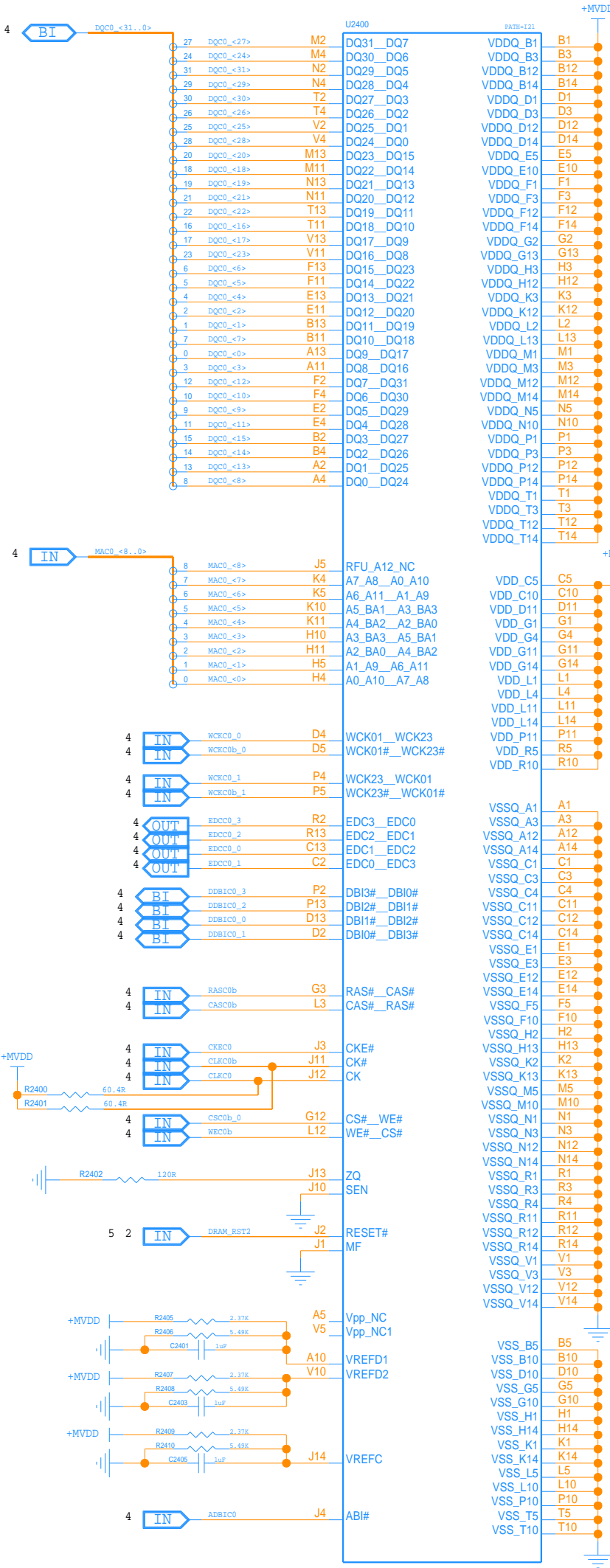


TITLE

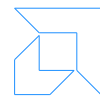
NOTES: NOTE

TITLE:

(5) GDDR5 Memory Channel C&D



AMD - GRAPHICS
1 COMMERCE VALLEY
MARKHAM, ONTARIO, L3T 7X6



SHEET: MEMORY CH CD

DATE: Thu Nov 03 14:06:56 2011

REV: 1.0

SHEET NUMBER: 5 OF 21

DOCUMENT NUMBER: 105-C38600-00

NOTES: NOTE

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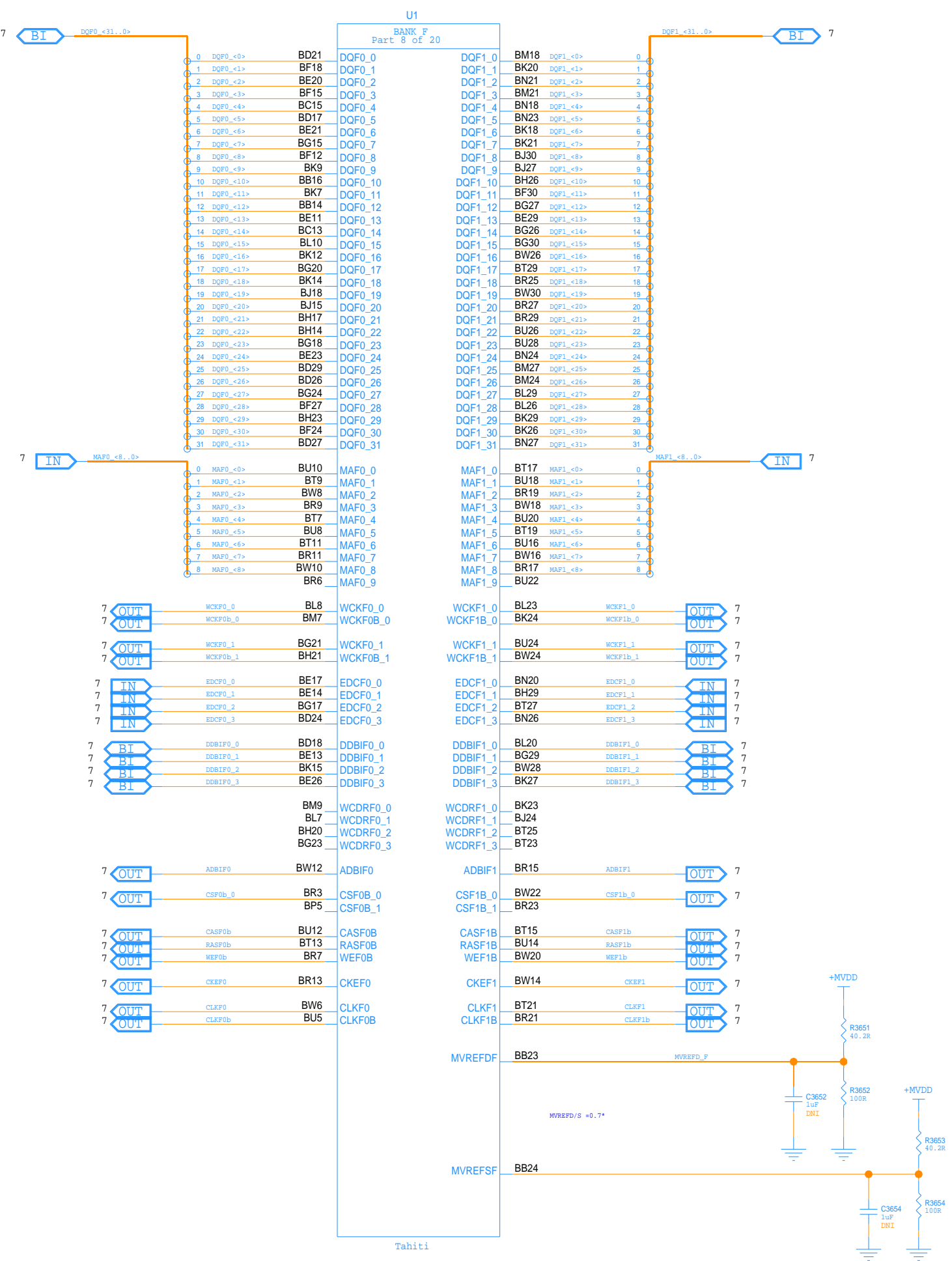
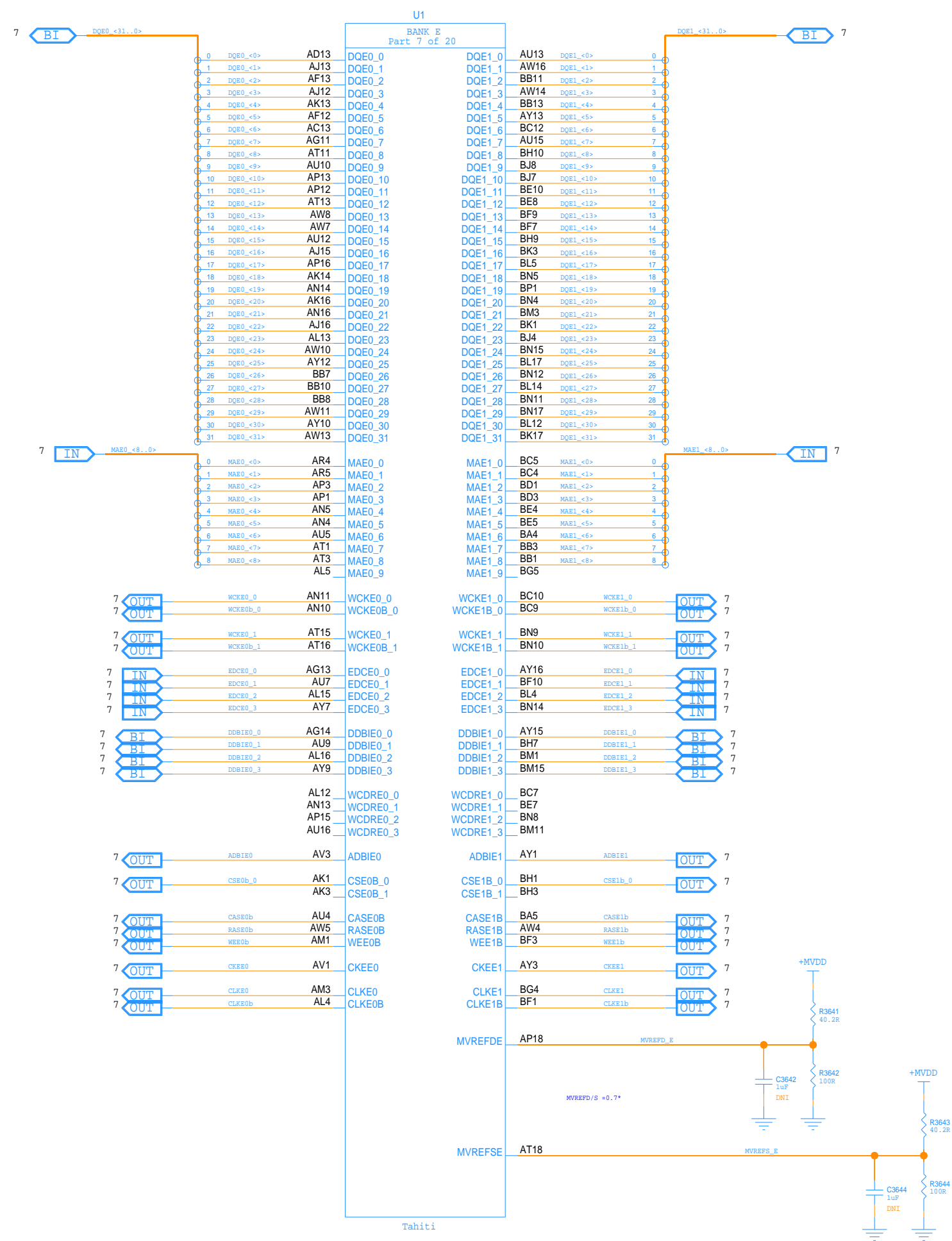
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(6) TAHITI Memory Channel E&F



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SHEET: TAHITI_MEM EF

DATE: Wed Nov 02 15:28:40 2011

REV: 1.0

SHEET NUMBER: 6 OF 21

DOCUMENT NUMBER: 105-C38600-00

NOTES: NOTE

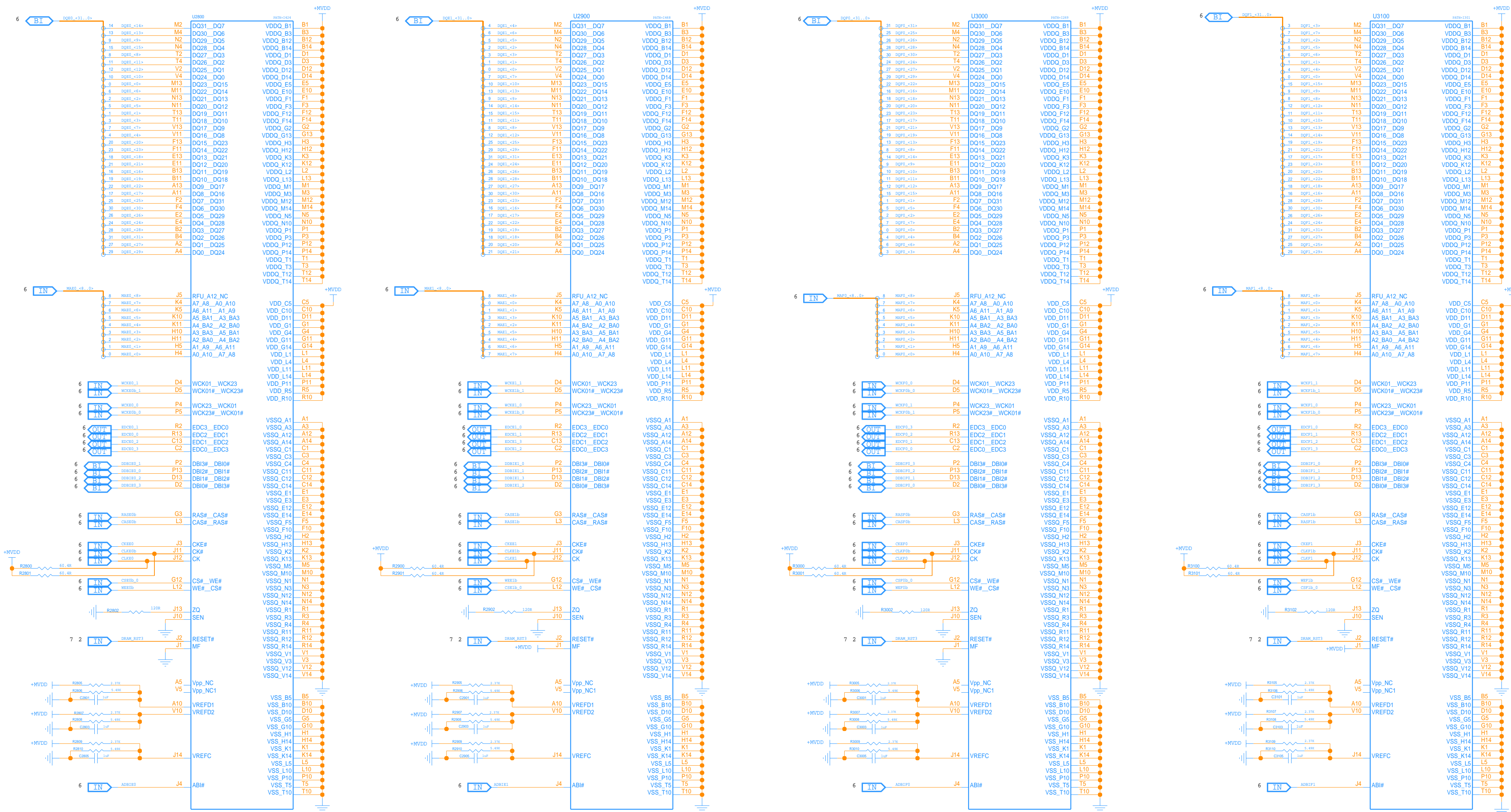
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TITLE:

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(7) GDDR5 Memory Channel E&F



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SHEET: MEMORY CH EF

DATE: Wed Nov 02 15:28:40 2011

REV: 1.0

SHEET NUMBER: 7 OF 21

DOCUMENT NUMBER: 105-C38600-00

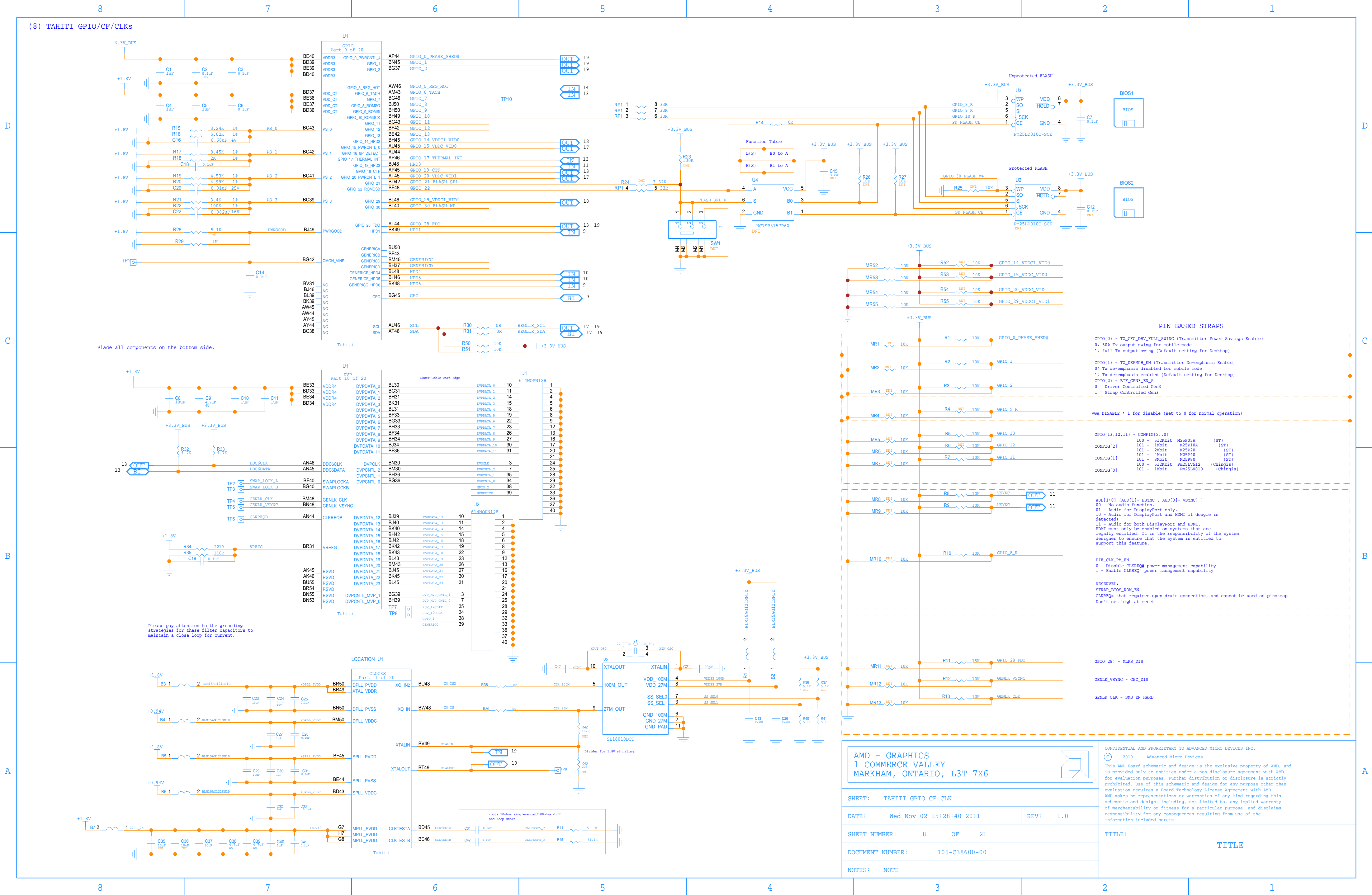
NOTES: NOTE

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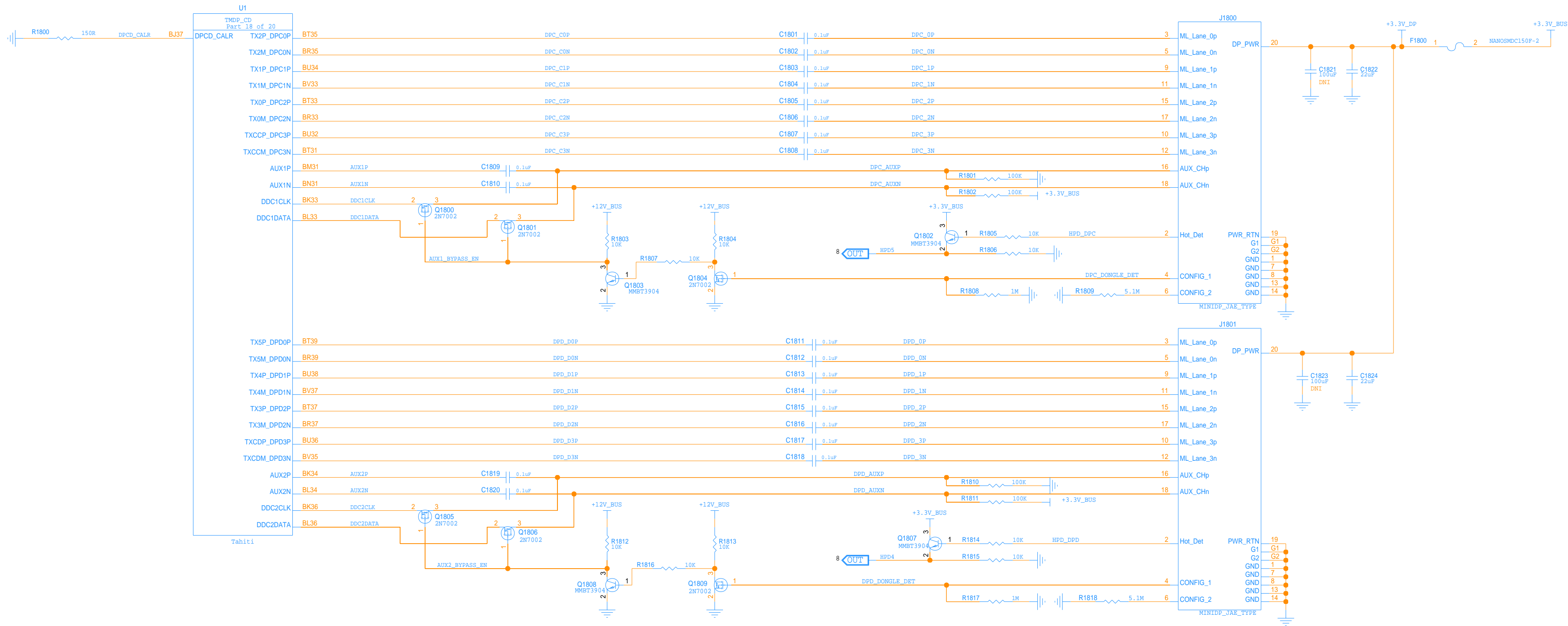
TITLE

REV: 1.0

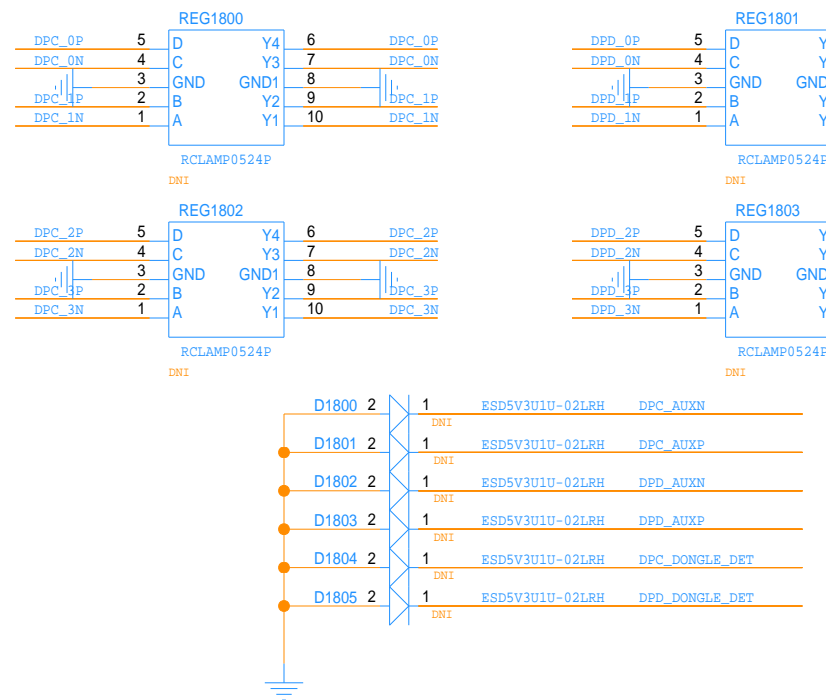
TITLE:

DOCUMENT NUMBER: 105-C38600-00

NOTES: NOTE



OPTIONAL ESD PROTECTION DIODES



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SHEET: TAHITI mDP mDP CD

DATE: Wed Nov 02 15:28:39 2011

REV: 1.0

SHEET NUMBER: 10 OF 21

DOCUMENT NUMBER: 105-C38600-00

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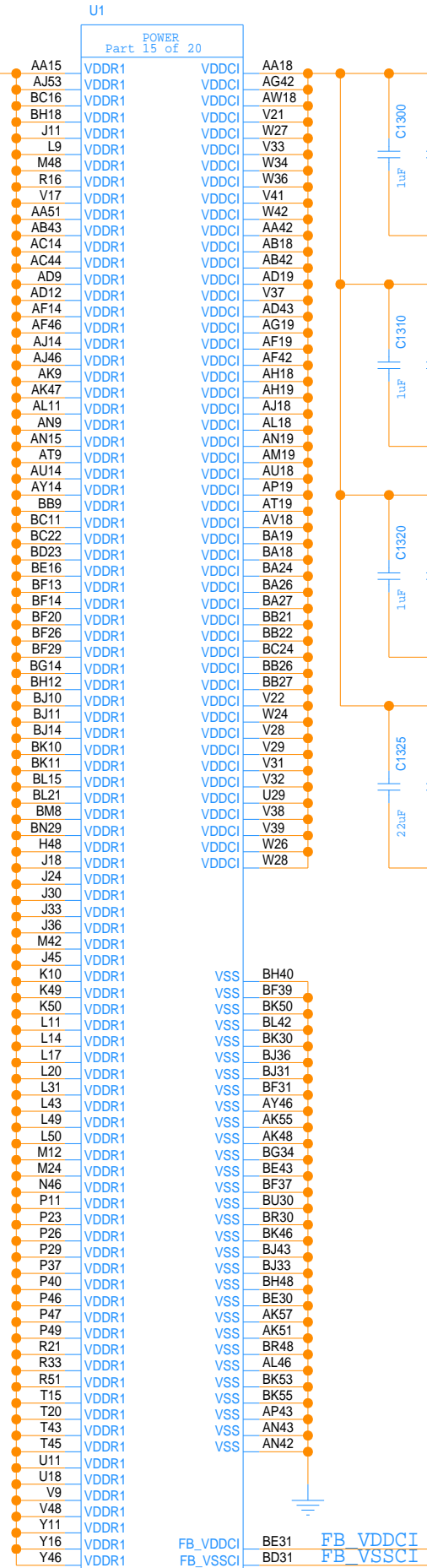
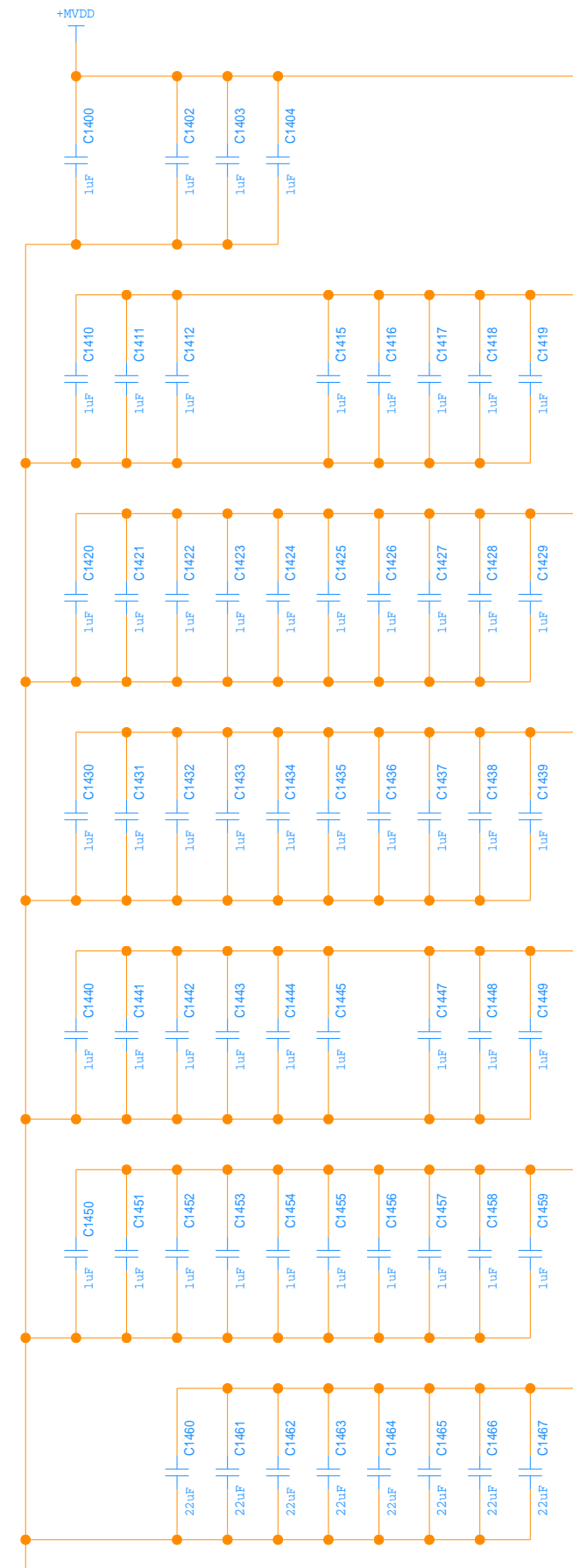
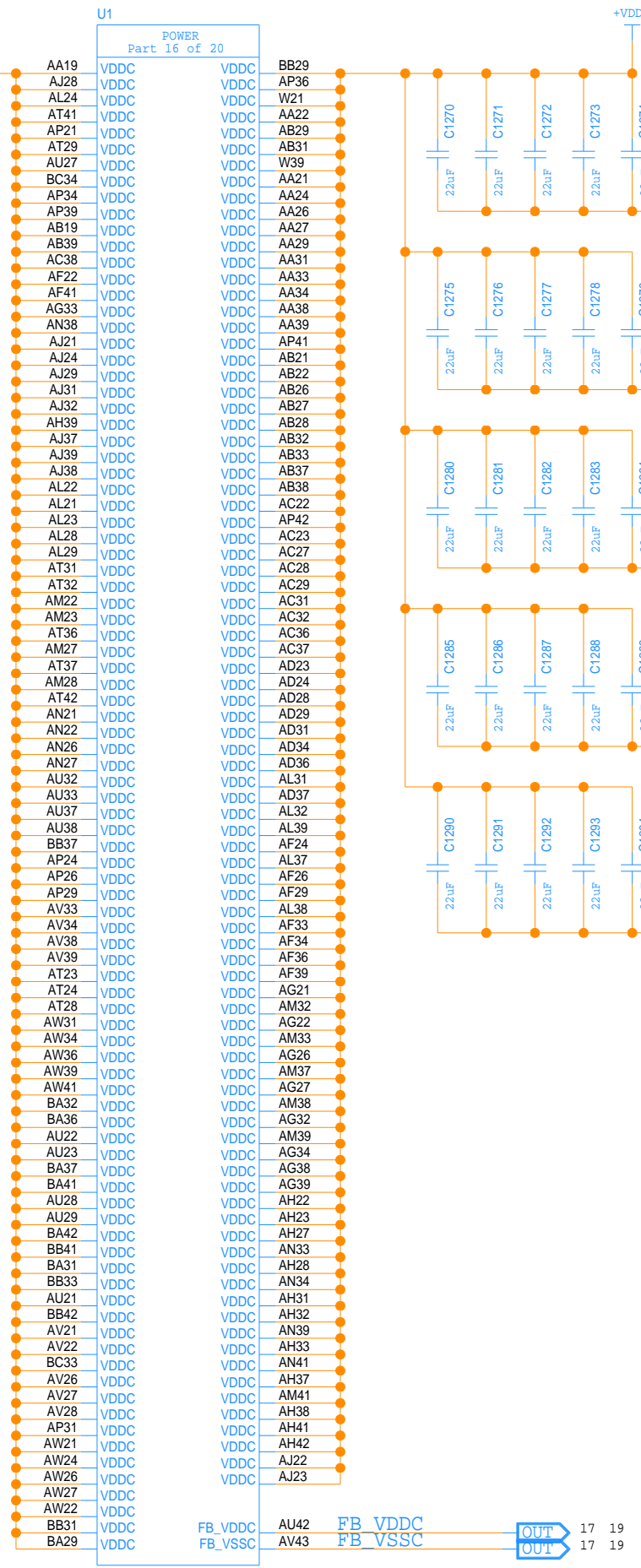
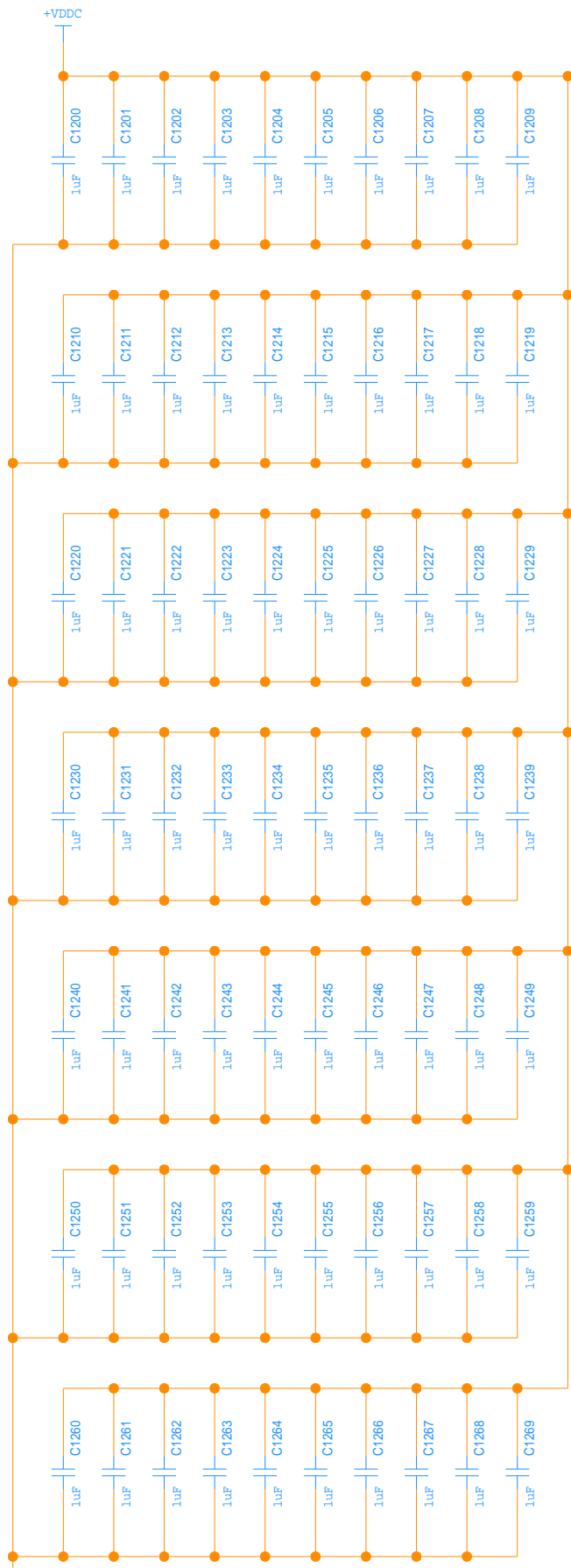
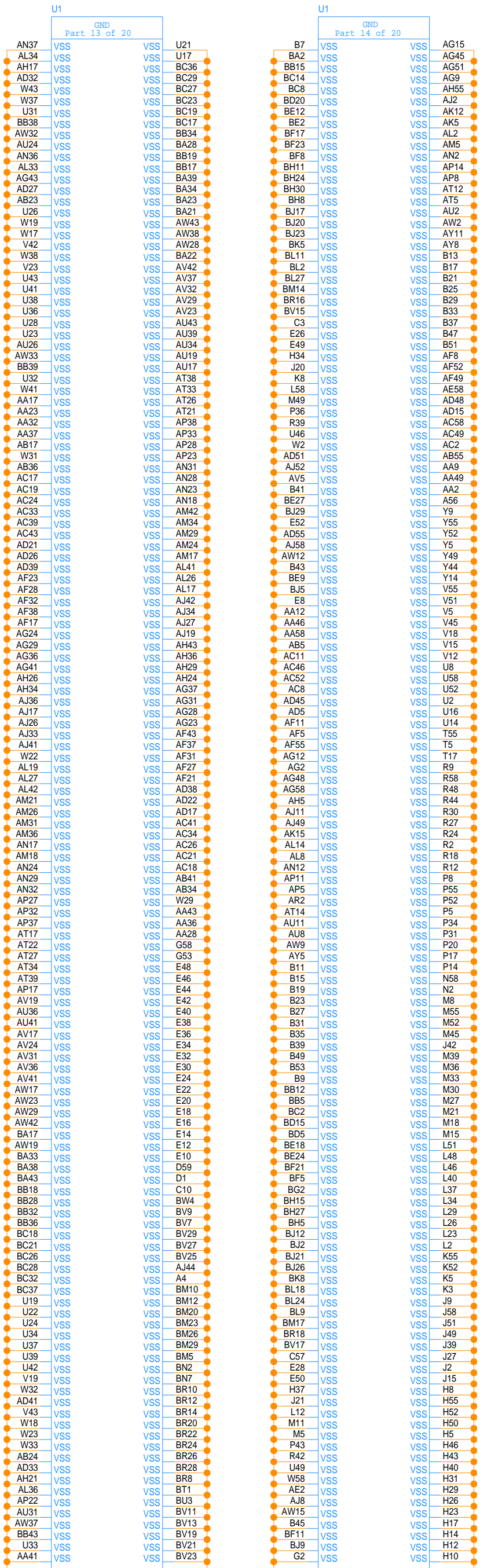
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MARKHAM, ONTARIO, L3T 7X6

SHEET: TAHITI POWER

DATE: Wed Nov 02 15:28:41 2011

SHEET NUMBER: 12 OF 21

DOCUMENT NUMBER: 105-C38600-00

NOTES: NOTE

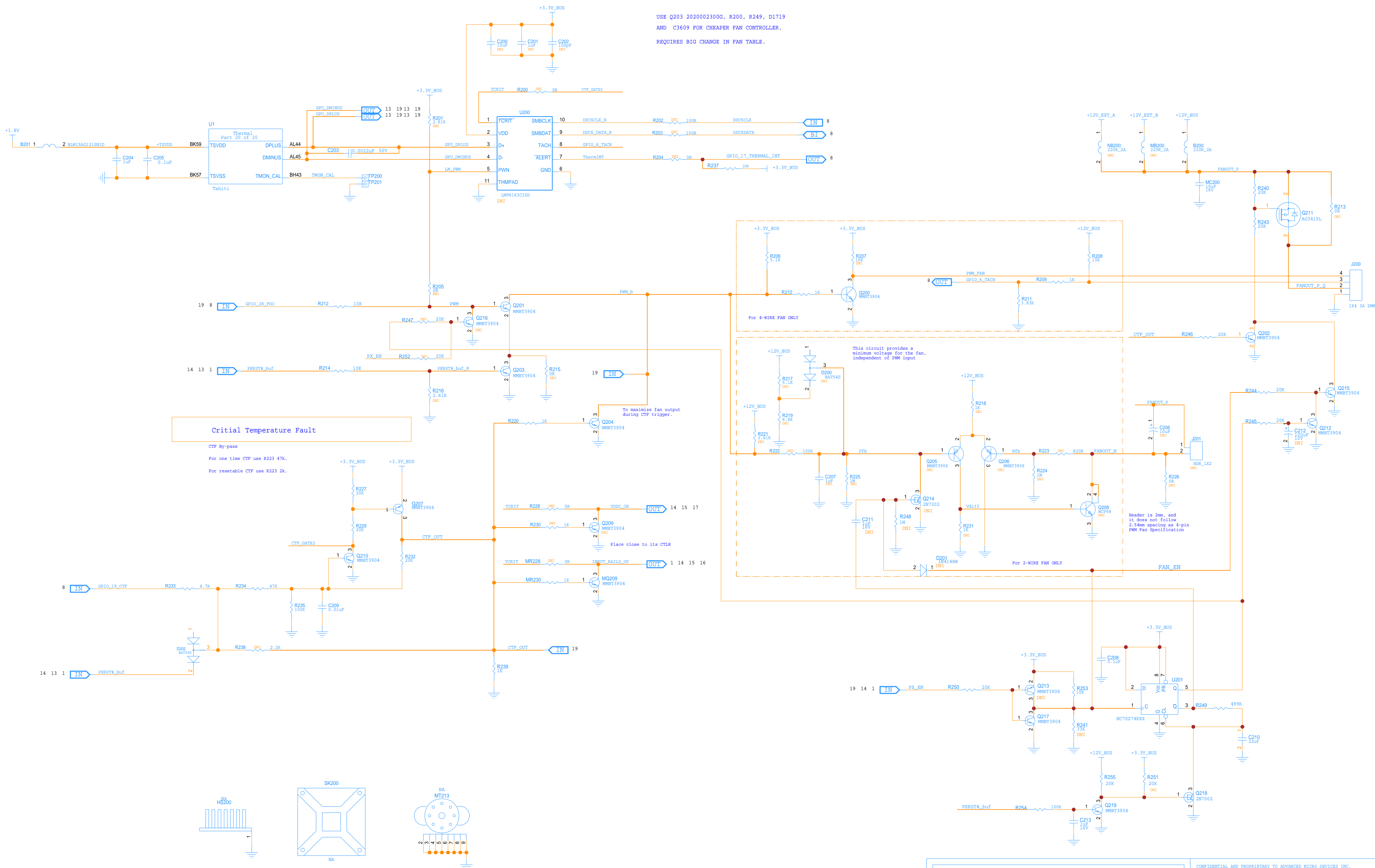
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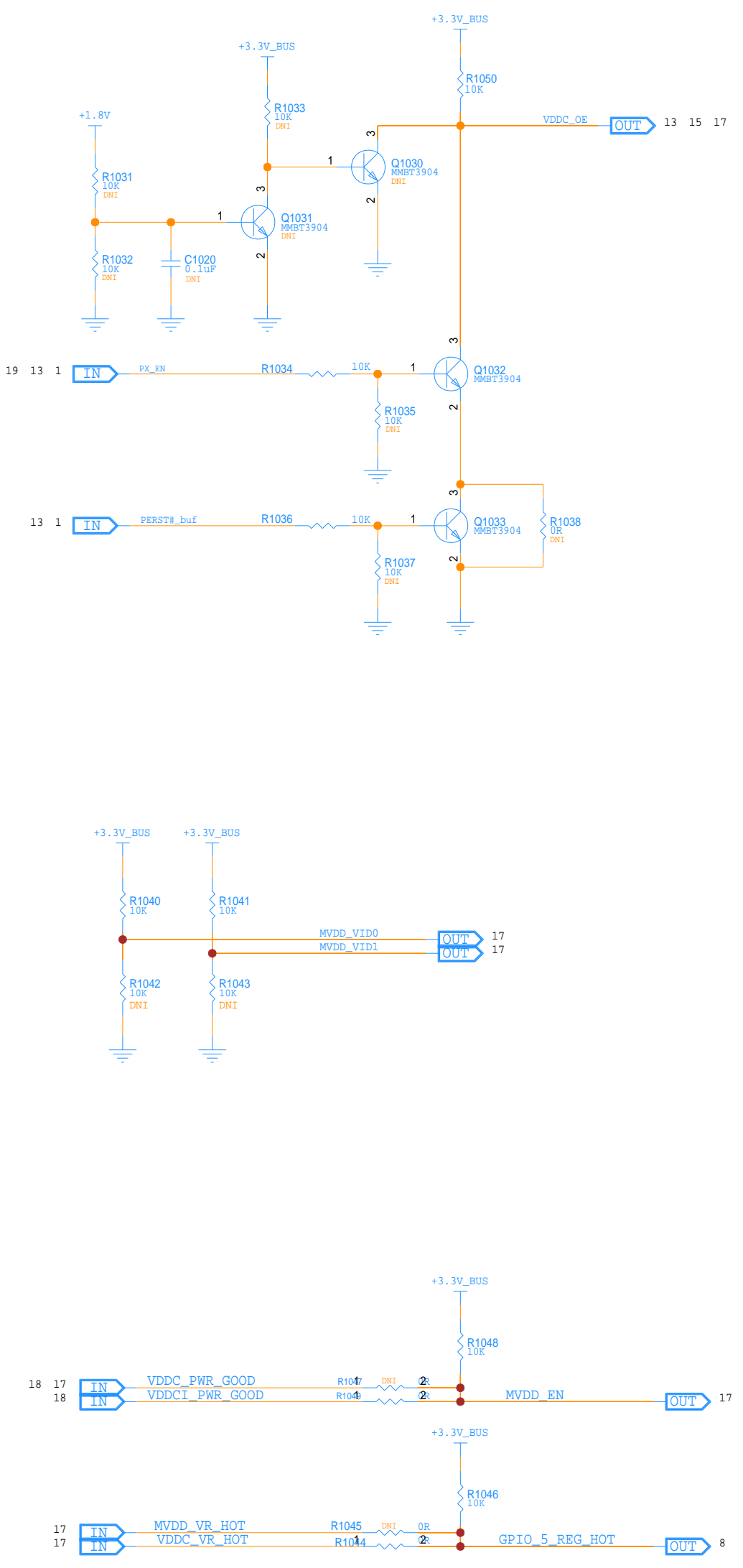
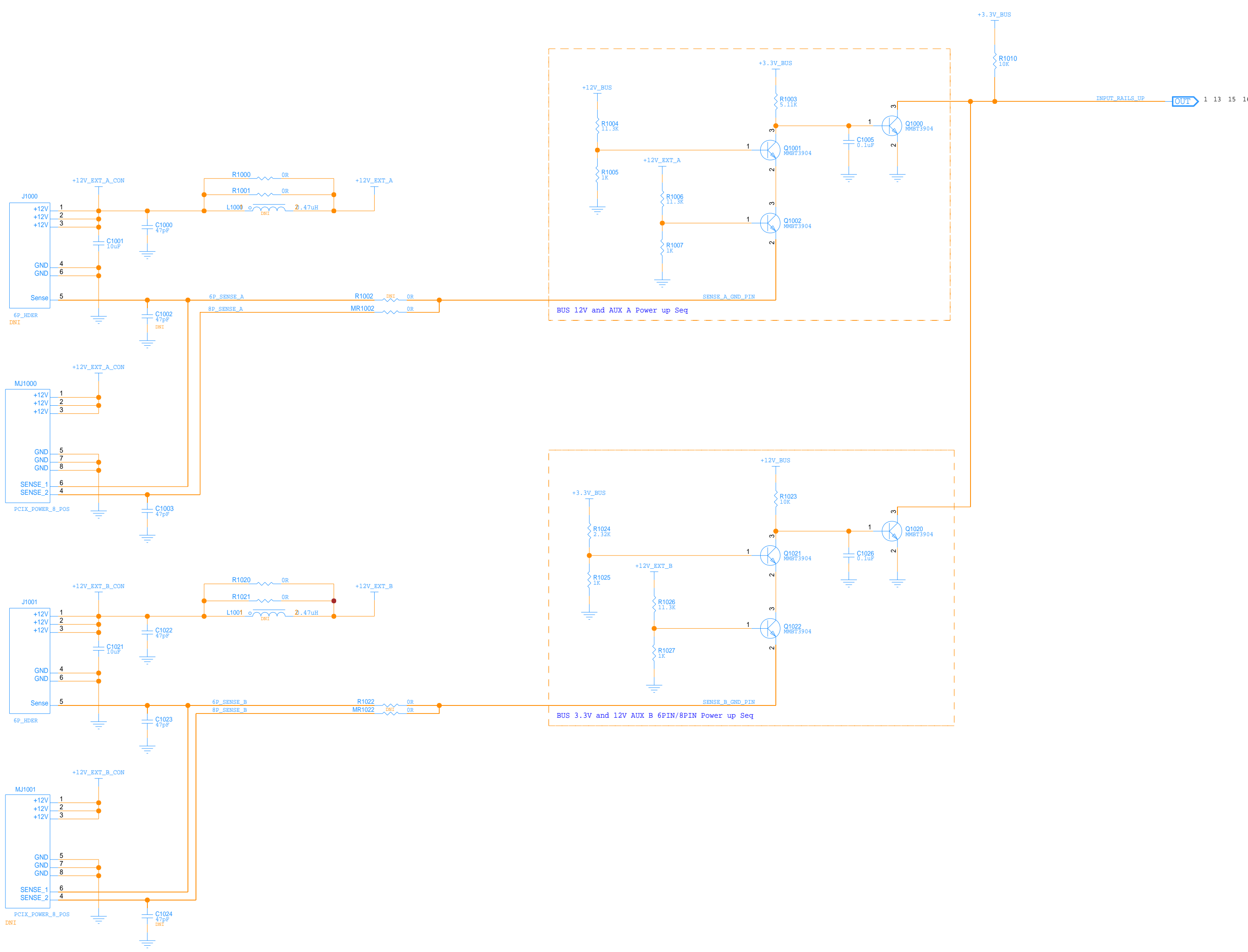
TITLE

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SHEET:	TAHITI THERMAL		TITLE: TITLE		
DATE:	Thu Nov 03 14:35:49 2011	REV:			
SHEET NUMBER:	13	OF	21		
DOCUMENT NUMBER:	105-C38600-00				
NOTES:	NOTE				

(14) POWER MGMNT



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SHEET:	POWER MNGMNT	REV:	1.0
DATE:	Wed Nov 02 15:28:38 2011	SHEET NUMBER:	14 OF 21
DOCUMENT NUMBER:	105-C38600-00	TITLE:	TITLE
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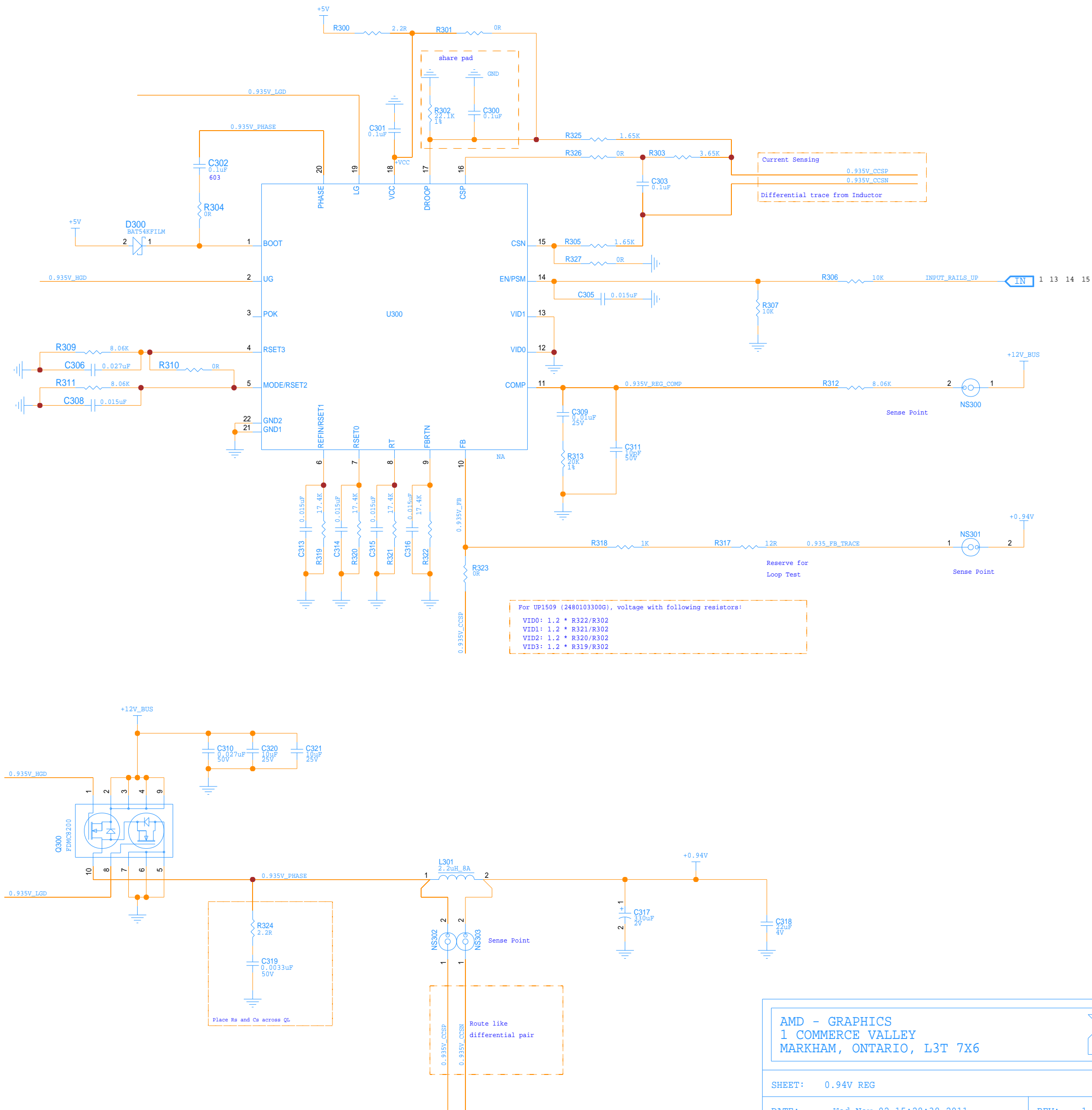


NOTES: NOTE

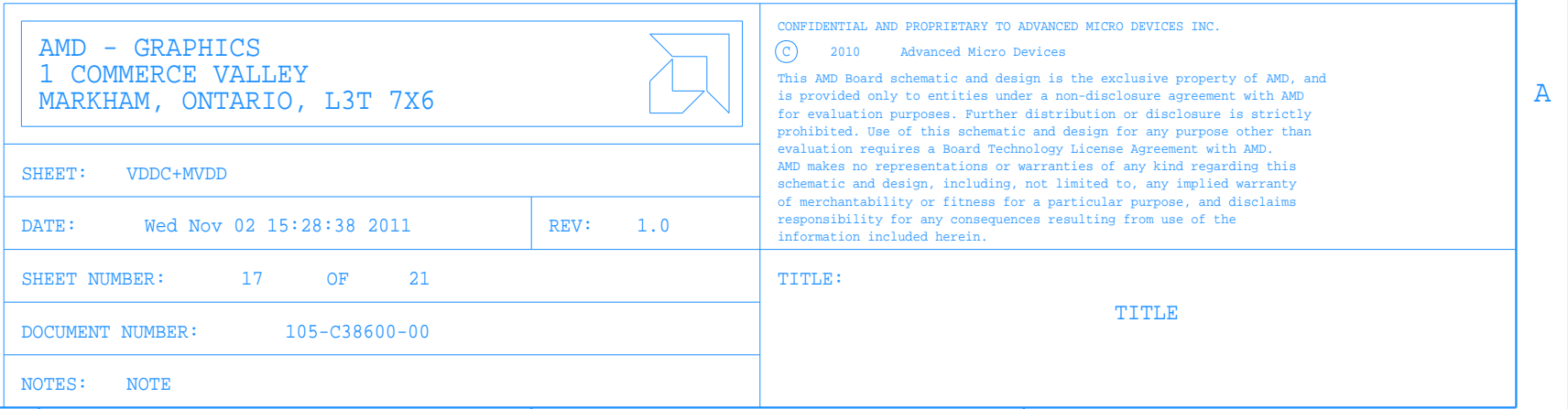
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(16) 0.94V REG



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SHEET: 0.94V REG		TITLE:	
DATE: Wed Nov 02 15:28:38 2011		REV: 1.0	
SHEET NUMBER: 16 OF 21		TITLE:	
DOCUMENT NUMBER: 105-C38600-00		TITLE:	
NOTES: NOTE		TITLE:	



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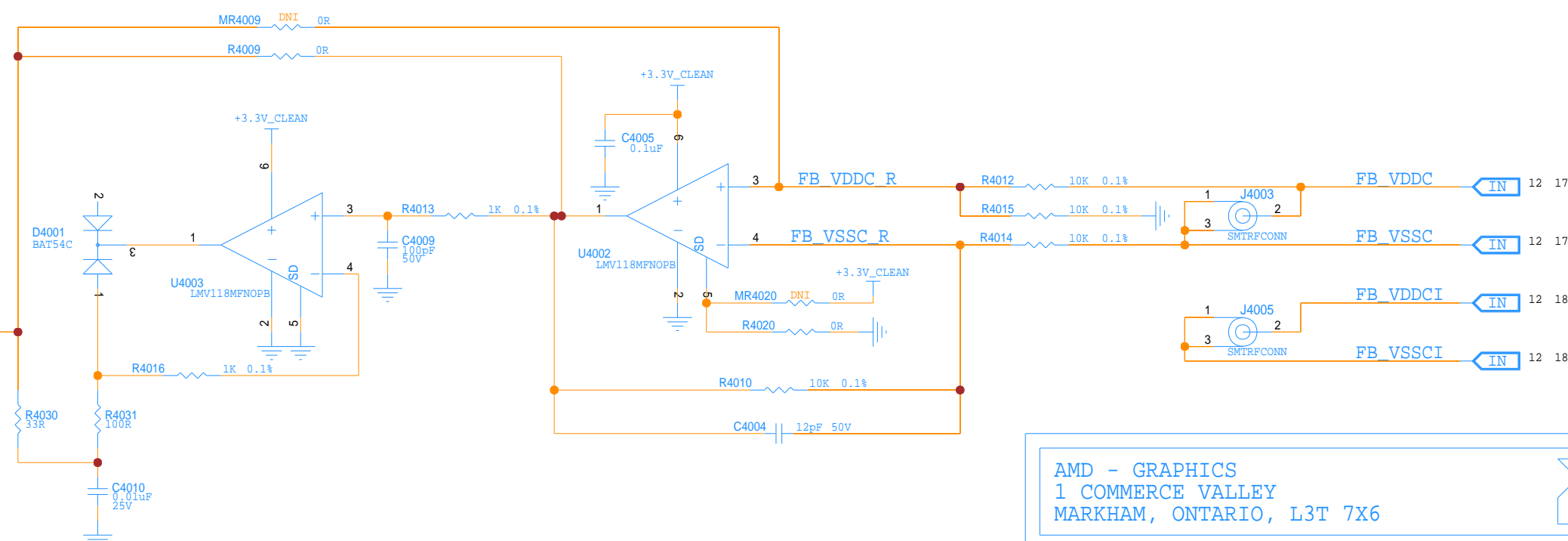
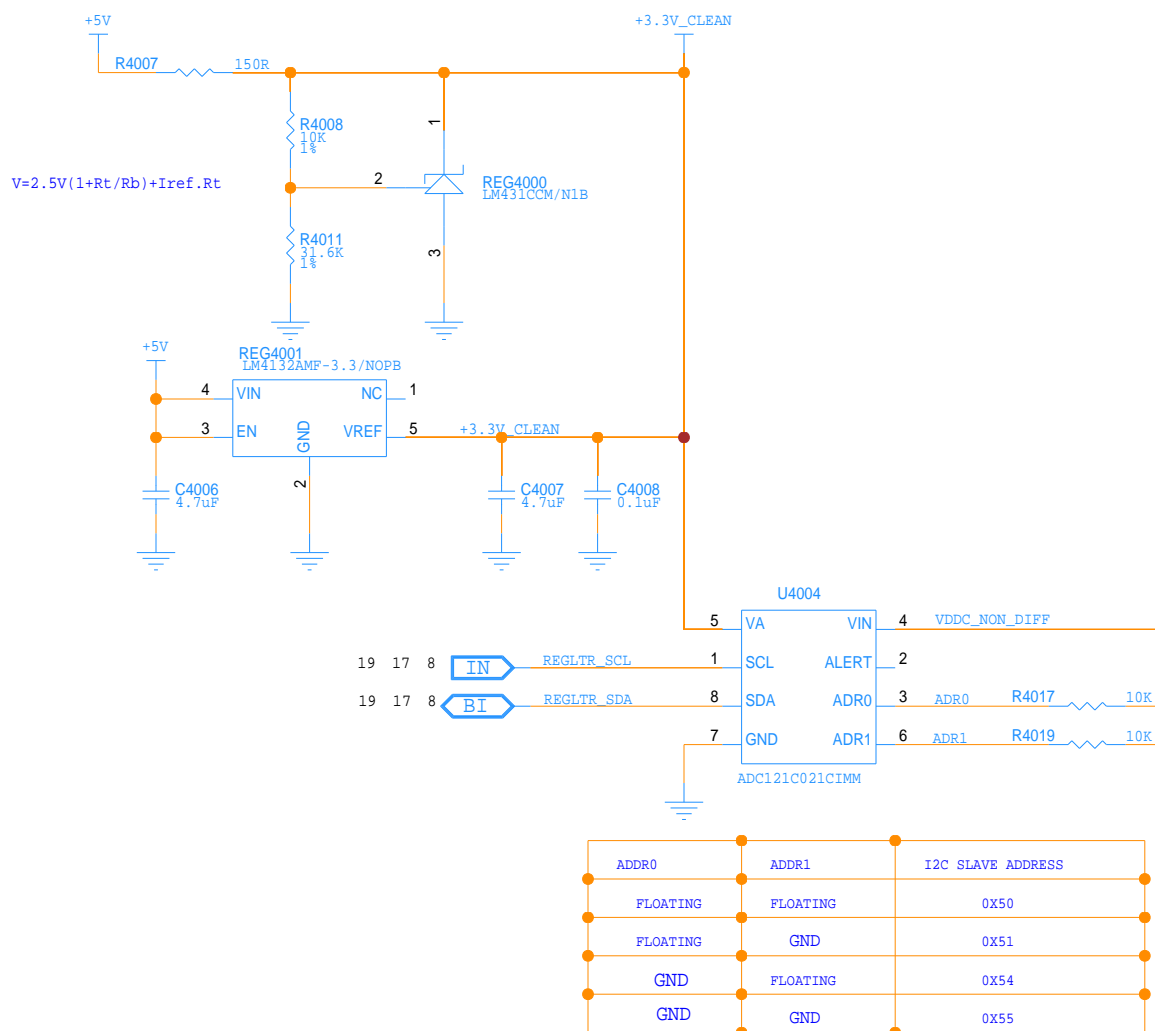
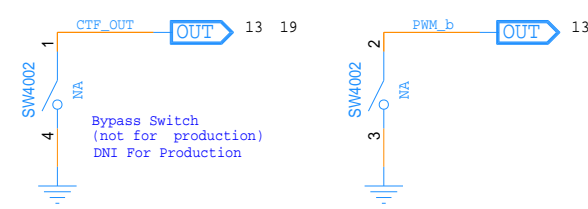
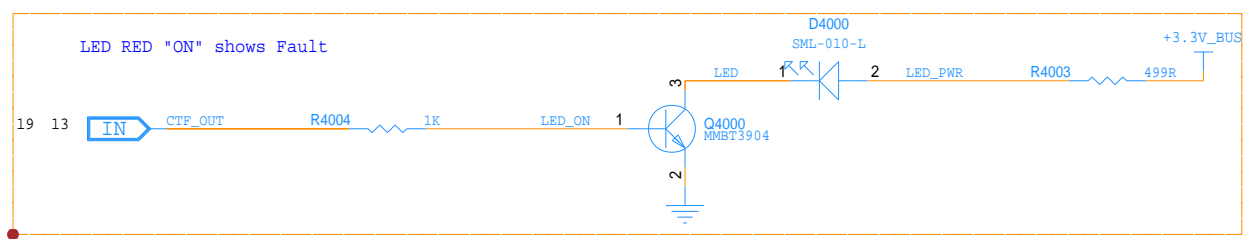
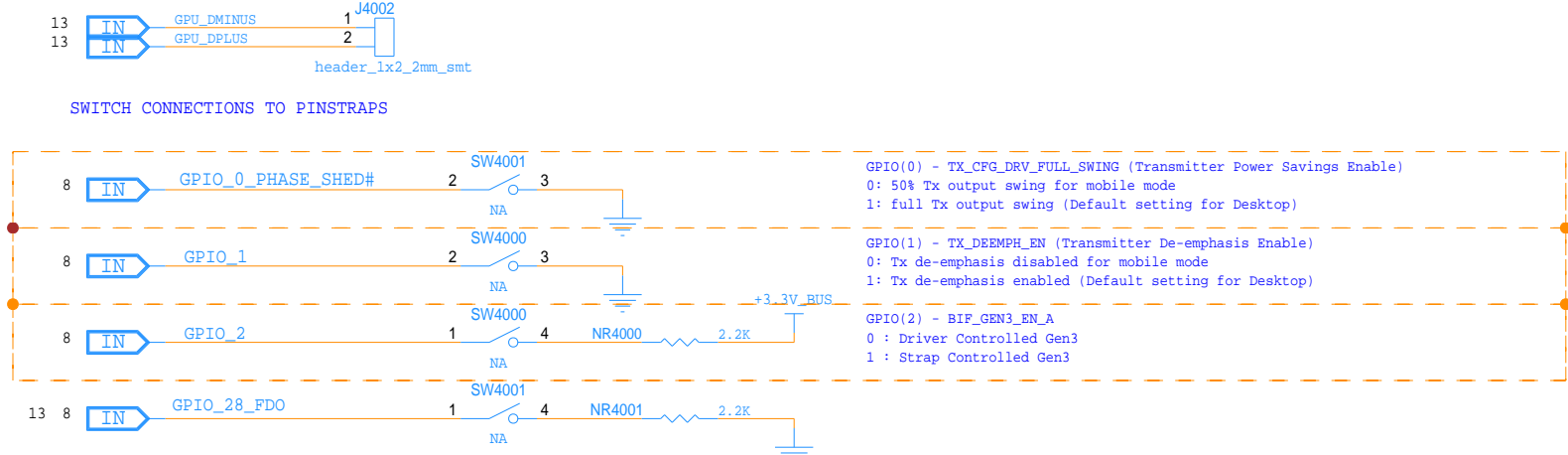
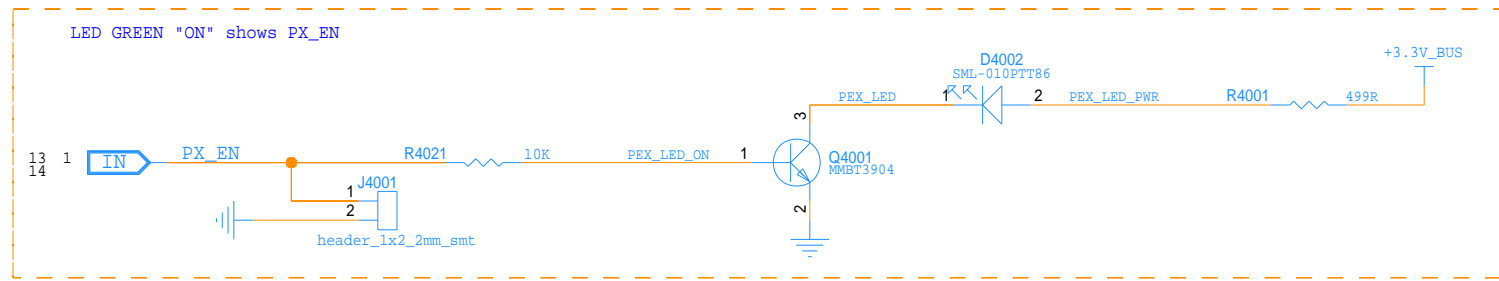
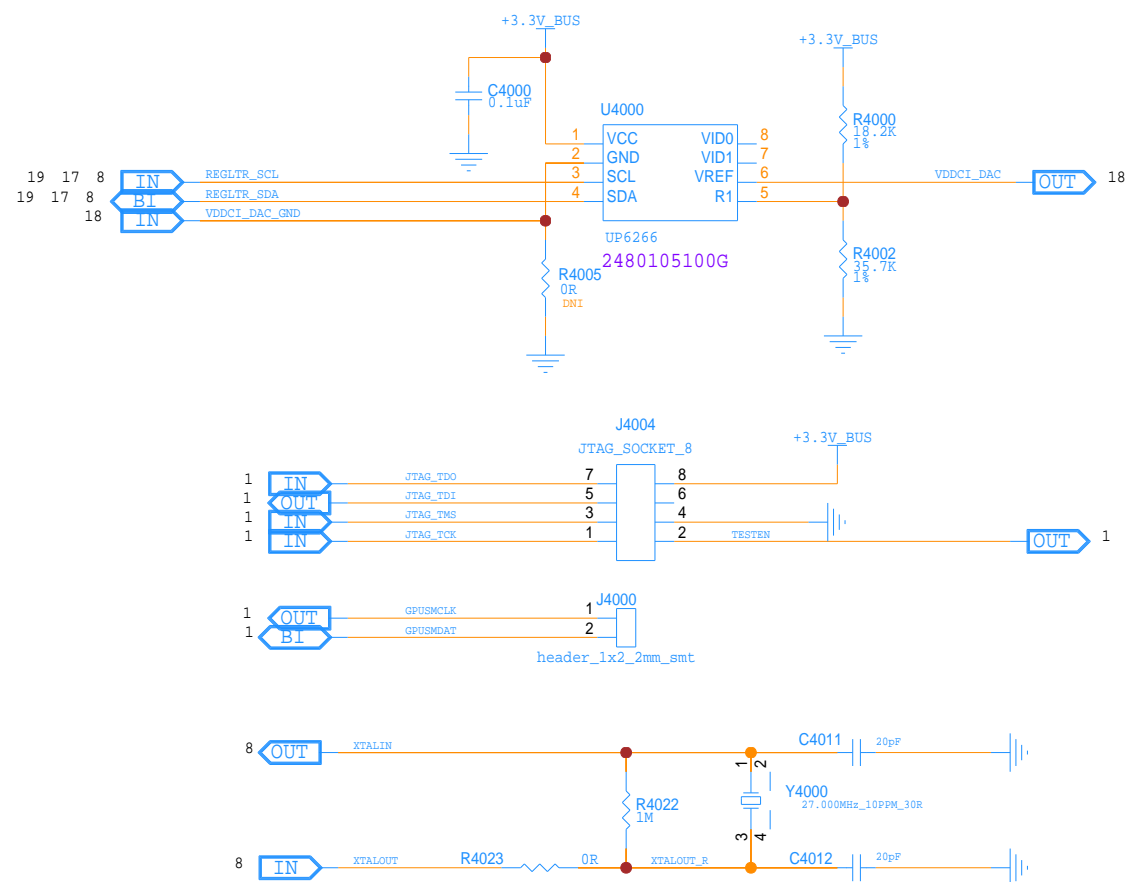
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(21) DEBUG CIRCUITS



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SHEET: DEBUG CIRCUITS

DATE: Wed Nov 02 15:28:38 2011

SHEET NUMBER: 19 OF 21

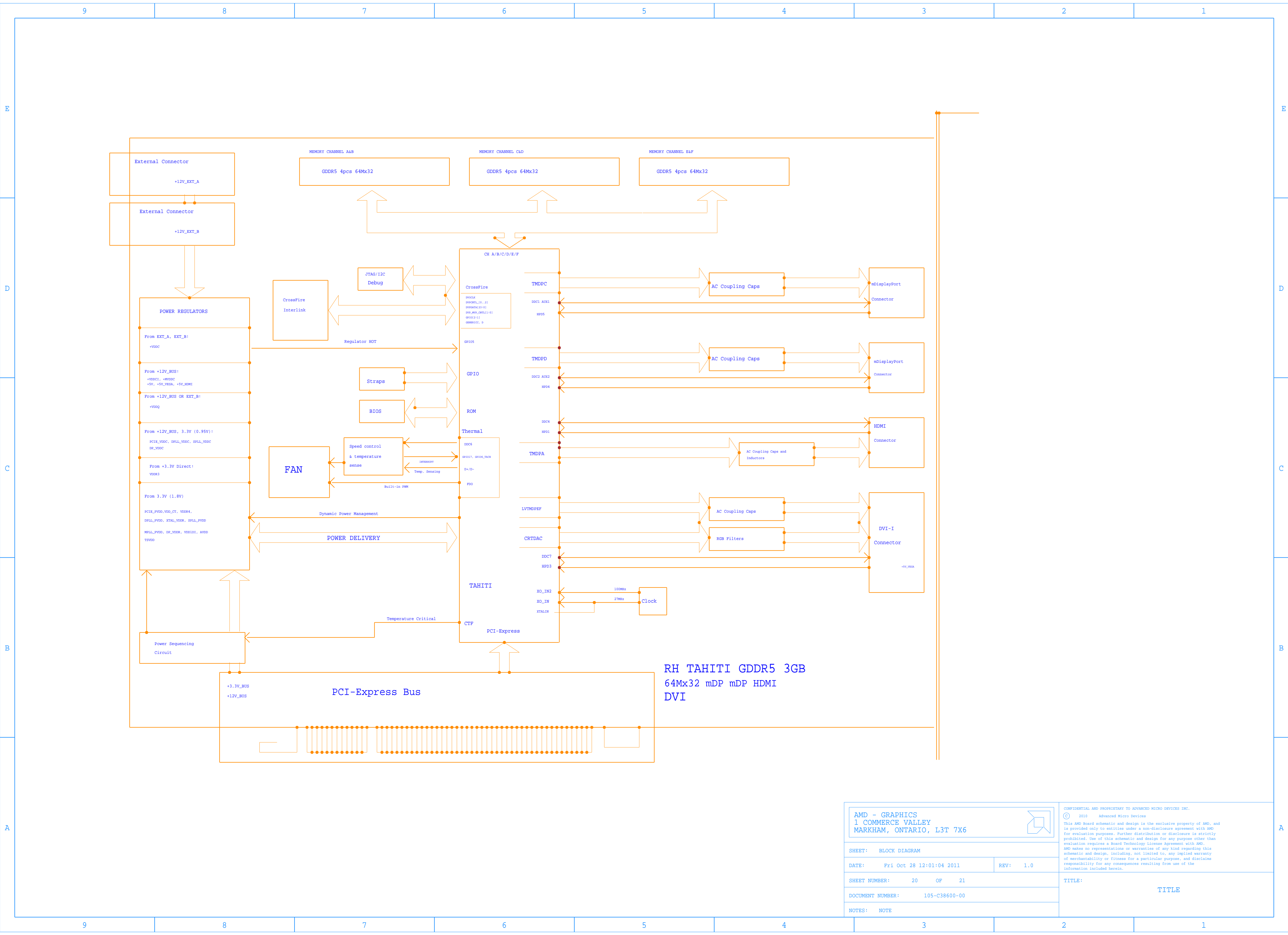
DOCUMENT NUMBER: 105-C38600-00

NOTES: NOTE

REV: 1.0

TITLE: TITLE

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SHEET: BLOCK DIAGRAM			<div>TITLE:</div> <div>TITLE</div>		
DATE: Fri Oct 28 12:01:04 2011		REV: 1.0			
SHEET NUMBER: 20 OF 21					
DOCUMENT NUMBER: 105-C38600-00					
NOTES: NOTE					

