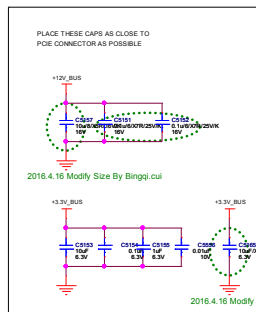
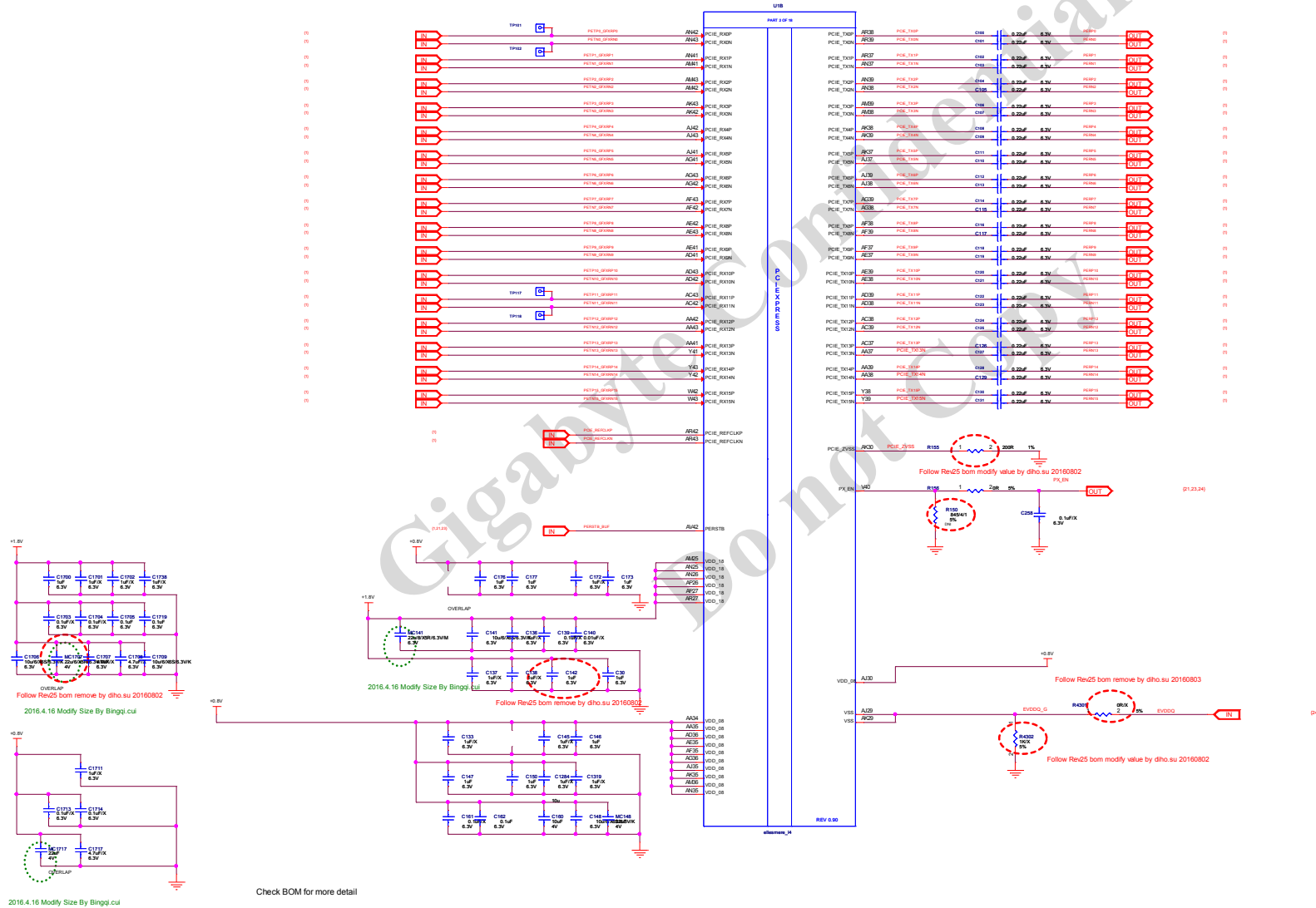


(1) PCI-EXPRESS EDGE CONNECTOR

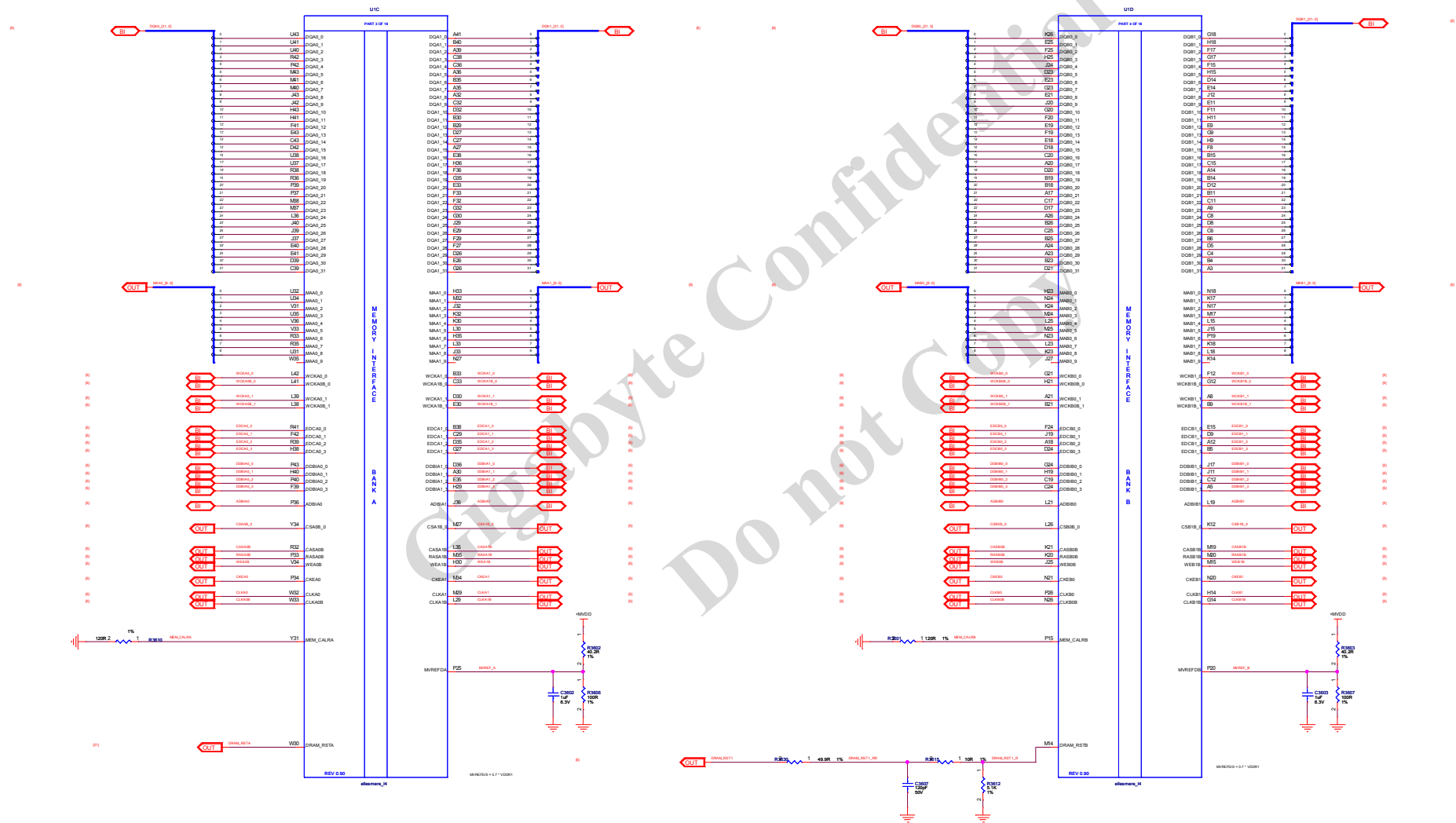


GIGABYTE			
Title PCI Express			
Size	Document Number		Rev
Custom	GV-RX570GAMING-4GD		1.0
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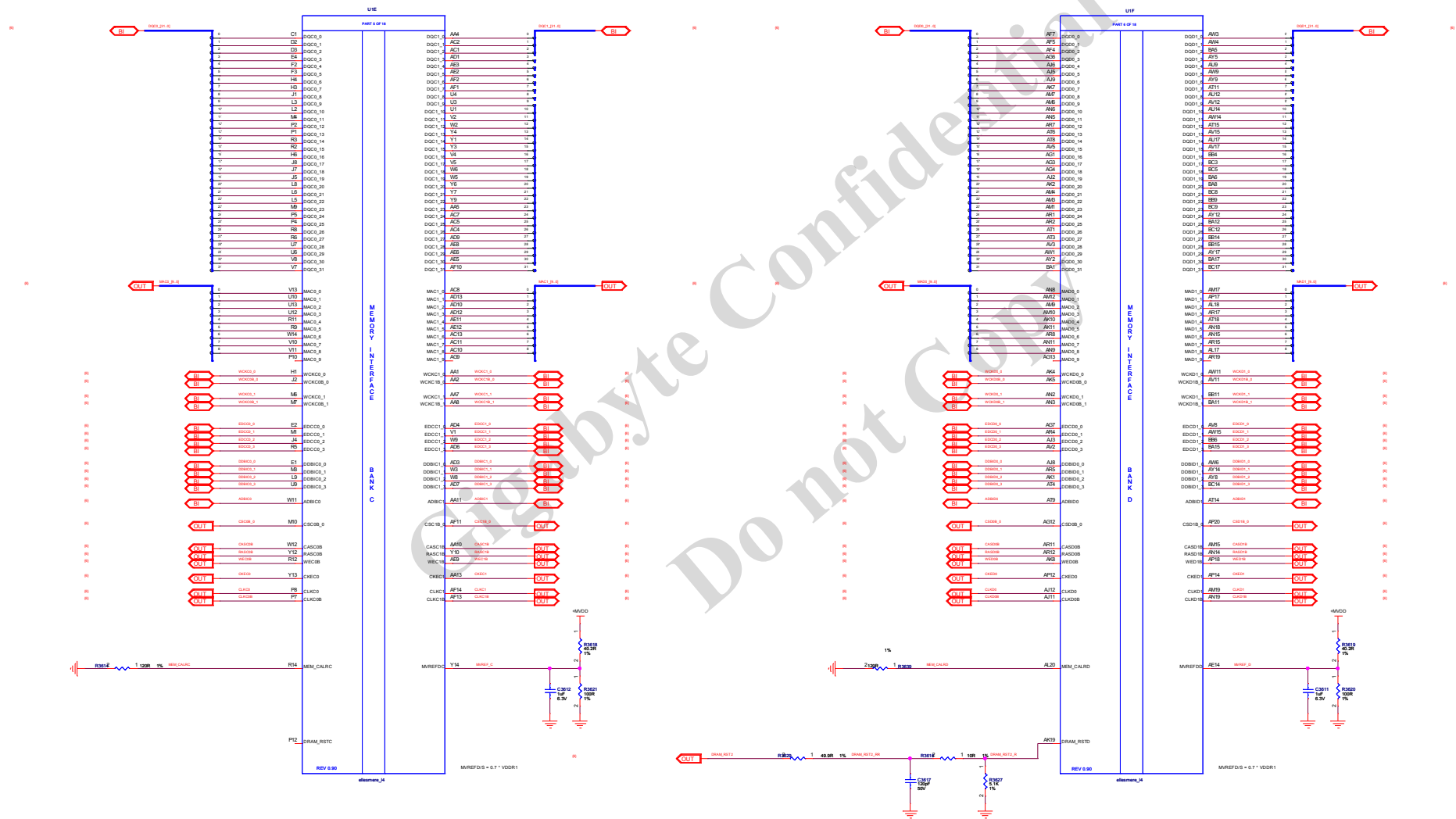
(2) ELLESMERE PCIE INTERFACE



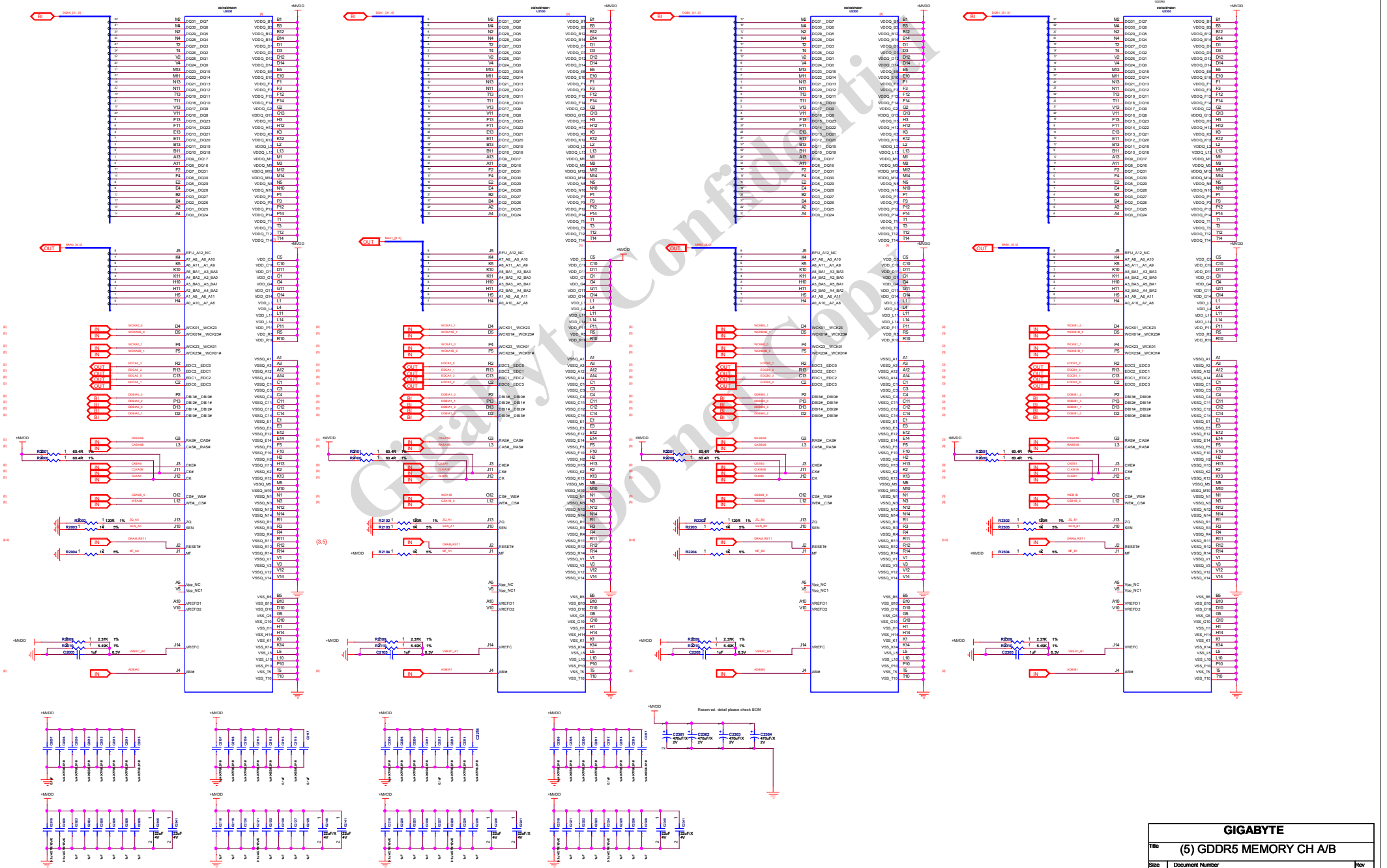
(3) ELLESMERE MEM INTERFACE CH A/B



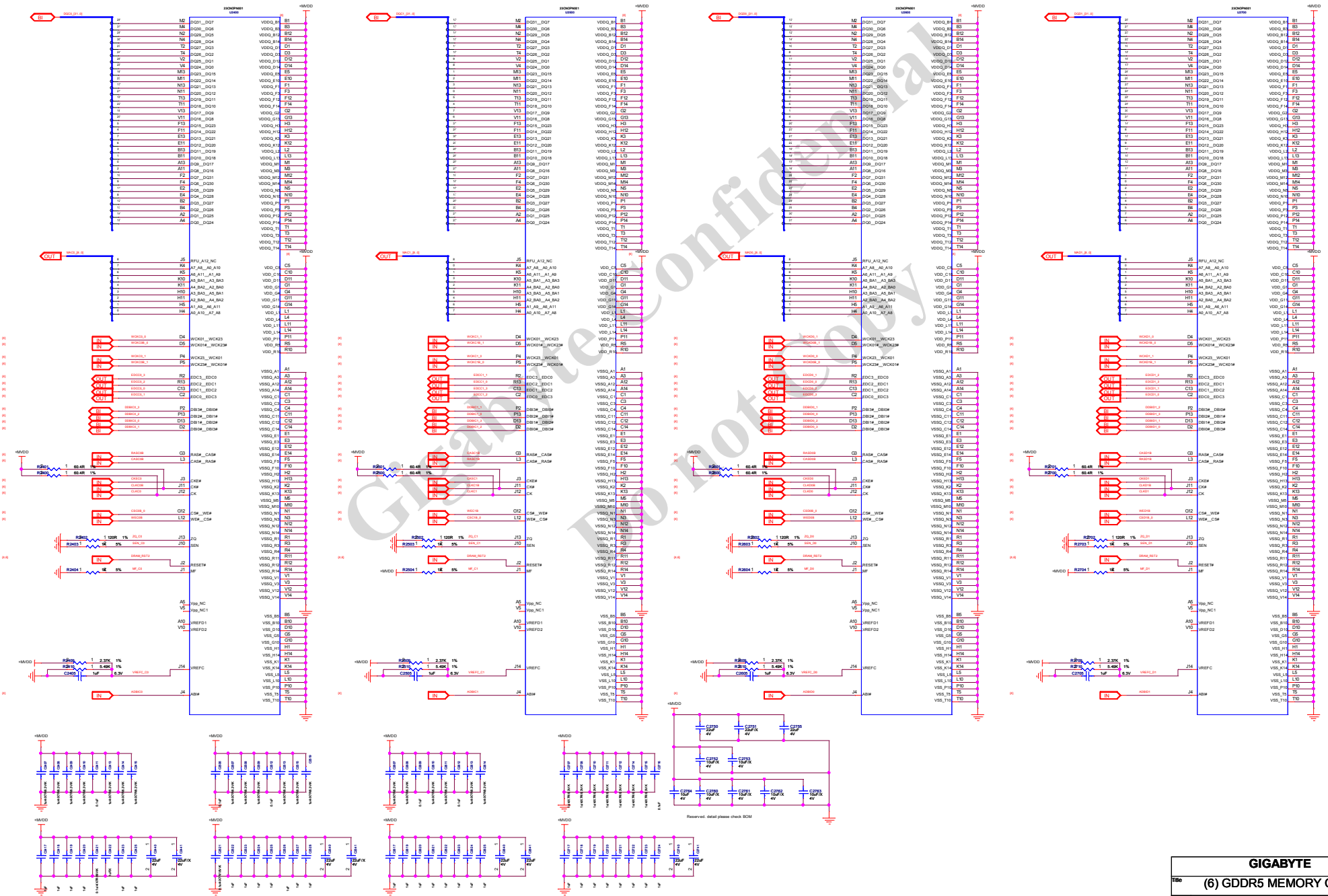
(4) ELLESMERE MEM INTERFACE CH C/D



(5) GDDR5 MEMORY CH A/B

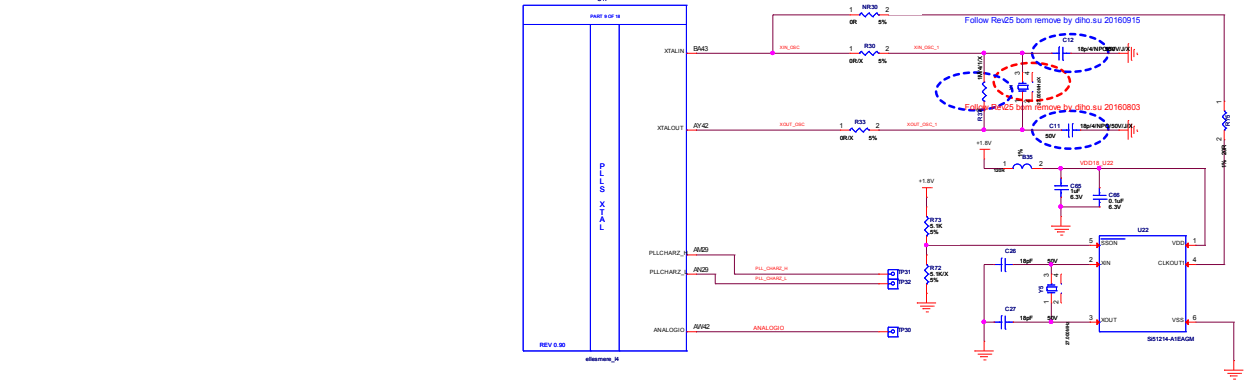
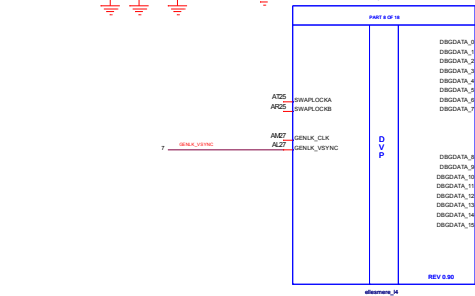
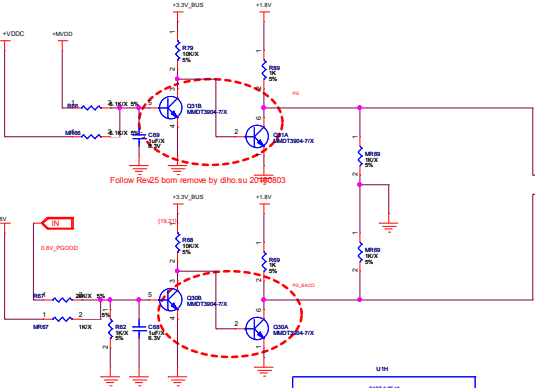


(6) GDDR5 MEMORY CH C/D



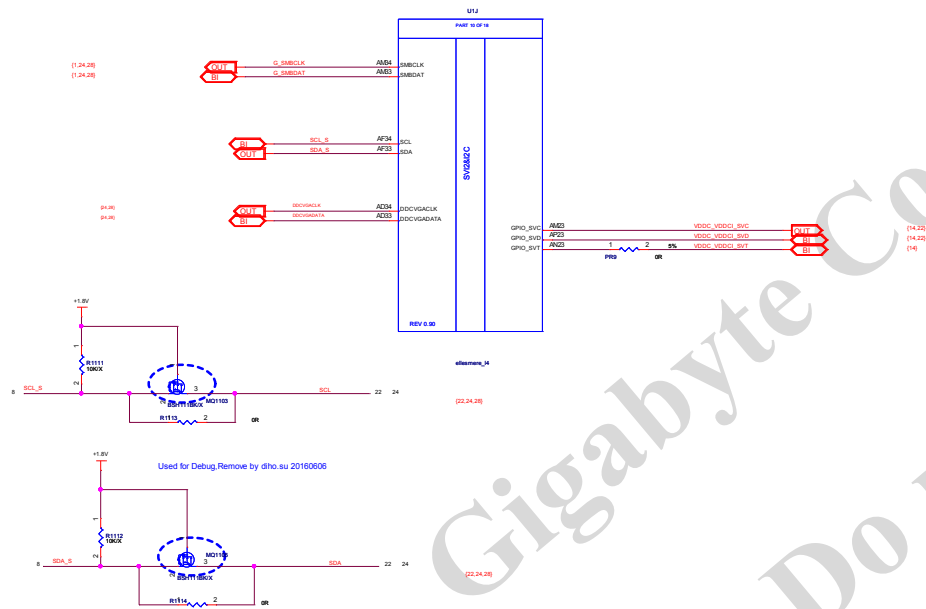
(7) ELLESMERE GPIO STRAP CF XTAL

SCUDBA BUS		
IO ADDRESS	FUNCTION	DEVICE
DDCVGA BUS		
IO ADDRESS	FUNCTION	DEVICE
0x00	EXT TEMP SENSOR	LM8003

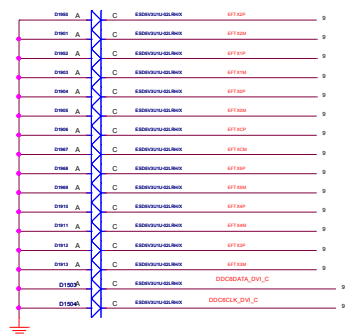


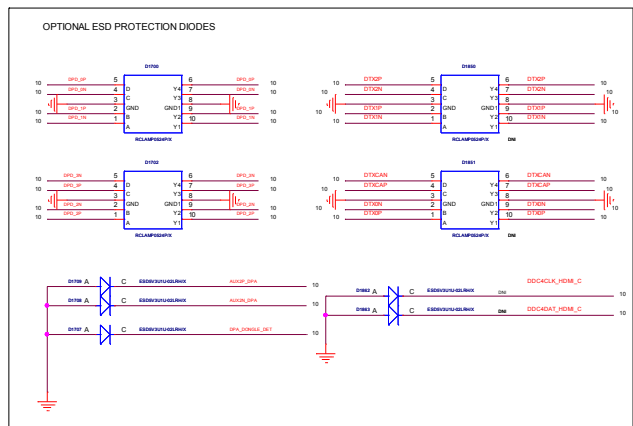
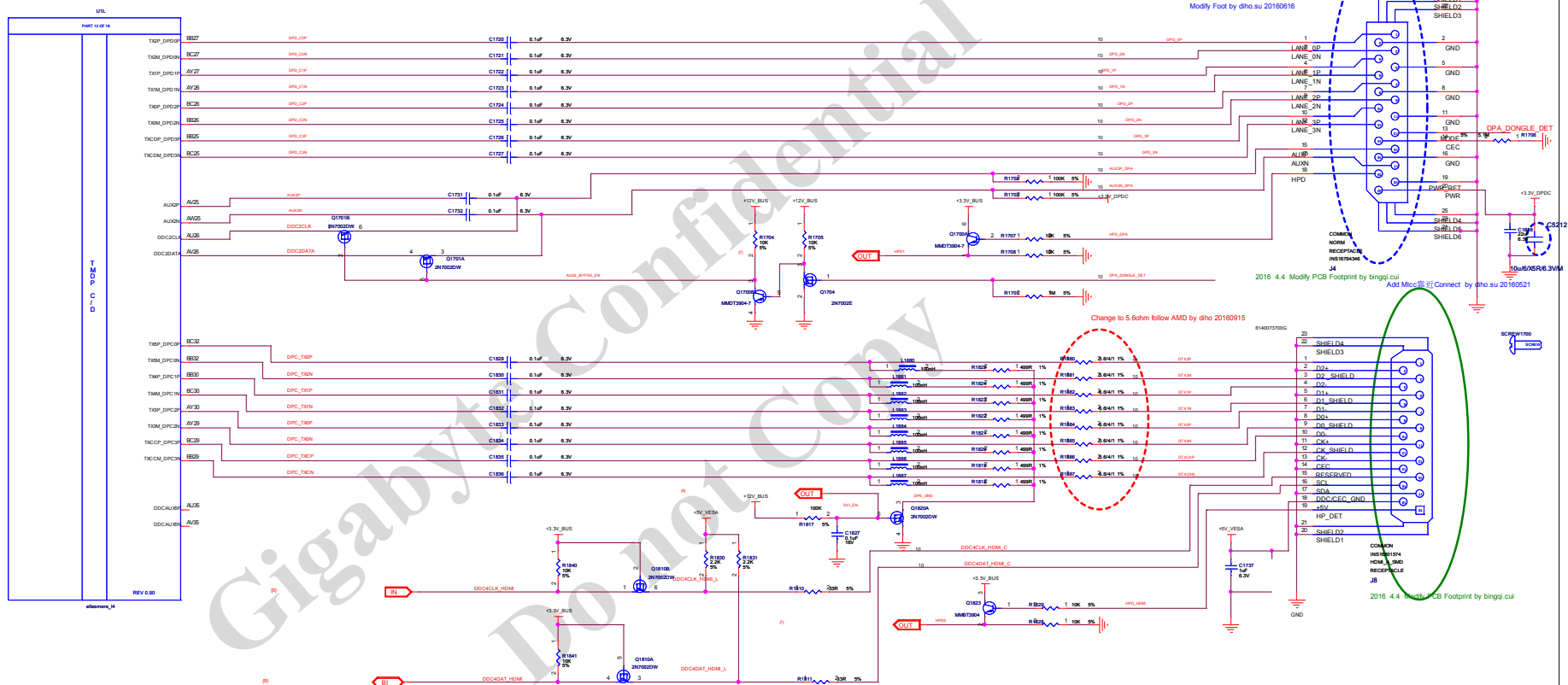
PIN BASED STRAPS		
2	1	7
3	2	8
4	3	9
5	4	10
6	5	11
7	6	12
8	7	13
9	8	14
10	9	15
11	10	16
12	11	17
13	12	18
14	13	19
15	14	20
16	15	21
17	16	22
18	17	23
19	18	24
20	19	25
21	20	26
22	21	27
23	22	28
24	23	29
25	24	30
26	25	31
27	26	32
28	27	33
29	28	34
30	29	35
31	30	36
32	31	37
33	32	38
34	33	39
35	34	40
36	35	41
37	36	42
38	37	43
39	38	44
40	39	45
41	40	46
42	41	47
43	42	48
44	43	49
45	44	50
46	45	51
47	46	52
48	47	53
49	48	54
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58	57	63
59	58	64
60	59	65
61	60	66
62	61	67
63	62	68
64	63	69
65	64	70
66	65	71
67	66	72
68	67	73
69	68	74
70	69	75
71	70	76
72	71	77
73	72	78
74	73	79
75	74	80
76	75	81
77	76	82
78	77	83
79	78	84
80	79	85
81	80	86
82	81	87
83	82	88
84	83	89
85	84	90
86	85	91
87	86	92
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91	90	96
92	91	97
93	92	98
94	93	99
95	94	100

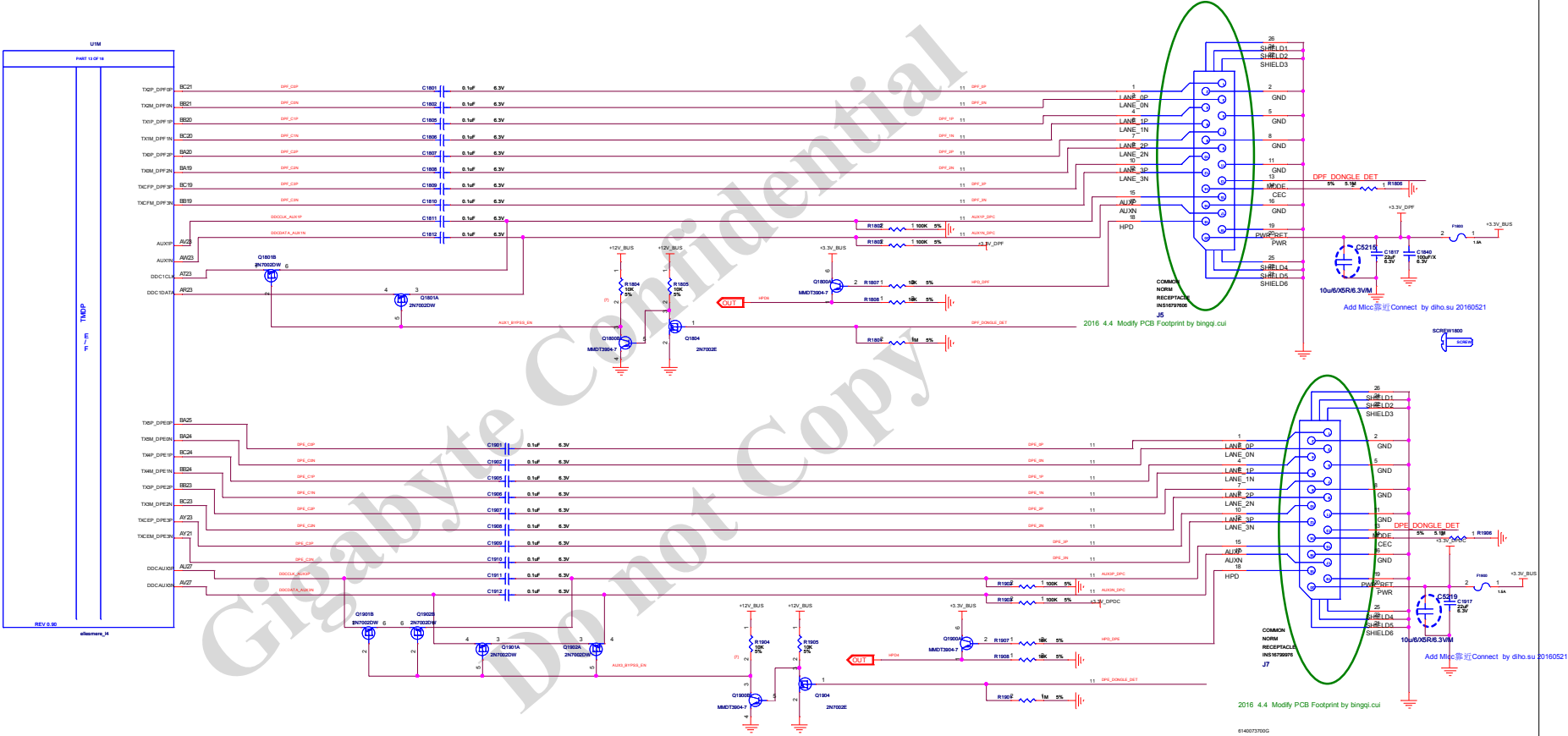
(8) ELLESMERE DAC1 LOCK



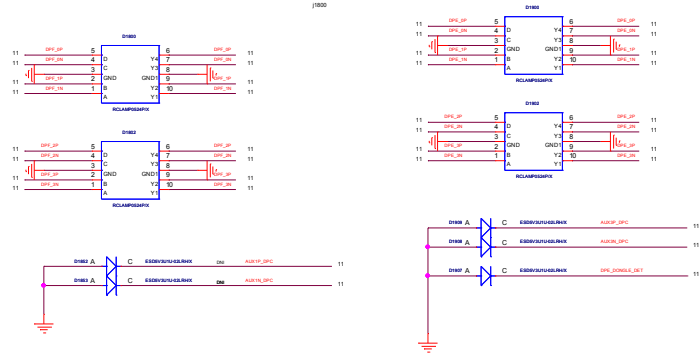
GIGABYTE			
Title ELLESMERE DAC1 LOCK			
Size Custom	Document Number GV-RX570GAMING-4GD		Rev 1.0
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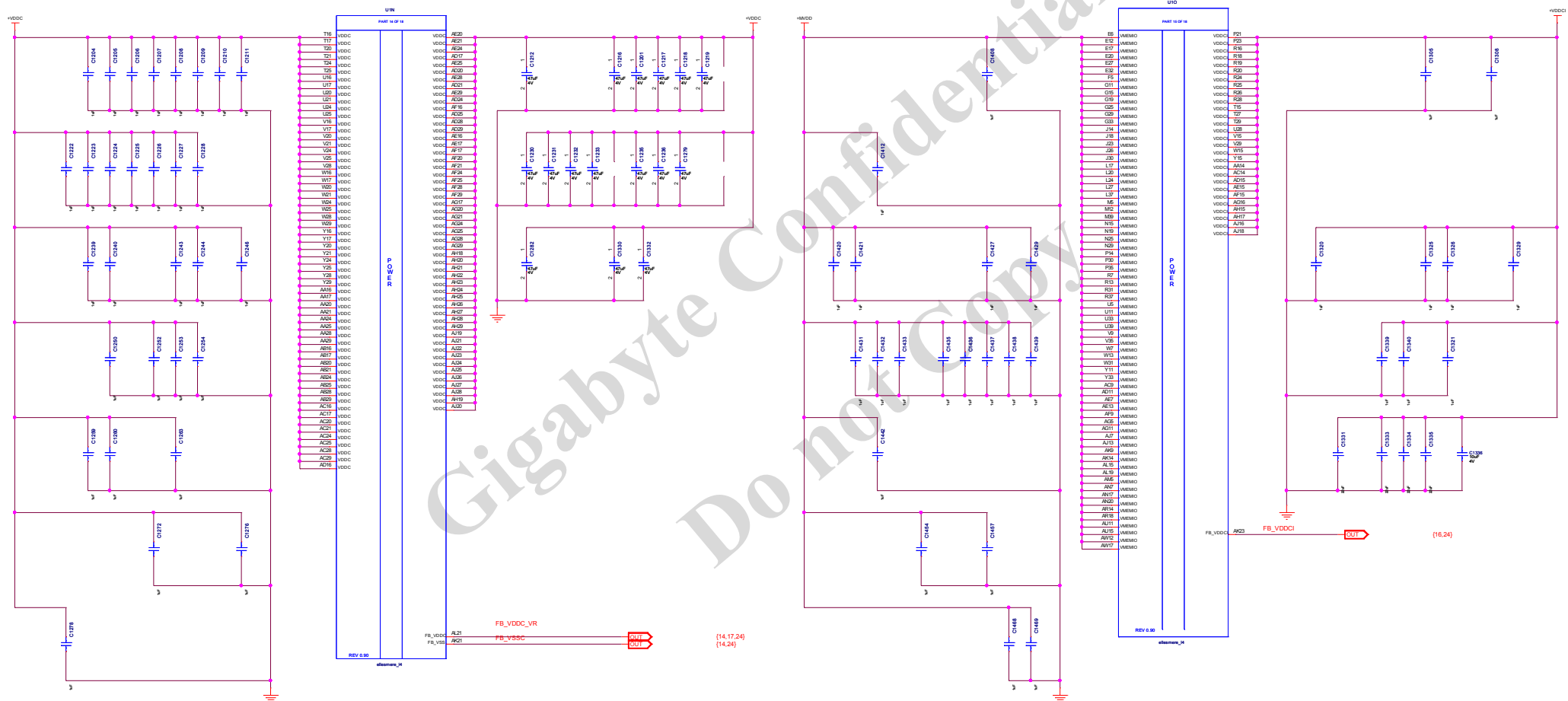


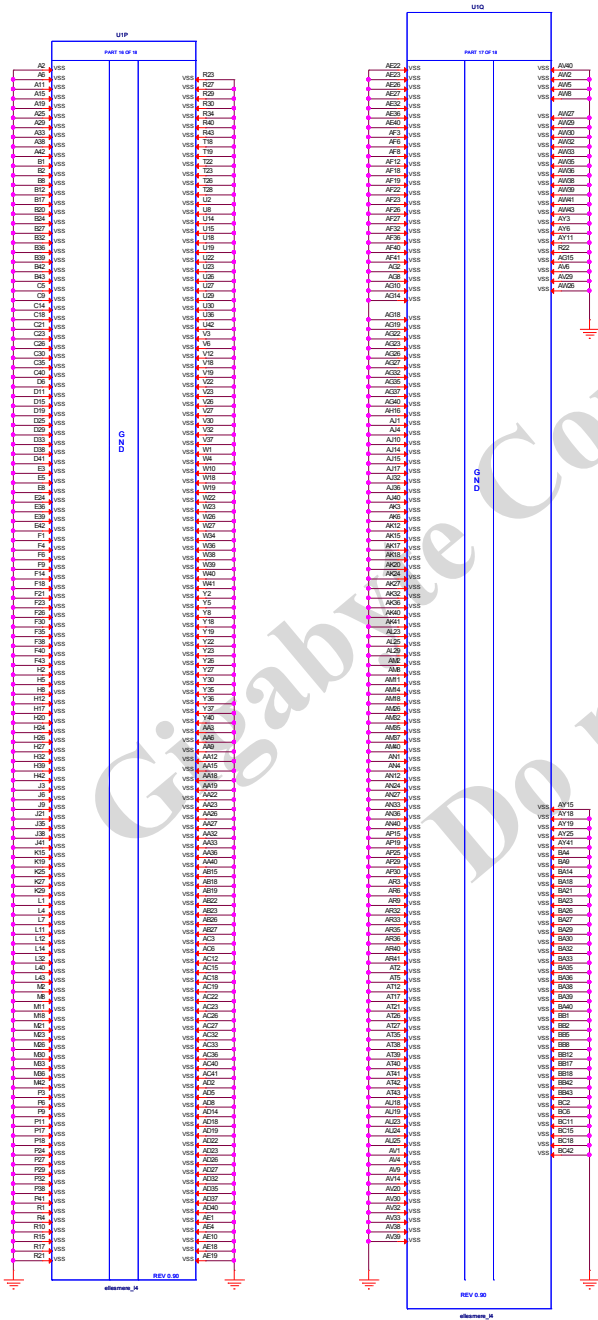
OPTIONAL ESD PROTECTION DIODES

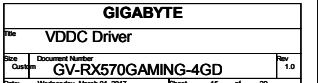


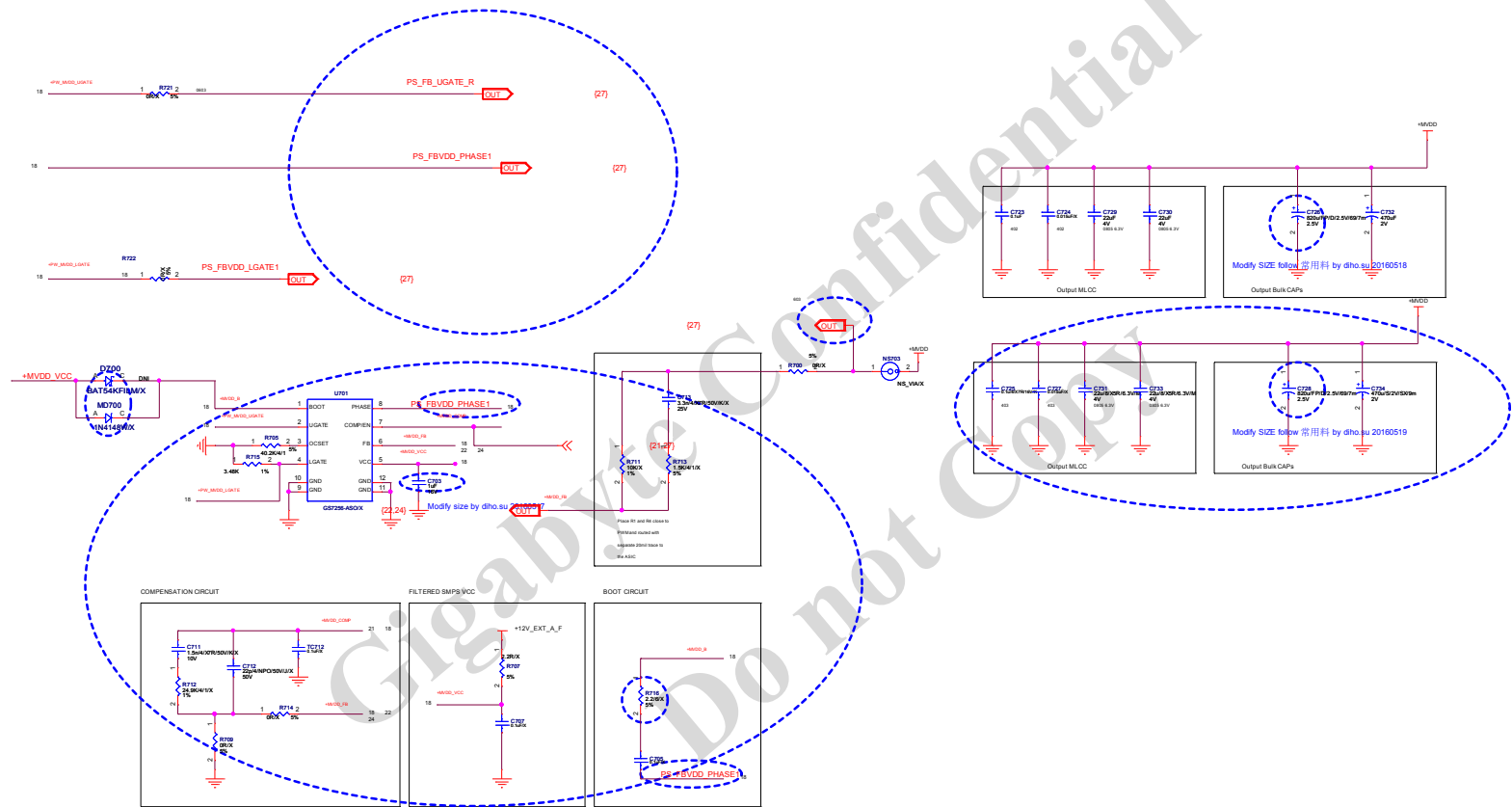
GIGABYTE			
Title ELLESMERE LVTMDP E/F			
Size	Document Number	Rev	
Customer	GV-RX570GAMING-4GD	1.0	
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(12) ELLESMERE POWER

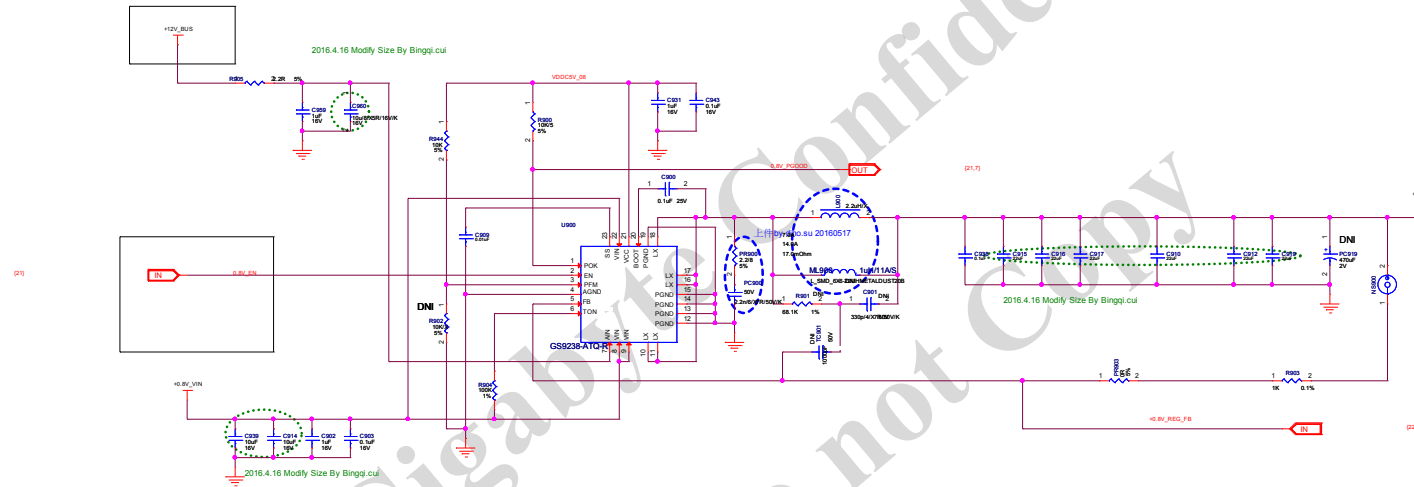








/X Single phase power by diho.su 20160517

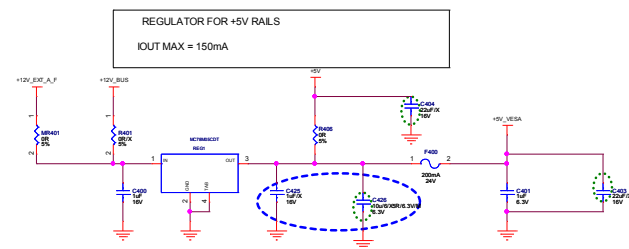


-2%

IOUT = 1.3A RMS MAX

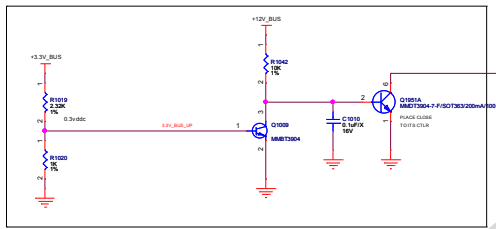
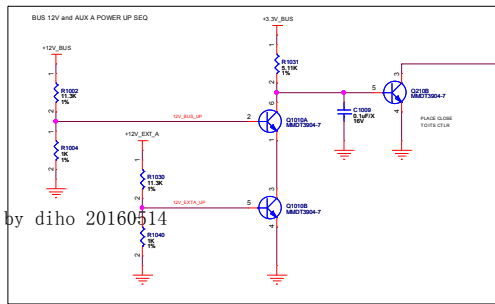
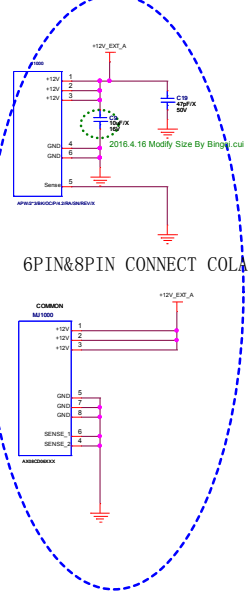
VOLT = Vref x (1 + RS/R4)

Modify size to 0603 save cost by dila 20160915

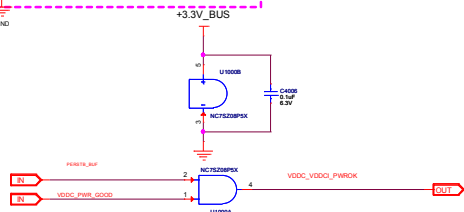
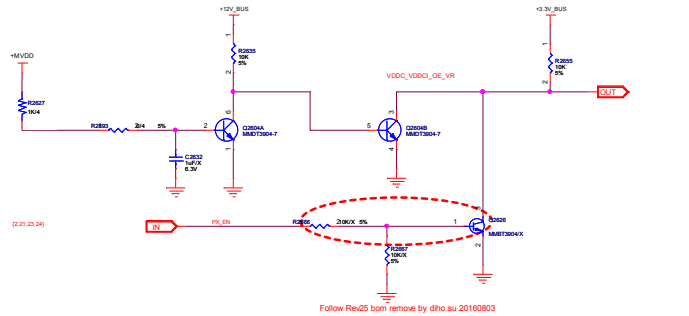
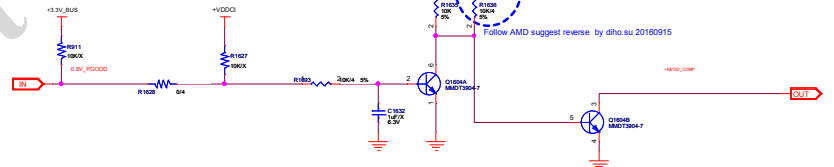
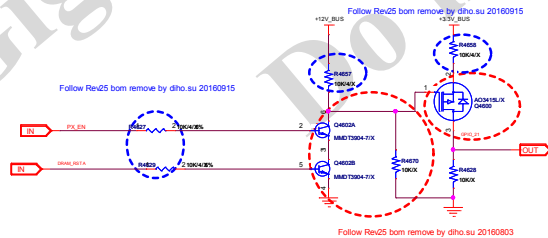
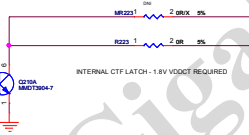
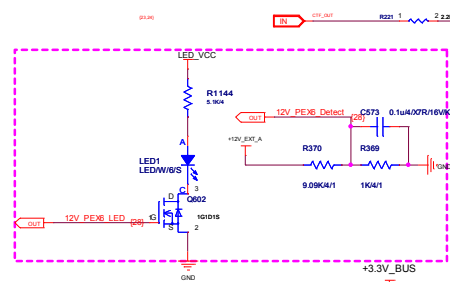
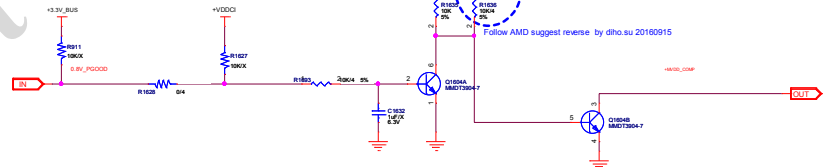
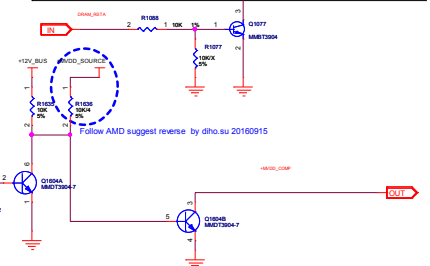
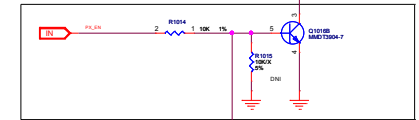
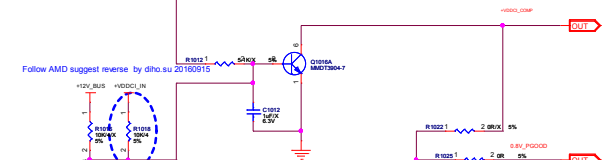
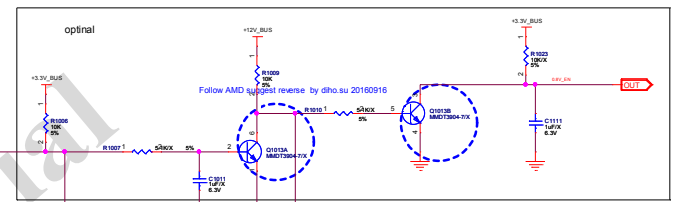
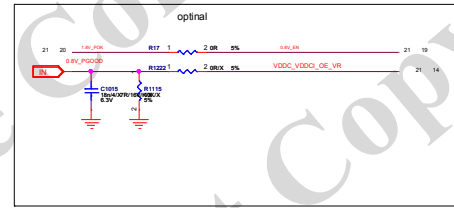


GIGABYTE			
Title SMALL RAIL REGULATORS			
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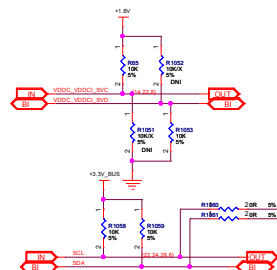
(19) POWER MANAGEMENT



POWER UP SEQUENCE		
BUS ANAL (3.3V12V UP) → +1.8V → 0.930V VDDC → VDDCI VVDD	WFI_VDDC VDDC → VDDCI VVDD	

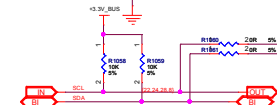


(14.22.8)



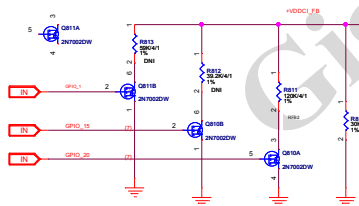
SVQ2 BOOT UP VOLTAGE (VDDQ/VDDCI)		
SVQ	SVQ	VOLTAGE
0	0	1.1V
0	1	1.0V
1	1	0.9V
1	1	0.8V

(22.24.28.8)

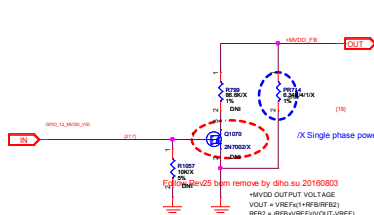


(22.24.28.8)

(22.24.28.8)

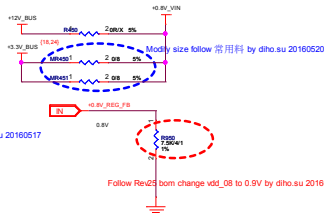


(14.24)



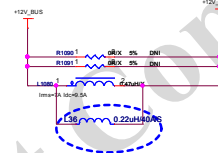
/X Single phase power by dho.su 20160517

Remove R25 bom remove by dho.su 20160803
+MISO OUTPUT VOLTAGE
 $V_{OUT} = V_{REF}(1 + R_{FB}/R_{FB2})$
 $R_{FB2} = (R_{FB}/V_{REF})(V_{OUT} - V_{REF})$

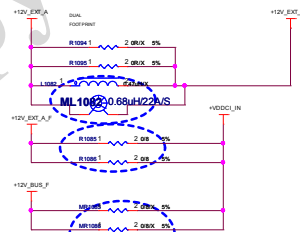


Follow Ren25 bom change vdd_08 to 0.9V by dho.su 20160803

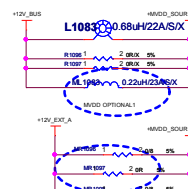
+12V_BUS INPUT



+12V_EXT_A INPUT

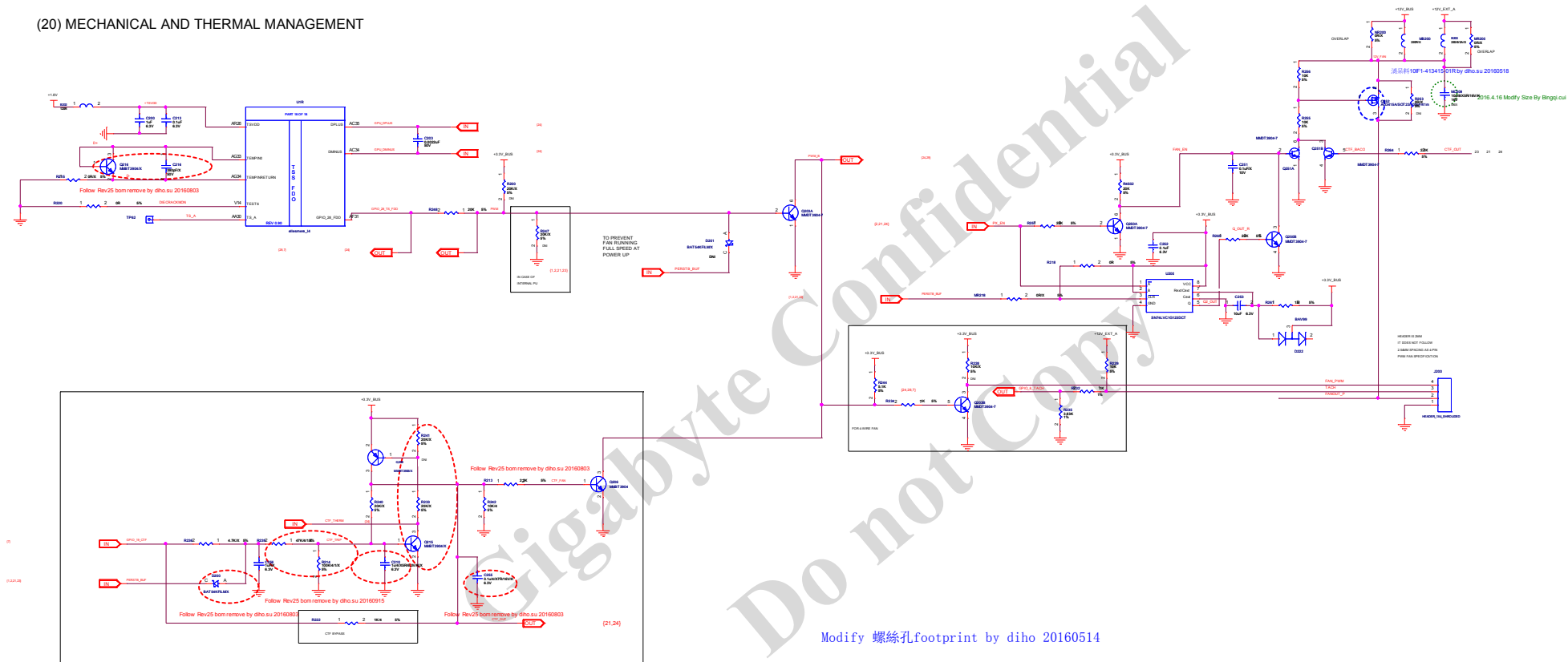


Modify size follow 常用料 by dho.su 20160519

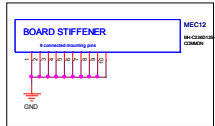


Modify size follow 常用料 by dho.su 20160518

(20) MECHANICAL AND THERMAL MANAGEMENT



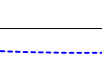
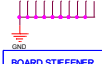
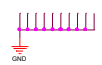
BK



MOS SINK



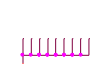
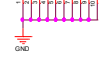
BKT SINK



GPU SINK

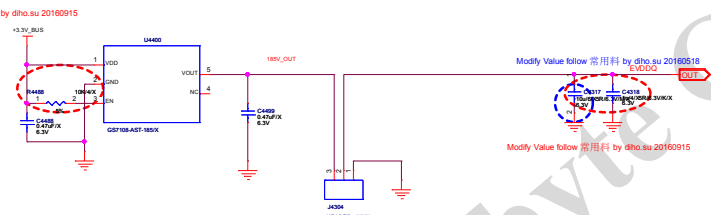
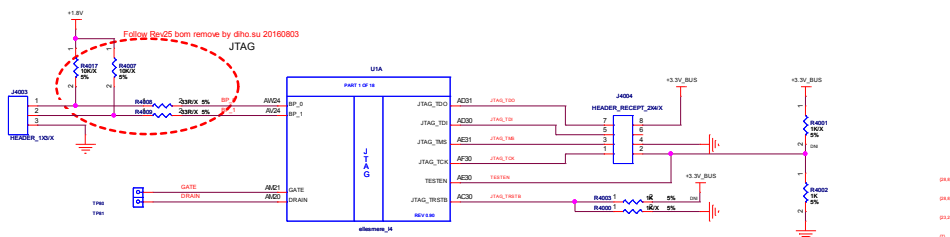


BOARD STIFFENER

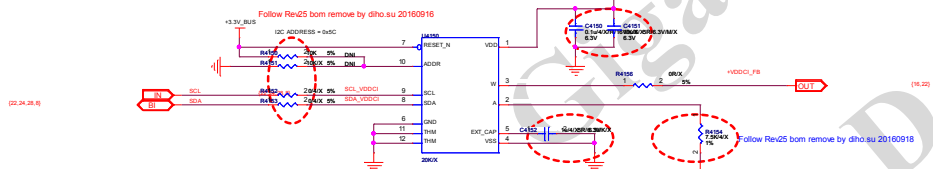


GIGABYTE			
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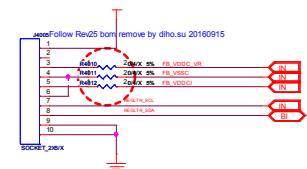
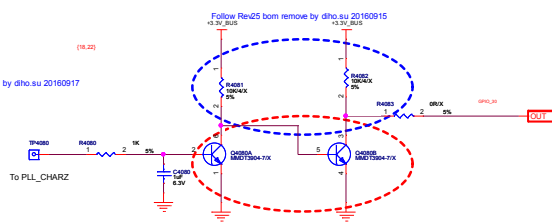
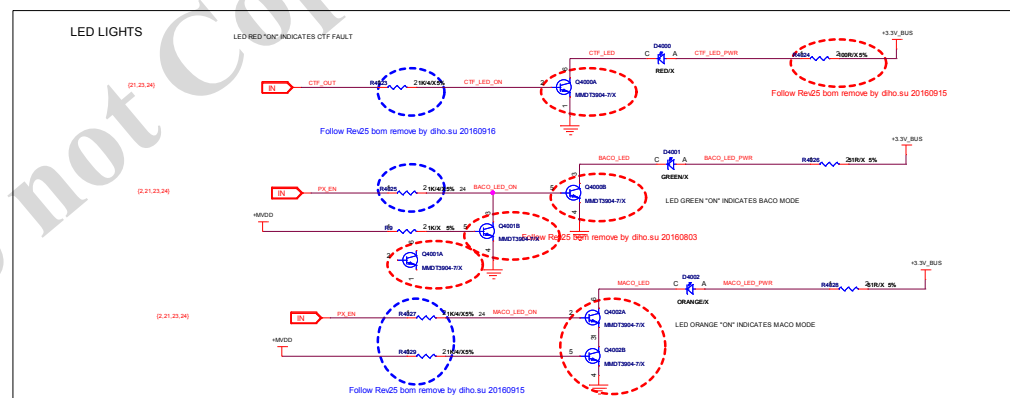
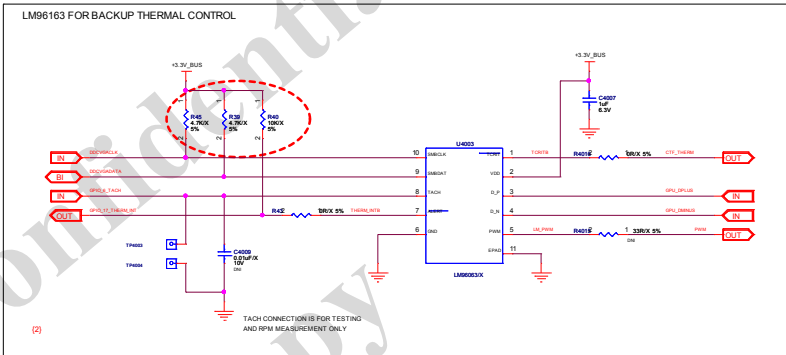
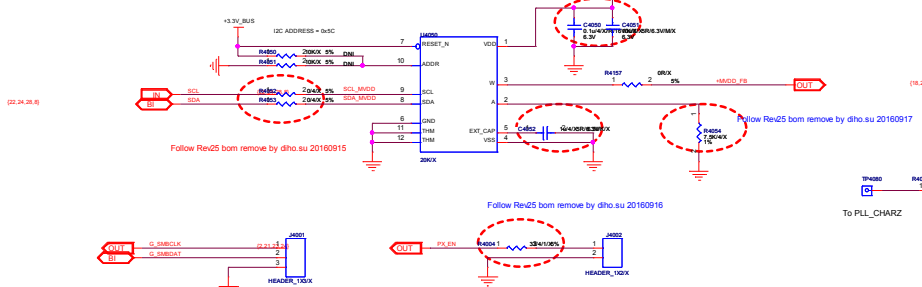
(21) DEBUG CIRCUITS



DIGITAL POTS

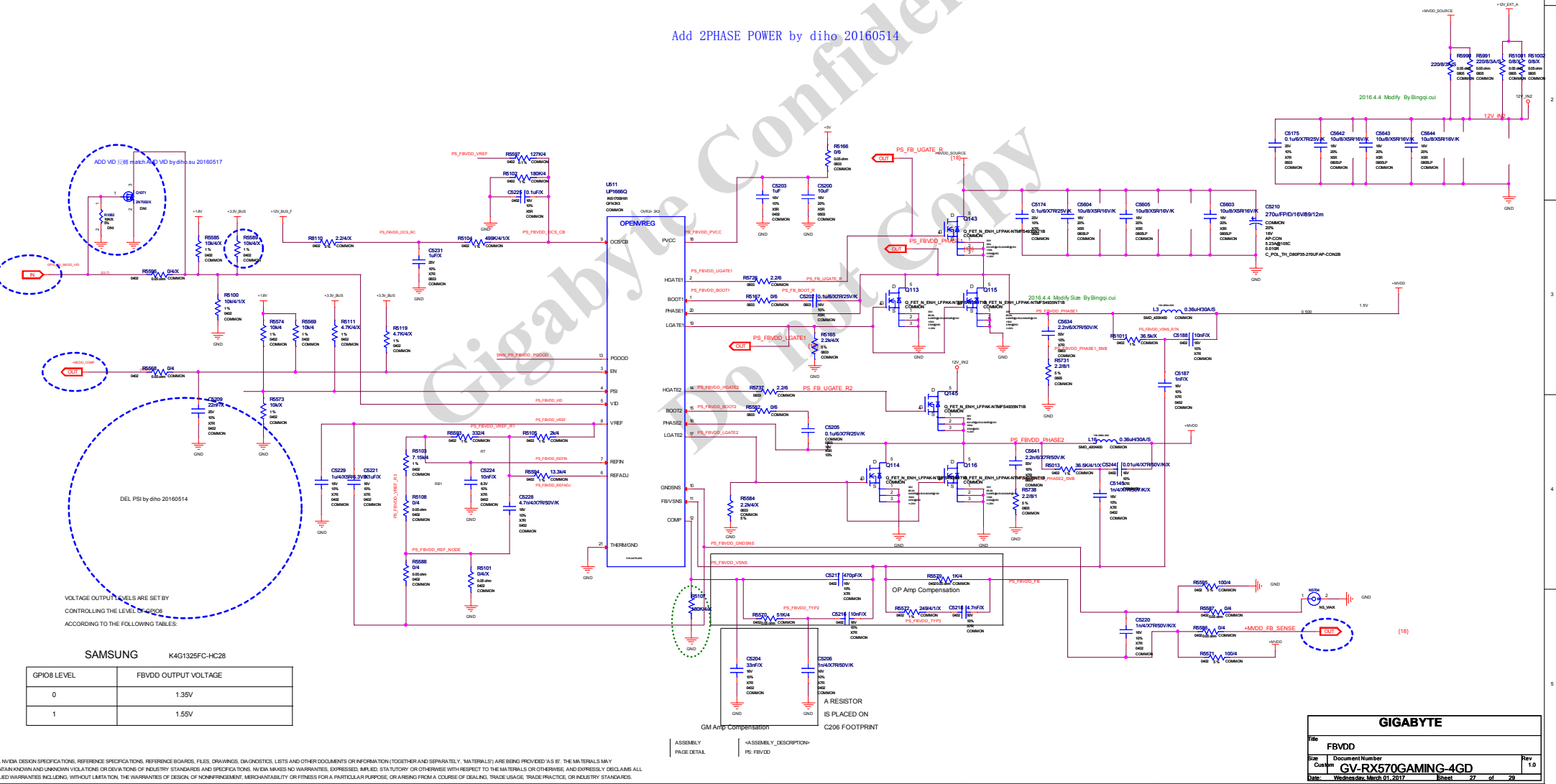


DIGITAL POTS



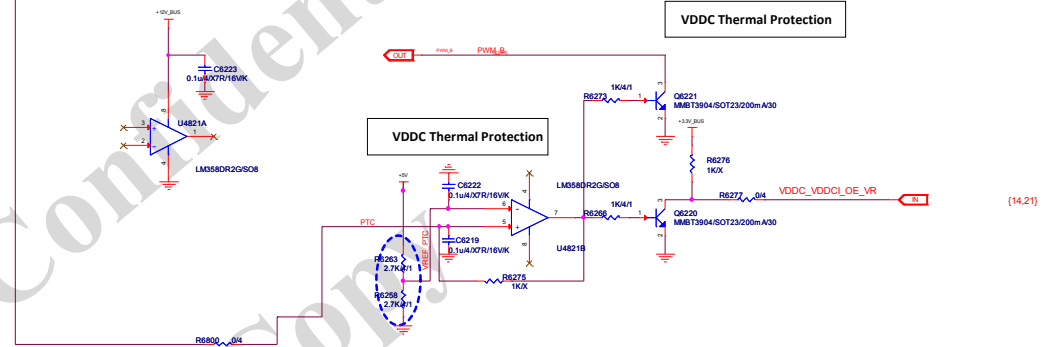
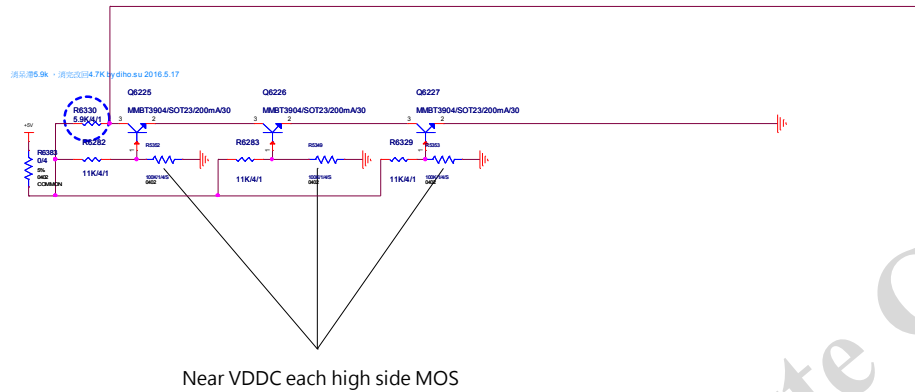
0	00A	00000000	
1	00B	00000000	1. Add SPI0 to SPI000 10000 2. Update SPI000 with response
2	00C	00000000	PC000: - add serial resistor R1000 - R1007 - add pull-down resistor L1000 - L1007 Remove C400, C403, V0400, C410, C414, R400
3	00D	00000000	Modify 1000 Design

GIGABYTE			
Title			
BLOCK DIAGRAM			
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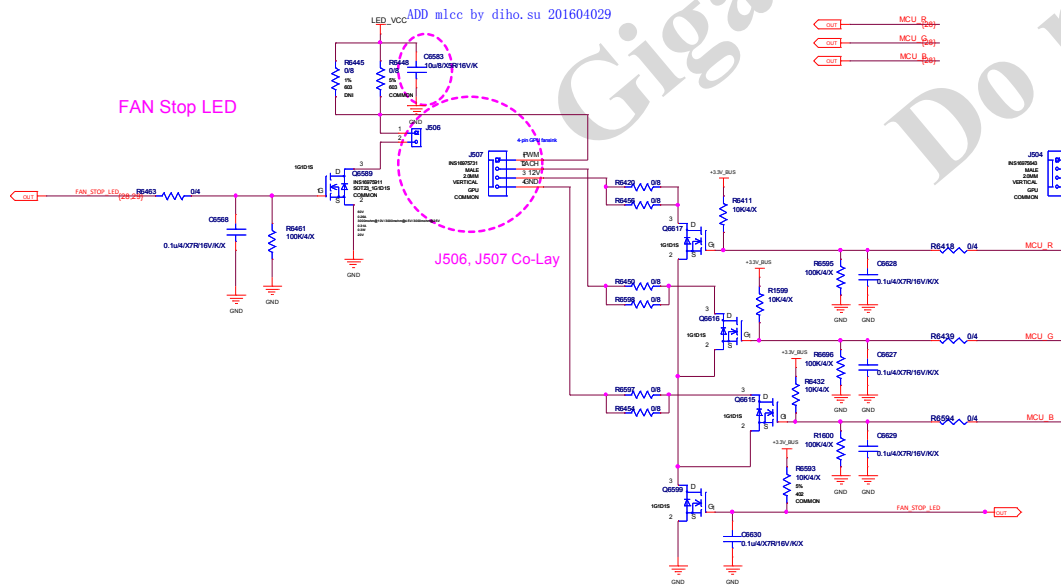




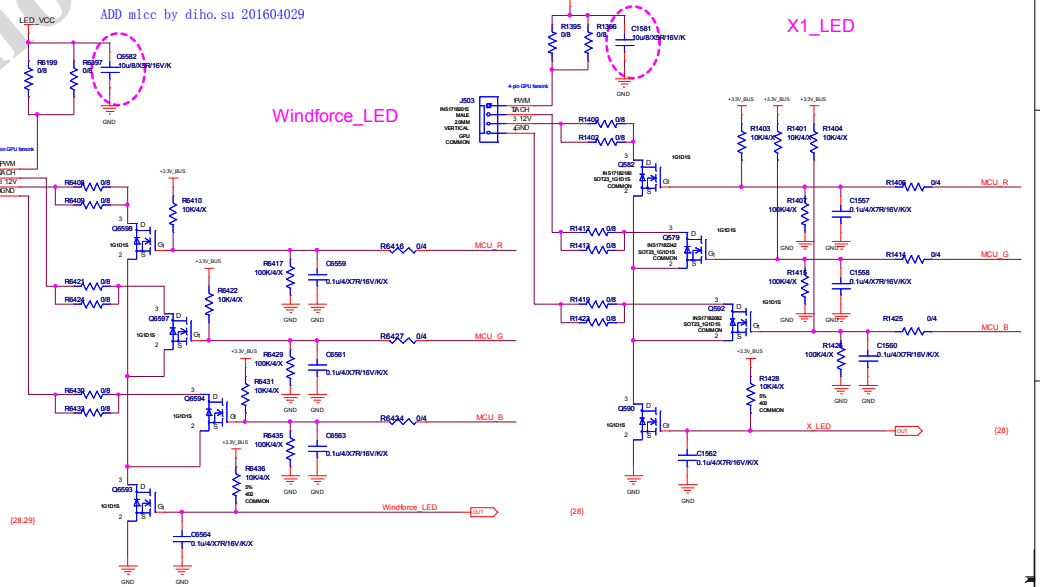
Add MOS VRHOT CIRCUIT



FAN Stop LED



Windforce_LED



X1_LED

