

GeFORCE2 MX 200,8MX16 SDR, 64MB,DVI-I, AGP4X

PCI DEVICE ID 0X0=0X111 FOR NV11(MX200).

STRAPS: AGP4X, SIDEBAND DIS, FAST WRITE DIS, AGP BIOS, NORMAL PCIAD, 14.318MHZ

HISTORY:

00A:  
X00: Based on MS-8839 (P60-A00) from RGB,TV change to DVI-I.

1. page 1 MS-8859 HISTORY
2. page 2 based on P60-A00 page 2
3. page 3 based on P60-A00 page 3
4. page 4 based on P60-A00 page 4 - change item
  - a.U100E remove to page 8.
  - b.DDCSCL/DDCSDA (AG7 and AH5) add 2.2k resistor pullups.
  - c.DACRED(AG10)/DACGREEN(AH10)/DACBLUE(AH9)/DACHSYNC(AG8)/DACVSYNC(AH7) follow P60-A00 page 5 solution.
  - d.TVCLKOUT(AD1)pull-low, location R215/100pF (for EMI solution).
5. page 5 based on P60-A00 page 6
6. page 6 based on P60-A00 page 7
7. page 7 based on P60-A00 page 8 - change item
  - a.ADD R599 and R598 for DFP[3].
8. page 8 based on P38-C02 page 12 - change item
  - a.add "DDCPower" circuit.
  - b.DFPCTX2(+/-),DFPTX1(+/-),DFPTX0(+/-),DFPTXC0(+/-) 8 line add comm-check for EMI solution.
9. page 9 based on P38-C02 page 13 - change item
  - a.U518,TL431 from S08 change to S07-23 package.
10. page 10 based on P60-A00 page 10

X01:  
1.page 4-change item


- a.ADD C818/4.7UF and C822/4.7UF for 5V bypass cap.
- b.ADD C819/4.7UF for 3.3V bypass cap.
- c.ADD R406/33R(DDCSCL) and R407/33R(DDCSDA).

2.page 8- cahnge item

- a.delete D200 (DDCPower)
- b.ADD D500(SDA) and D501(SCL).
- c.ADD R400-R423/50R for EMI solution (NO\_STUFF)
- d.ADD R502/2.2k(I2C2SDA) and R503/2.2k(I2C2SCL) pullup.
- e.ADD R506/33R(DFPBSEL) and R507/33R(DFPSEL)
- f.C141(GPIO1) NO\_STUFF

X02:  
1. page 9-ADD R601/33R(DFPDSEL)  
Delete D801,C868,C844,C846,C845(for layout)

- PAGE OVERVIEW
- 1 TOP (this) PAGE
  - 2 NVXX HOST
  - 3 AGP I/O
  - 4 NVXX DAC
  - 5 NVXX FBA/FBB
  - 6 FB SDR 4MX16 SDRAM
  - 7 ROM & STRAPS
  - 8 DVI-I I/O-TMDS
  - 9 DFP/DVI
  - 10 power supply

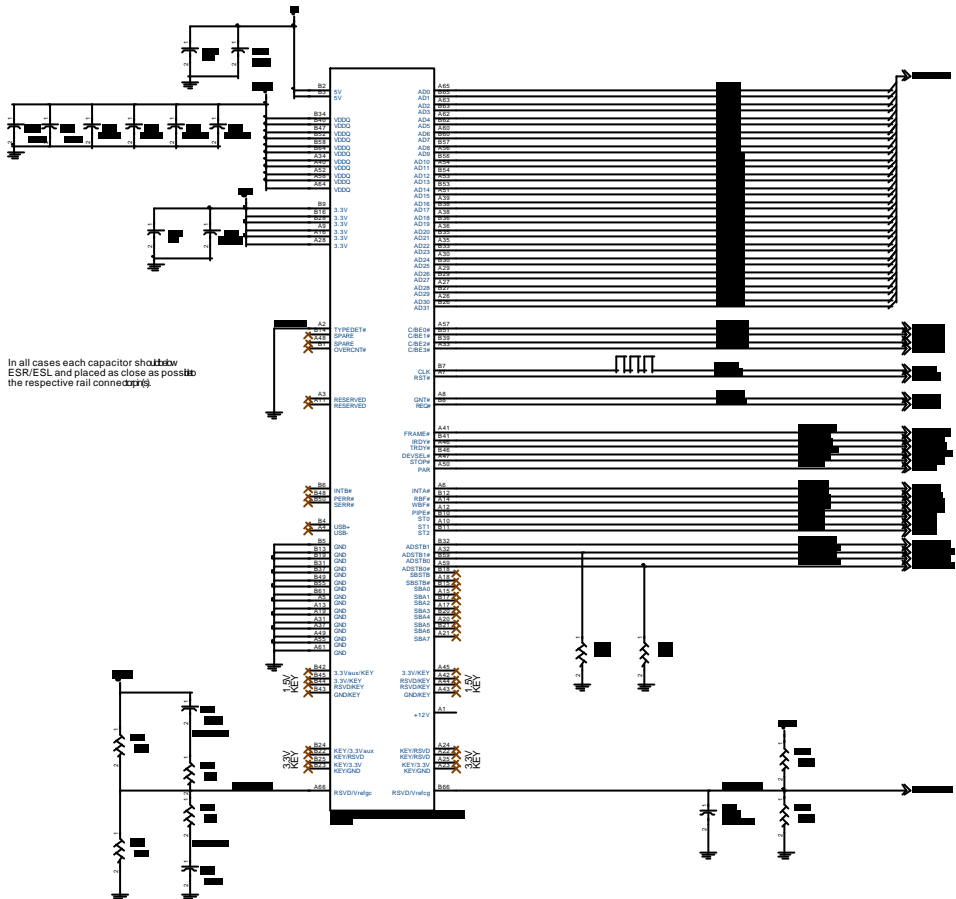


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8859 based on MS-8839



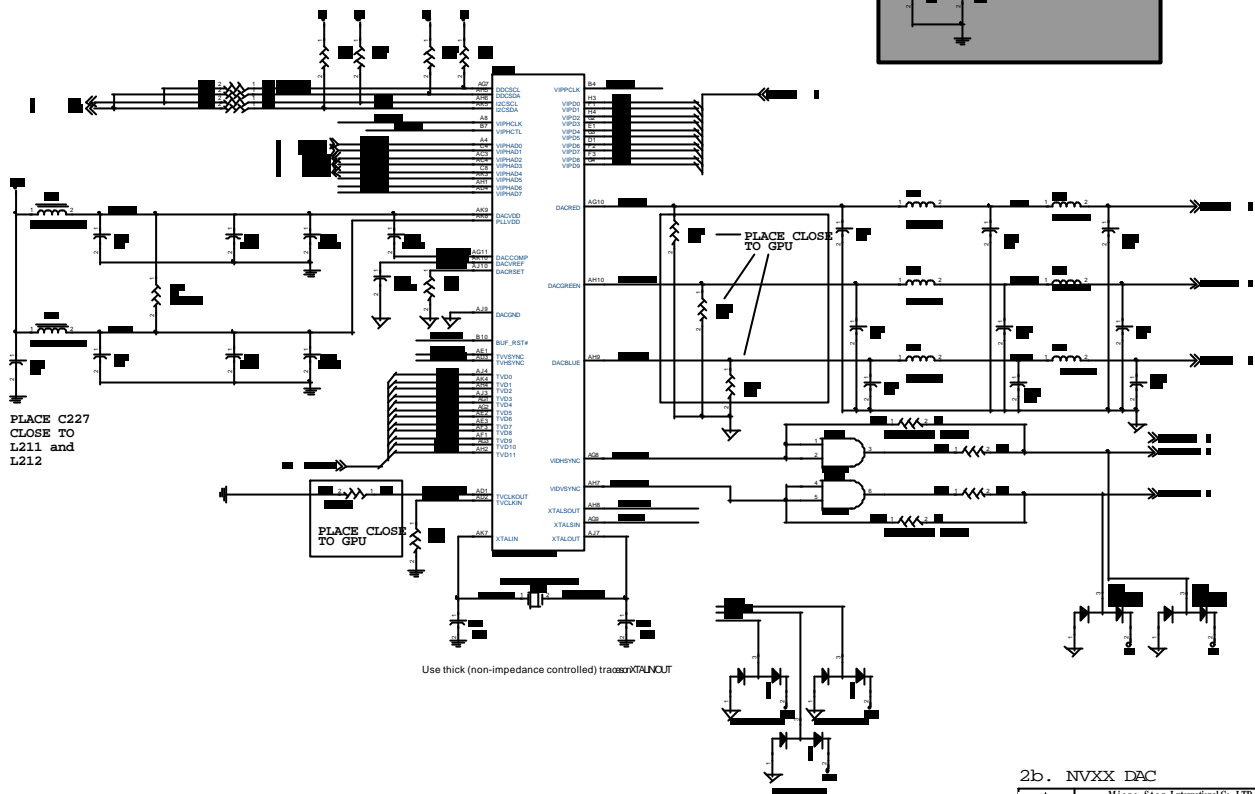
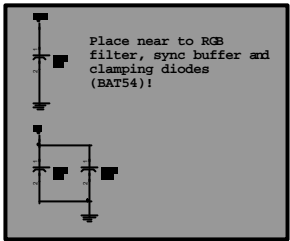
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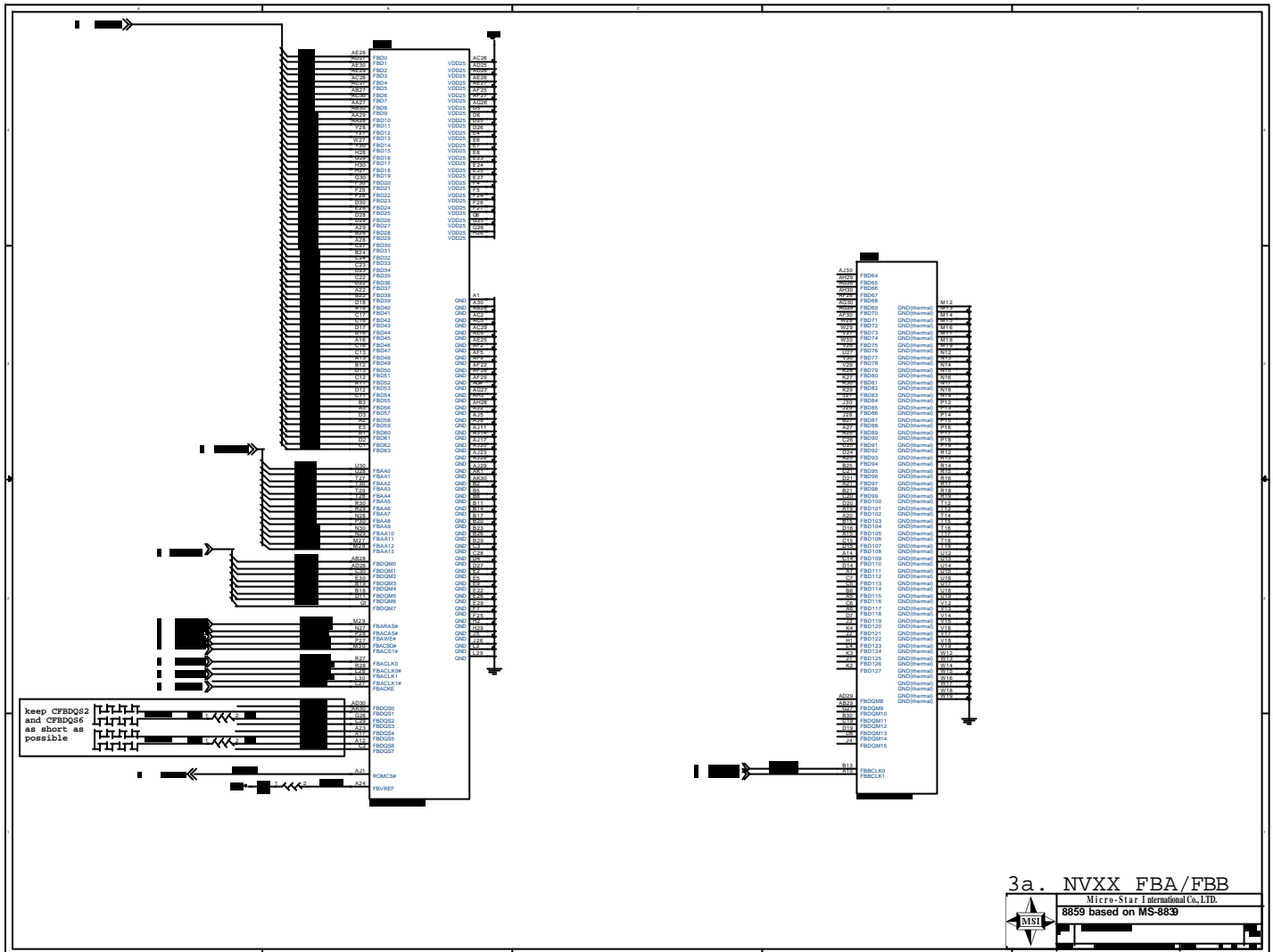


1b1. AGP I/O

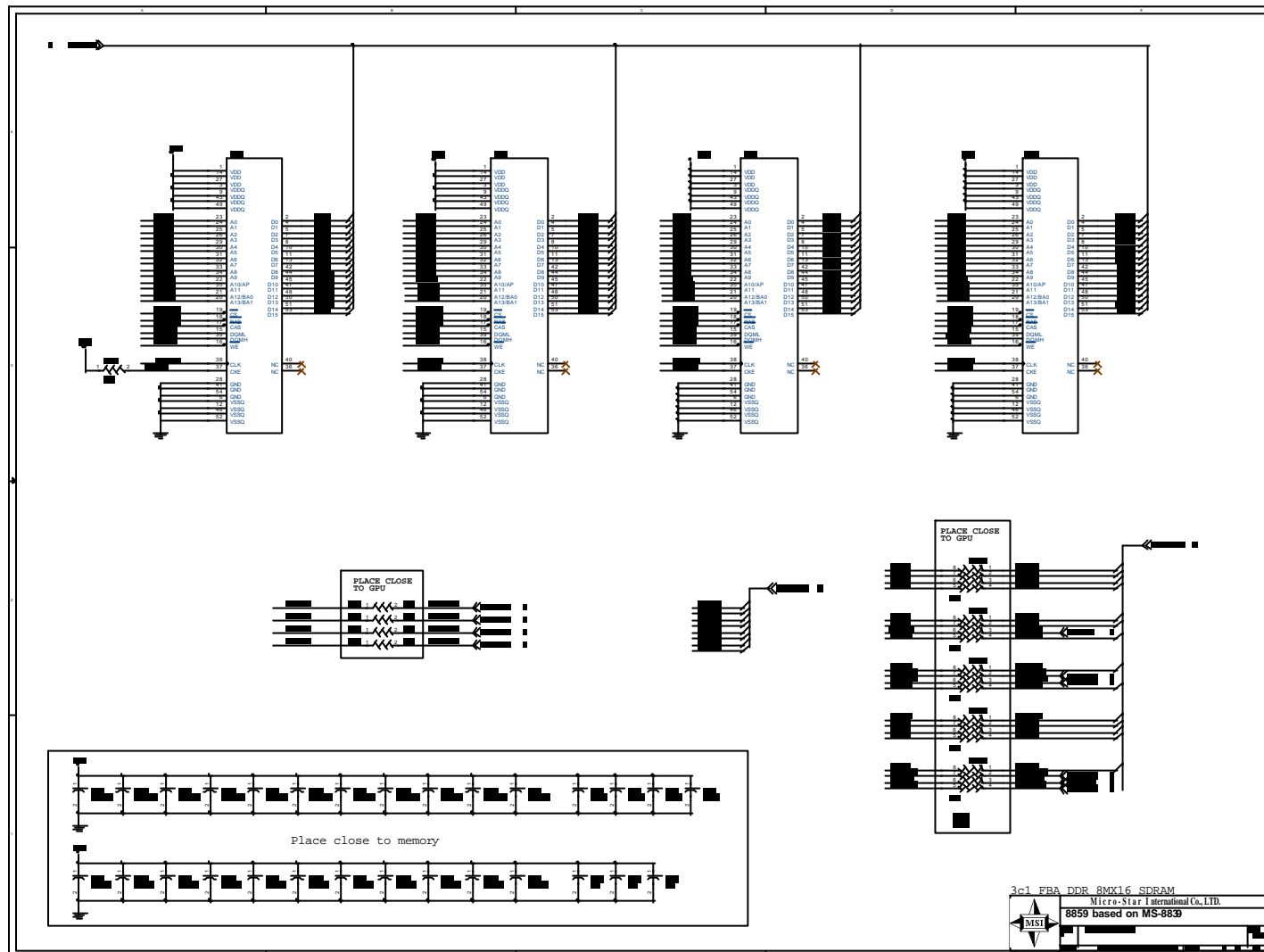
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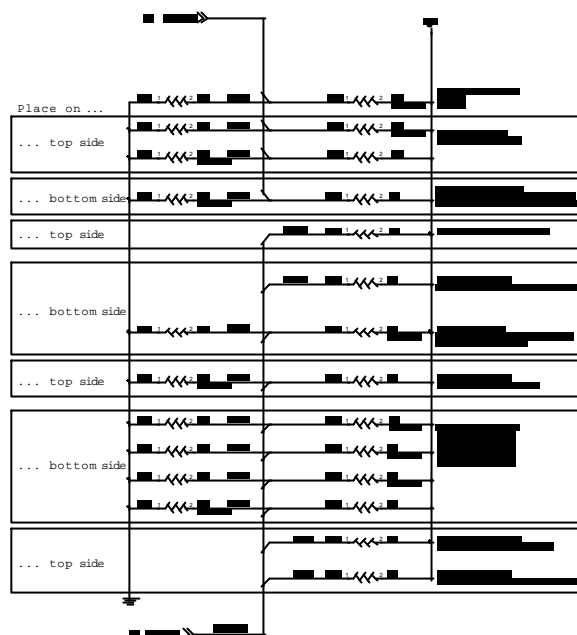
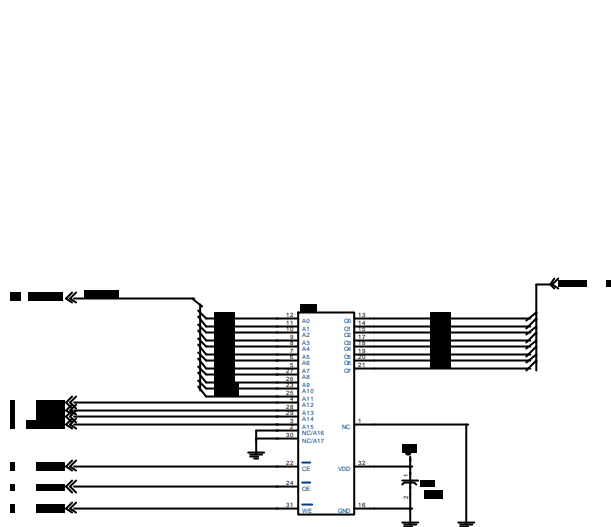


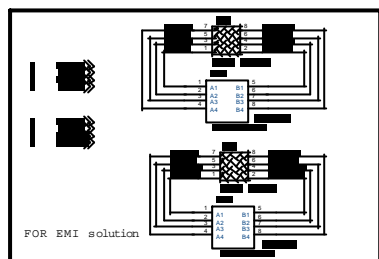




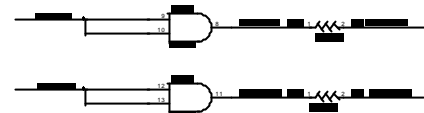
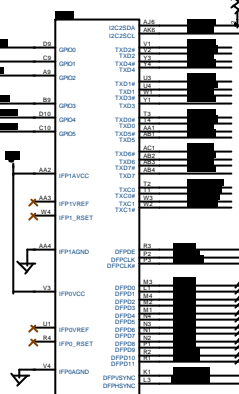
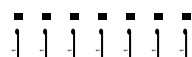
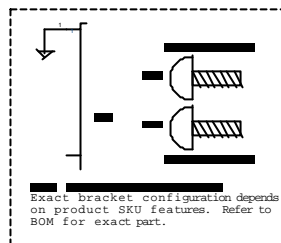
3a. NVXX FBA/FBB  
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FOR EMI solution

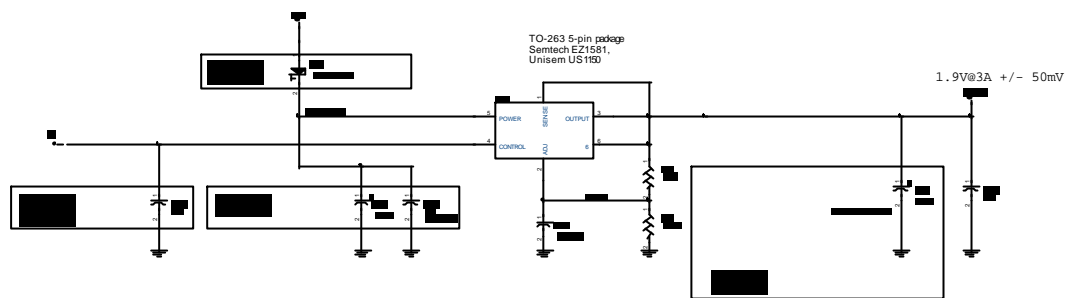


Some SKUs do not include this feature. Refer to BOM for stuffing status.

5a. DVI-I I/O  
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# 6a. POWER SUPPLY

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