

AGP spacing rules

IN	PCIDB31..0.	20MIL
IN	PCICRE3..0.	20MIL
IN	PCICK	20MIL
IN	PCIRIS*	10MIL
IN	PCIREO	10MIL
IN	PCINT	10MIL
IN	PCIFRAME	10MIL
IN	PCIIRADY	10MIL
IN	PCITRDY	10MIL
IN	PCISTOP	10MIL
IN	PCIDEUSEL	10MIL
IN	PCIPAR	10MIL
IN	PCINTA	10MIL
IN	PCINTB	10MIL
IN	AGPBB	10MIL
IN	AGPBHI	20MIL
IN	AGPBHLO	20MIL
IN	AGPS70	15MIL
IN	AGPS11	15MIL
IN	AGPS12	15MIL
IN	AGPAUSTHOI	25MIL
IN	AGPAUSTWOS	25MIL
IN	AGPAUSTBLS	25MIL
IN	AGPSBSTF	25MIL
IN	AGPSBSTF	25MIL
IN	AGPSBSTF	25MIL
IN	AGPSBA7..0.	20MIL
IN	AGPURECG	10MIL
IN	AGPURETC	10MIL
IN	AGPBHE T	10MIL
IN	AGPSOT	10MIL



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DETAIL	DRAWING DETAIL
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NAME	BLOTTER	DATE	SEP 03 2

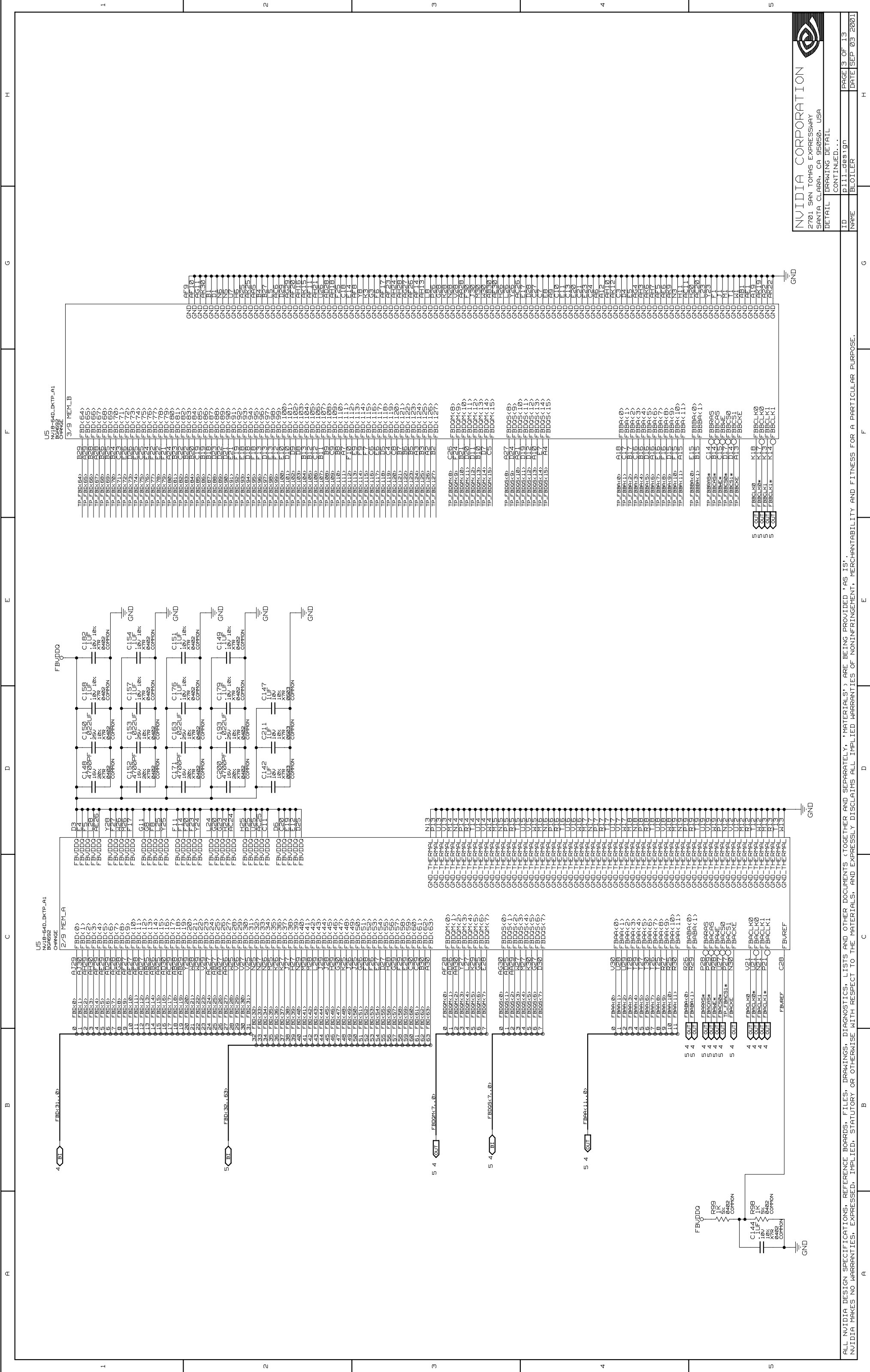
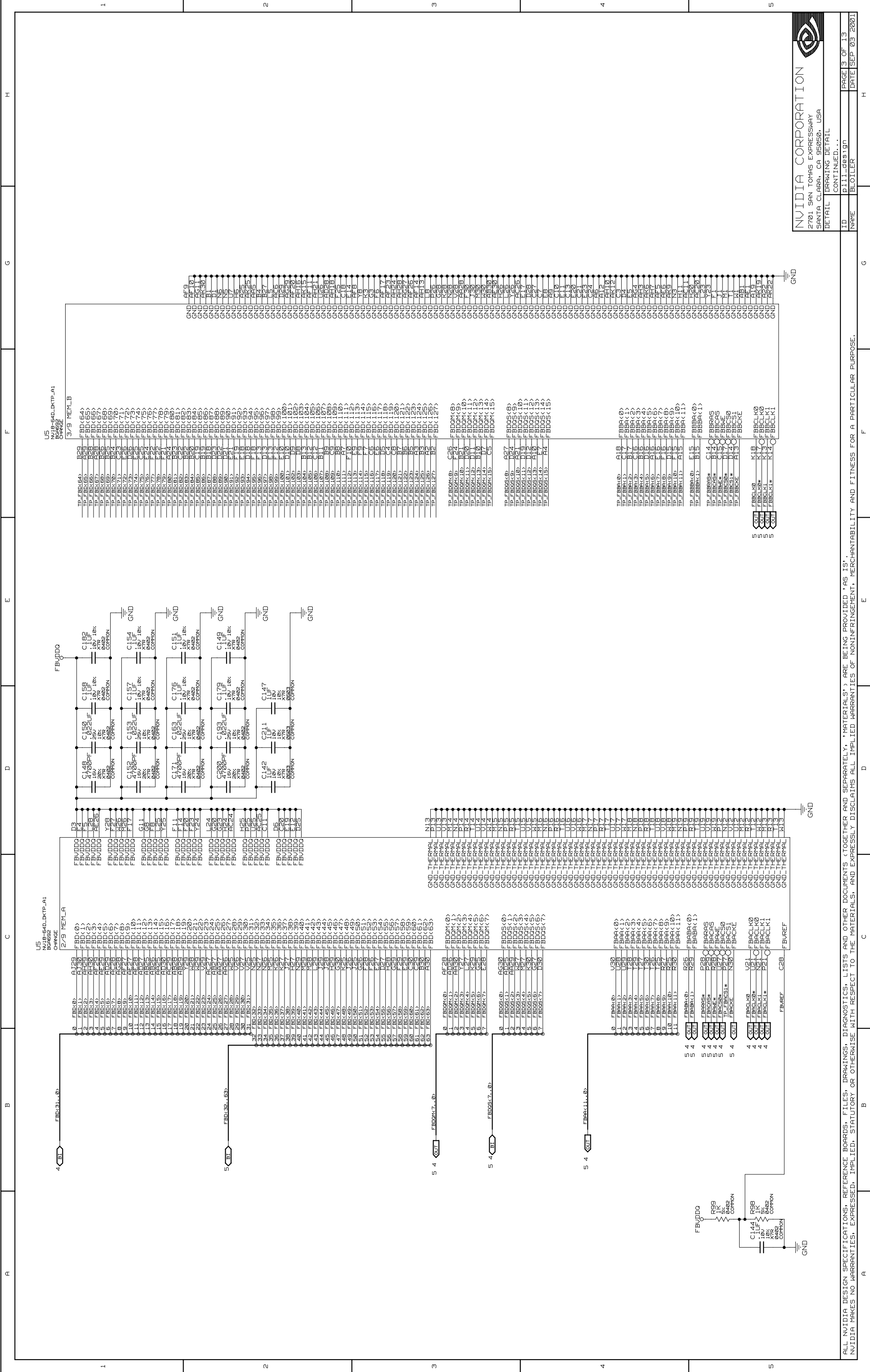
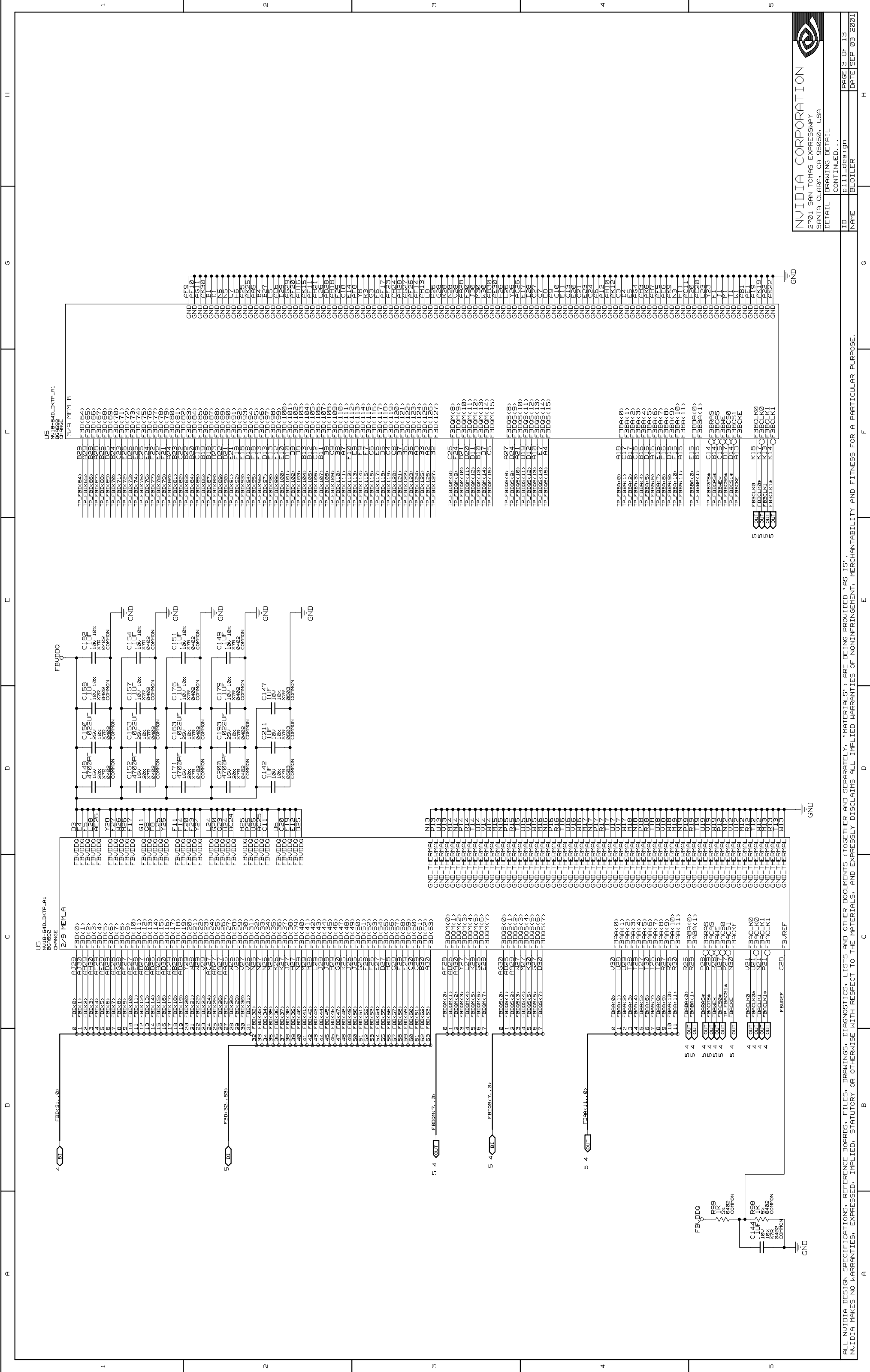
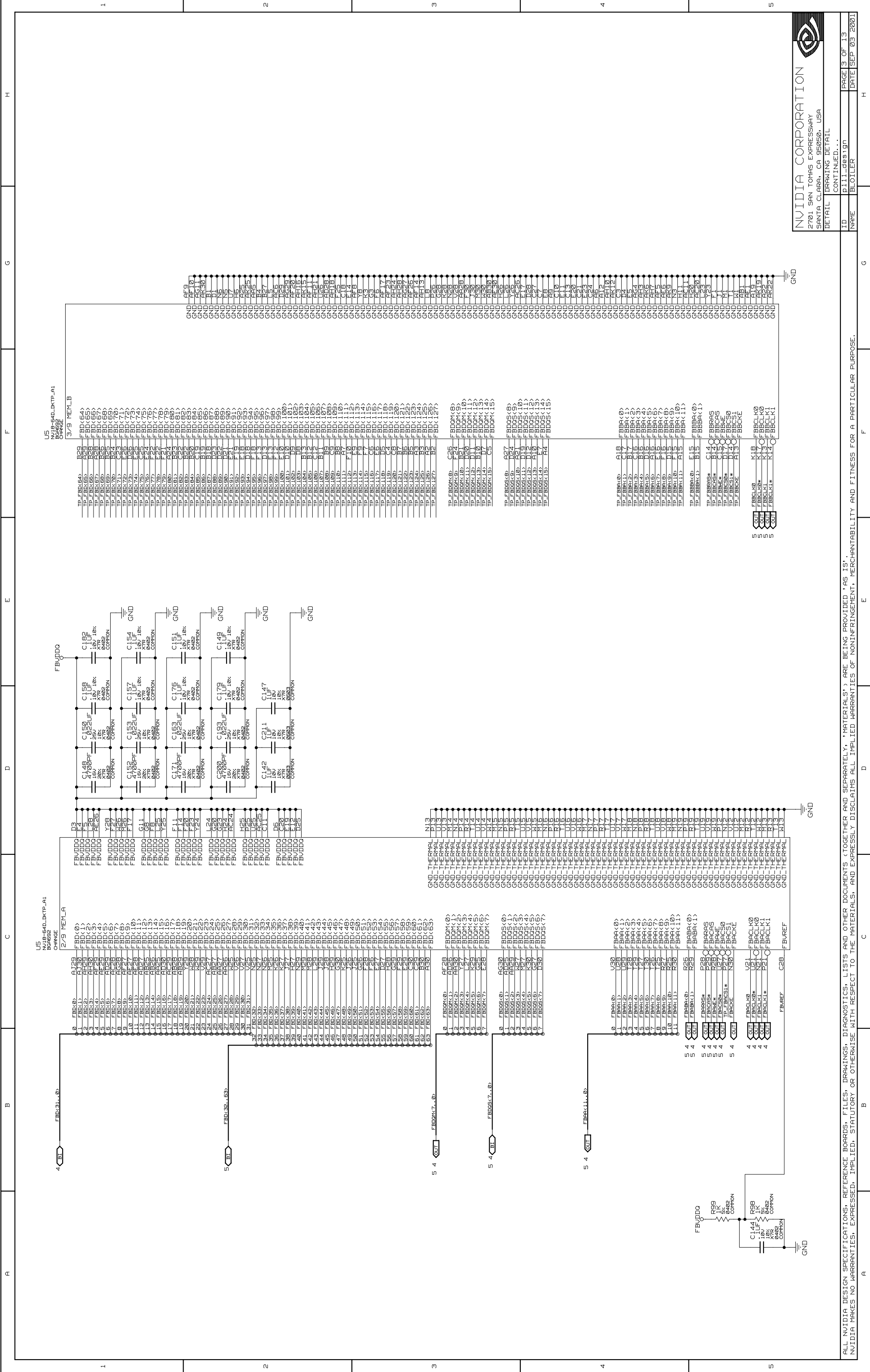
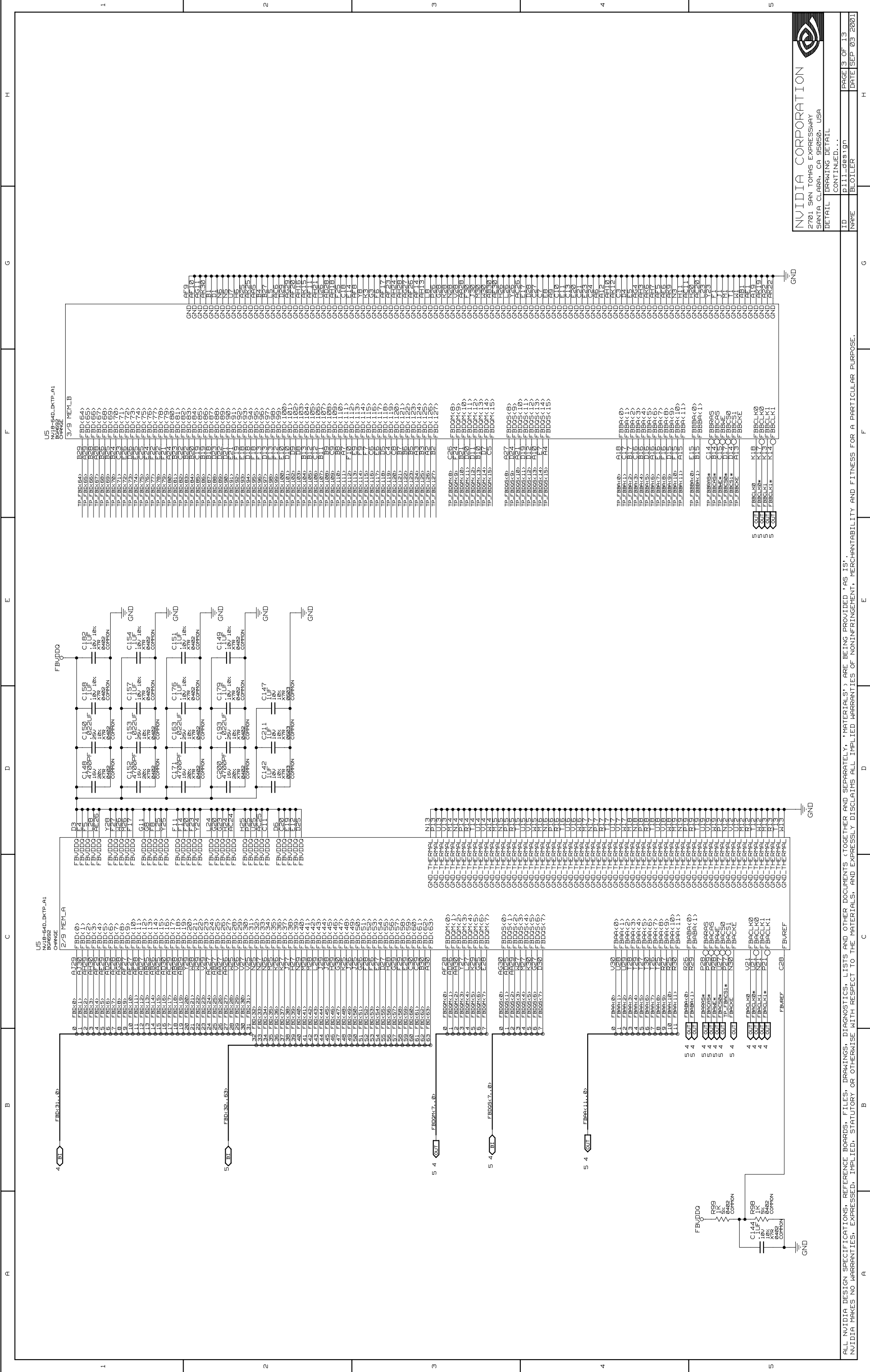
[illegible]

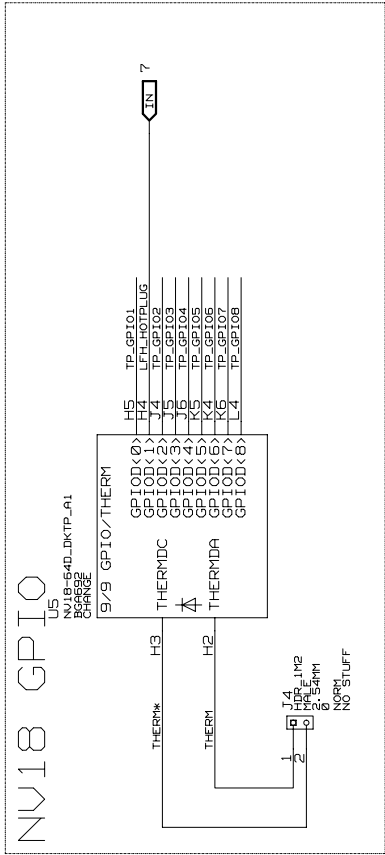
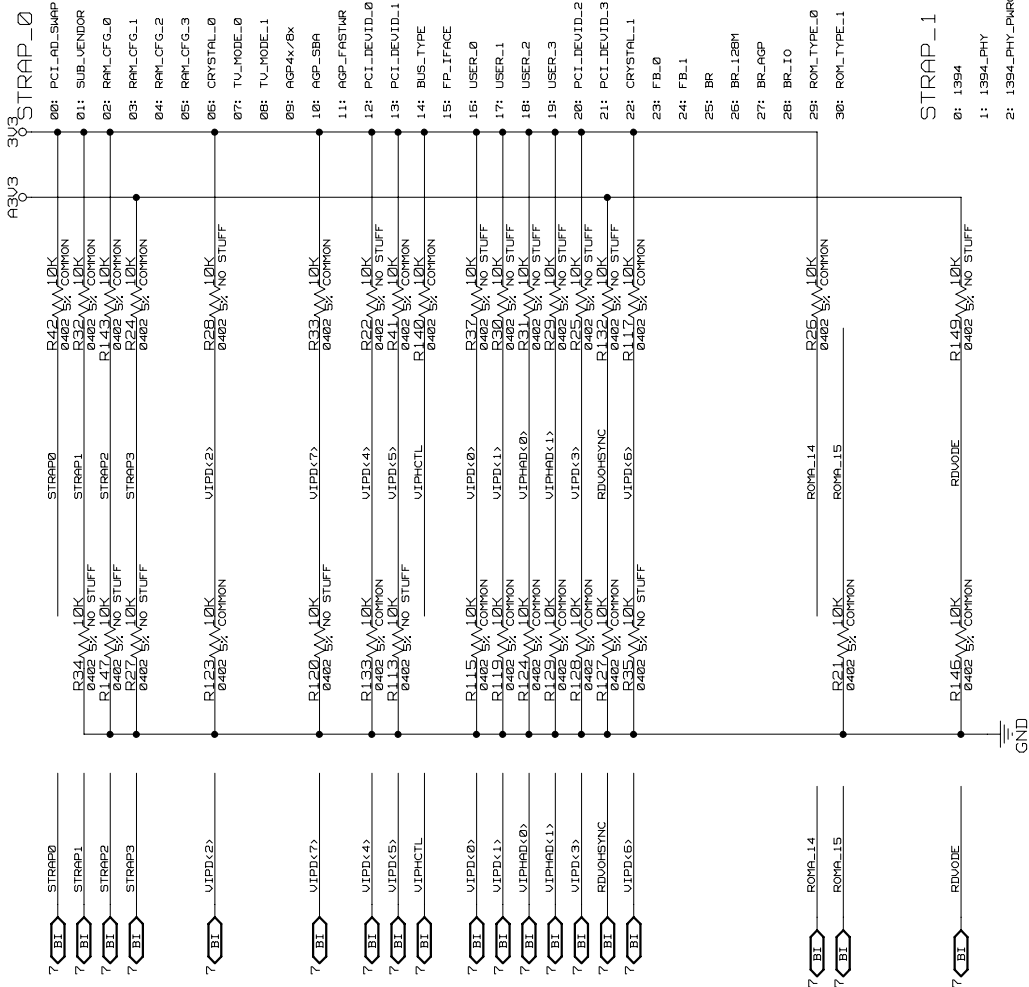
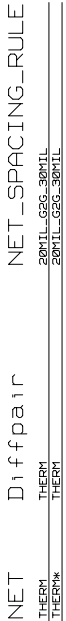
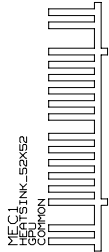
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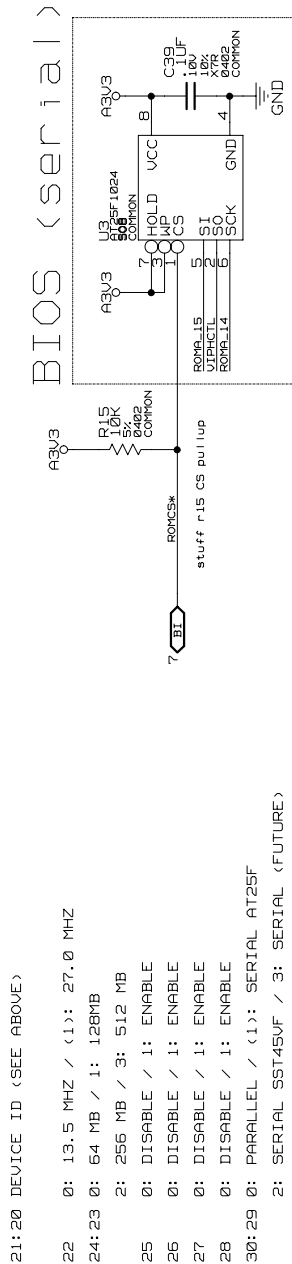
U	U	U	U	U	U
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L
J
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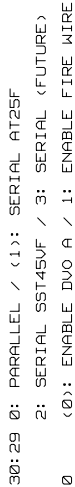


10mil spacing with 10 mil thick traces

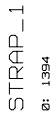


PCI DevID Chart

5000-10111-0000-000	=	0x182	=	0010	=	NV18-64D	=	NV18-64D	=	VGA0
5000-50111-0000-000	=	0x18B	=	1011	=	NV18-GL-V-A1	=	NV18-GL-V-A1	=	LEH0
5000-50111-0001-000	=	0x18A	=	1010	=	NV18-L-A1	=	NV18-L-A1	=	LEH1

$$NV18 = 0 \times 18?$$


0: DISABLE / 1: ENABLE



```
1: 1394_PHY
2: 1394_PHY_PWRCLASS[0]
3: 1394_PHY_PWRCLASS[1]
4: UGA_DEVICE
5: MEM_LSB_SWAP
6: BR_LAST_DEV
7: BR_BAR1_BOASTONLY
```

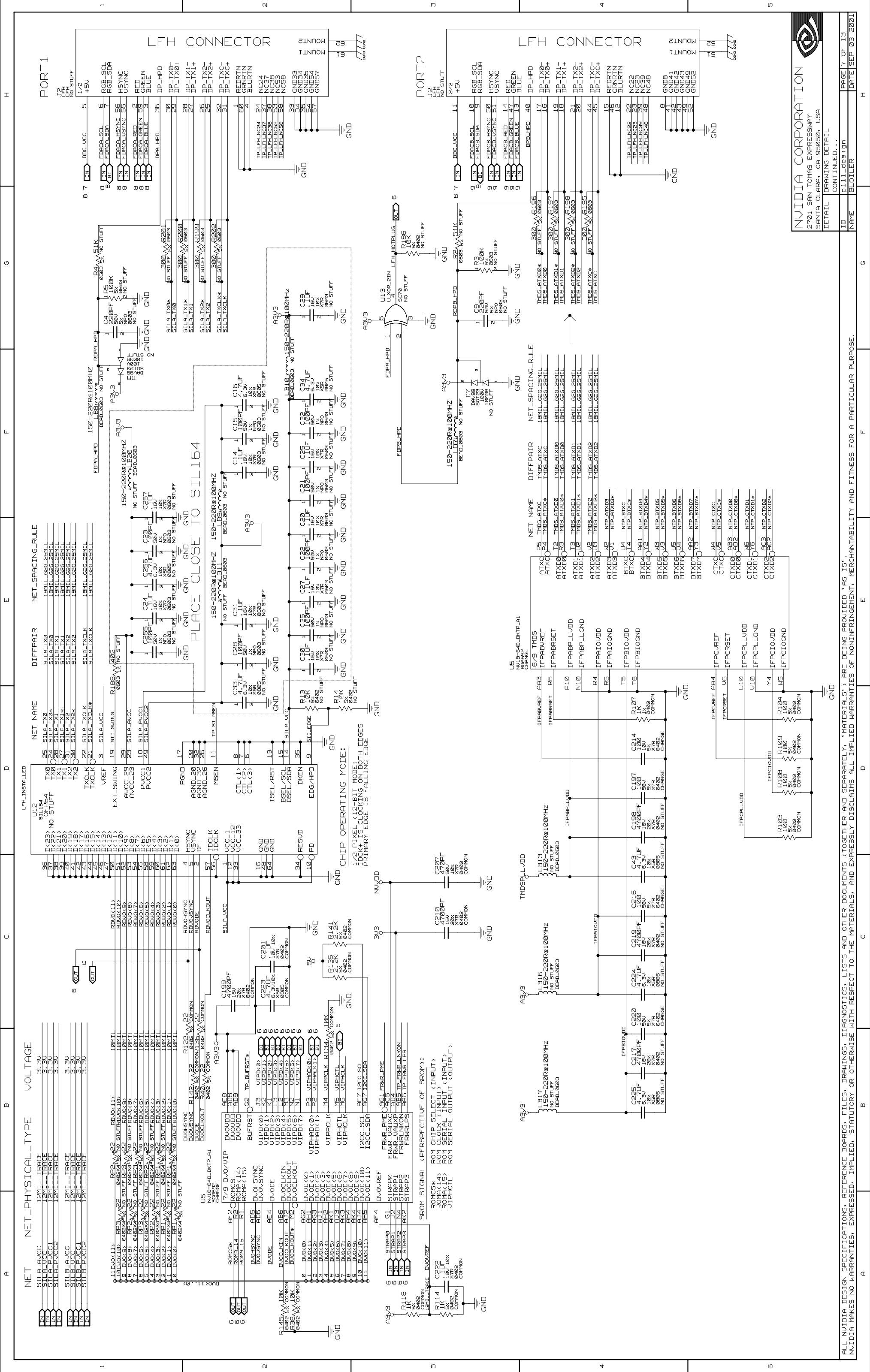


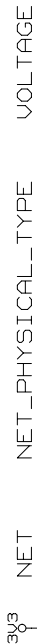
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DETAIL	DRAWING DETAIL
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NAME	BLOILER	DATE	SEP 03 2001

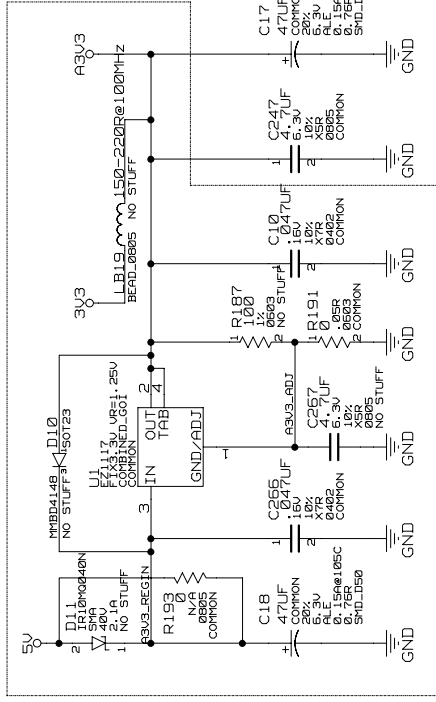
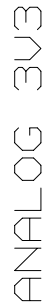
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NET_PHYSICAL_TYPE

VOLTAGE

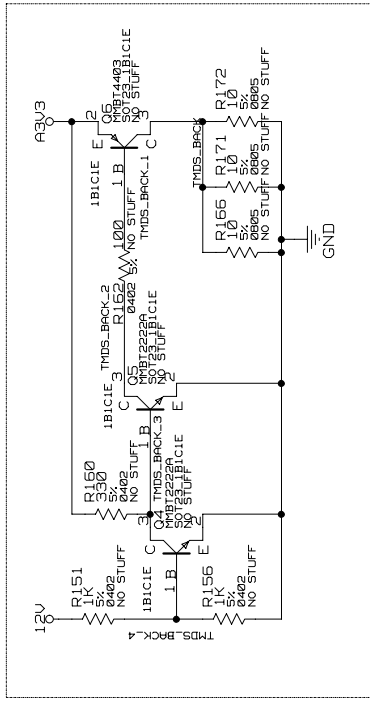
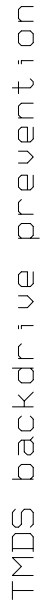


ASSEMBLY=A3V3_REG

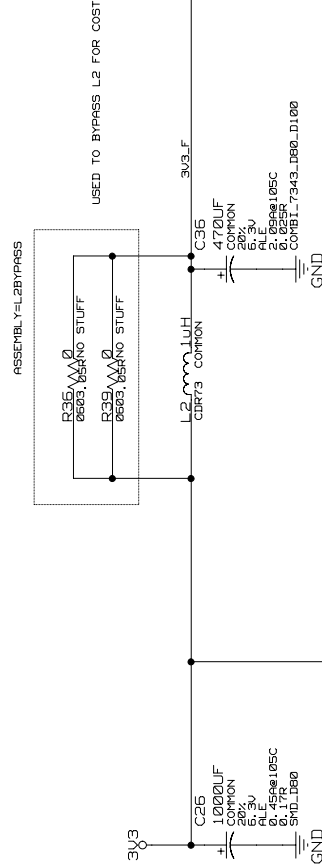
$$V_{out} = V_{REF} \left(1 + \left(\frac{R586}{R588} \right) + \left(\frac{I_{adj}}{I_{adj}} \right) \times \left(\frac{R586}{R588} \right) \right)$$

$$3.28 = 1.25 \left(1 + \left(\frac{162}{100} \right) + \left(\frac{55\mu A}{55\mu A} \right) \times \left(\frac{162}{100} \right) \right)$$

DEFAULT CONFIGURATION IS 3.3V FIXED, NO VOUT FORMULA NEEDED

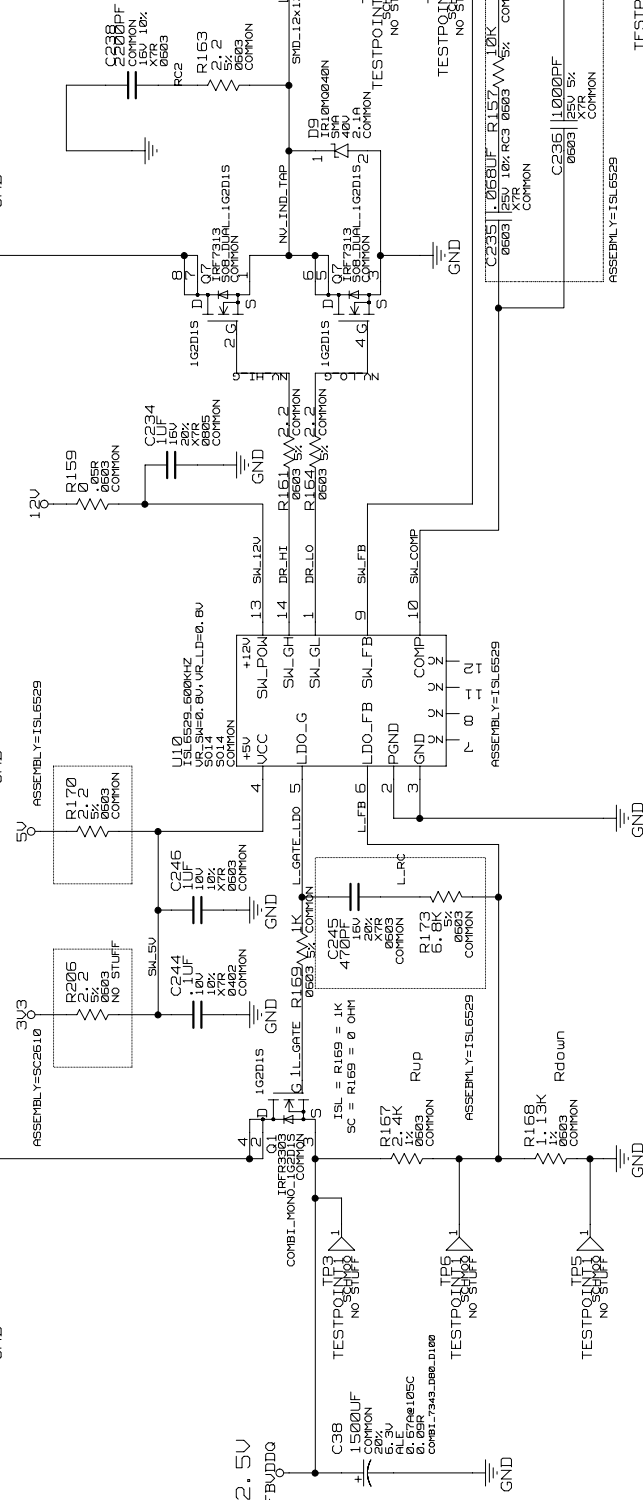
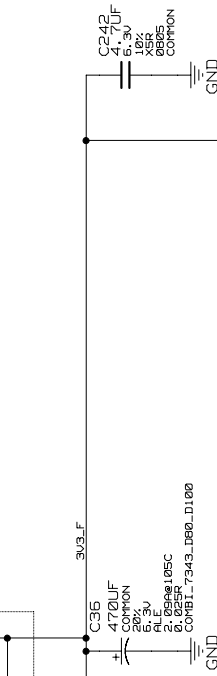


ASSEMBLY=TMDS_BACKDRIVE



ASSEMBLY=L2BYPASS

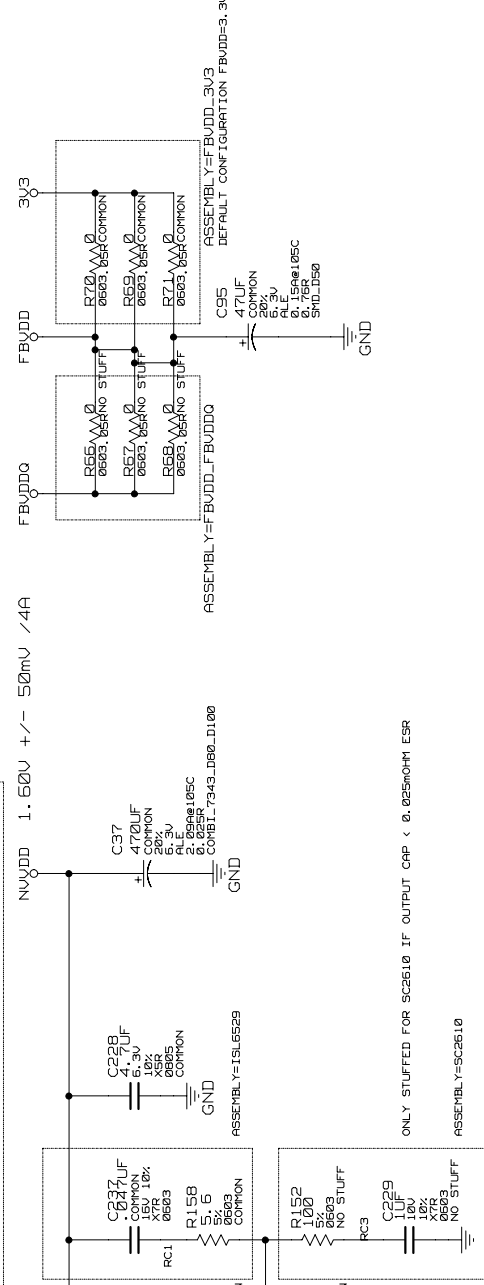
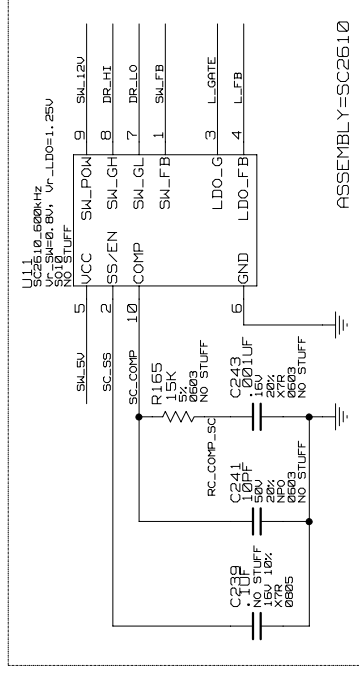
USED TO BYPASS L2 FOR COST



$$FBVDD = V_{Ref} * (1 + R_{up} / R_{down})$$

ISL6529 $2.5V = 0.8V * (1 + (2.4k / 1.13k))$

SC2610 2.5V = 1.25V * (1 + 100 / 100)



ONLY STUFFED FOR SC2610 IF OUTPUT CAP < 0.025mOHM ESR

ASSEMBLY=SC2610

ISL6529 and SC2610 uses same VREF 0.8V on PWM (switcher)

$$R140 = (R139 \times 0.8) / (vout - 0.8)$$

$$V_{OUT} = 0.8 * (1 + (R139 / R1$$

$$1.65 = 0.8 * (1 + (1k / 1k))$$



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