# P2011-A01

## GF108/GK107 128-bit DDR3 256Mx16
## DVI-I DL + DVI-D/HDMI + mHDMI/DP

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2012/02/14 1. P3 add R220 10Kohm
2. P9 add IFPB_TXDn RC and U13
3. P10 add IFPC_AUX protection diodes
4. P11 add IFPC_AUX protection diodes
5. P12 add IFPC_AUX protection diodes
6. P16 FAN to 4pin3
7. P19 change MOS & L7
8. U7 & U8 change to 8120A/B
9. P17 change L9
10. P18 CHANGE L6
11.P16 新增光學點~阻抗測試線


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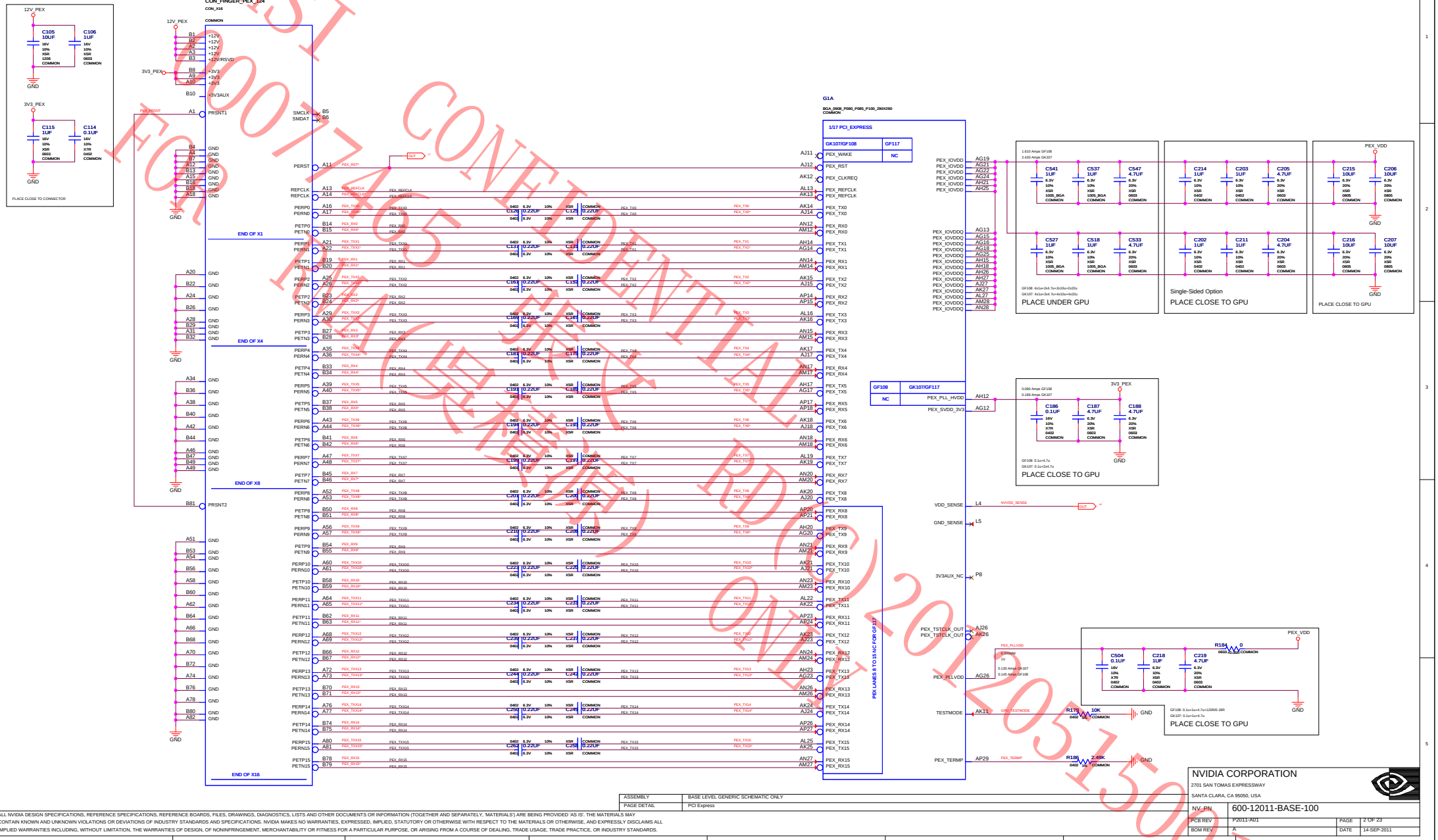
# PCI Express

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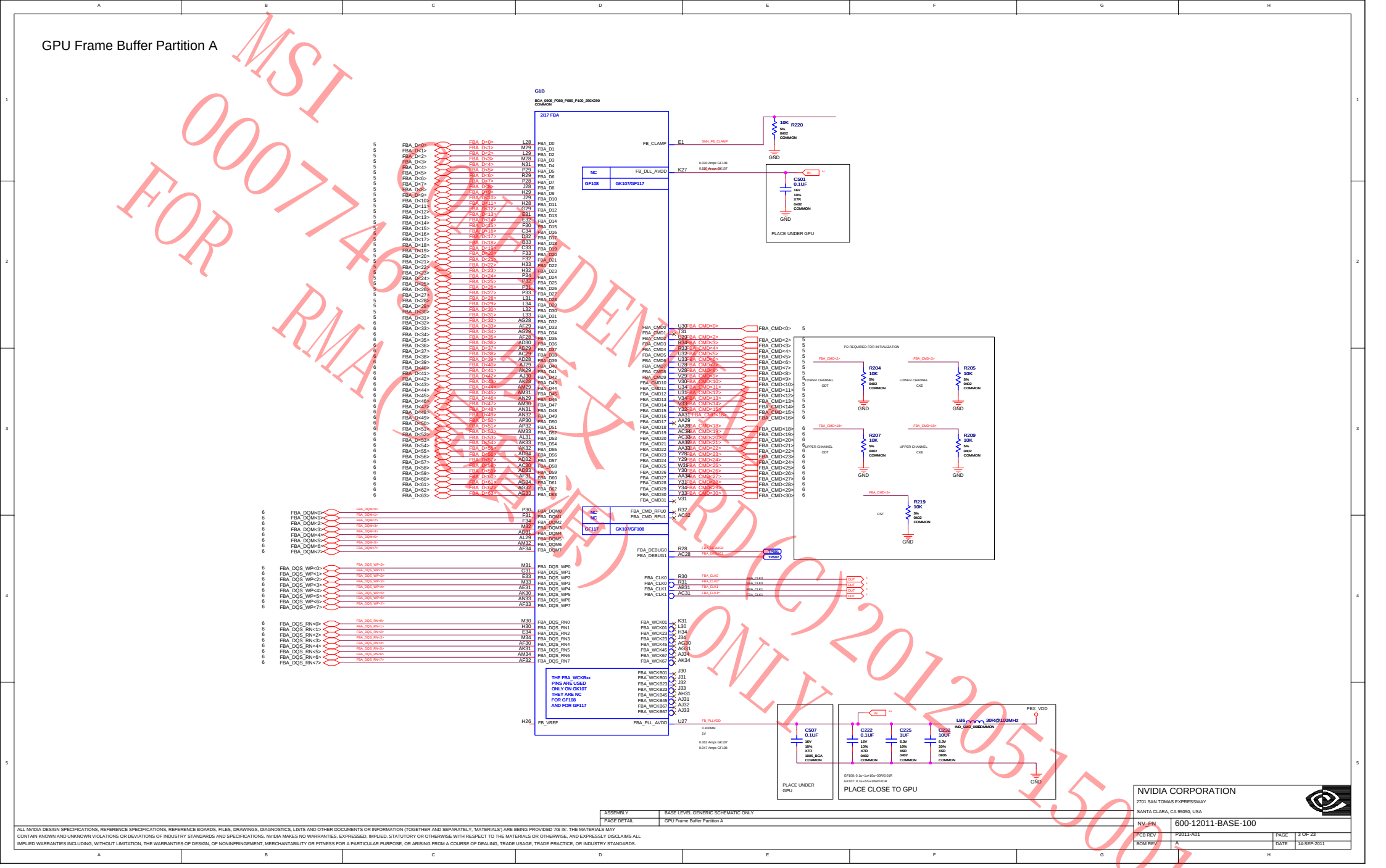
# GPU Frame Buffer Partition A

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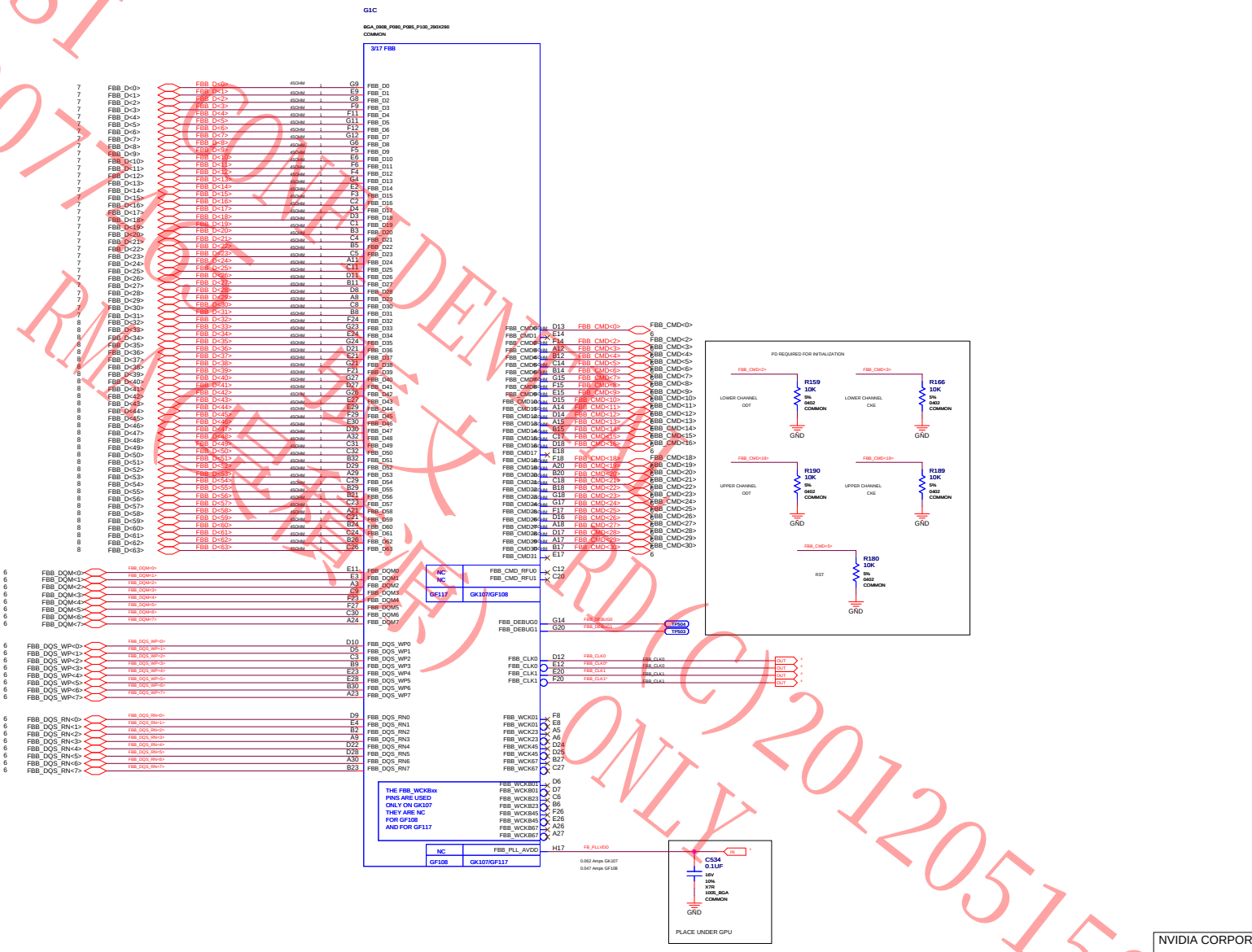
GPU Frame Buffer Partition B

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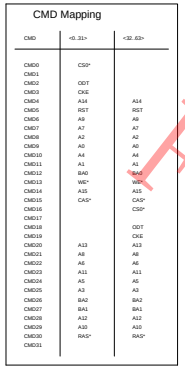
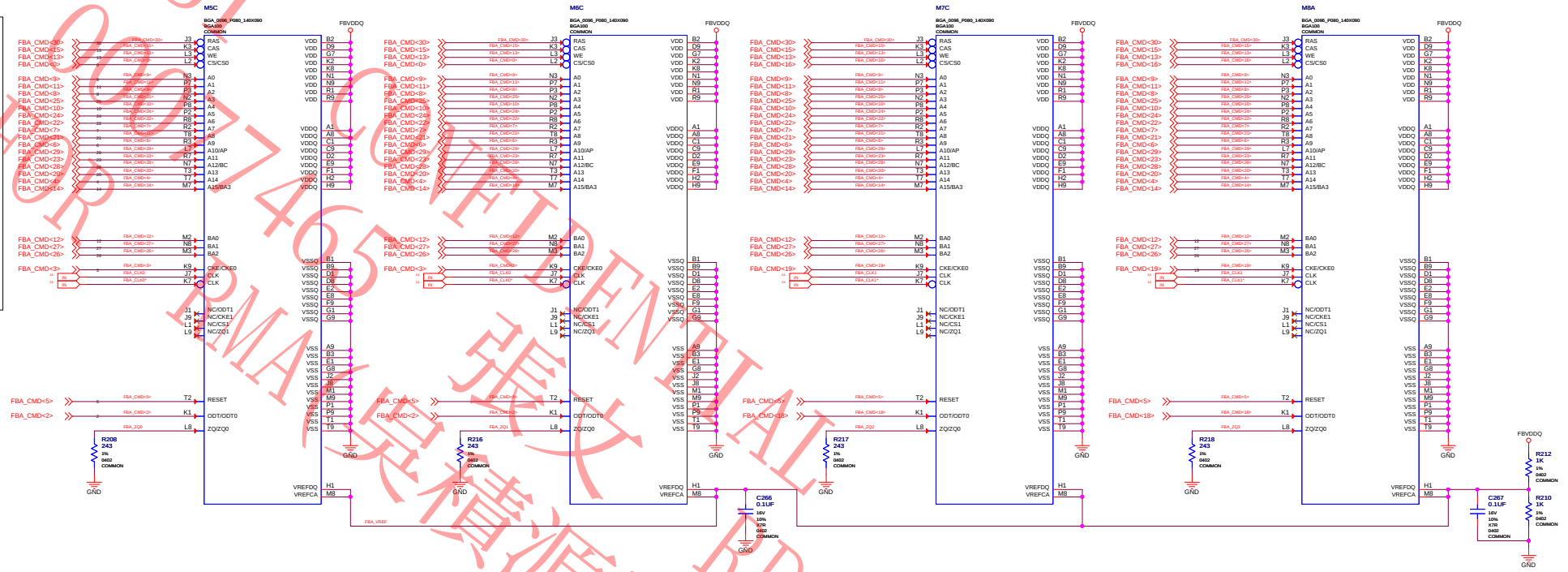
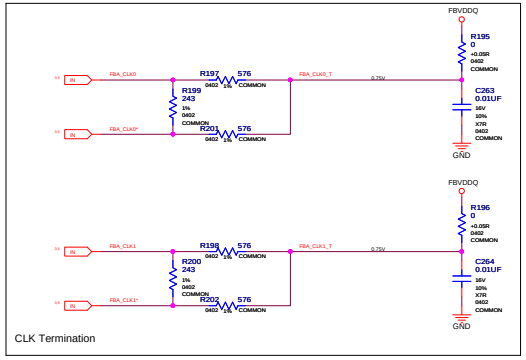
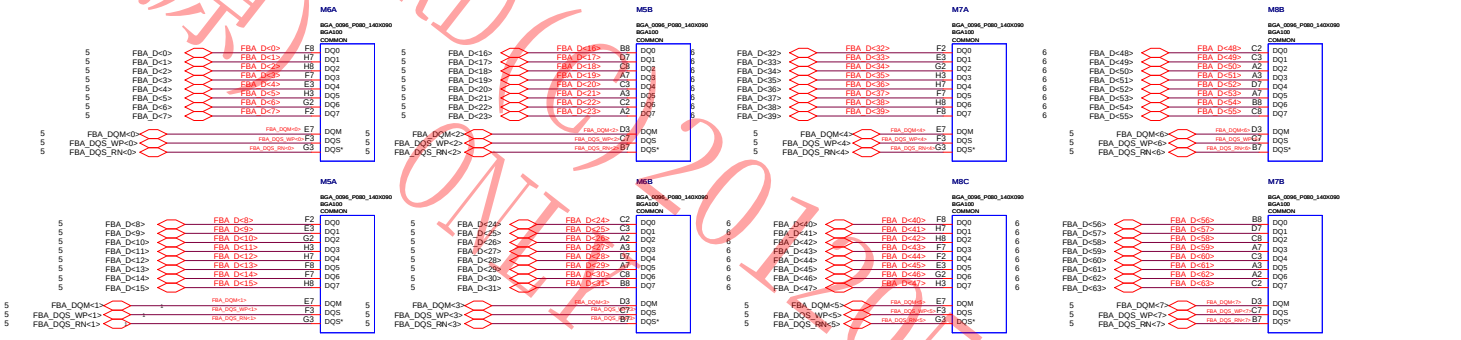
# FBA Partition

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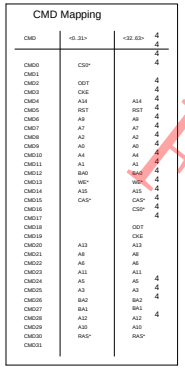
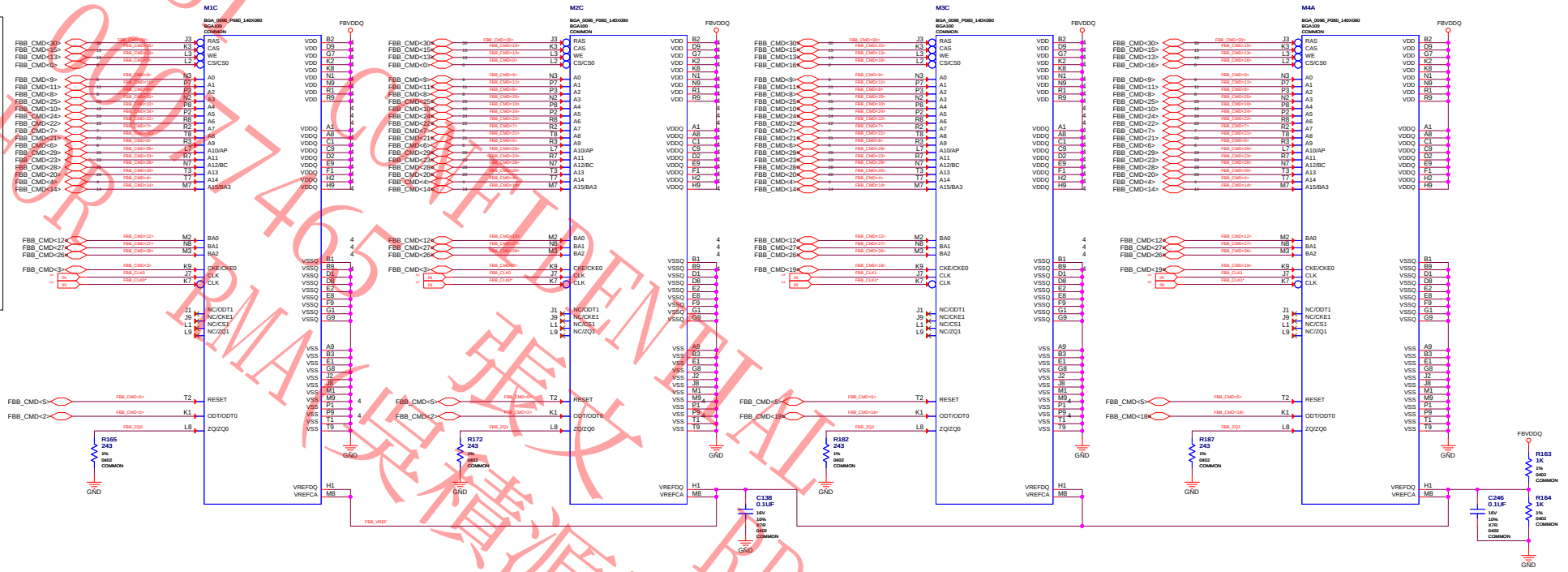
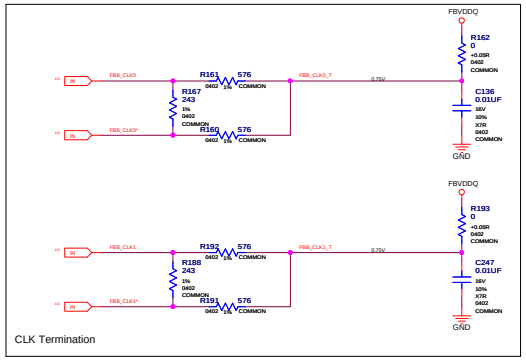
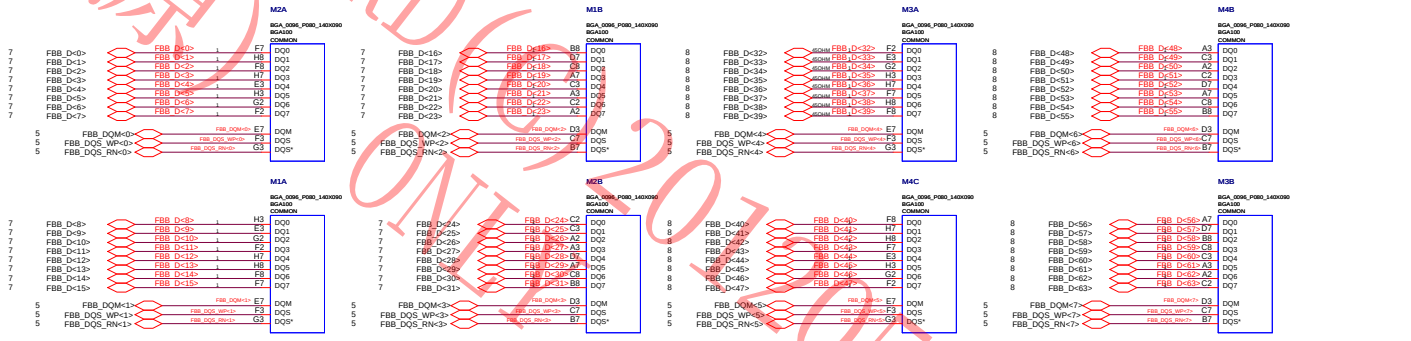
# FBB Partition

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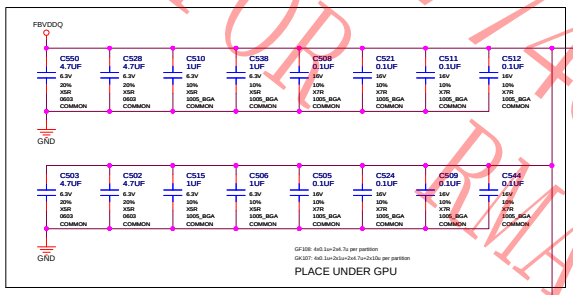
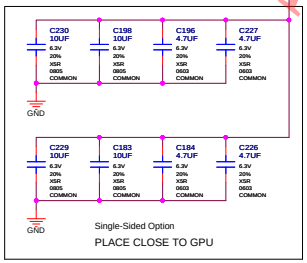
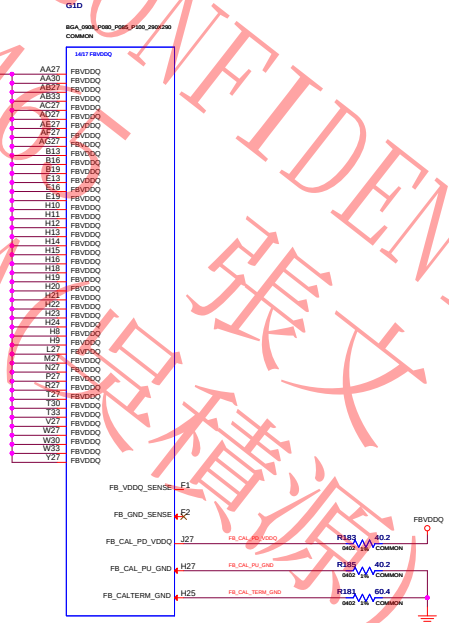
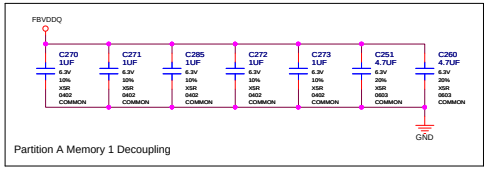
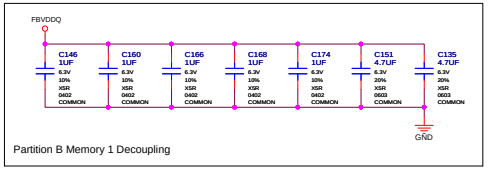
# FB Decoupling

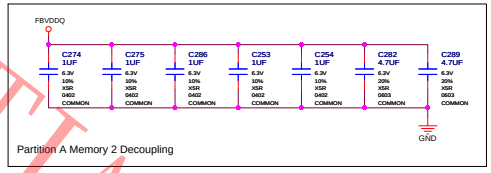


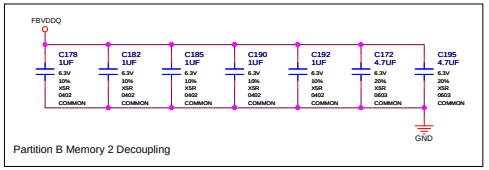



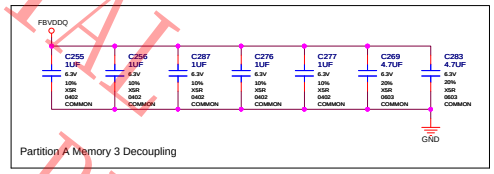



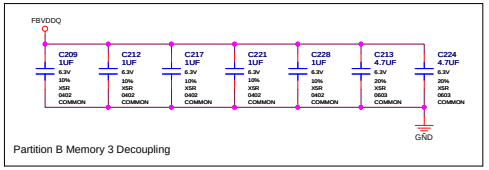



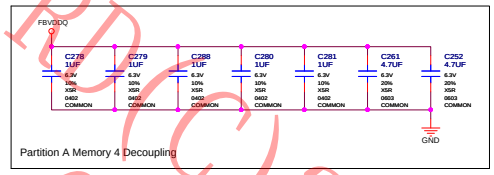



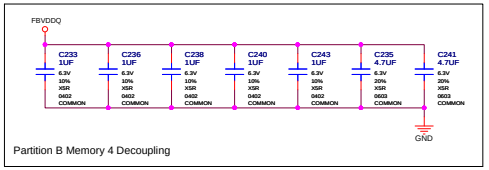

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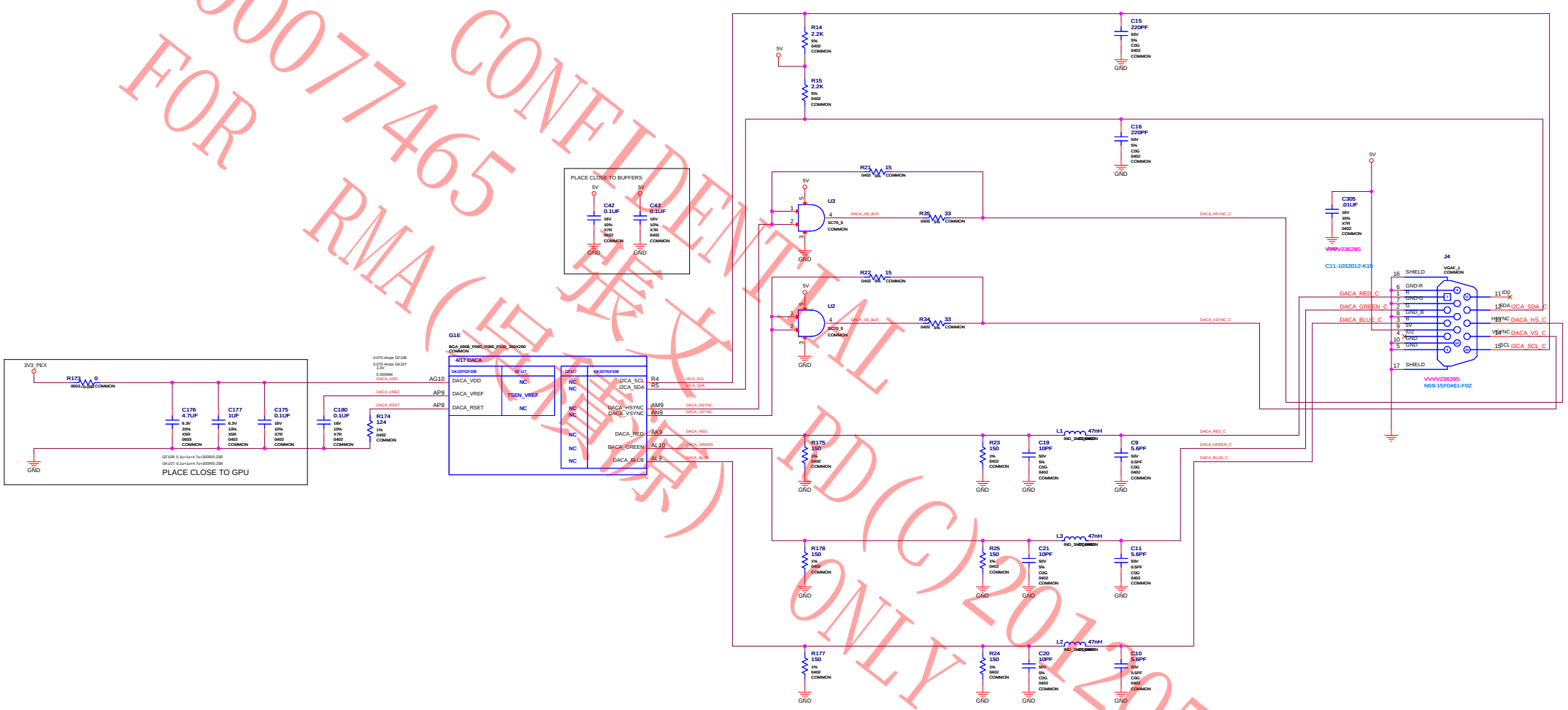
# DACA VGA

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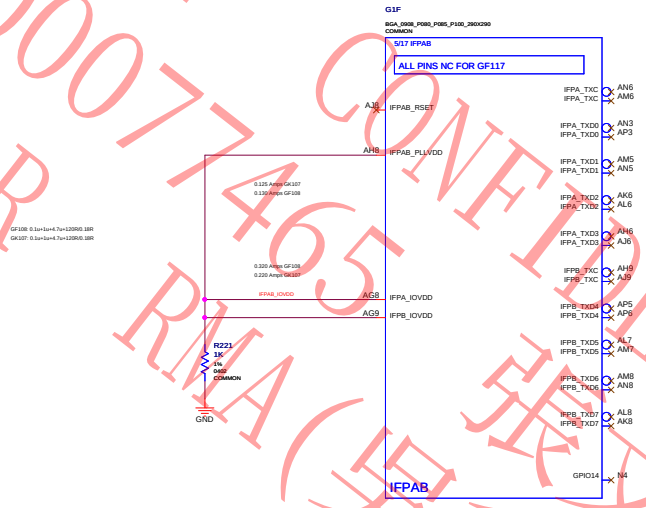
# IFPAB TMDS

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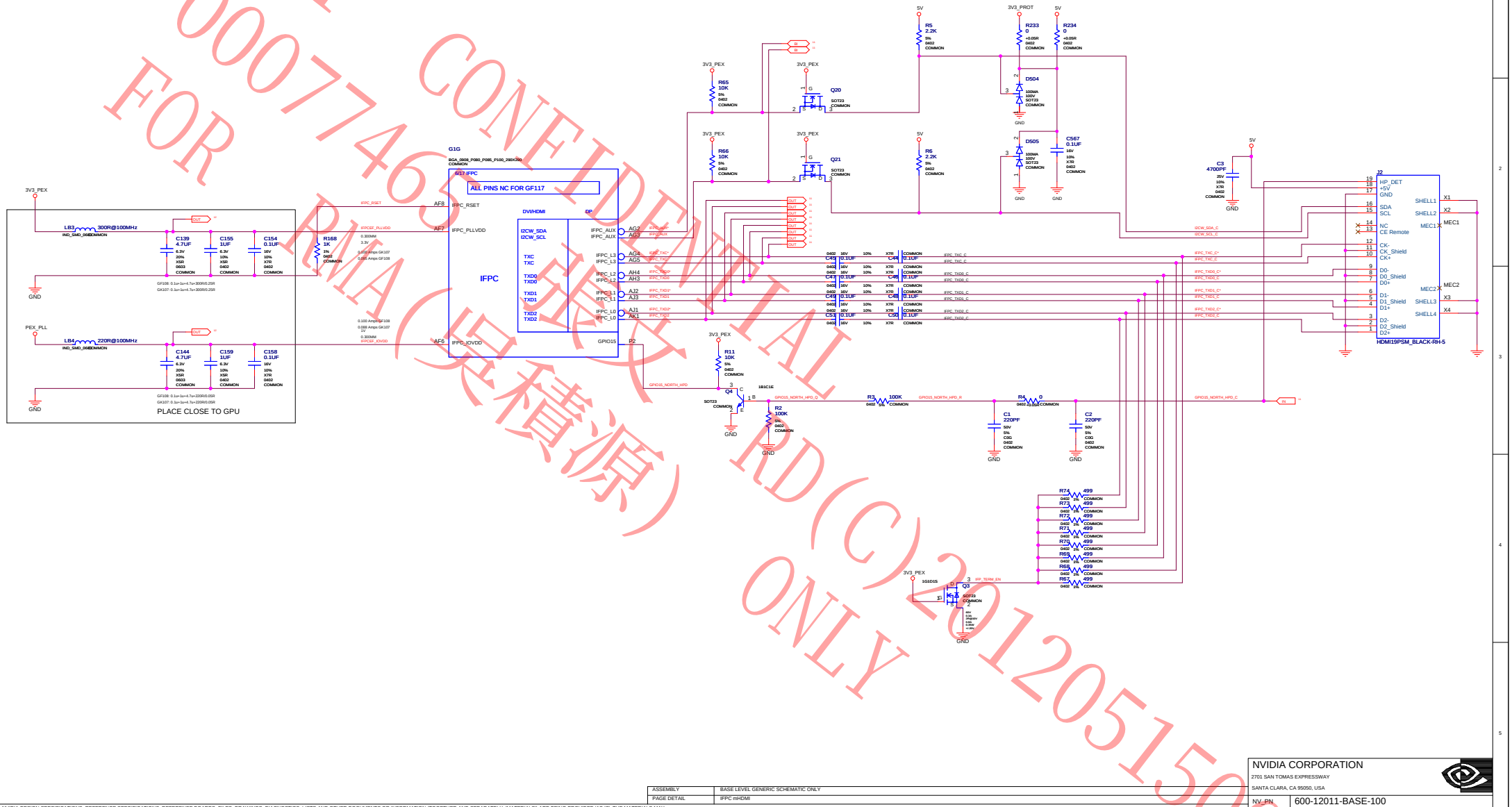
# IFPC mHDMI

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IFPC DP

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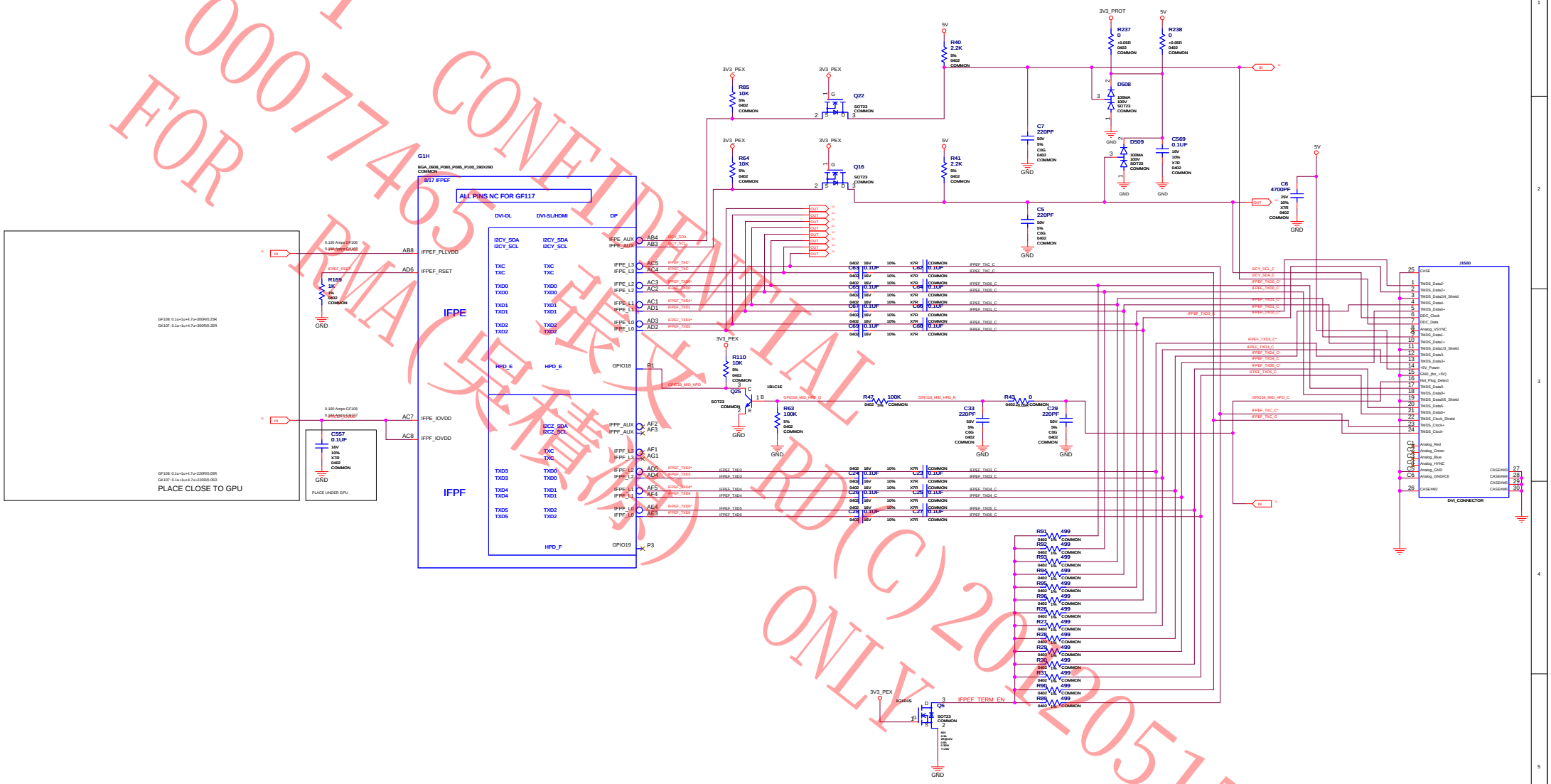
| BOM REV | A | DATE | 14-SEP-2011 |

# IFPEF TMDS

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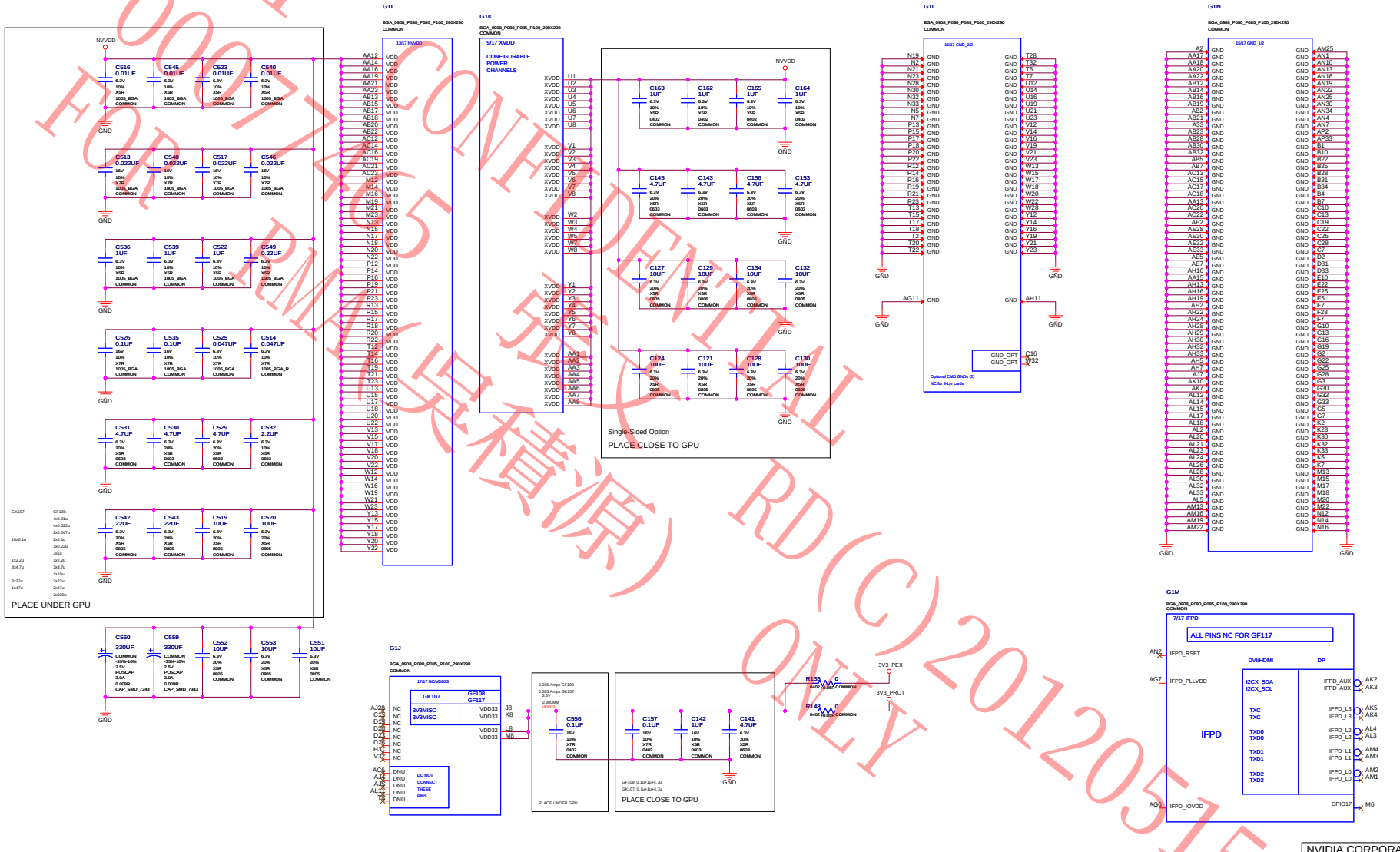
# Core Decoupling

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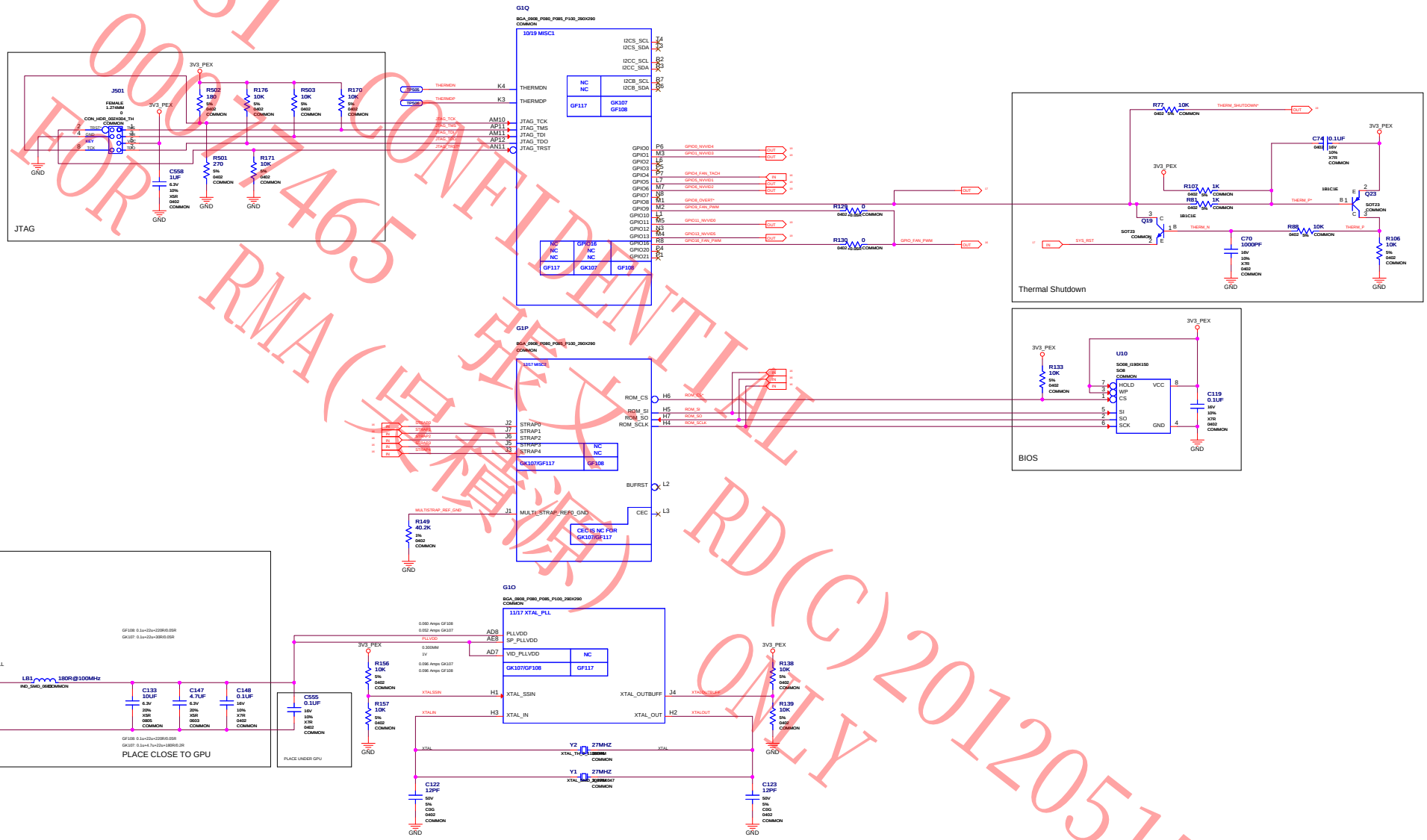
# Misc Interfaces

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# Straps, Thermal, Mechanical

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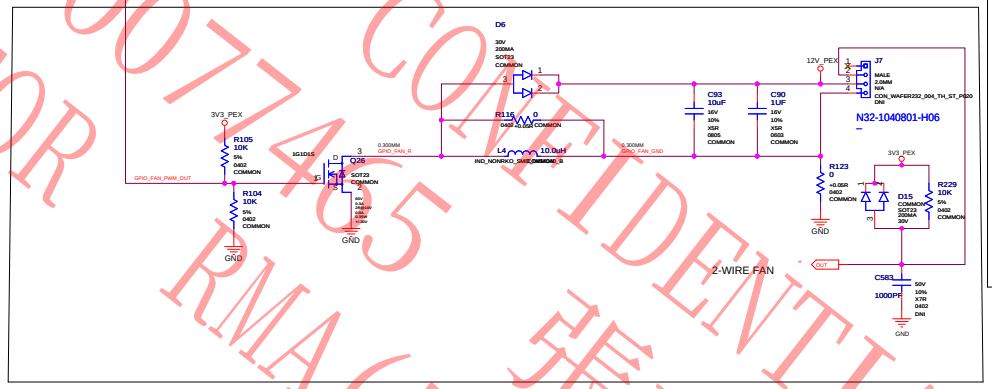


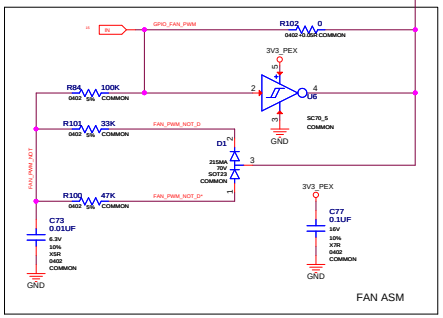


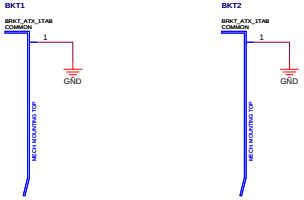


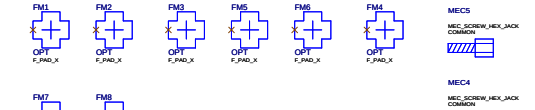


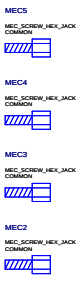


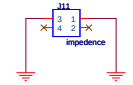


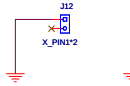


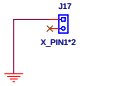


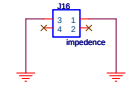


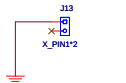


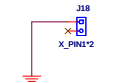

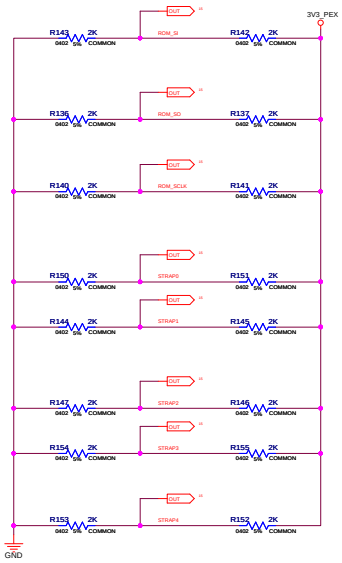


## GK107 Straps

| Bit | Signal | Values | | | |
|---|---|---|---|---|---|
| 03: | RAMCFG[3] | 0000 | 64Mx16 128bit | 1000 | 64Mx16 64bit |
| | | 0001 | 64Mx16 128bit Micron | 1001 | 64Mx16 64bit Micron |
| 02: | RAMCFG[2] | 0010 | 64Mx16 128bit Hynix | 1010 | 64Mx16 64bit Hynix |
| | | 0011 | 64Mx16 128bit Samsung | 1011 | 64Mx16 64bit Samsung |
| 01: | RAMCFG[1] | 0100 | 128Mx16 128bit | 1100 | 128Mx16 64bit |
| | | 0101 | 128Mx16 128bit Micron | 1101 | 128Mx16 64bit Micron |
| 00: | RAMCFG[0] | 0110 | 128Mx16 128bit Hynix | 1110 | 128Mx16 64bit Hynix |
| | | 0111 | 128Mx16 128bit Samsung | 1111 | 128Mx16 64bit Samsung |
| 03: | FB[1] | 01 | 128MB | | |
| 02: | FB[0] | | | | |
| 01: | SMB_ALT_ADDR | 0 | DISABLED | 1 | ENABLED |
| 00: | VGA_DEVICE | 0 | DISABLED | 1 | ENABLED |
| 03: | PCI_DEVID_EXT | 0 | GK107-200-A1 | 0 | GK107-300-A1 |
| | | 0 | GK107-300-A1 | 0 | GK107-400-A1 |
| 02: | SUB_VENDOR | 0 | NO_BIOS | 1 | BIOS |
| 01: | PCI_DEVID_EXT_BITS | 0 | GK107-200-A1 | 0 | GK107-320-A1 |
| | | 0 | GK107-300-A1 | 0 | GK107-400-A1 |
| 00: | PEX_PLL_EN_TERM100 | 0 | DIS | 1 | EN |
| 03: | USER[3] | 0000 | INIT | | |
| 02: | USER[2] | | | | |
| 01: | USER[1] | | | | |
| 00: | USER[0] | | | | |
| 03: | 3GIO_PADCFG_LUT_ADR[3] | 0000 | DSKTOP_DEFAULT | 1000 | DSKTOP_HTHRS |
| | | 0001 | MOBILE_DEFAULT | 1001 | MOBILE_HTHRS_NAMP |
| 02: | 3GIO_PADCFG_LUT_ADR[2] | 0010 | MOBILE_NTHRS_LLAMP | 1010 | MOBILE_HTHRS_LLAMP |
| | | 0011 | MOBILE_NTHRS_LAMP | 1011 | MOBILE_HTHRS_LAMP |
| 01: | 3GIO_PADCFG_LUT_ADR[1] | 0100 | MOBILE_NTHRS_HAMP | 1100 | MOBILE_HTHRS_HAMP |
| | | 0101 | MOBILE_NTHRS_HHAMP | 1101 | MOBILE_HTHRS_HHAMP |
| 00: | 3GIO_PADCFG_LUT_ADR[0] | 0110 | MOBILE_NTHRS_HHHAMP | 1110 | MOBILE_HTHRS_HHHAMP |
| | | 0111 | MOBILE_NTHRS_HHHHAMP | 1111 | MOBILE_HTHRS_HHHHAMP |
| 03: | PCI_DEVID[3] | 0010 | GK107-200-A1 | | |
| 02: | PCI_DEVID[2] | 0001 | GK107-300-A1 | | |
| 01: | PCI_DEVID[1] | 0001 | GK107-320-A1 | | |
| 00: | PCI_DEVID[0] | 0000 | GK107-400-A1 | | |
| 03: | SOR3_EXPOSED | 0 | DISABLED | 1 | ENABLED |
| 02: | SOR2_EXPOSED | 0 | DISABLED | 1 | ENABLED |
| 01: | SOR1_EXPOSED | 0 | DISABLED | 1 | ENABLED |
| 00: | SOR0_EXPOSED | 0 | DISABLED | 1 | ENABLED |
| 02: | PCIE_SPEED_CHANGE_GEN3 | 0 | DISABLED | 1 | ENABLED |
| 01: | PCIE_MAX_SPEED | 0 | GEN1 | 1 | GEN23 |
| 00: | DP_PLL_VOD_33V | 0 | USE18V | 1 | USE33V |

## GF108 Straps

| Bit | Signal | Values | | | |
|---|---|---|---|---|---|
| 03: | RAMCFG[3] | 0000 | 64Mx16 128bit | 1000 | 64Mx16 64bit |
| | | 0001 | 64Mx16 128bit Micron | 1001 | 64Mx16 64bit Micron |
| 02: | RAMCFG[2] | 0010 | 64Mx16 128bit Hynix | 1010 | 64Mx16 64bit Hynix |
| | | 0011 | 64Mx16 128bit Samsung | 1011 | 64Mx16 64bit Samsung |
| 01: | RAMCFG[1] | 0100 | 128Mx16 128bit | 1100 | 128Mx16 64bit |
| | | 0101 | 128Mx16 128bit Micron | 1101 | 128Mx16 64bit Micron |
| 00: | RAMCFG[0] | 0110 | 128Mx16 128bit Hynix | 1110 | 128Mx16 64bit Hynix |
| | | 0111 | 128Mx16 128bit Samsung | 1111 | 128Mx16 64bit Samsung |
| 03: | XCLK_417 | 0 | DISABLED | 1 | ENABLED |
| 02: | FB[0] | 1 | 128MB | | |
| 01: | SMB_ALT_ADDR | 0 | DISABLED | 1 | ENABLED |
| 00: | VGA_DEVICE | 0 | DISABLED | 1 | ENABLED |
| 03: | PCI_DEVID_EXT | 0 | GF108-350-A1 | | |
| | | 0 | GF108-450-A1 | | |
| 02: | SUB_VENDOR | 0 | NO_BIOS | 1 | BIOS |
| 01: | SLOT_CLK_CFG | 0 | DISABLED | 1 | ENABLED |
| 00: | PEX_PLL_EN_TERM100 | 0 | DIS | 1 | EN |
| 03: | USER[3] | 0000 | INIT | | |
| 02: | USER[2] | | | | |
| 01: | USER[1] | | | | |
| 00: | USER[0] | | | | |
| 03: | 3GIO_PADCFG_LUT_ADR[3] | 0000 | DSKTOP_DEFAULT | 1000 | DSKTOP_HTHRS |
| | | 0001 | MOBILE_DEFAULT | 1001 | MOBILE_HTHRS_NAMP |
| 02: | 3GIO_PADCFG_LUT_ADR[2] | 0010 | MOBILE_NTHRS_LLAMP | 1010 | MOBILE_HTHRS_LLAMP |
| | | 0011 | MOBILE_NTHRS_LAMP | 1011 | MOBILE_HTHRS_LAMP |
| 01: | 3GIO_PADCFG_LUT_ADR[1] | 0100 | MOBILE_NTHRS_HAMP | 1100 | MOBILE_HTHRS_HAMP |
| | | 0101 | MOBILE_NTHRS_HHAMP | 1101 | MOBILE_HTHRS_HHAMP |
| 00: | 3GIO_PADCFG_LUT_ADR[0] | 0110 | MOBILE_NTHRS_HHHAMP | 1110 | MOBILE_HTHRS_HHHAMP |
| | | 0111 | MOBILE_NTHRS_HHHHAMP | 1111 | MOBILE_HTHRS_HHHHAMP |
| 03: | PCI_DEVID[3] | 0001 | GF108-350-A1 | | |
| 02: | PCI_DEVID[2] | 0000 | GF108-450-A1 | | |
| 01: | PCI_DEVID[1] | | | | |
| 00: | PCI_DEVID[0] | | | | |

## Multilevel Straps

| | | | |
|---|---|---|---|
| 0000 | 5.1K to GND | 1000 | 5.1K to VCC |
| 0001 | 10K to GND | 1001 | 10K to VCC |
| 0010 | 15K to GND | 1010 | 15K to VCC |
| 0011 | 20K to GND | 1011 | 20K to VCC |
| 0100 | 24.9K to GND | 1100 | 24.9K to VCC |
| 0101 | 30.1K to GND | 1101 | 30.1K to VCC |
| 0110 | 34.8K to GND | 1110 | 34.8K to VCC |
| 0111 | 45.3K to GND | 1111 | 45.3K to VCC |

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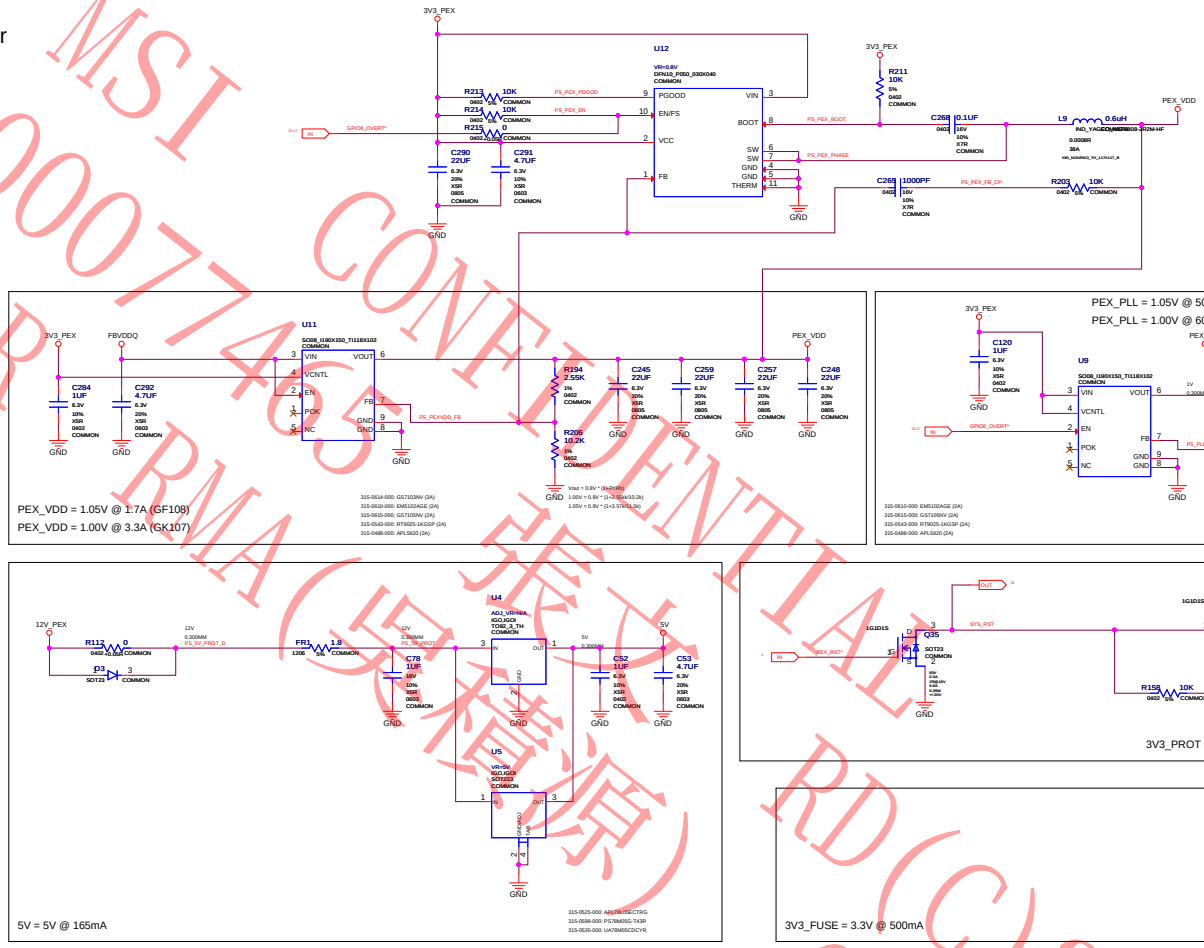
# Power Supply I: Misc Power

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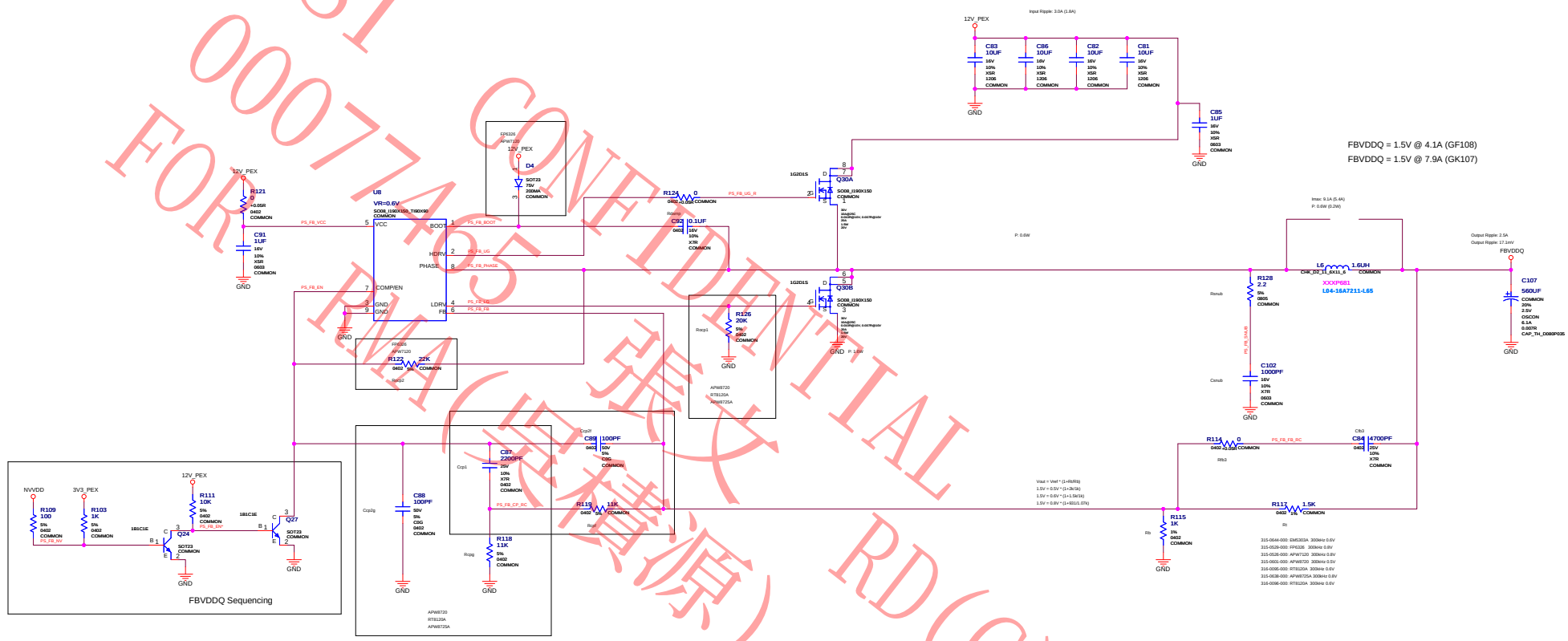
# Power Supply II: FBVDD/Q

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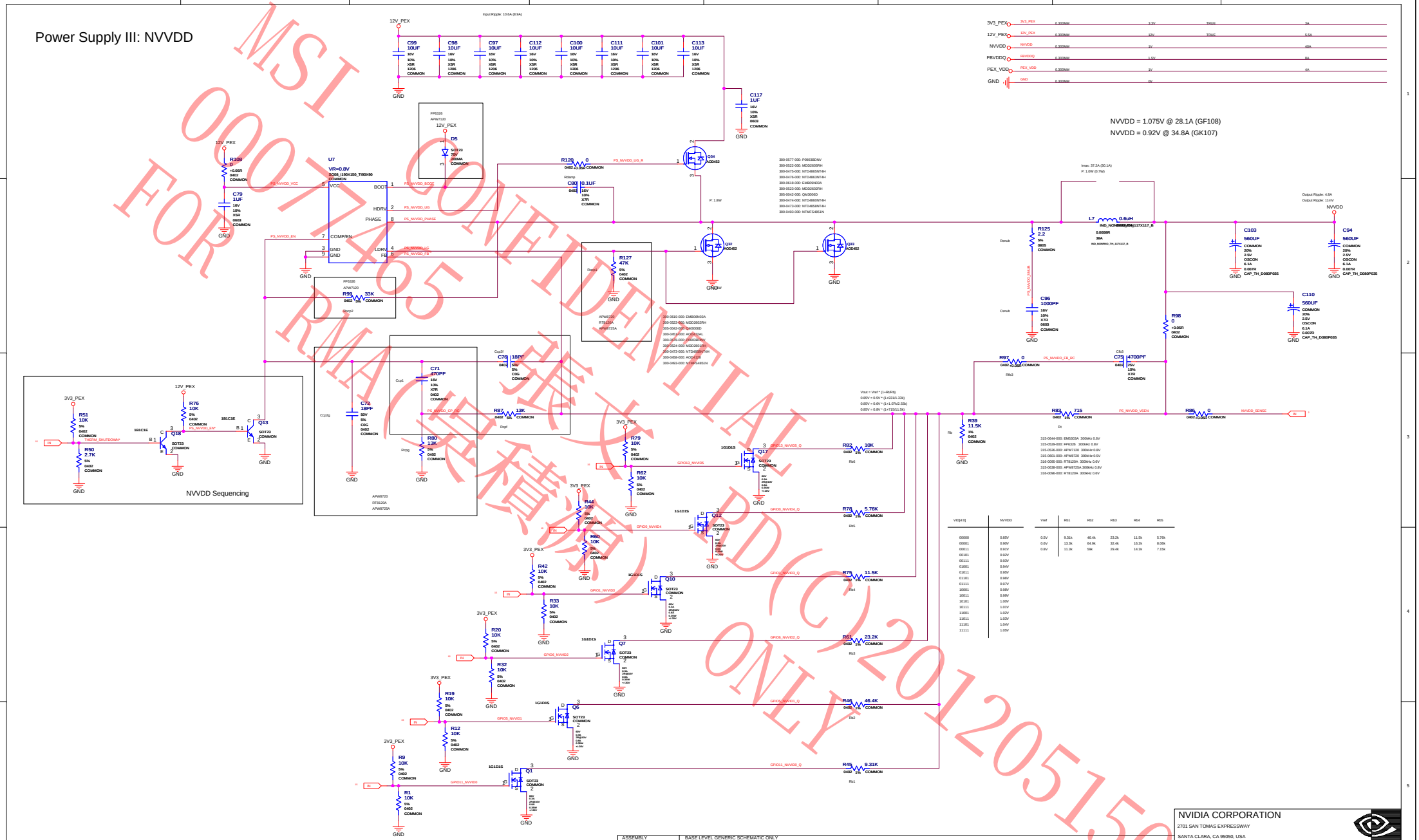
| BOM REV | A | DATE | 14-SEP-2011 |

# Power Supply III: NVVDD




| VID[4:0] | NVVDD |
|---|---|
| 00000 | 0.85V |
| 00001 | 0.90V |
| 00011 | 0.91V |
| 00101 | 0.92V |
| 00111 | 0.92V |
| 01001 | 0.94V |
| 01011 | 0.95V |
| 01101 | 0.96V |
| 01111 | 0.97V |
| 10001 | 0.98V |
| 10011 | 0.99V |
| 10101 | 1.00V |
| 10111 | 1.01V |
| 11001 | 1.02V |
| 11011 | 1.03V |
| 11101 | 1.04V |
| 11111 | 1.05V |

| Vref | Rb1 | Rb2 | Rb3 | Rb4 | Rb5 |
|---|---|---|---|---|---|
| 0.5V | 9.31k | 46.4k | 23.2k | 11.5k | 5.76k |
| 0.6V | 13.3k | 64.9k | 32.4k | 16.2k | 8.06k |
| 0.8V | 11.3k | 59k | 29.4k | 14.3k | 7.15k |

| | |
|---|---|
| ASSEMBLY | BASE LEVEL GENERIC SCHEMATIC ONLY |
| PAGE DETAIL | Power Supply III: NVVDD |


NVIDIA CORPORATION
2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA


| | | | |
|---|---|---|---|
| NV_PN | 600-12011-BASE-100 | | |
| PCB REV | P2011-A01 | PAGE | 19 OF 23 |
| BOM REV | A | DATE | 14-SEP-2011 |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Title: Basenet Report
Design: design
Date: Sep 14 13:55:43 2011

Base nets and synonyms for
design_lib.DESIGN(@design_lib.design)\sch
_1(i)
Base Signal Location([Zone][Bit])

3V3_PEX 19.1F
12V_PEX 19.1F
DACA_BLUE 8.4D
DACA_BLUE_C 8.4C< 9.2F<
DACA_GREEN 8.3D
DACA_GREEN_C 8.4D< 9.2F<
DACA_HSYNC 8.3D
DACA_HSYNC_C 8.2D< 9.3F<
DACA_VS_BUF 8.2E
DACA_RED 8.3D
DACA_RED_C 8.3C< 9.2F<
DACA_RSET 8.3B
DACA_VDD 8.3B
DACA_VREF 8.3B
DACA_VSYNC 8.3D
DACA_VSYNC_C 8.2D< 9.2F<
DACA_VS_BUF 8.3E
DP_MODE 11.1G
DP_MODE* 11.2E
DP_MODE_C* 11.2H
DP_MODE_Q* 11.2H
FAN_PWM_NOT 16.2E
FAN_PWM_NOT_D 16.2F
FAN_PWM_NOT_DF 16.2F
FBA_CLK0 3.4F> 5.2B< 5.2C<
5.4A<
FBA_CLK0* 3.4F> 5.2B< 5.2C<
5.4A<
FBA_CLK1_T 5.6B
FBA_CMD<0> 3.2E 5.1B 5.1C
FBA_CMD<30..0> 3.1F> 5.1B<
FBA_CMD<2> 3.2E 3.5E 5.3B 5.3C
FBA_CMD<3> 3.2E 3.5F 5.2B 5.2C
FBA_CMD<4> 3.3E 5.2B 5.3C 5.2E
5.2G
FBA_CMD<5> 3.3E 3.5F 5.3B 5.3C
5.3E 5.3G
FBA_CMD<6> 3.3E 5.2B 5.2C 5.2E
5.2G
FBA_CMD<7> 3.3E 5.2B 5.2C 5.2E
5.2G
FBA_CMD<8> 3.3E 5.1B 5.1C 5.1E
5.1G
FBA_CMD<9> 3.3E 5.1B 5.1C 5.1E
5.1G
FBA_CMD<10> 3.3E 5.1B 5.1C 5.1E
5.1G
FBA_CMD<11> 3.3E 5.1B 5.1C 5.1E
5.1G
FBA_CMD<12> 3.3E 5.2B 5.2C 5.2E
5.2G
FBA_CMD<13> 3.3E 5.1B 5.1C 5.1E
5.1G
FBA_CMD<14> 3.3E 5.2B 5.2C 5.2E
5.2G
FBA_CMD<15> 3.3E 5.1B 5.1C 5.1E
5.1G
FBA_CMD<16> 3.3E 5.1E 5.1G
FBA_CMD<18> 3.3E 3.3E 5.3E 5.3G
FBA_CMD<19> 3.3E 3.3F 5.3E 5.3G
FBA_CMD<20> 3.3E 5.2B 5.2C 5.2E
5.2G
FBA_VREF 5.3C
FBA_ZQ0 5.3B
FBA_ZQ1 5.3C
FBA_ZQ2 5.3E
FBA_ZQ3 5.3G
FBA_CLK0 4.4F> 6.2B< 6.2C<
6.4A<
FBA_CLK0* 4.4F> 6.3B< 6.2C<
6.4A<
FBA_CLK0_T 6.4B
FBA_CLK1 4.4F> 6.2E< 6.2G<
6.5A<
FBA_CLK1* 4.4F> 6.3E< 6.2G<
6.5A<
FBA_CLK1_T 6.5B
FBA_CMD<0> 4.2E 6.1B 6.1C
FBA_CMD<30..0> 4.2F> 6.1B<
FBA_CMD<2> 4.2E 4.3C 6.3B 6.1C
FBA_CMD<3> 4.3C 4.3F 6.3B 6.2C
FBA_CMD<4> 4.3E 6.2B 6.2C 6.2E
6.2G
FBA_CMD<5> 4.3E 4.3F 6.3B 6.3C
6.3E 6.3G
FBA_CMD<6> 4.3E 6.2B 6.2C 6.2E
6.2G
FBA_CMD<7> 4.3E 6.2B 6.2C 6.2E
6.2G
FBA_CMD<8> 4.3E 6.1B 6.1C 6.1E
6.1G
FBA_CMD<9> 4.3E 6.1B 6.1C 6.1E
6.1G
FBA_CMD<10> 4.3E 6.1B 6.1C 6.1E
6.1G
FBA_CMD<11> 4.3E 6.1B 6.1C 6.1E
6.1G
FBA_CMD<12> 4.3E 6.2B 6.2C 6.2E
6.2G
FBA_CMD<13> 4.3E 6.1B 6.1C 6.1E
6.1G
FBA_CMD<14> 4.3E 6.2B 6.2C 6.2E
6.2G
FBA_CMD<15> 4.3E 6.1B 6.1C 6.1E
6.1G
FBA_CMD<16> 4.3E 6.1B 6.1C 6.1E
6.1G
FBA_CMD<18> 4.3C 6.4D
FBA_CMD<19> 4.3C 6.1C 6.1G
FBA_CMD<20> 4.3E 4.3E 6.3E 6.3G
FBA_CMD<19> 4.3E 4.3F 6.2E 6.2G
FBA_CMD<20> 4.3E 6.2B 6.2C 6.2E
6.2G
FBA_CMD<21> 4.3E 6.2B 6.2C 6.2E
6.2G
FBA_CMD<22> 4.3E 6.2B 6.2C 6.2E
6.2G
FBA_CMD<23> 4.3E 6.2B 6.2C 6.2E
6.2G
FBA_CMD<24> 4.3E 6.1B 6.1C 6.1E
6.1G
FBA_CMD<25> 4.3E 6.2B 6.2C 6.2E
6.2G
FBA_CMD<27> 4.3D 6.2E 6.2G 6.2E
6.2G
FBA_CMD<28> 4.3E 6.2B 6.2C 6.2E
6.2G
FBA_CMD<29> 4.3E 6.1B 6.1C 6.1E
6.1G
FBA_CMD<30> 4.3E 6.2B 6.2C 6.2E
6.2G
FBA_DQS_RN<0> 4.4C 6.4D
FBA_DQS_RN<7..0> 4.4B> 6.4C<>
FBA_DQS_RP<0> 4.4C 6.4D
FBA_DQS_RP<7..0> 4.4B> 6.4C<>
FBA_DQM<0> 4.4C 6.5D
FBA_DQM<1> 4.4C 6.5D
FBA_DQM<2> 4.4C 6.5E
FBA_DQM<3> 4.4C 6.5E
FBA_DQM<4> 4.4C 6.4F
FBA_DQM<5> 4.4C 6.5F
FBA_DQM<6> 4.4C 6.4G
FBA_DQM<7> 4.4C 6.5G
FBA_DQS_RN<0> 4.4C 6.4D
FBA_DQS_RN<7..0> 4.4B> 6.4C<>
FBA_DQS_RP<0> 4.4C 6.5D
FBA_DQS_RN<2> 4.4C 6.4E
FBA_DQS_RN<3> 4.4C 6.5E
FBA_DQS_RN<4> 4.4C 6.4F
FBA_DQS_RN<5> 4.4C 6.5F
FBA_DQS_RN<6> 4.4C 6.4G
FBA_DQS_RN<7> 4.4C 6.5G
FBA_DQS_WP<0> 4.4C 6.4D
FBA_DQS_WP<7..0> 4.4B> 6.4C<>
FBB_CMD<15> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<16> 4.3E 6.1C 6.1G
FBB_CMD<18> 4.3E 4.3E 6.3E 6.3G
FBB_CMD<19> 4.3E 4.3F 6.2E 6.2G
FBB_CMD<20> 4.3E 6.2B 6.2C 6.2E
6.2G
FBB_CMD<21> 4.3E 6.2B 6.2C 6.2E
6.2G
FBB_CMD<22> 4.3E 6.2B 6.2C 6.2E
6.2G
FBB_CMD<23> 4.3E 6.2B 6.2C 6.2E
6.2G
FBB_CMD<24> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<25> 4.3E 6.2B 6.2C 6.2E
6.2G
FBB_CMD<27> 4.3D 6.2B 6.2C 6.2E
6.2G
FBB_CMD<28> 4.3E 6.2B 6.2C 6.2E
6.2G
FBB_CMD<29> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<30> 4.3E 6.2B 6.2C 6.2E
6.2G
FBB_DQS_RN<0> 4.4C 6.4D
FBB_DQS_RN<7..0> 4.4B> 6.4C<>
FBB_DQS_RP<0> 4.4C 6.5D
FBB_DQS_RN<2> 4.4C 6.4E
FBB_DQS_RN<3> 4.4C 6.5E
FBB_DQS_RN<4> 4.4C 6.4F
FBB_DQS_RN<5> 4.4C 6.5F
FBB_DQS_RN<6> 4.4C 6.4G
FBB_DQS_RN<7> 4.4C 6.5G
FBB_DQS_WP<0> 4.4C 6.4D
FBB_DQS_WP<7..0> 4.4B> 6.4C<>
FBB_DQS_WP<1> 4.4C 6.5D
FBB_DQS_WP<2> 4.4C 6.4E
FBB_DQS_WP<3> 4.4C 6.5E
FBB_DQS_WP<4> 4.4C 6.4F
FBB_DQS_WP<5> 4.4C 6.5F
FBB_DQS_WP<6> 4.4C 6.4G
FBB_DQS_WP<7> 4.4C 6.5G
FBB_VREF 6.3C
FBB_ZQ0 6.3B
FBB_ZQ1 6.3C
FBB_ZQ2 6.3E
FBB_ZQ3 6.3G
FBVDDQ 19.1F
FB_CAL_PU_VDDQ 7.4C
FB_CAL_PU_GND 7.4C
FB_CAL_TERM_GND 7.4C
FB_PLLVDD 3.1E< 3.5F< 4.5E<
GPIO1_NVVID4 15.2D> 19.4D<
GPIO3_NVVID3_Q 19.3E
GPIO4_NVVID3 15.2E> 19.4C<
GPIO1_NVVID3_Q 19.4E
GPIO4_FAN_TACH 15.2E< 16.1C>
GPIO5_NVVID3 15.2D> 19.5C<
GPIO5_NVVID1_Q 19.5E
GPIO6_NVVID2 15.2E> 19.4C<
GPIO6_NVVID2_Q 19.4E
GPIO8_OVERT* 15.2F> 17.1C< 17.2E<
GPIO9_FAN_PWM 15.2D
GPIO11_NVVID0 15.2E> 19.5C<
GPIO11_NVVID0_Q 19.5E
GPIO13_NVVID5 15.2E> 19.3D<
GPIO13_NVVID5_Q 19.3E
GPIO14_DVI_HPD 9.2D
GPIO14_DVI_HPD_C 9.2G
GPIO15_DVI_HPD_Q 9.3E
GPIO15_DVI_HPD_R 9.3E
GPIO15_NORTH_HPD 10.5D
GPIO15_NORTH_HPD_C 10.3G< 11.3F>
GPIO15_NORTH_HPD_Q 10.3E
GPIO15_NORTH_HPD_R 10.3E
GPIO16_FAN_PWM 15.2D
GPIO18_MID_HPD 12.2D
GPIO18_MID_HPD_C 12.4G< 13.2F>
GPIO18_MID_HPD_Q 12.3E
GPIO18_MID_HPD_R 12.3E
GPIO_FAN_GND 16.2D
GPIO_FAN_PWM 15.2F> 16.1E<
GPIO_FAN_PWM_OUT 16.2B
GPU_FUSEMODE 9.5F
I2CA_SCL 8.1G<> 9.2F<
I2CA_SDA 8.2G<> 9.2F<>
I2CW_SCL_C 10.2G
I2CW_SCL_Q 11.2E
I2CW_SDA_C 10.2G
I2CW_SDA_Q 11.1E
I2CY_SCL 12.2D
I2CY_SCL_C 12.2G< 13.2F<
I2CY_SDA 12.2D
I2CY_SDA_C 12.2G<> 13.2F<>
IFPAB_IOVDD 9.2C
IFPAB_PLLVDD 9.2C
IFPAB_TXC 9.2D
IFPAB_TXC* 9.1D
IFPAB_TXD0 9.2D
IFPAB_TXD0* 9.2D
IFPAB_TXD1 9.2D
IFPAB_TXD1* 9.2D
IFPAB_TXD2 9.2D
IFPAB_TXD2* 9.2D
IFPAB_TXD3 9.2D
IFPAB_TXD3* 9.2D
IFPAB_TXD4 9.2D
IFPAB_TXD4* 9.2D
IFPAB_TXD5 9.2D
IFPAB_TXD5* 9.2D
IFPAB_TXD6 9.2D
IFPAB_TXD6* 9.2D
IFPCEF_IOVDD 10.3C< 12.3F<
IFPCEF_PLLVDD 10.2E< 12.2E<
IFPC_AUX 10.1E<> 11.2C<>
IFPC_AUX* 10.1E<> 11.2C<>
IFPC_AUX_C 11.2F
IFPC_AUX_C* 11.2F
IFPC_L0_C 11.3F
IFPC_L0_C* 11.3F
IFPC_L1_C 11.3F
IFPC_L1_C* 11.3F
IFPC_L2_C 11.3F
IFPC_L2_C* 11.3F
IFPC_L3_C 11.2F
IFPC_L3_C* 11.2F
IFPC_RSET 10.3B
IFPC_TXC 10.3E> 11.3D<
IFPC_TXC* 10.2E> 11.2D<
IFPC_TXC_C 10.2G
IFPC_TXC_C* 10.2G
IFPC_TXD0 10.2E> 11.3D<
IFPC_TXD0* 10.3C> 11.3D<
IFPC_TXD0_C 10.3G
IFPC_TXD0_C* 10.3G
IFPC_TXD1 10.3E> 11.3D<
IFPC_TXD1* 10.3C> 11.3D<
IFPC_TXD1_C 10.3G
IFPC_TXD1_C* 10.3G
IFPC_TXD2 10.2E> 11.3D<
IFPC_TXD2* 10.3C> 11.3D<
IFPC_TXD2_C 10.3G
IFPC_TXD2_C* 10.3G
IFPEF_RSET 12.2B
IFPEF_TXC 12.2E> 13.2D<
IFPEF_TXC* 12.3C> 13.2D<
IFPEF_TXC_C 13.3G
IFPEF_TXC_C* 12.3G
IFPEF_TXC_C2 13.2G
IFPEF_TXC_C2* 13.2G
IFPEF_TXD0 12.2E> 13.3D<
IFPEF_TXD0* 12.2C> 13.3D<
IFPEF_TXD0_C 12.3G
IFPEF_TXD0_C* 12.2G
IFPEF_TXD1 12.2E> 13.3D<
IFPEF_TXD1* 12.2C> 13.3D<
IFPEF_TXD1_C 12.3G
IFPEF_TXD1_C* 13.3G
IFPEF_TXD2 12.2E> 13.3D<
IFPEF_TXD2* 12.2C> 13.3D<
IFPEF_TXD2_C 12.3G
IFPEF_TXD2_C* 12.3G
IFPEF_TXD3 12.3D
IFPEF_TXD3* 12.3D
IFPEF_TXD3_C 12.3G
IFPEF_TXD3_C* 12.3G
IFPEF_TXD4 12.4D
IFPEF_TXD4* 12.4D
IFPEF_TXD4_C 12.3G
IFPEF_TXD4_C* 12.3G
IFPEF_TXD5 12.4D
IFPEF_TXD5* 12.4D
IFPEF_TXD5_C 12.3G
IFPEF_TXD5_C* 12.3G
IFP_TERM_EN 10.4F> 12.5E< 13.3E<
JTAG_TCK 15.2C
JTAG_TDI 15.2C
JTAG_TDO 15.2C
JTAG_TMS 15.2C
JTAG_TRST* 15.2C
MULTISTRAP_REF_GND 15.3C
NVVDD 19.1F
NVVDD_SENSE 2.4G> 19.3H>
PEX_PLLVDD 2.4F
PEX_PRSNT 2.1A
PEX_REFCLK 2.2B
PEX_REFCLK* 2.2B
PEX_RST* 2.2C> 17.3D<
PEX_RX0 2.2B
PEX_RX0* 2.2B
PEX_RX1 2.2B
PEX_RX1* 2.2B
PEX_RX2 2.2B
PEX_RX2* 2.3B
PEX_RX3 2.3B
PEX_RX3* 2.3B
PEX_RX4 2.4B
PEX_RX4* 2.4B
PEX_RX5 2.3B
PEX_RX5* 2.3B
PEX_RX6 2.3B
PEX_RX6* 2.3B
PEX_RX7 2.3B
PEX_RX7* 2.3B
PEX_RX8 2.4B
PEX_RX8* 2.4B
PEX_RX9 2.4B
PEX_RX9* 2.4B
PEX_RX10 2.4B
PEX_RX10* 2.4B
PEX_RX11 2.4B
PEX_RX11* 2.4B
PEX_RX12 2.4B
PEX_RX12* 2.5B
PEX_RX13 2.5B
PEX_RX13* 2.5B
PEX_RX14 2.5B
PEX_RX14* 2.5B
PEX_RX15 2.5B
PEX_RX15* 2.5B
PEX_VDD 19.1F
PLLVDD 15.4C
PS_5V_PROT 17.3C
PS_5V_PROT_D 17.3B
PS_FB_BOOT 18.2C
PS_FB_CP_RC 18.3C
PS_FB_EN 18.2B
PS_FB_EN* 18.3B
PS_FB_FB 18.2C
PS_FB_FB_RC 18.3F
PS_FB_LG 18.2C
PS_FB_NV 18.3A
PS_FB_PHASE 18.2C
PS_FB_SNUB 18.3F
PS_FB_UG 18.2C
PS_FB_VCC 18.2B
PS_NVVDD_BOOT 19.2C
PS_NVVDD_CP_RC 19.3C
PS_NVVDD_EN 19.3B
PS_NVVDD_FB 19.2C
PS_NVVDD_FB_RC 19.3F
PS_NVVDD_LG 19.2C
PS_NVVDD_PHASE 19.2C
PS_NVVDD_SNUB 19.2F
PS_NVVDD_UG 19.2C
PS_NVVDD_UG_R 19.1D
PS_NVVDD_VCC 19.3B
PS_NVVDD_VSEN 19.3G
PS_PEXVDD_FB 17.2C
PS_PEX_BOOT 17.1E
PS_PEX_EN 17.1D
PS_PEX_EN* 17.4F
PS_PEX_PGOOD 17.1D
PS_PEX_PHASE 17.1E
PS_PLL_ADJ 17.2F
PS_PLL_VCC 15.3D
ROM_SCLK 15.3E< 16.3B>
ROM_SI 15.3E< 16.3B>
ROM_SO 15.3E< 16.3B>
SNN_3V3AUX 2.1A
SNN_3V3AUX_GPU 2.4F
SNN_3V3FUSE_OC 17.4E
SNN_B 12.3G
SNN_BURST* 15.3D
SNN_CEC 15.3D
SNN_DP_CEC 11.2H
SNN_FBA0_J1 5.2B
SNN_FBA0_J9 5.2B
SNN_FBA0_L0 5.2E
SNN_FBA0_L1 5.2B
SNN_FBA0_L9 5.2B
SNN_FBA1_J1 5.2C
SNN_FBA1_J9 5.2C
SNN_FBA1_L1 5.2C
SNN_FBA1_L9 5.2C
SNN_FBA2_J1 5.2B
SNN_FBA2_J9 5.2E
SNN_FBA2_L1 5.2E
SNN_FBA2_L9 5.2E
SNN_FBA3_J1 5.2G
SNN_FBA3_J9 5.2G
SNN_FBA3_L1 5.2G
SNN_FBA3_L9 5.2G
SNN_FBB_CMD1 4.3E
SNN_FBB_CMD17 4.3E
SNN_FBB_CMD_RFU0 4.3E
SNN_FBB_CMD_RFU1 4.4E
SNN_FBB_CMD_RFU2 4.4E
SNN_FBB_VCK31 4.4E
SNN_FBB_WCK01* 4.4E
SNN_FBB_WCK23 4.4E
SNN_FBB_WCK23* 4.4E
SNN_FBB_WCK45 4.4E
SNN_FBB_WCK45* 4.4E
SNN_FBB_WCK67 4.4E
SNN_FBB_WCK67* 4.4E
SNN_FBB_WCKB01 4.4E
SNN_FBB_WCKB01* 4.4E
SNN_FBB_WCKB23 4.5E
SNN_FBB_WCKB23* 4.5E
SNN_FBB_WCKB45 4.5E
SNN_FBB_WCKB45* 4.5E
SNN_FBB_WCKB67 4.5E
SNN_FBB_WCKB67* 4.5E
SNN_FBVDDQ_PROBE 7.3C
SNN_FB_CLAMP 3.1E
SNN_FB_VREF 3.5C
SNN_G 12.3G
SNN_GND_PROBE 7.3C
SNN_GND_SENSE 2.4F
SNN_GPIO2 15.2D
SNN_GPIO3 15.2D
SNN_GPIO10 15.2D
SNN_GPIO12 15.2D
SNN_GPIO17 14.5H
SNN_GPIO18 12.4D
SNN_GPIO20 15.2D
SNN_GPIO21 15.2D
SNN_GPU_AC6 14.5B
SNN_GPU_AJ4 14.5B
SNN_GPU_AJ5 14.5B
SNN_GPU_AJ28 14.4B
SNN_GPU_AL11 14.5B
SNN_GPU_C15 14.5B
SNN_GPU_C16 14.3F
SNN_GPU_D19 14.5B
SNN_GPU_D20 14.5B
SNN_GPU_D23 14.5B
SNN_GPU_D26 14.5B
SNN_GPU_H31 14.5B
SNN_GPU_T8 14.5B
SNN_GPU_Y32 14.5B
SNN_GPU_W32 14.3F
SNN_HDMI_CEC 10.2G
SNN_HDMI_RSVD 10.2G
SNN_HSYNC 12.4C
SNN_I2CB_SCL 15.1D
SNN_I2CB_SDA 15.1D
SNN_I2CC_SCL 15.1D
SNN_I2CC_SDA 15.1D
SNN_I2CS_SCL 15.1D
SNN_I2CS_SDA 15.1D
SNN_IFPAB_RSET 9.2C
SNN_IFPA_TXD3 9.2D
SNN_IFPA_TXD7# 9.2D
SNN_IFPB_TXC 9.2D
SNN_IFPB_TXC* 9.2D
SNN_IFPB_TXD7 9.3D
SNN_IFPB_TXD7* 9.3D
SNN_IFPD_AUX 14.4H
SNN_IFPD_AUX* 14.4H
SNN_IFPD_IOVDD 14.5F
SNN_IFPD_L0 14.5H
SNN_IFPD_L0* 14.5H
SNN_IFPD_L1 14.5H
SNN_IFPD_L1* 14.5H
SNN_IFPD_L2 14.5H
SNN_IFPD_L2* 14.5H
SNN_IFPD_L3 14.5H
SNN_IFPD_L3* 14.4H
SNN_IFPD_PLLVDD 14.4F
SNN_IFPD_RSET 14.4F
SNN_IFPF_AUX 12.3D
SNN_IFPF_AUX* 12.3D
SNN_IFPF_TXC 12.3D
SNN_IFPF_TXC* 12.3D
SNN_LHDMI_CEC 13.2G
SNN_LHDMI_RSVD 13.2G
SNN_MECH_D1A 16.3F
SNN_MECH_D1B 16.3F
SNN_MECH_D1C 16.3F
SNN_MECH_D2A 16.3F
SNN_MECH_D2B 16.3F
SNN_MECH_D2C 16.3F
SNN_MECH_D2A 16.3G
SNN_MECH_D3B 16.3G
SNN_MECH_D3C 16.3G
SNN_MECH_D4A 16.3G
SNN_MECH_D4B 16.3G
SNN_MECH_D4C 16.3G
SNN_MECH_D5A 16.3F
SNN_MECH_D5B 16.3F
SNN_MECH_D5C 16.3F
SNN_MECH_D6A 16.4F
SNN_MECH_D6B 16.4F
SNN_MECH_D6C 16.4F
SNN_MECH_D7A 16.3G
SNN_MECH_D7B 16.3G
SNN_MECH_D7C 16.3G
SNN_MECH_D8A 16.4G
SNN_MECH_D8B 16.4G
SNN_MECH_D8C 16.4G
SNN_PEX_CLKREQ* 2.2E
SNN_PEX_TSTCLK_OUT 2.4F
SNN_PEX_TSTCLK_OUT 2.4F
-
SNN_PEX_WAKE* 2.2E
SNN_PS_PEX_NC 17.2B
SNN_PS_PEX_POK 17.2B
SNN_PS_PLL_NC 17.3E
SNN_PS_PLL_POK 17.2E
SNN_R 12.3C
SNN_SMCLK 2.1B
SNN_SMDAT 2.1B
SNN_VSYNC 12.3G
STRAP0 15.3C< 16.4B>
STRAP1 15.3C< 16.4B>
STRAP2 15.3C< 16.4B>
STRAP3 15.3C< 16.4B>
STRAP4 15.3C< 16.5B>
SYS_RST 15.2F< 17.3E>
THERMDN 15.1C
THERMDP 15.1C
THERM_N 15.2G
THERM_P 15.2H
THERM_P* 15.2G
THERM_SHUTDOWN* 15.1H< 19.3A<
VDD33 14.4D
XTALIN 15.4C
XTALOUT 15.4D
XTALOUTBUFF 15.4D
XTALSSIN 15.4C

| | |
|---|---|
| ASSEMBLY | BASE LEVEL GENERIC SCHEMATIC ONLY |
| PAGE DETAIL | Basenet Report |

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2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA

| | | | |
|---|---|---|---|
| NV_PN | 600-12011-BASE-100 | | |
| PCB REV | P2011-A01 | PAGE | 20 OF 23 |
| BOM REV | A | DATE | 14-SEP-2011 |

MSI 6007N5 CONFIDENTIAL FOR(C)2012051501 RMA保修用文件 ONLY

Title: Netsbypage Report
Design: design
Date: Sep 14 13:55:43 2011

Page wise report of nets and their base nets

| Net Name | Location[Zone][Bit]] |
|---|---|
| GPU_TESTMODE | 2.5F |
| NVVDD_SENSE | 2.4G> 19.3H< |
| PEX_PLLVDD | 2.4F |
| PEX_PRSNT | 2.1A |
| PEX_REFCLK | 2.2B |
| PEX_REFCLK* | 2.2B |
| PEX_RST* | 2.2C< 17.3D< |
| PEX_RX0 | 2.2B |
| PEX_RX0* | 2.2B |
| PEX_RX1 | 2.2B |
| PEX_RX1* | 2.2B |
| PEX_RX2 | 2.2B |
| PEX_RX2* | 2.3B |
| PEX_RX3 | 2.3B |
| PEX_RX3* | 2.3B |
| PEX_RX4 | 2.3B |
| PEX_RX4* | 2.3B |
| PEX_RX5 | 2.3B |
| PEX_RX5* | 2.3B |
| PEX_RX6 | 2.3B |
| PEX_RX6* | 2.3B |
| PEX_RX7 | 2.3B |
| PEX_RX7* | 2.4B |
| PEX_RX8 | 2.4B |
| PEX_RX8* | 2.4B |
| PEX_RX9 | 2.4B |
| PEX_RX9* | 2.4B |
| PEX_RX10 | 2.4B |
| PEX_RX10* | 2.4B |
| PEX_RX11 | 2.4B |
| PEX_RX11* | 2.4B |
| PEX_RX12 | 2.4B |
| PEX_RX12* | 2.5B |
| PEX_RX13 | 2.5B |
| PEX_RX13* | 2.5B |
| PEX_RX14 | 2.5B |
| PEX_RX14* | 2.5B |
| PEX_RX15 | 2.5B |
| PEX_RX15* | 2.5B |
| PEX_TERMP | 2.5F |
| PEX_TX0 | 2.2E |
| PEX_TX0* | 2.2E |
| PEX_TX1 | 2.2E |
| PEX_TX1* | 2.2E |
| PEX_TX2 | 2.2E |
| PEX_TX2* | 2.2E |
| PEX_TX3 | 2.3E |
| PEX_TX3* | 2.3E |
| PEX_TX4 | 2.3E |
| PEX_TX4* | 2.3E |
| PEX_TX5 | 2.3E |
| PEX_TX5* | 2.3E |
| PEX_TX6 | 2.3E |
| PEX_TX6* | 2.3E |
| PEX_TX7 | 2.3E |
| PEX_TX7* | 2.3E |
| PEX_TX8 | 2.4E |
| PEX_TX8* | 2.4E |
| PEX_TX9 | 2.4E |
| PEX_TX9* | 2.4E |
| PEX_TX10 | 2.4E |
| PEX_TX10* | 2.4E |
| PEX_TX11 | 2.4E |
| PEX_TX11* | 2.4E |
| PEX_TX12 | 2.4E |
| PEX_TX12* | 2.4E |
| PEX_TX13 | 2.5E |
| PEX_TX13* | 2.5E |
| PEX_TX14 | 2.5E |
| PEX_TX14* | 2.5E |
| PEX_TX15 | 2.5E |
| PEX_TX15* | 2.5E |
| PEX_TX0D | 2.2B |
| PEX_TX0D* | 2.2B |
| PEX_TX1D | 2.2B |
| PEX_TX1D* | 2.2B |
| PEX_TX2D | 2.2B |
| PEX_TX2D* | 2.3B |
| PEX_TX3D | 2.3B |
| PEX_TX3D* | 2.3B |
| PEX_TX4D | 2.3B |
| PEX_TX4D* | 2.3B |
| PEX_TX5D | 2.3B |
| PEX_TX5D* | 2.3B |
| PEX_TX6D | 2.3B |
| PEX_TX6D* | 2.3B |
| PEX_TX7D | 2.3B |
| PEX_TX7D* | 2.3B |
| PEX_TX8D | 2.4B |
| PEX_TX8D* | 2.4B |
| PEX_TX9D | 2.4B |
| PEX_TX9D* | 2.4B |
| PEX_TX10D | 2.4B |
| PEX_TX10D* | 2.4B |
| PEX_TX11D | 2.4B |
| PEX_TX11D* | 2.4B |
| PEX_TX12D | 2.4B |
| PEX_TX12D* | 2.4B |
| PEX_TX13D | 2.5B |
| PEX_TX13D* | 2.5B |
| PEX_TX14D | 2.5B |
| PEX_TX14D* | 2.5B |
| PEX_TX15D | 2.5B |
| PEX_TX15D* | 2.5B |
| SNN_3V3AUX | 2.1A |
| SNN_3V3AUX_GPU | 2.4F |
| SNN_GND_SENSE | 2.4F |
| SNN_PEX_CLKREQ* | 2.2E |

| | |
|---|---|
| SNN_PEX_TSTCLK_OUT | 2.4E |
| SNN_PEX_TSTCLK_OUT* | 2.4E |
| SNN_PEX_WAKE* | 2.2C |
| SNN_SMCLK | 2.1E |
| SNN_SMDAT | 2.1E |
| FBA_CLK0 | 3.4F> 5.2B< 5.2C< 5.4A< |
| FBA_CLK0* | 3.4F> 5.2B< 5.2C< 5.4A< |
| FBA_CLK1 | 3.4F> 5.2B< 5.2G< 5.5A< |
| FBA_CLK1* | 3.4F> 5.2E< 5.2G< 5.5A< |
| FBA_CMD<0> | 3.3E 5.1B 5.1C |
| FBA_CMD<30..0> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<1> | 3.3E 5.2B 5.3B 5.3C |
| FBA_CMD<2> | 3.3E 3.3F 5.2B 5.3C |
| FBA_CMD<3> | 3.3E 3.3F 5.2B 5.2C |
| FBA_CMD<4> | 3.3E 3.3F 5.2B 5.2C |
| FBA_CMD<5> | 3.3E 3.3F 5.3B 5.3C 5.3E 5.3G |
| FBA_CMD<6> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<7> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<8> | 3.3E 5.1E 5.1G |
| FBA_CMD<18> | 3.3E 3.3E 5.3E 5.3G |
| FBA_CMD<19> | 3.3E 3.3E 5.3E 5.3G |
| FBA_CMD<19> | 3.3E 3.3F 5.2E 5.2G |
| FBA_CMD<20> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<21> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<22> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<22> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<24> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<25> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<26> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<27> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<28> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<29> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<30> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_D<0> | 3.1C 5.4D |
| FBA_D<63..0> | 3.3C 5.5G |
| FBA_D<1> | 3.2C 5.4D |
| FBA_D<2> | 3.1C 5.4D |
| FBA_D<3> | 3.1C 5.4D |
| FBA_D<4> | 3.1C 5.4D |
| FBA_D<5> | 3.1C 5.4D |
| FBA_D<6> | 3.1C 5.4D |
| FBA_D<7> | 3.2C 5.4D |
| FBA_D<8> | 3.2C 5.4D |
| FBA_D<9> | 3.2C 5.4D |
| FBA_D<10> | 3.2C 5.4D |
| FBA_D<11> | 3.2C 5.5D |
| FBA_D<12> | 3.2C 5.5D |
| FBA_D<13> | 3.2C 5.5D |
| FBA_D<14> | 3.2C 5.5D |
| FBA_D<15> | 3.2C 5.5D |
| FBA_D<16> | 3.2C 5.4E |
| FBA_D<17> | 3.2C 5.4E |
| FBA_D<18> | 3.2C 5.4E |
| FBA_D<19> | 3.2C 5.4E |
| FBA_D<20> | 3.2C 5.4E |
| FBA_D<21> | 3.2C 5.4E |
| FBA_D<22> | 3.2C 5.4E |
| FBA_D<23> | 3.2C 5.4E |
| FBA_D<24> | 3.2C 5.4E |
| FBA_D<25> | 3.2C 5.4E |
| FBA_D<26> | 3.2C 5.4E |
| FBA_D<27> | 3.2C 5.5E |
| FBA_D<28> | 3.2C 5.5E |
| FBA_D<29> | 3.2C 5.5E |
| FBA_D<30> | 3.2C 5.5E |
| FBA_D<31> | 3.2C 5.5E |
| FBA_D<32> | 3.3C 5.4F |
| FBA_D<33> | 3.3C 5.4F |
| FBA_D<34> | 3.3C 5.4F |
| FBA_D<35> | 3.3C 5.4F |
| FBA_D<36> | 3.3C 5.4F |
| FBA_D<37> | 3.3C 5.4F |
| FBA_D<38> | 3.3C 5.4F |
| FBA_D<39> | 3.3C 5.4F |
| FBA_D<40> | 3.3C 5.4F |
| FBA_D<41> | 3.3C 5.4F |
| FBA_D<42> | 3.3C 5.4F |
| FBA_D<43> | 3.3C 5.5F |
| FBA_D<44> | 3.3C 5.5F |
| FBA_D<45> | 3.3C 5.5F |
| FBA_D<46> | 3.3C 5.5F |
| FBA_D<47> | 3.3C 5.5F |
| FBA_D<48> | 3.3C 5.4G |
| FBA_D<49> | 3.3C 5.4G |
| FBA_D<50> | 3.3C 5.4G |
| FBA_D<51> | 3.3C 5.4G |
| FBA_D<52> | 3.3C 5.4G |
| FBA_D<53> | 3.3C 5.4G |
| FBA_D<54> | 3.3C 5.4G |
| FBA_D<55> | 3.3C 5.4G |
| FBA_D<56> | 3.3C 5.4G |
| FBA_D<57> | 3.3C 5.4G |
| FBA_D<58> | 3.3C 5.5G |
| FBA_D<59> | 3.3C 5.5G |
| FBA_D<60> | 3.3C 5.5G |
| FBA_D<61> | 3.3C 5.5G |
| FBA_D<62> | 3.3C 5.5G |
| FBA_D<63> | 3.3C 5.5G |
| FBA_DEBUG0 | 3.4E |
| FBA_DEBUG1 | 3.4E |
| FBA_DQM<3> | 3.3C 5.4D |
| FBA_DQM<7..0> | 3.4C 5.5G |
| FBA_DQM<1> | 3.4C 5.5D |
| FBA_DQM<2> | 3.4C 5.4E |
| FBA_DQM<3> | 3.4C 5.5E |
| FBA_DQM<4> | 3.4C 5.4F |
| FBA_DQM<5> | 3.4C 5.5F |
| FBA_DQM<6> | 3.4C 5.4G |
| FBA_DQM<7> | 3.4C 5.5G |
| FBA_DQS_RN<0> | 3.4C 5.4D |
| FBA_DQS_RN<7..0> | 3.4C 5.5G |
| FBA_DQS_RN<1> | 3.4C 5.5D |
| FBA_DQS_RN<2> | 3.4C 5.4E |
| FBA_DQS_RN<3> | 3.4C 5.5E |
| FBA_DQS_RN<4> | 3.4C 5.4F |
| FBA_DQS_RN<5> | 3.4C 5.5F |
| FBA_DQS_RN<6> | 3.4C 5.4G |
| FBA_DQS_RN<7> | 3.4C 5.5G |
| FBA_DQS_WP<0> | 3.4C 5.4D |
| FBA_DQS_WP<7..0> | 3.4C 5.5G |
| FBA_DQS_WP<1> | 3.4C 5.5D |
| FBA_DQS_WP<2> | 3.4C 5.4E |
| FBA_DQS_WP<3> | 3.4C 5.5E |
| FBA_DQS_WP<4> | 3.4C 5.4F |
| FBA_DQS_WP<5> | 3.4C 5.5F |
| FBA_DQS_WP<6> | 3.4C 5.4G |
| FBA_DQS_WP<7> | 3.4C 5.5G |
| FB_PLLVDD | 3.1E< 3.5F< 4.5E< |
| FB_PLLVDD | 3.1E< 3.5F< 4.5E< |
| SNN_FBA_CMD1 | 3.2E |
| SNN_FBA_CMD17 | 3.3E |
| SNN_FBA_CMD18 | 3.3E |
| SNN_FBA_CMD_RFU0 | 3.4E |
| SNN_FBA_CMD_RFU1 | 3.4E |
| SNN_FBA_WCK01 | 3.4E |
| SNN_FBA_WCK01* | 3.4E |
| SNN_FBA_WCK23 | 3.4E |
| SNN_FBA_WCK23* | 3.4E |
| SNN_FBA_WCK45 | 3.4E |
| SNN_FBA_WCK45* | 3.4E |
| SNN_FBA_WCK67 | 3.4E |
| SNN_FBA_WCK67* | 3.4E |
| SNN_FBA_WCK801 | 3.4E |
| SNN_FBA_WCK801* | 3.4E |
| SNN_FBA_WCK823 | 3.5E |
| SNN_FBA_WCK823* | 3.5E |
| SNN_FBA_WCK845 | 3.5E |
| SNN_FBA_WCK845* | 3.5E |
| SNN_FBA_WCK867 | 3.5E |
| SNN_FBA_WCK867* | 3.5E |
| SNN_FB_CLAMP | 3.1E |
| SNN_FB_VREF | 3.5C |
| FBB_CLK0 | 4.4F> 6.2B< 6.2C< 6.4A< |
| FBB_CLK0* | 4.4F> 6.2B< 6.2C< 6.4A< |
| FBB_CLK1 | 4.4F> 6.2E< 6.2G< 6.5A< |
| FBB_CLK1* | 4.4F> 6.2E< 6.2G< 6.5A< |
| FBB_CMD<0> | 4.2E 6.1B 6.1C |
| FBB_CMD<30..0> | 4.3E 6.1B 6.1C 6.1E 6.1G |
| FBB_CMD<2> | 4.2E 4.3E 6.3B 6.3C |
| FBB_CMD<2> | 4.2E 4.3E 6.3B 6.3C |
| FBB_CMD<3> | 4.2E 4.3F 6.2B 6.2C |
| FBB_CMD<3> | 4.2E 4.3F 6.2B 6.2C |
| FBB_CMD<4> | 4.3E 6.2B 6.2C 6.2E 6.2G |
| FBB_CMD<5> | 4.3E 4.3F 6.3B 6.3C 6.3E 6.3G |
| FBB_CMD<5> | 4.3E 4.3F 6.3B 6.3C 6.3E 6.3G |
| FBB_CMD<6> | 4.3E 6.2B 6.2C 6.2E 6.2G |
| FBB_CMD<7> | 4.3E 6.2B 6.2C 6.2E 6.2G |
| FBB_CMD<8> | 4.3E 6.1B 6.1C 6.1E 6.1G |
| FBB_CMD<9> | 4.3E 6.1B 6.1C 6.1E 6.1G |
| FBB_CMD<10> | 4.3E 6.1B 6.1C 6.1E 6.1G |
| FBB_CMD<11> | 4.3E 6.1B 6.1C 6.1E 6.1G |
| FBB_CMD<12> | 4.3E 6.2B 6.2C 6.2E 6.2G |
| FBB_CMD<13> | 4.3E 6.1B 6.1C 6.1E 6.1G |
| FBB_CMD<14> | 4.3E 6.2B 6.2C 6.2E 6.2G |
| FBB_CMD<15> | 4.3E 6.1B 6.1C 6.1E 6.1G |
| FBB_CMD<16> | 4.3E 6.1B 6.1G |
| FBB_CMD<18> | 4.3E 4.3E 6.3E 6.3G |
| FBB_CMD<18> | 4.3E 4.3E 6.3E 6.3G |
| FBB_CMD<19> | 4.3E 4.3F 6.2E 6.2G |
| FBB_CMD<19> | 4.3E 4.3F 6.2E 6.2G |
| FBB_CMD<20> | 4.3E 6.2B 6.2C 6.2E 6.2G |
| FBB_CMD<21> | 4.3E 6.2B 6.2C 6.2E 6.2G |
| FBB_CMD<22> | 4.3E 6.2B 6.2C 6.2E 6.2G |
| FBB_CMD<23> | 4.3E 4.3E 6.3E 6.3G |
| FBB_CMD<24> | 4.3E 6.1B 6.1C 6.1E 6.1G |
| FBB_CMD<25> | 4.3E 6.1B 6.1C 6.1E 6.1G |
| FBB_CMD<26> | 4.3E 6.2B 6.2C 6.2E 6.2G |
| FBB_CMD<27> | 4.3E 6.2B 6.2C 6.2E 6.2G |
| FBB_CMD<28> | 4.3E 6.2B 6.2C 6.2E 6.2G |
| FBB_CMD<29> | 4.3E 6.2B 6.2C 6.2E 6.2G |
| FBB_CMD<30> | 4.3E 6.1B 6.1C 6.1E 6.1G |
| FBB_D<0> | 4.1C 6.4D |
| FBB_D<63..0> | 4.3C 6.5G |
| FBB_D<1> | 4.1C 6.4D |
| FBB_D<2> | 4.1C 6.4D |
| FBB_D<3> | 4.1C 6.4D |
| FBB_D<4> | 4.1C 6.4D |
| FBB_D<5> | 4.1C 6.4D |
| FBB_D<6> | 4.1C 6.4D |
| FBB_D<7> | 4.2C 6.4D |
| FBB_D<8> | 4.2C 6.4G |
| FBB_D<9> | 4.2C 6.4G |
| FBB_D<10> | 4.2C 6.4G |
| FBB_D<11> | 4.2C 6.5D |
| FBB_D<12> | 4.2C 6.5D |
| FBB_D<13> | 4.2C 6.5D |
| FBB_D<14> | 4.2C 6.5D |
| FBB_D<15> | 4.2C 6.5D |
| FBB_D<16> | 4.2C 6.4E |
| FBB_D<17> | 4.2C 6.4E |
| FBB_D<18> | 4.2C 6.4E |
| FBB_D<19> | 4.2C 6.4E |
| FBB_D<20> | 4.2C 6.4E |
| FBB_D<21> | 4.2C 6.4E |
| FBB_D<22> | 4.2C 6.4E |
| FBB_D<23> | 4.2C 6.4E |
| FBB_D<24> | 4.2C 6.4E |
| FBB_D<25> | 4.2C 6.4E |
| FBB_D<26> | 4.2C 6.4E |
| FBB_D<27> | 4.2C 6.5E |
| FBB_D<28> | 4.2C 6.5E |
| FBB_D<29> | 4.2C 6.5E |
| FBB_D<30> | 4.2C 6.5E |
| FBB_D<31> | 4.2C 6.5E |
| FBB_D<32> | 4.2C 6.4F |
| FBB_D<33> | 4.2C 6.4F |
| FBB_D<34> | 4.2C 6.4F |
| FBB_D<35> | 4.2C 6.4F |
| FBB_D<36> | 4.2C 6.4F |
| FBB_D<37> | 4.2C 6.4F |
| FBB_D<38> | 4.2C 6.4F |
| FBB_D<39> | 4.2C 6.4F |
| FBB_D<40> | 4.2C 6.4F |
| FBB_D<41> | 4.2C 6.4F |
| FBB_D<42> | 4.2C 6.4F |
| FBB_D<43> | 4.2C 6.5F |
| FBB_D<44> | 4.2C 6.5F |
| FBB_D<45> | 4.2C 6.5F |
| FBB_D<46> | 4.2C 6.5F |
| FBB_D<47> | 4.2C 6.5F |
| FBB_D<48> | 4.3C 6.4G |
| FBB_D<49> | 4.3C 6.4G |
| FBB_D<50> | 4.3C 6.4G |
| FBB_D<51> | 4.3C 6.4G |
| FBB_D<52> | 4.3C 6.4G |
| FBB_D<53> | 4.3C 6.4G |
| FBB_D<54> | 4.3C 6.4G |
| FBB_D<55> | 4.3C 6.4G |
| FBB_D<56> | 4.3C 6.4G |
| FBB_D<57> | 4.3C 6.4G |
| FBB_D<58> | 4.3C 6.4G |
| FBB_D<59> | 4.3C 6.5G |
| FBB_D<60> | 4.3C 6.5G |
| FBB_D<61> | 4.3C 6.5G |
| FBB_D<62> | 4.3C 6.5G |
| FBB_D<63> | 4.3C 6.5G |
| FBB_DEBUG0 | 4.4E |
| FBB_DEBUG1 | 4.4E |
| FBB_DQM<3> | 4.3C 6.4D |
| FBB_DQM<7..0> | 4.4C 6.5G |
| FBB_DQM<1> | 4.4C 6.5D |
| FBB_DQM<2> | 4.4C 6.4E |
| FBB_DQM<3> | 4.4C 6.5E |
| FBB_DQM<4> | 4.4C 6.4F |
| FBB_DQM<5> | 4.4C 6.5F |
| FBB_DQM<6> | 4.4C 6.4G |
| FBB_DQM<7> | 4.4C 6.5G |
| FBB_DQS_RN<0> | 4.4C 6.4D |
| FBB_DQS_RN<7..0> | 4.4C 6.5G |
| FBB_DQS_RN<1> | 4.4C 6.5D |
| FBB_DQS_RN<2> | 4.4C 6.4E |
| FBB_DQS_RN<3> | 4.4C 6.5E |
| FBB_DQS_RN<4> | 4.4C 6.4F |
| FBB_DQS_RN<5> | 4.4C 6.5F |

| | |
|---|---|
| FBB_DQS_RN<6> | 4.4C 6.4G |
| FBB_DQS_RN<7> | 4.4C 6.5G |
| FBB_DQS_WP<0> | 4.4C 6.4D |
| FBB_DQS_WP<7..0> | 4.4C 6.5G |
| FBB_DQS_WP<1> | 4.4C 6.5D |
| FBB_DQS_WP<2> | 4.4C 6.4E |
| FBB_DQS_WP<3> | 4.4C 6.5E |
| FBB_DQS_WP<4> | 4.4C 6.4F |
| FBB_DQS_WP<5> | 4.4C 6.5F |
| FBB_DQS_WP<6> | 4.4C 6.4G |
| FBB_DQS_WP<7> | 4.4C 6.5G |
| FB_PLLVDD | 3.1E< 3.5F< 4.5E< |
| SNN_FBB_CMD1 | 4.2E |
| SNN_FBB_CMD17 | 4.3E |
| SNN_FBB_CMD_RFU0 | 4.3E |
| SNN_FBB_CMD_RFU1 | 4.4E |
| SNN_FBB_WCK01 | 4.4E |
| SNN_FBB_WCK01* | 4.4E |
| SNN_FBB_WCK23 | 4.4E |
| SNN_FBB_WCK23* | 4.4E |
| SNN_FBB_WCK45 | 4.4E |
| SNN_FBB_WCK45* | 4.4E |
| SNN_FBB_WCK67 | 4.4E |
| SNN_FBB_WCK67* | 4.4E |
| SNN_FBB_WCKB01 | 4.4E |
| SNN_FBB_WCKB01* | 4.4E |
| SNN_FBB_WCKB23 | 4.5E |
| SNN_FBB_WCKB23* | 4.5E |
| SNN_FBB_WCKB45 | 4.5E |
| SNN_FBB_WCKB45* | 4.5E |
| SNN_FBB_WCKB67 | 4.5E |
| SNN_FBB_WCKB67* | 4.5E |
| FBA_CLK0 | 3.4F> 5.2B< 5.2C< 5.4A< |
| FBA_CLK0 | 3.4F> 5.2B< 5.2C< 5.4A< |
| FBA_CLK0* | 3.4F> 5.2B< 5.2C< 5.4A< |
| FBA_CLK0* | 3.4F> 5.2B< 5.2C< 5.4A< |
| FBA_CLK0* | 3.4F> 5.2B< 5.2C< 5.4A< |
| FBA_CLK0_T | 5.4B |
| FBA_CLK1 | 3.4F> 5.2E< 5.2G< 5.5A< |
| FBA_CLK1 | 3.4F> 5.2E< 5.2G< 5.5A< |
| FBA_CLK1* | 3.4F> 5.2E< 5.2G< 5.5A< |
| FBA_CLK1* | 3.4F> 5.2E< 5.2G< 5.5A< |
| FBA_CLK1* | 3.4F> 5.2E< 5.2G< 5.5A< |
| FBA_CLK1_T | 5.5B |
| FBA_CMD<0> | 3.2E 5.1B 5.1C |
| FBA_CMD<0> | 3.2E 5.1B 5.1C |
| FBA_CMD<30..0> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<2> | 3.2E 3.3E 5.3B 5.3C |
| FBA_CMD<2> | 3.2E 3.3E 5.3B 5.3C |
| FBA_CMD<3> | 3.2E 3.3F 5.2B 5.2C |
| FBA_CMD<3> | 3.2E 3.3F 5.2B 5.2C |
| FBA_CMD<4> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<4> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<4> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<4> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<5> | 3.3E 3.3F 5.3B 5.3C 5.3E 5.3G |
| FBA_CMD<5> | 3.3E 3.3F 5.3B 5.3C 5.3E 5.3G |
| FBA_CMD<5> | 3.3E 3.3F 5.3B 5.3C 5.3E 5.3G |
| FBA_CMD<5> | 3.3E 3.3F 5.3B 5.3C 5.3E 5.3G |
| FBA_CMD<6> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<6> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<6> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<6> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<7> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<7> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<7> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<7> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<8> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<8> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<8> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<8> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<9> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<10> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<10> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<10> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<10> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<11> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<11> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<11> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<11> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<12> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<12> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<12> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<12> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<13> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<13> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<13> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<13> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<14> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<14> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<14> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<14> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<15> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<15> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<15> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<15> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<16> | 3.3E 5.1E 5.1G |
| FBA_CMD<18> | 3.3E 3.3E 5.3E 5.3G |
| FBA_CMD<18> | 3.3E 3.3E 5.3E 5.3G |
| FBA_CMD<19> | 3.3E 3.3F 5.2E 5.2G |
| FBA_CMD<19> | 3.3E 3.3F 5.2E 5.2G |
| FBA_CMD<20> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<20> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<20> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<20> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<21> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<21> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<21> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<21> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<22> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<22> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<22> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<22> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<23> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<23> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<23> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<23> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<24> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<24> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<24> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<24> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<25> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<25> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<25> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<25> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<26> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<26> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<26> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<26> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<27> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<27> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<27> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<27> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<28> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<28> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<28> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<28> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<29> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<29> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<29> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<29> | 3.3E 5.2B 5.2C 5.2E 5.2G |
| FBA_CMD<30> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<30> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<30> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_CMD<30> | 3.3E 5.1B 5.1C 5.1E 5.1G |
| FBA_D<0> | 3.1C 5.4D |
| FBA_D<63..0> | 3.3C 5.5G |
| FBA_D<1> | 3.1C 5.4D |
| FBA_D<2> | 3.1C 5.4D |
| FBA_D<3> | 3.1C 5.4D |
| FBA_D<4> | 3.1C 5.4D |
| FBA_D<5> | 3.1C 5.4D |
| FBA_D<6> | 3.1C 5.4D |
| FBA_D<7> | 3.2C 5.4D |
| FBA_D<8> | 3.2C 5.4D |
| FBA_D<9> | 3.2C 5.4D |
| FBA_D<10> | 3.2C 5.4D |
| FBA_D<11> | 3.2C 5.5D |
| FBA_D<12> | 3.2C 5.5D |
| FBA_D<13> | 3.2C 5.5D |
| FBA_D<14> | 3.2C 5.5D |
| FBA_D<15> | 3.2C 5.5D |
| FBA_D<16> | 3.2C 5.4E |
| FBA_D<17> | 3.2C 5.4E |
| FBA_D<18> | 3.2C 5.4E |
| FBA_D<19> | 3.2C 5.4E |
| FBA_D<20> | 3.2C 5.4E |
| FBA_D<21> | 3.2C 5.4E |
| FBA_D<22> | 3.2C 5.4E |
| FBA_D<23> | 3.2C 5.4E |
| FBA_D<24> | 3.2C 5.4E |
| FBA_D<25> | 3.2C 5.4E |
| FBA_D<26> | 3.2C 5.4E |
| FBA_D<27> | 3.2C 5.5E |
| FBA_D<28> | 3.2C 5.5E |
| FBA_D<29> | 3.2C 5.5E |
| FBA_D<30> | 3.2C 5.5E |
| FBA_D<31> | 3.2C 5.5E |
| FBA_D<32> | 3.3C 5.4F |
| FBA_D<33> | 3.3C 5.4F |
| FBA_D<34> | 3.3C 5.4F |
| FBA_D<35> | 3.3C 5.4F |
| FBA_D<36> | 3.3C 5.4F |
| FBA_D<37> | 3.3C 5.4F |
| FBA_D<38> | 3.3C 5.4F |
| FBA_D<39> | 3.3C 5.4F |
| FBA_D<40> | 3.3C 5.4F |
| FBA_D<41> | 3.3C 5.4F |
| FBA_D<42> | 3.3C 5.4F |
| FBA_D<43> | 3.3C 5.5F |
| FBA_D<44> | 3.3C 5.5F |
| FBA_D<45> | 3.3C 5.5F |
| FBA_D<46> | 3.3C 5.5F |
| FBA_D<47> | 3.3C 5.5F |
| FBA_D<48> | 3.3C 5.4G |
| FBA_D<49> | 3.3C 5.4G |
| FBA_D<50> | 3.3C 5.4G |
| FBA_D<51> | 3.3C 5.4G |
| FBA_D<52> | 3.3C 5.4G |
| FBA_D<53> | 3.3C 5.4G |
| FBA_D<54> | 3.3C 5.4G |
| FBA_D<55> | 3.3C 5.4G |
| FBA_D<56> | 3.3C 5.4G |
| FBA_D<57> | 3.3C 5.4G |
| FBA_D<58> | 3.3C 5.4G |
| FBA_D<59> | 3.3C 5.5G |
| FBA_D<60> | 3.3C 5.5G |
| FBA_D<61> | 3.3C 5.5G |
| FBA_D<62> | 3.3C 5.5G |
| FBA_D<63> | 3.3C 5.5G |
| FBA_DQM<3> | 3.3C 5.4D |
| FBA_DQM<7..0> | 3.4C 5.5G |
| FBA_DQM<1> | 3.4C 5.5D |
| FBA_DQM<2> | 3.4C 5.4E |
| FBA_DQM<3> | 3.4C 5.5E |
| FBA_DQM<4> | 3.4C 5.4F |
| FBA_DQM<5> | 3.4C 5.5F |
| FBA_DQM<6> | 3.4C 5.4G |
| FBA_DQM<7> | 3.4C 5.5G |
| FBA_DQS_RN<0> | 3.4C 5.

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FBA_DQS_RN<4> 3.4C 5.4F
FBA_DQS_RN<5> 3.4C 5.5F
FBA_DQS_RN<6> 3.4C 5.4G
FBA_DQS_RN<7> 3.4C 5.5G
FBA_DQS_WP<0> 3.4C 5.4D
FBA_DQS_WP<7..0> 3.4C 5.5G
FBA_DQS_WP<1> 3.4C 5.5D
FBA_DQS_WP<2> 3.4C 5.4E
FBA_DQS_WP<3> 3.4C 5.5E
FBA_DQS_WP<4> 3.4C 5.4F
FBA_DQS_WP<5> 3.4C 5.5F
FBA_DQS_WP<6> 3.4C 5.4G
FBA_DQS_WP<7> 3.4C 5.5G
FBA_VREF 5.3C
FBA_ZQ0 5.3B
FBA_ZQ1 5.3C
FBA_ZQ2 5.3E
FBA_ZQ3 5.3G
SNN_FBA0_J1 5.2B
SNN_FBA0_J9 5.2B
SNN_FBA0_L1 5.2B
SNN_FBA0_L9 5.2B
SNN_FBA1_J1 5.2C
SNN_FBA1_J9 5.2C
SNN_FBA1_L1 5.2C
SNN_FBA1_L9 5.2C
SNN_FBA2_J1 5.2E
SNN_FBA2_J9 5.2E
SNN_FBA2_L1 5.2E
SNN_FBA2_L9 5.2E
SNN_FBA3_J1 5.2G
SNN_FBA3_J9 5.2G
SNN_FBA3_L1 5.2G
SNN_FBA3_L9 5.2G
FBB_CLK0 4.4F> 6.2B< 6.2C<
6.4A<
FBB_CLK0 4.4F> 6.2B< 6.2C<
6.4A<
FBB_CLK0 4.4F> 6.2B< 6.2C<
6.4A<
FBB_CLK0* 4.4F> 6.2B< 6.2C<
6.4A<
FBB_CLK0* 4.4F> 6.2B< 6.2C<
6.4A<
FBB_CLK0_T 6.4B
FBB_CLK1 4.4F> 6.2E< 6.2C<
6.5A<
FBB_CLK1 4.4F> 6.2E< 6.2C<
6.5A<
FBB_CLK1 4.4F> 6.2E< 6.2C<
6.5A<
FBB_CLK1* 4.4F> 6.2E< 6.2C<
6.5A<
FBB_CLK1* 4.4F> 6.2E< 6.2C<
6.5A<
FBB_CLK1* 4.4F> 6.2E< 6.2C<
6.5A<
FBB_CLK1_T 6.5B
FBB_CMD<0> 4.2E 6.1B 6.1C
FBB_CMD<0> 4.2E 6.1B 6.1C
FBB_CMD<30..0> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<2> 4.2E 4.3E 6.3B 6.3C
FBB_CMD<2> 4.2E 4.3E 6.3B 6.3C
FBB_CMD<3> 4.2E 4.3F 6.2B 6.2C
FBB_CMD<3> 4.2E 4.3F 6.2B 6.2C
FBB_CMD<4> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<4> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<4> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<4> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<5> 4.3E 4.3F 6.3B 6.3C
6.3E 6.3G
FBB_CMD<5> 4.3E 4.3F 6.3B 6.3C
6.3E 6.3G
FBB_CMD<5> 4.3E 4.3F 6.3B 6.3C
6.3E 6.3G
FBB_CMD<6> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<6> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<6> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<6> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<7> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<7> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<7> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<8> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<8> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<8> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<9> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<9> 4.3E 6.1B 6.1C 6.1E
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FBB_CMD<9> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<9> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<10> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<10> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<10> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<10> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<11> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<11> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<11> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<11> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<12> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<12> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<12> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<13> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<13> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<13> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<14> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<14> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<14> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<15> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<15> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<15> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<16> 4.3E 6.1E 6.1G
FBB_CMD<16> 4.3E 6.1E 6.1G
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FBB_CMD<18> 4.3E 4.3E 6.3E 6.3G
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6.2G
FBB_CMD<20> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<20> 4.3E 6.2B 6.2C 6.3E
6.2G
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6.2G
FBB_CMD<21> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<21> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<22> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<22> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<22> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<23> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<23> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<24> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<24> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<24> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<25> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<25> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<25> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<26> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<26> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<26> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<27> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<27> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<27> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<28> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<28> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<28> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<28> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<29> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<29> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<29> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<29> 4.3E 6.2B 6.2C 6.3E
6.2G
FBB_CMD<30> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<30> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<30> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_CMD<31> 4.3E 6.1B 6.1C 6.1E
6.1G
FBB_D<0> 4.1C 6.4D
FBB_D<63..0> 4.3C 6.5G
FBB_D<1> 4.1C 6.4D
FBB_D<2> 4.1C 6.4D
FBB_D<3> 4.1C 6.4D
FBB_D<4> 4.1C 6.4D
FBB_D<5> 4.1C 6.4D
FBB_D<6> 4.1C 6.4D
FBB_D<7> 4.2C 6.4D
FBB_D<8> 4.2C 6.4D
FBB_D<9> 4.2C 6.4D
DACA_RED_C 6.3G> 9.2F<
DACA_RSET 8.3B
DACA_VDD 8.3B
DACA_VREF 8.3B
DACA_VSYNC 8.3D
DACA_VSYNC_C 8.3G> 9.3F<
DACA_H3_BUF 8.2E
DACA_RED 8.3D
DACA_RED_C 6.3G> 9.2F<
DACA_RSET 8.3B
DACA_VDD 8.3B
DACA_VREF 8.3B
DACA_VSYNC 8.3D
DACA_VSYNC_C 8.3G> 9.3F<
DACA_H3_BUF 8.3E
I2CA_SCL 8.1C<> 9.2F<
I2CA_SDA 8.2C<> 9.2F<
DACA_BLUE_C 8.4G> 9.3F<
DACA_GREEN_C 8.4G> 9.3F<
DACA_HSYNC_C 8.3G> 9.3F<
DACA_RED_C 8.3G> 9.2F<
DACA_VSYNC_C 8.3G> 9.3F<
GPIO14_DVI_HPD 9.3G
GPIO14_DVI_HPD_C 9.2G
GPIO14_DVI_HPD_Q 9.3E
GPIO14_DVI_HPD_R 9.3E
I2CA_SCL 8.1C<> 9.2F<
I2CA_SDA 8.2C<> 9.2F<
IFPAB_IOVDD 9.2C
IFPAB_PLLVDD 9.2C
IFPAB_TXC 9.2D
IFPAB_TXC* 9.1D
IFPAB_TXD0 9.2D
IFPAB_TXD0* 9.2D
IFPAB_TXD1 9.2D
IFPAB_TXD1* 9.2D
IFPAB_TXD2 9.2D
IFPAB_TXD2* 9.2D
IFPAB_TXD4 9.2D
IFPAB_TXD4* 9.2D
IFPAB_TXDS 9.2D
IFPAB_TXDS* 9.2D
IFPAB_TXD6 9.3D
IFPAB_TXD6* 9.3D
GPIO15_NORTH_HPD 10.3D
GPIO15_NORTH_HPD_C 10.3G< 11.3F>
GPIO15_NORTH_HPD_Q 10.3E
GPIO15_NORTH_HPD_R 10.3E
I2CW_SCL_C 10.2G
I2CW_SDA_C 10.2G
IFPCEF_IOVDD 10.3B< 12.3B<
IFPCEF_PLLVDD 10.2B< 12.2B<
IFPC_AUX 10.1E<> 11.2D<>
IFPC_AUX* 10.1E<> 11.2D<>
IFPC_RSET 10.2B
IFPC_TXC 10.2C< 11.3D<
IFPC_TXC* 10.2C< 11.2D<
IFPC_TXC_C 10.2G
IFPC_TXC_C* 10.2G
IFPC_TXD0 10.2C< 11.3D<
IFPC_TXD0* 10.2C< 11.3D<
IFPC_TXD0_C 10.3G
IFPC_TXD0_C* 10.3G
IFPC_TXD1 10.2C< 11.3D<
IFPC_TXD1* 10.2C< 11.3D<
IFPC_TXD1_C 10.3G
IFPC_TXD1_C* 10.3G
IFPC_TXD2 10.2C< 11.3D<
IFPC_TXD2* 10.2C< 11.3D<
IFPC_TXD2_C 10.3G
IFPC_TXD2_C* 10.3G
IFP_TERM_EN 10.4F> 12.5E< 13.3E<
SNN_HDMI_CEC 10.2G
SNN_HDMI_RSVD 10.2G
DP_MODE 11.1G
DP_MODE* 11.3E
DP_MODE_C* 11.2H
DP_MODE_Q* 11.2H
GPIO15_NORTH_HPD_C 10.3G< 11.3F>
I2CW_SCL_Q 11.2E
I2CW_SDA_Q 11.1E
IFPC_AUX 10.1E<> 11.2D<>
IFPC_AUX* 10.1E<> 11.2D<>
IFPC_AUX_C 11.2F
IFPC_AUX_C* 11.2F
IFPC_L0_C 11.3F
IFPC_L0_C* 11.3F
IFPC_L1_C 11.3F
IFPC_L1_C* 11.3F
IFPC_L2_C 11.3F
IFPC_L2_C* 11.3F
IFPC_L3_C 11.2F
IFPC_L3_C* 11.2F
IFPC_TXC 10.2C< 11.3D<
IFPC_TXC* 10.2C< 11.2D<
IFPC_TXD0 10.2E< 11.3D<
IFPC_TXD0* 10.2E< 11.3D<
IFPC_TXD1 10.2E< 11.3D<
IFPC_TXD1* 10.2E< 11.3D<
IFPC_TXD2 10.2E< 11.3D<
IFPC_TXD2* 10.2E< 11.3D<
IFP_DP_CEC 11.3H
GPIO38_MID_HPD 12.3D
GPIO38_MID_HPD_C 12.4G< 13.2F>
GPIO38_MID_HPD_Q 12.3E
GPIO38_MID_HPD_R 12.3E
I2CY_SCL 12.2D
I2CY_SCL_C 12.2G> 13.2F<
I2CY_SDA 12.2D
I2CY_SDA_C 12.2G<> 13.2F<>
IFPCEF_IOVDD 10.3B< 12.3B<
IFPCEF_PLLVDD 10.2B< 12.2B<
IFPEF_RSET 12.2B
IFPEF_TXC 12.2E< 13.2D<
IFPEF_TXC* 12.2D< 13.2D<
IFPEF_TXC_C 12.3G
IFPEF_TXC_C* 12.3G
IFPEF_TXD0 12.2E< 13.3D<
IFPEF_TXD0* 12.2E< 13.3D<
IFPEF_TXD0_C 12.3G
IFPEF_TXD0_C* 12.3G
IFPEF_TXD1 12.2E< 13.3D<
IFPEF_TXD1* 12.2E< 13.3D<
IFPEF_TXD1_C 12.3G
IFPEF_TXD1_C* 12.3G
IFPEF_TXD2 12.2E< 13.3D<
IFPEF_TXD2* 12.2E< 13.3D<
IFPEF_TXD2_C 12.3G
IFPEF_TXD2_C* 12.3G
IFPEF_TXD3 12.3D
IFPEF_TXD3* 12.3D
IFPEF_TXD3_C 12.3G
IFPEF_TXD3_C* 12.3G
IFPEF_TXD4 12.4D
IFPEF_TXD4* 12.4D
IFPEF_TXD4_C 12.3G
IFPEF_TXD4_C* 12.3G
IFPEF_TXD5 12.4D
IFPEF_TXD5* 12.4D
IFPEF_TXD5_C 12.3G
IFPEF_TXD5_C* 12.3G
IFP_TERM_EN 10.4F> 12.5E< 13.3E<
SNN_8 12.3G
SNN_G 12.3G
SNN_GPIO19 12.4D
SNN_HSYNC 12.4G
SNN_IFPF_AUX 12.3D
SNN_IFPF_AUX* 12.3D
SNN_IFPF_TXC 12.3D
SNN_IFPF_TXC* 12.3D
SNN_R 12.3G
SNN_VSYNC 12.3G
GPIO38_MID_HPD_C 12.4G< 13.2F>
I2CY_SCL_C 12.2G> 13.2F<
I2CY_SDA_C 12.2G<> 13.2F<>
IFPEF_TXC 12.2E< 13.2D<
IFPEF_TXC* 12.2E< 13.2D<
IFPEF_TXC_C2 13.2G
IFPEF_TXC_C2* 13.2G
IFPEF_TXD0 12.2E< 13.3D<
IFPEF_TXD0* 12.2E< 13.3D<
IFPEF_TXD0_C2 13.3G
IFPEF_TXD0_C2* 13.2G
IFPEF_TXD1 12.2E< 13.3D<
IFPEF_TXD1* 12.2E< 13.3D<
IFPEF_TXD1_C2 13.3G
IFPEF_TXD1_C2* 13.3G
IFPEF_TXD2 12.2E< 13.3D<
IFPEF_TXD2* 12.2E< 13.3D<
IFPEF_TXD2_C2 13.3G
IFPEF_TXD2_C2* 13.3G
IFP_TERM_EN 10.4F> 12.5E< 13.3E<
SNN_HDMI_CEC 13.2C
SNN_LHDMI_RSVD 13.2G
SNN_GPIO17 14.5H
SNN_GPU_AD6 14.5B
SNN_GPU_AJ4 14.5B
SNN_GPU_AJ5 14.5B
SNN_GPU_AJ10 14.4B
SNN_GPU_AL11 14.5B
SNN_GPU_C15 14.5B
SNN_GPU_C16 14.3F
SNN_GPU_D19 14.5B
SNN_GPU_D20 14.5B
SNN_GPU_D23 14.5B
SNN_GPU_D26 14.5B
SNN_GPU_H31 14.5B
SNN_GPU_T8 14.5B
SNN_GPU_V32 14.5B
SNN_GPU_W32 14.3F
SNN_IFPD_AUX 14.4H
SNN_IFPD_AUX* 14.4H
SNN_IFPD_IOVDD 14.5F
SNN_IFPD_L0 14.5H
SNN_IFPD_L0* 14.5H
SNN_IFPD_L1 14.5H
SNN_IFPD_L1* 14.5H
SNN_IFPD_L2 14.5H
SNN_IFPD_L2* 14.5H
SNN_IFPD_L3 14.5H
SNN_IFPD_L3* 14.4H
SNN_IFPD_PLLVDD 14.4F
SNN_IFPD_RSET 14.4F
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GPIO0_NVVID4 15.2C> 19.4D<
GPIO1_NVVID3 15.2C> 19.4C<
GPIO4_FAN_TACH 15.2E< 16.1C>
GPIO5_NVVID1 15.2E> 19.5C<
GPIO6_NVVID2 15.2E> 19.4C<
GPIO8_OVERT* 15.2F> 17.1C< 17.2E<
GPIO9_FAN_PWM 15.2D
GPIO11_NVVID0 15.2E> 19.5C<
GPIO13_NVVID5 15.2E> 19.3D<
GPIO16_FAN_PWM 15.2D
GPIO_FAN_PWM 15.3F> 16.1E<
JTAG_TCK 15.2C
JTAG_TDI 15.2C
JTAG_TDO 15.2C
JTAG_TMS 15.2C
JTAG_TRST* 15.2C
MULTISTRAP_REF_GND 15.3C
PLLVDD 15.4C
ROM_CS* 15.3D
ROM_SCLK 15.3E< 16.3B>
ROM_SI 15.3E< 16.3B>
ROM_SO 15.3E< 16.3B>
SNN_BURST* 15.3D
SNN_CEC 15.3D
SNN_GPIO2 15.2D
SNN_GPIO3 15.2D
SNN_GPIO7 15.2D
SNN_GPIO10 15.2D
SNN_GPIO12 15.2D
SNN_GPIO20 15.2D
SNN_GPIO21 15.2D
SNN_I2CB_SCL 15.1D
SNN_I2CB_SDA 15.1D
SNN_I2CC_SCL 15.1D
SNN_I2CC_SDA 15.3D
SNN_I2CS_SCL 15.1D
SNN_I2CS_SDA 15.1D
STRAP0 15.3C< 16.4B>
STRAP1 15.3C< 16.4B>
STRAP2 15.3C< 16.4B>
STRAP3 15.3C< 16.4B>
STRAP4 15.3C< 16.4B>
SYS_RST 15.2F< 17.3E>
THERMDN 15.1C
THERMDP 15.1C
THERM_N 15.2G
THERM_P 15.2H
THERM_PP 15.2G
THERM_SHUTDOWN* 15.1H> 19.3A<
XTALIN 15.4C
XTALOUT 15.4D
XTALOUTBUFF 15.4D
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FAN_PWM_NOT 16.2E
FAN_PWM_NOT_D 16.2F
FAN_PWM_NOT_D* 16.2F
GPIO4_FAN_TACH 15.2E< 16.1C>
GPIO_FAN_GND 16.2D
GPIO_FAN_PWM 15.3F> 16.1E<
GPIO_FAN_PWM_OUT 16.2B
GPIO_FAN_R 16.2C
ROM_SCLK 15.3E< 16.3B>
ROM_SI 15.3E< 16.3B>
ROM_SO 15.3E< 16.3B>
SNN_MECH_D1A 16.3F
SNN_MECH_D1B 16.3F
SNN_MECH_D1C 16.3F
SNN_MECH_D2A 16.3F
SNN_MECH_D2B 16.3F
SNN_MECH_D2C 16.3F
SNN_MECH_D3A 16.3G
SNN_MECH_D3B 16.3G
SNN_MECH_D3C 16.3G
SNN_MECH_D4A 16.3G
SNN_MECH_D4B 16.3G
SNN_MECH_D4C 16.3G
SNN_MECH_D5A 16.3F
SNN_MECH_D5B 16.3F
SNN_MECH_D5C 16.3F
SNN_MECH_D6A 16.4F
SNN_MECH_D6B 16.4F
SNN_MECH_D6C 16.4F
SNN_MECH_D7A 16.3G
SNN_MECH_D7B 16.3G
SNN_MECH_D7C 16.3G
SNN_MECH_D8A 16.4G
SNN_MECH_D8B 16.4G
SNN_MECH_D8C 16.4G
STRAP0 15.3C< 16.4B>
STRAP1 15.3C< 16.4B>
STRAP2 15.3C< 16.4B>
STRAP3 15.3C< 16.4B>
STRAP4 15.3C< 16.5B>
GPIO8_OVERT* 15.2F< 17.1C< 17.2E<
GPIO9_OVERT* 15.3> < 17.1C< 17.2E<
PEX_RST* 2.2C< 17.3F<
PS_5V_PROT 17.3C
PS_5V_PROT_D 17.3B
PS_PEXVDD_FB 17.2C
PS_PEX_BOOT 17.1E
PS_PEX_EN 17.1D
PS_PEX_FB_CP 17.1E
PS_PEX_PGOOD 17.1D
PS_PEX_PHASE 17.1E
PS_PLL_ADJ 17.2F
SNN_3V3RUSE_OC 17.4E
SNN_PS_PEX_NC 17.2B
SNN_PS_PEX_POK 17.2B
SNN_PS_PLL_NC 17.2E
SNN_PS_PLL_POK 17.2E
SYS_RST 15.2F< 17.3E>
PS_FB_BOOT 18.2C
PS_FB_CP_RC 18.3C
PS_FB_EN 18.2B
PS_FB_EN* 18.3B
PS_FB_FB 18.2C
PS_FB_FB_RC 18.3F
PS_FB_LG 18.2C
PS_FB_NV 18.3A
PS_FB_PHASE 18.2C
PS_FB_SNUB 18.3F
PS_FB_UG 18.2C
PS_FB_UG_R 18.2D
PS_FB_VCC 18.2B
3V3_PEX 19.1F
12V_PEX 19.1F
FBVDDQ 19.1F
GPIO0_NVVID4 15.2E> 19.4D<
GPIO0_NVVID4_Q 19.3E
GPIO1_NVVID3 15.2E> 19.4C<
GPIO1_NVVID3_Q 19.4E
GPIO5_NVVID1 15.2E< 19.5C<
GPIO5_NVVID1_Q 19.5E
GPIO6_NVVID2 15.2E> 19.4C<
GPIO8_NVVID2_Q 19.4E
GPIO11_NVVID0 15.2E> 19.5C<
GPIO11_NVVID0_Q 19.5E
GPIO13_NVVID5 15.2E> 19.3D<
GPIO13_NVVID5_Q 19.3E
NVVDD 19.1F
NVVDD_SENSE 2.4G> 19.3H<
PEX_VDD 19.1F
PS_NVVDD_BOOT 19.2C
PS_NVVDD_CF_RC 19.3C
PS_NVVDD_EN 19.2B
PS_NVVDD_ENF 19.3B
PS_NVVDD_FB 19.2C
PS_NVVDD_FB_RC 19.3F
PS_NVVDD_LG 19.2C
PS_NVVDD_PHASE 19.2C
PS_NVVDD_SNUB 19.2F
PS_NVVDD_UG 19.2C
PS_NVVDD_UG_R 19.1D
PS_NVVDD_VCC 19.2B
PS_NVVDD_VSEN 19.3G
THERM_SHUTDOWN* 15.1H> 19.3A<

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