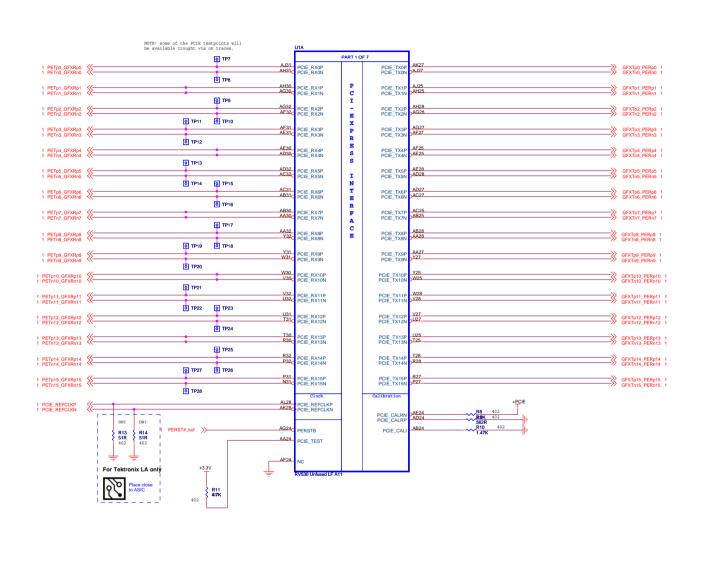




 $\dot{\uparrow}$

ANALOG GROUND



ATI Technologies Inc.

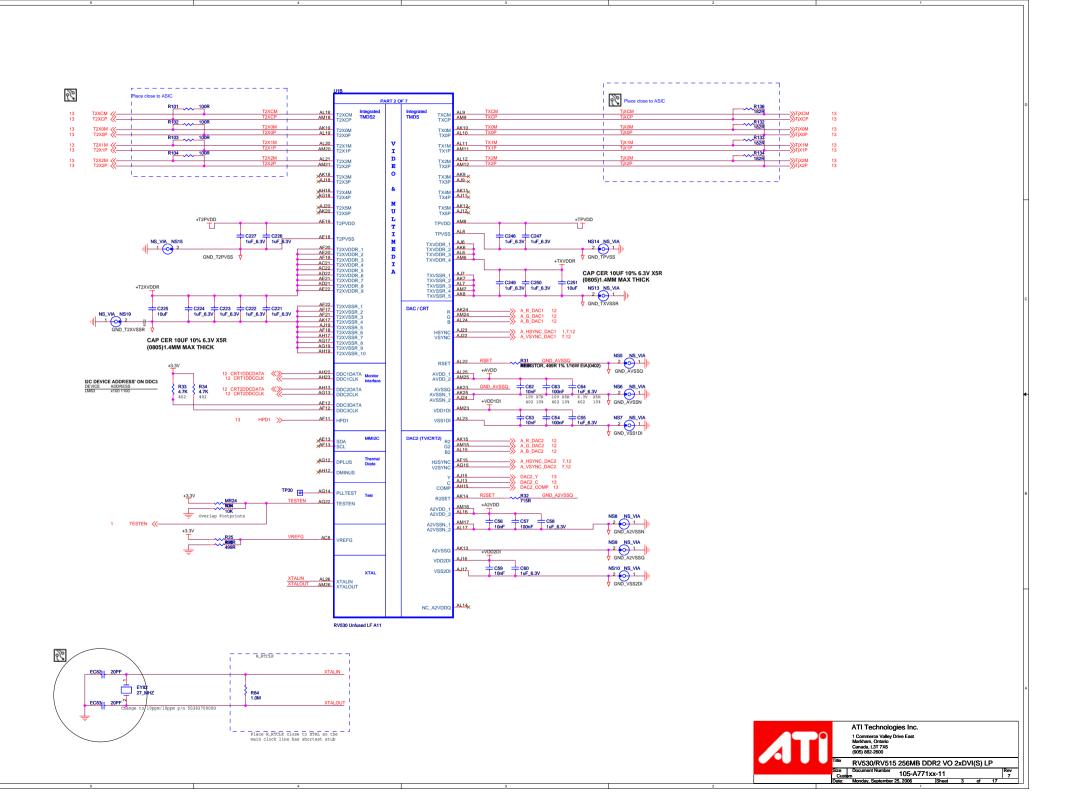
1 Commerce Valley Drive East
Meditham, Ottatio
Consols, 157726
(0) 982-2000

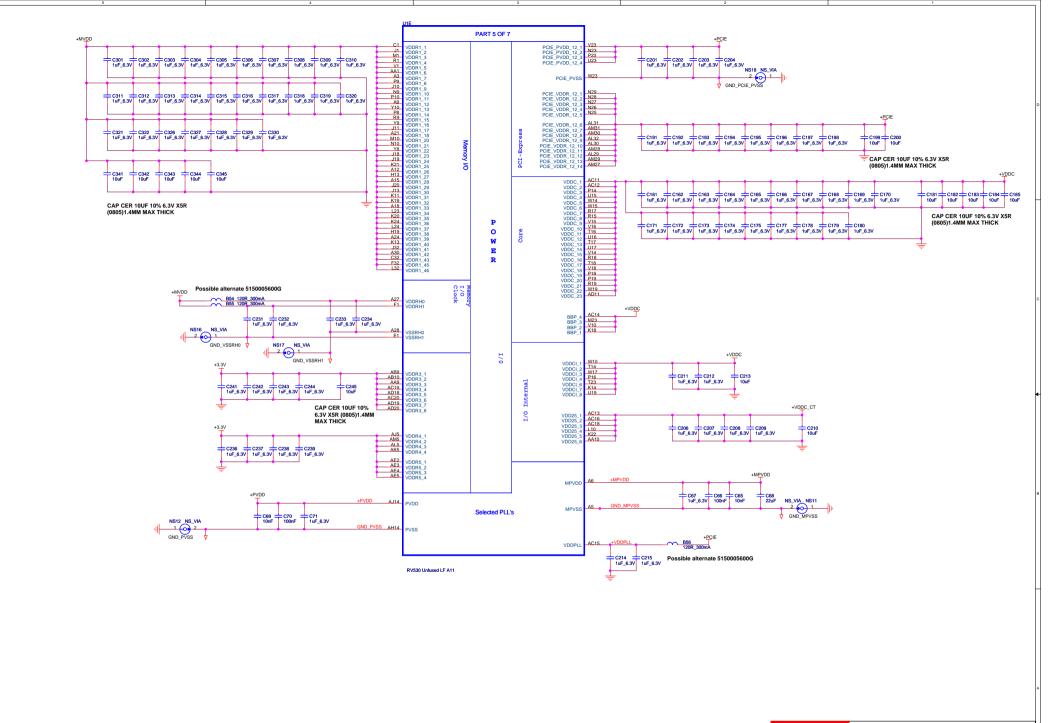
Table

Stop
C
Document Number
105-A771xx-11

Dotte: Thursday, September 21, 2006

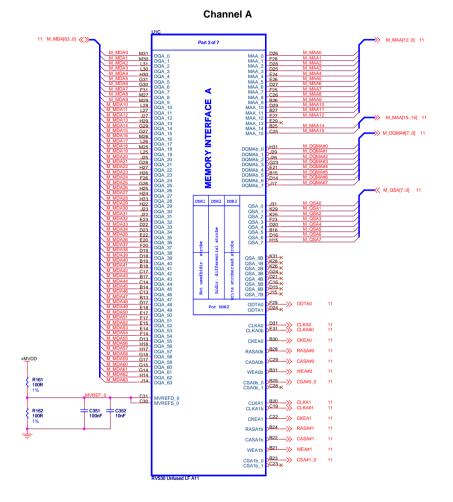
Sheet 2 of 17



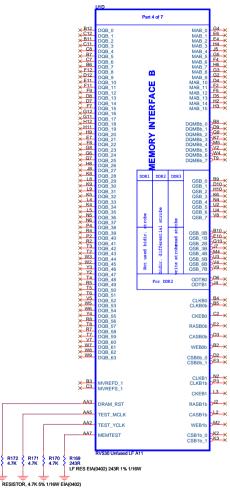




RV530 MEMORY CHANNELS A and B



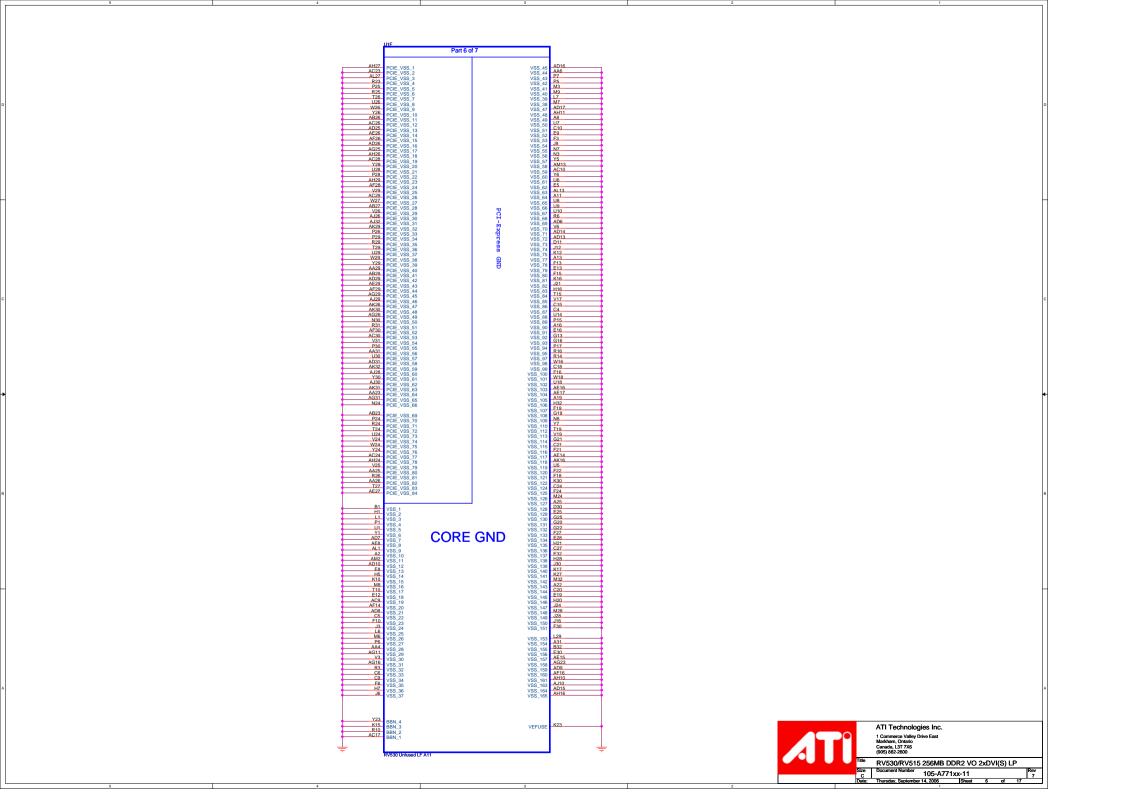
Channel B

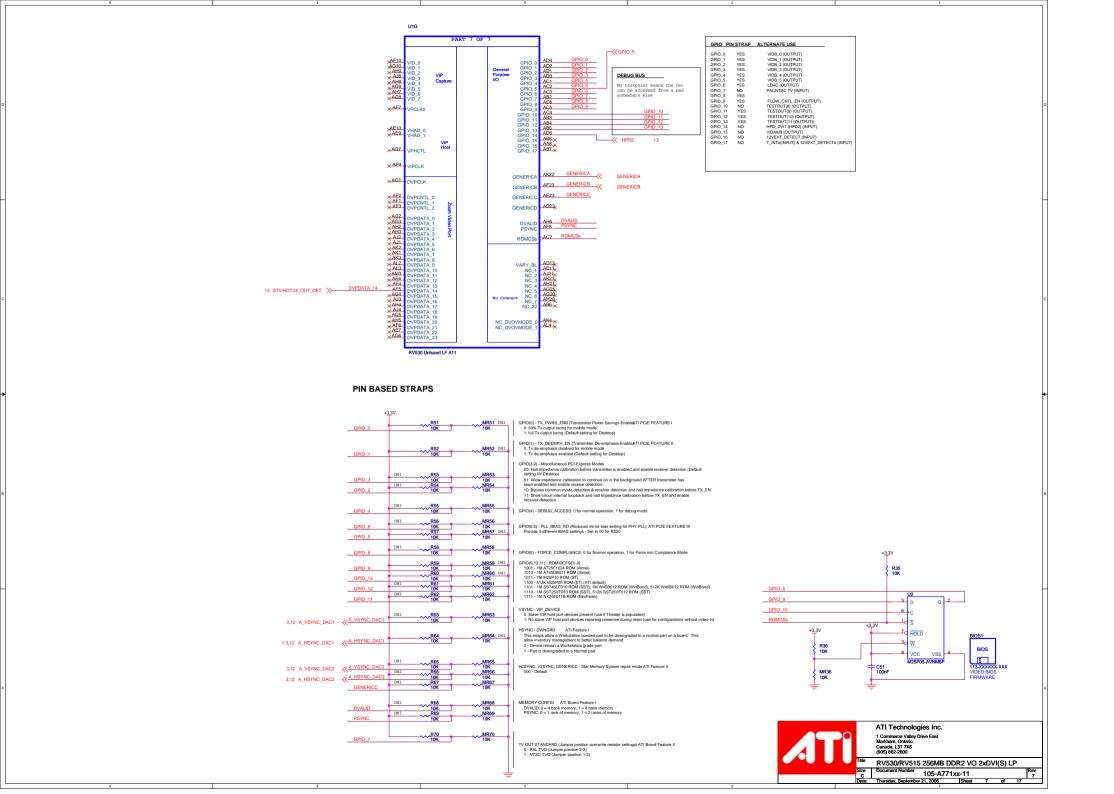


R172 R171 4.7K 4.7K

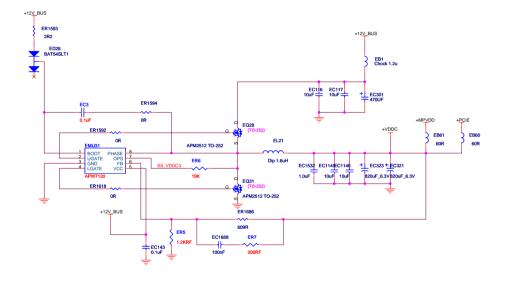
R170 4.7K





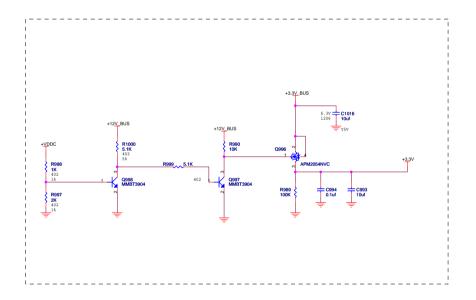


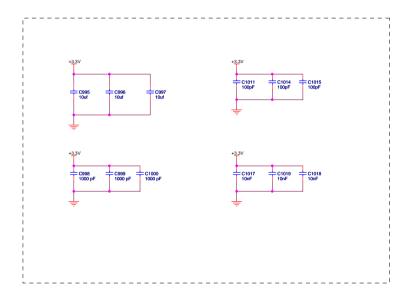
CORE REGULATOR +VDDC



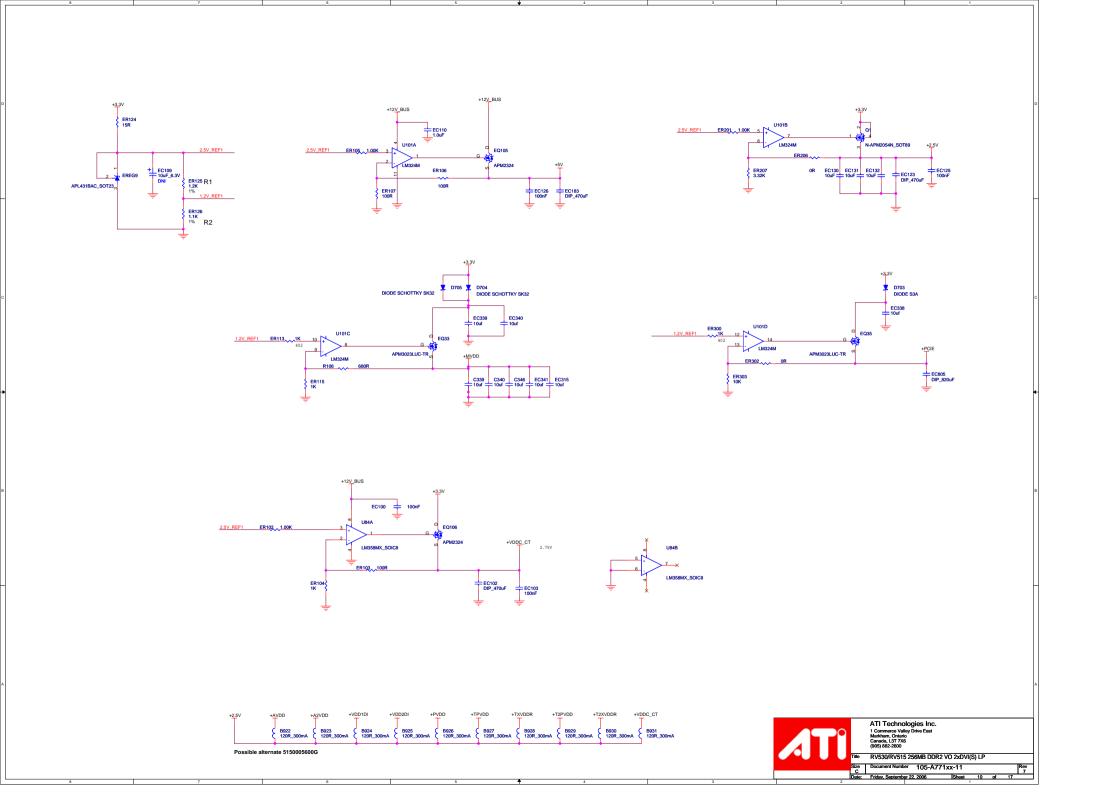












CHANNEL A: RANK 0 128MB DDR2 5 M_QSA[7..0] 《>> 5 M MDA[63..0] ≪≫ VDDQ1 VDDQ2 VDDQ3 VDDQ4 VDDQ6 VDDQ7 VDDQ8 VDDQ8 VDDQ10 CLKA#0 K8 CLKA0 CKEA1 >>-CKEA1 >>-→ +MVDD CASA#1 >> L7 B201 120R_300mA M Possible alternate 5150005600G M B202 120R_300mA Possible alternate 5150005600G B203 120R_300mA Possible alternate 5150005600G B204 120R_300mA Possible alternate 5150005600G VDDL VSSDL VDDL VSSDL VDDL VSSDL VDDL VSSDL ODTA0 >>-M_QSA2 F7 R211 10R E8 VSSQ1 VSSQ2 VSSQ3 VSSQ4 VSSQ5 VSSQ6 VSSQ7 VSSQ8 VSSQ9 VSSQ10 M_QSA1 B7 R214 10R A8 M_QSA7 R216______10R M_QSA4 R218 ______ 10R R212 ~~ NC#A2 NC#E2 VC#L1 VC#R3 IC#R7 IC#R8 VSS1 VSS2 VSS3 VSS4 VSS5 VSS1 VSS2 VSS3 VSS4 VSS5 R202 4.99K R204 4.99K R206 4.99K + C463 100nF R208 4.99K C413 100nF C438 100nF C406 C407 C408 C409 C410 1uF_6.3V 1uF_6.3V 1uF_6.3V 1uF_6.3V 402 402 R222 56R 402 R209 4.99K R219 4.99K ATI Technologies Inc. R210 4.99K R220 4.99K RV530/RV515 256MB DDR2 VO 2xDVI(S) LP

