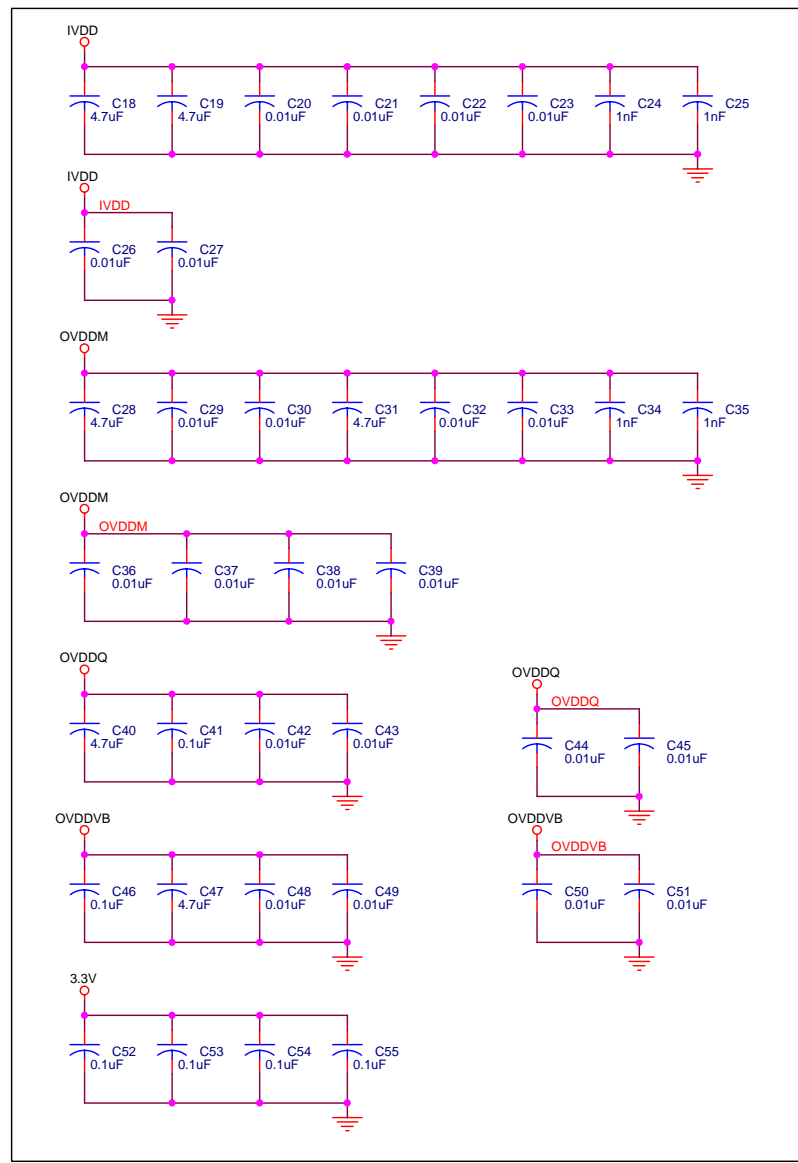
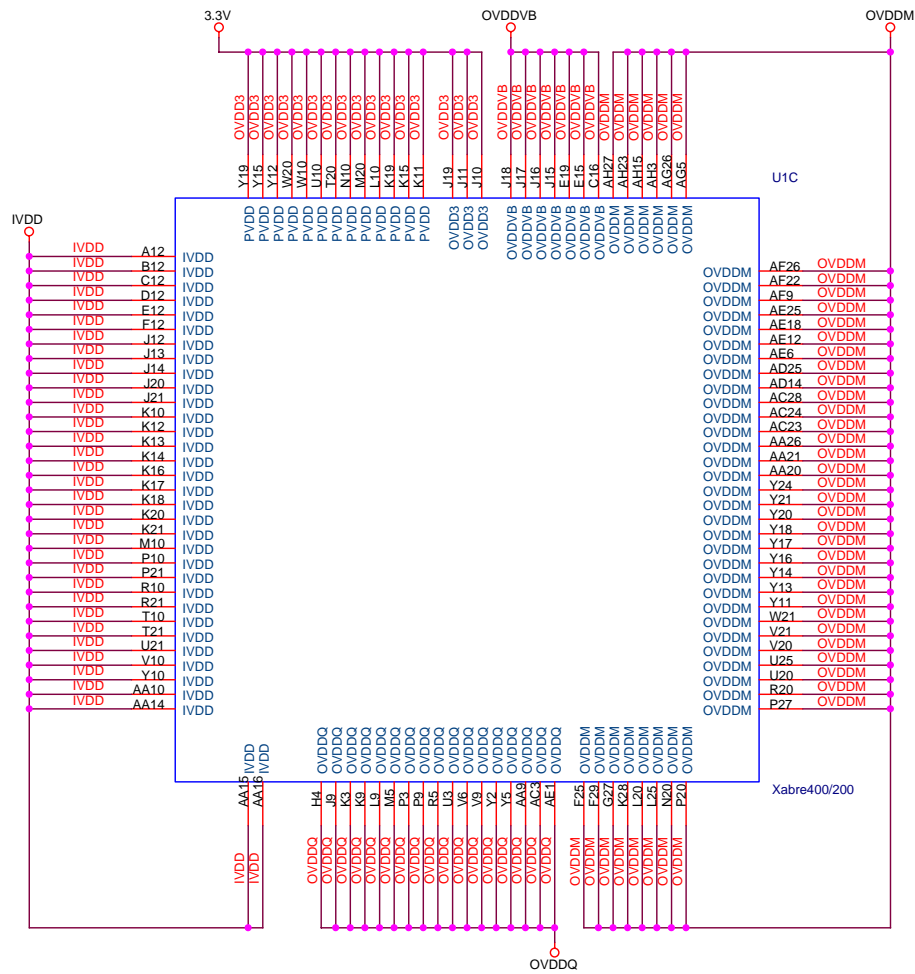
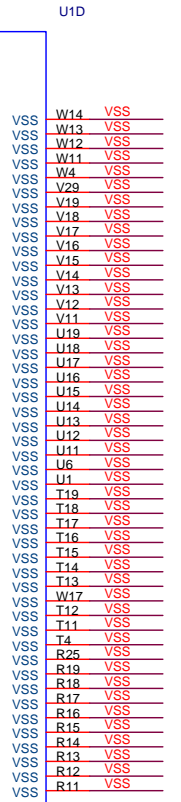
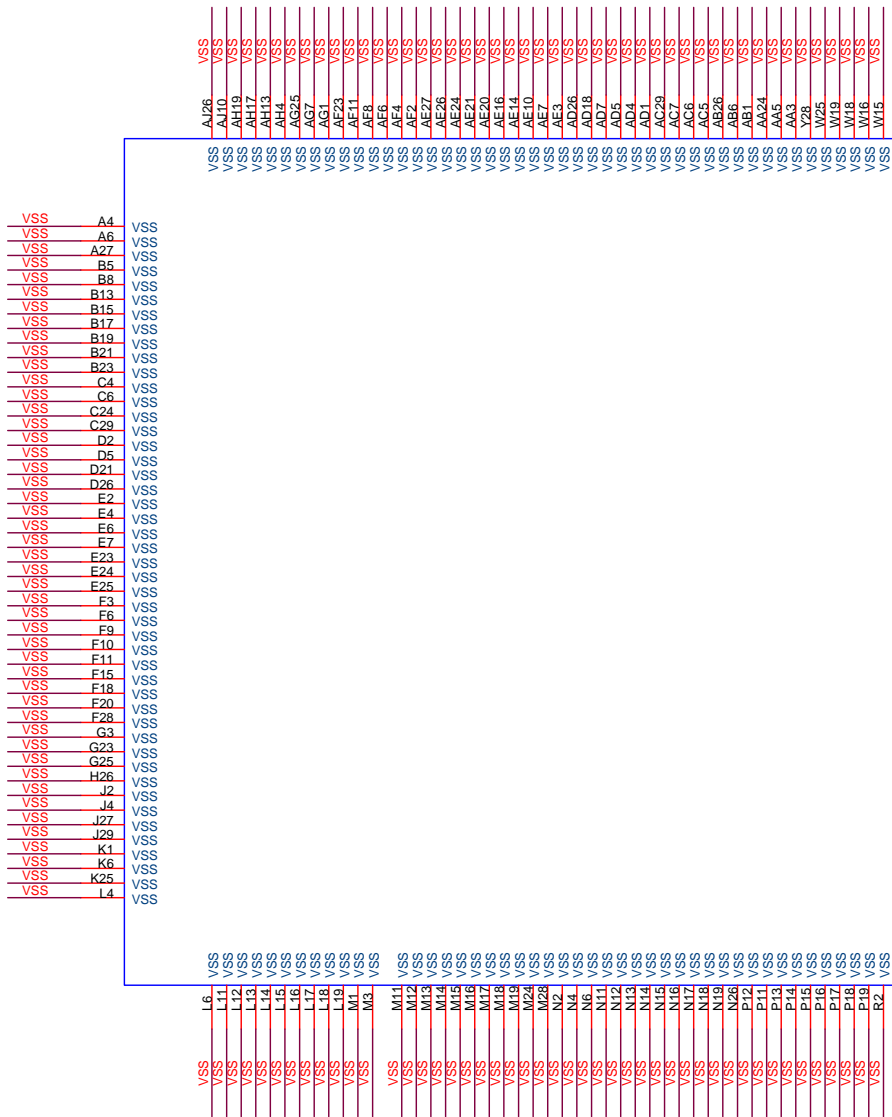


Xabre		
File Name:XABRE_2.SCH		
Title		
Xabre_B		
Size	Document Number	Rev
B	Xabre 4M/8MX16 DDR (Xabre012)	0.9
Date:	Thursday, January 16, 2003	Sheet 2 of 12

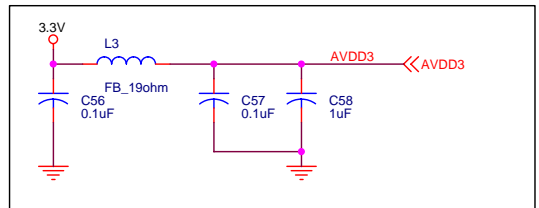


BYPASS CAPACITOR FOR Xabre (SOLDER SIDE)

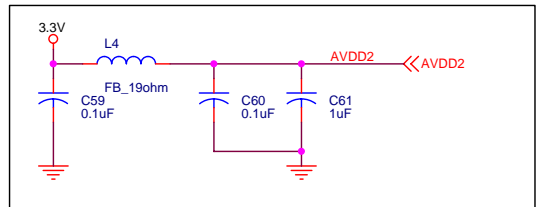
Xabre		
File Name:XABRE_3.SCH		
Title		
Xabre_C		
Size	Document Number	Rev
B	Xabre 4M/8MX16 DDR (Xabre012)	0.9
Date:	Thursday, January 16, 2003	Sheet 3 of 12



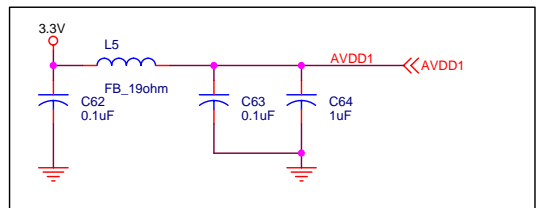
Xabre400/200



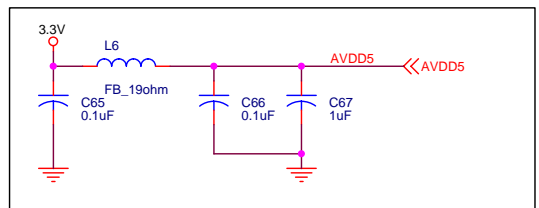
MCLK GEN.



ECLK GEN.

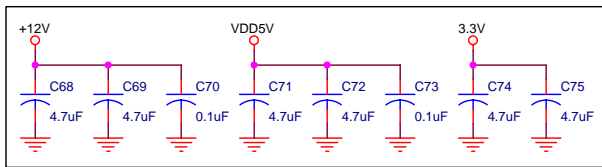


DCLK GEN.

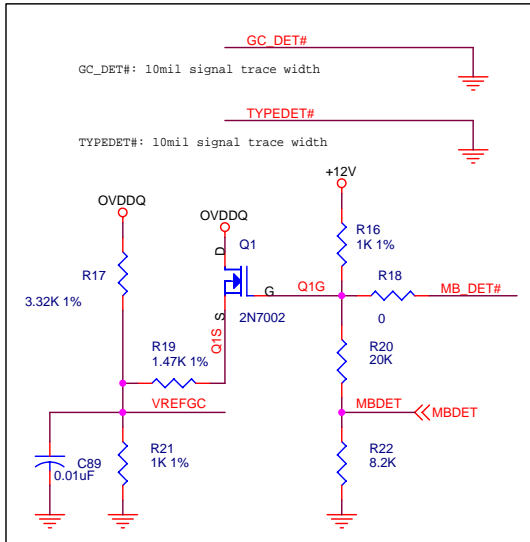
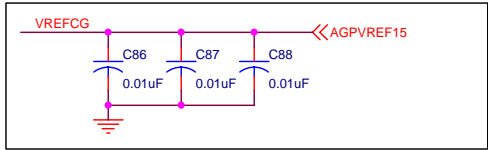


BCLK DLL

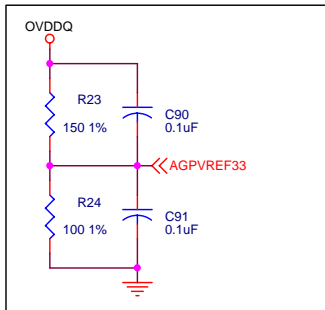
Xabre		
File Name:XABRE_4.SCH		
Title		
Xabre_D		
Size	Document Number	Rev
B	Xabre 4M/8MX16 DDR (Xabre012)	0.9
Date:	Thursday, January 16, 2003	Sheet 4 of 12



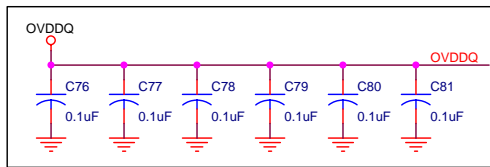
Bypass capacitor for AGP connector



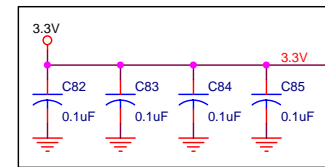
1.5V level for AGP4X AND AGP8X Modes



3.3V level for AGP2X Mode



Bypass capacitor for AGP connector

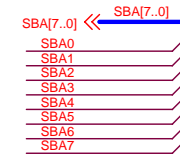
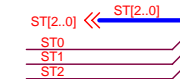
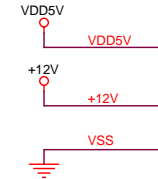
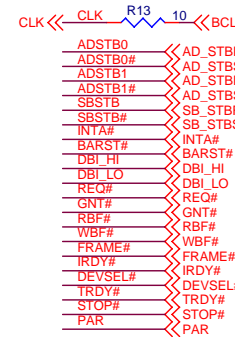


Bypass capacitor for AGP connector

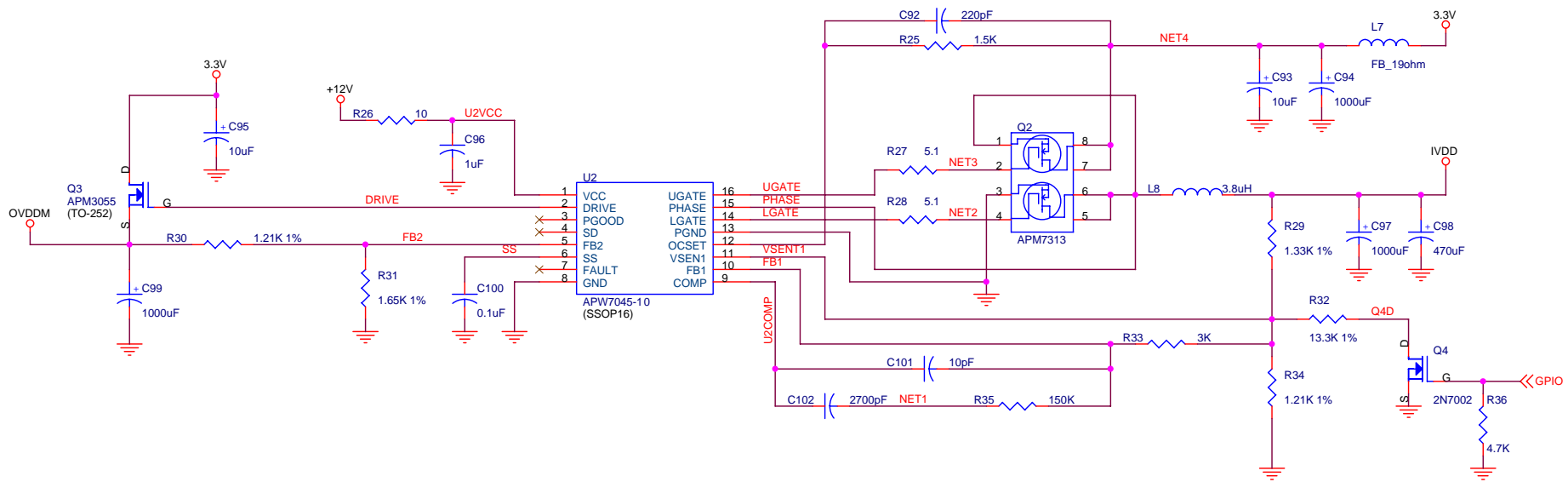
VDD5V	B01	OVRCNT	A01	+12V
VDD5V	B02	5.0V	A02	TYPEDET#
VDD5V	B03	5.0V	A03	GC_DET#
VSS	B04	USB+	A04	VSS
VSS	B05	GND	A05	INTA#
CLK	B06	INTB#	A06	INTA#
REQ#	B07	CLK	A07	BARST#
3.3V	B08	REQ	A08	GNT#
ST0	B09	VCC3.3	A09	ST1
ST2	B10	ST0	A10	MB_DET#
RBF#	B11	ST2	A11	DBI_HI
VSS	B12	RBF	A12	DBI_HI
DBI_LO	B13	GND	A13	VSS
SBA0	B14	DBI_LO	A14	WBF#
3.3V	B15	WBF	A15	SBA1
SBA2	B16	SBA0#	A16	3.3V
SBSTB	B17	VCC3.3	A17	SBA3
VSS	B18	SBA2#	A18	SBSTB#
VSS	B19	SB_STBF	A19	VSS
SBA4	B20	GND	A20	SBA5
SBA6	B21	SBA4#	A21	SBA7
	B22	SBA6#	A22	
	B23	RESERVED	A23	
	B24	GND	A24	
	B25	3.3AUX	A25	
AD31	B26	VCC3.3	A26	AD30
AD29	B27	AD30	A27	AD28
3.3V	B28	AD28	A28	3.3V
AD27	B29	VCC3.3	A29	AD26
AD25	B30	AD26	A30	AD24
VSS	B31	AD24	A31	VSS
ADSTB1	B32	GND	A32	ADSTB1#
AD23	B33	AD_STF1	A33	C#BE3
OVDDQ	B34	AD23	A34	OVDDQ
AD19	B35	VDDQ1.5	A35	AD22
VSS	B36	AD21	A36	AD20
AD17	B37	AD19	A37	VSS
C#BE2	B38	GND	A38	AD18
OVDDQ	B39	AD17	A39	AD16
IRDY#	B40	C#BE2	A40	OVDDQ
	B41	VDDQ1.5	A41	FRAME#
	B42	FRAME	A42	
	B43	RES#KEY	A43	
	B44	GND#KEY	A44	
	B45	RES#KEY	A45	
DEVSEL#	B46	3.3#KEY	A46	TRDY#
OVDDQ	B47	DEVSEL	A47	STOP#
	B48	VDDQ1.5	A48	
	B49	PERR	A49	VSS
	B50	GND	A50	PAR
C#BE1	B51	SERR	A51	AD15
OVDDQ	B52	C#BE1	A52	OVDDQ
AD14	B53	VDDQ1.5	A53	AD13
AD12	B54	AD14	A54	AD11
VSS	B55	AD12	A55	VSS
AD10	B56	GND	A56	AD9
AD8	B57	AD10	A57	C#BE0
OVDDQ	B58	AD8	A58	OVDDQ
ADSTB0	B59	VDDQ1.5	A59	ADSTB0#
AD7	B60	AD_STF0	A60	AD8
VSS	B61	AD7	A61	VSS
AD5	B62	GND	A62	AD4
AD3	B63	AD5	A63	AD2
OVDDQ	B64	AD3	A64	OVDDQ
AD1	B65	VDDQ1.5	A65	AD0
VREFCG	B66	AD1	A66	VREFCG

AGP3.0-B

AGP3.0-A

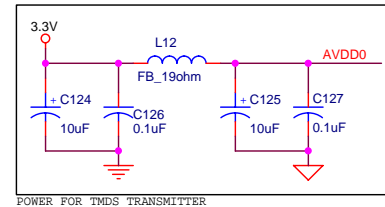
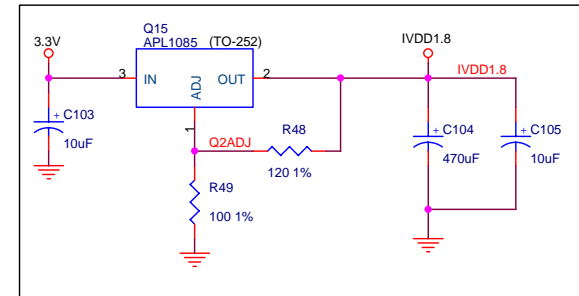
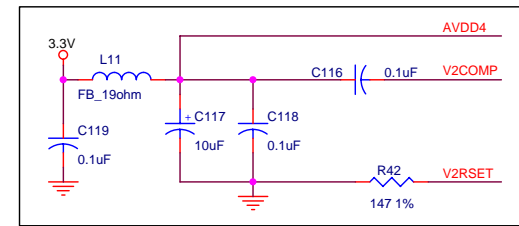
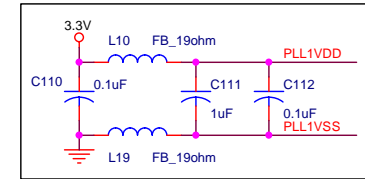
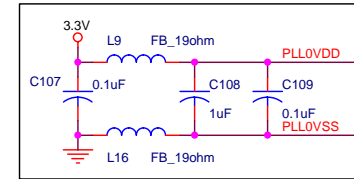
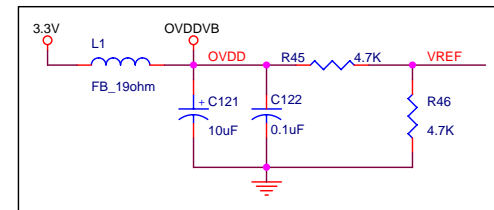
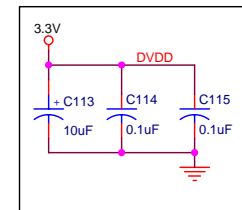
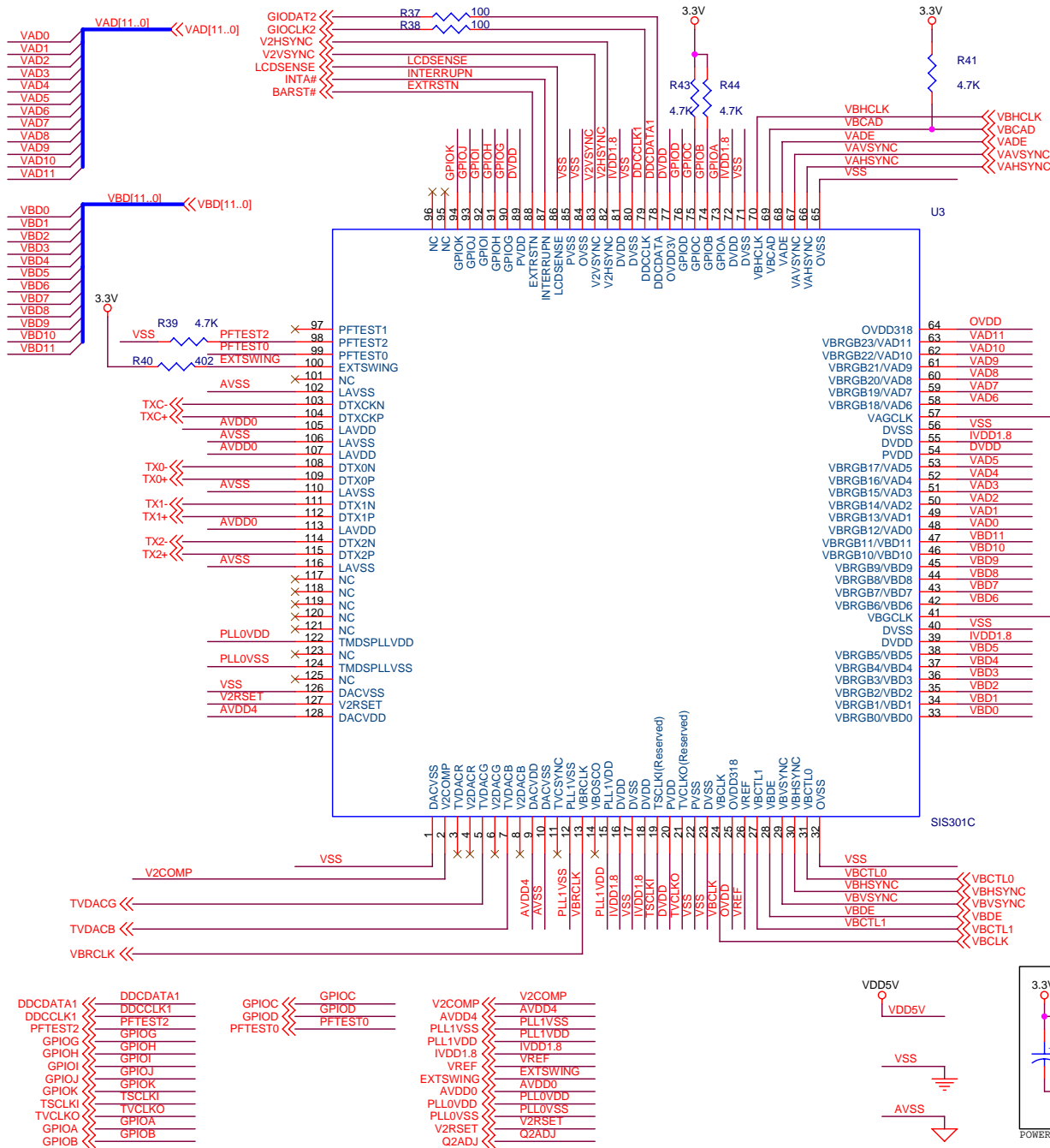


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File Name:AGP8X.SCH		
Title		
AGP		
Size	Document Number	Rev
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Date:	Thursday, January 16, 2003	Sheet 5 of 12

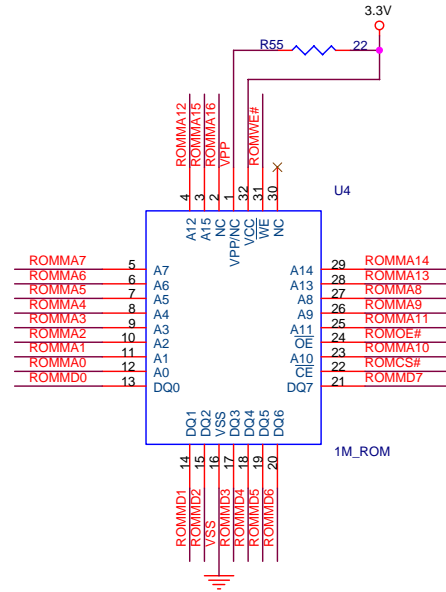
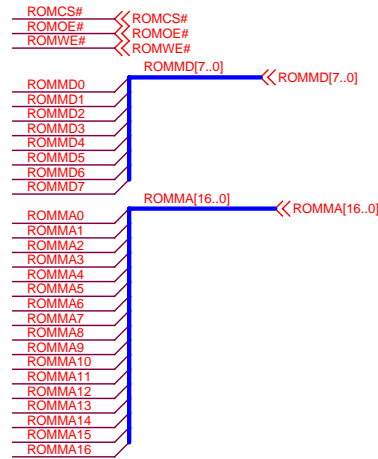
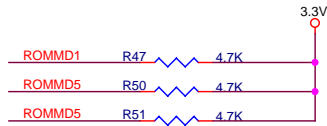


U2VCC	U2VCC
DRIVE	DRIVE
FB2	FB2
SS	SS
SS	SS
U2COMP	U2COMP
NET1	NET1
FB1	FB1
VSENT1	VSENT1
LGATE	LGATE
PHASE	PHASE
UGATE	UGATE
NET2	NET2
NET3	NET3
NET4	NET4
Q4D	Q4D

Xabre		
File Name:POWER.SCH		
Title		
Power		
Size	Document Number	Rev
B	Xabre 4M/8MX16 DDR (Xabre012)	0.9
Date:	Thursday, January 16, 2003	Sheet 6 of 12

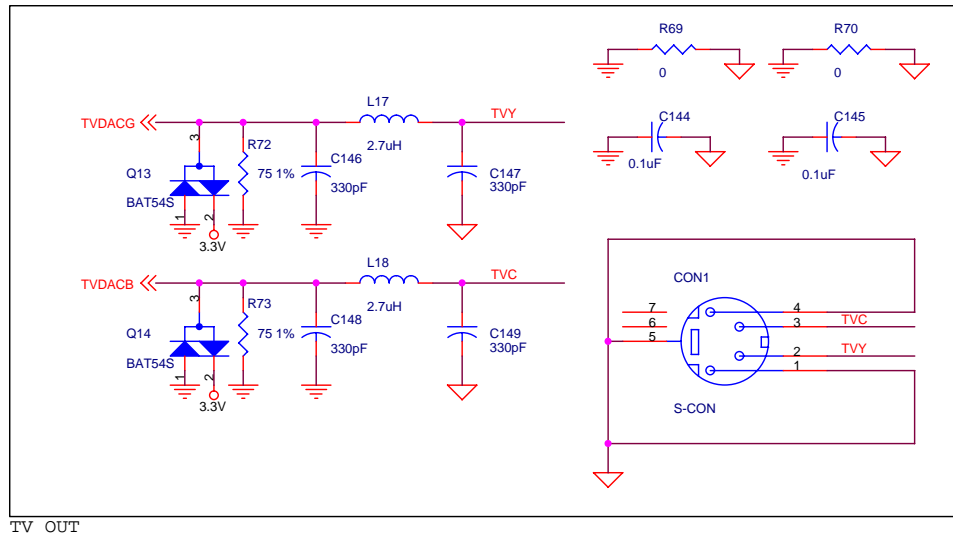
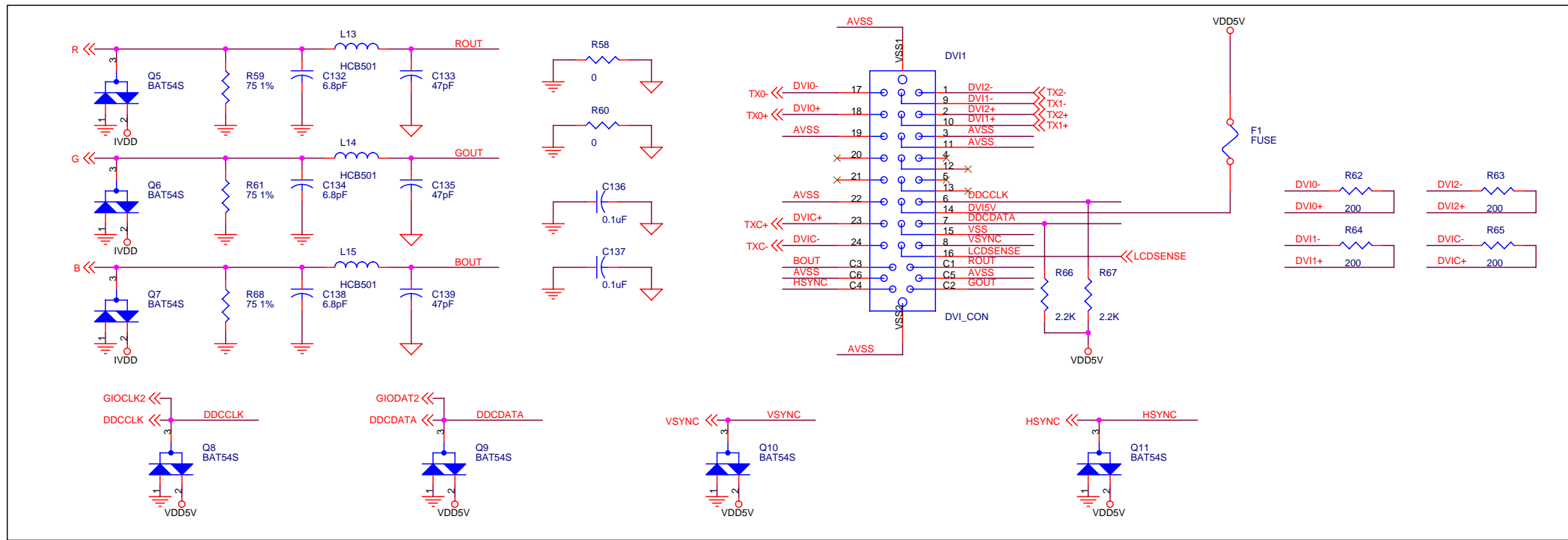


Xabre		
File Name:SiS301C.SCH		
Title		
SiS301C		
Size	Document Number	Rev
	Xabre 4M/8MX16 DDR (Xabre012)	
Date:	Thursday, January 16, 2003	Sheet 7 of 12



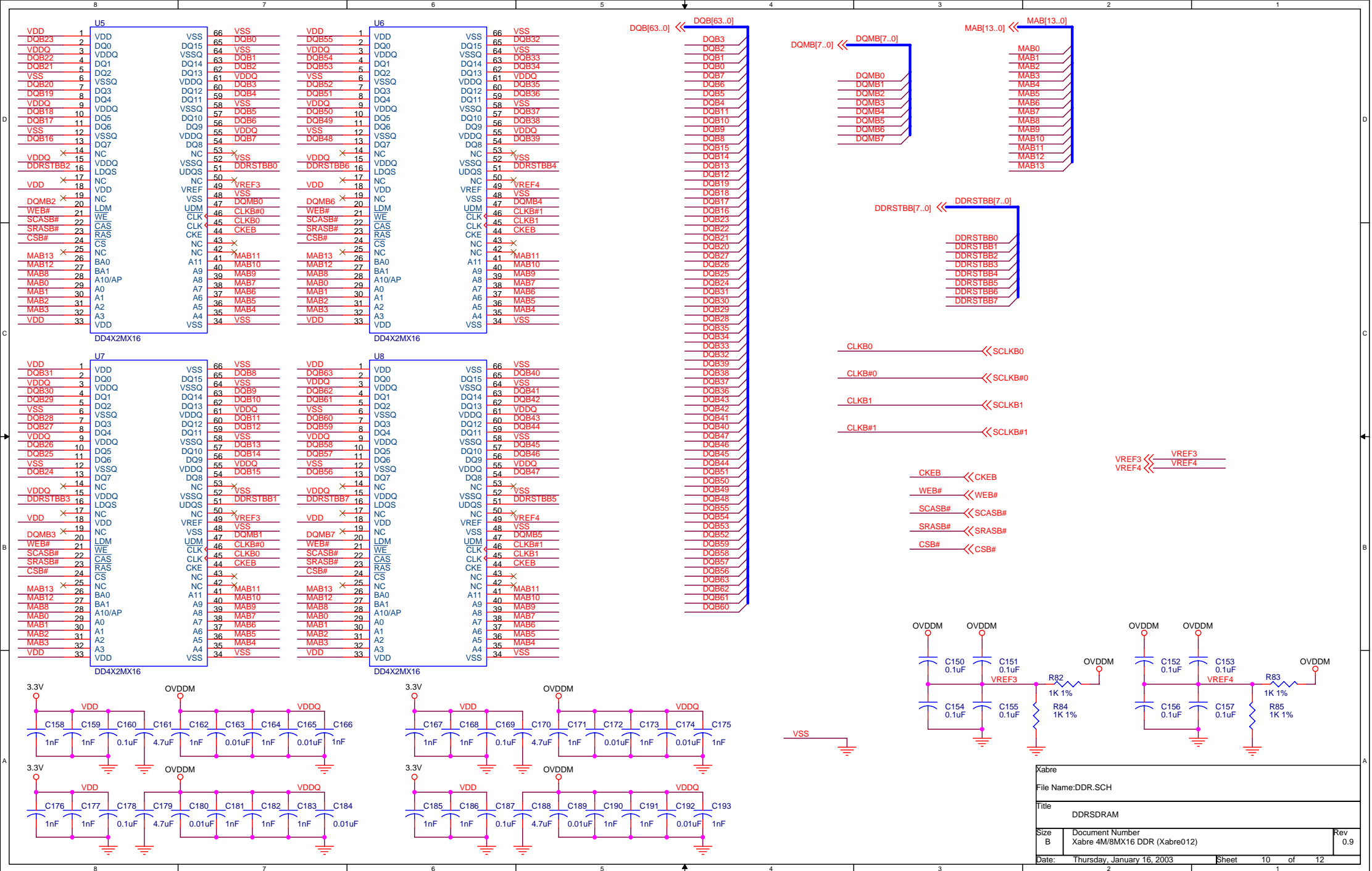
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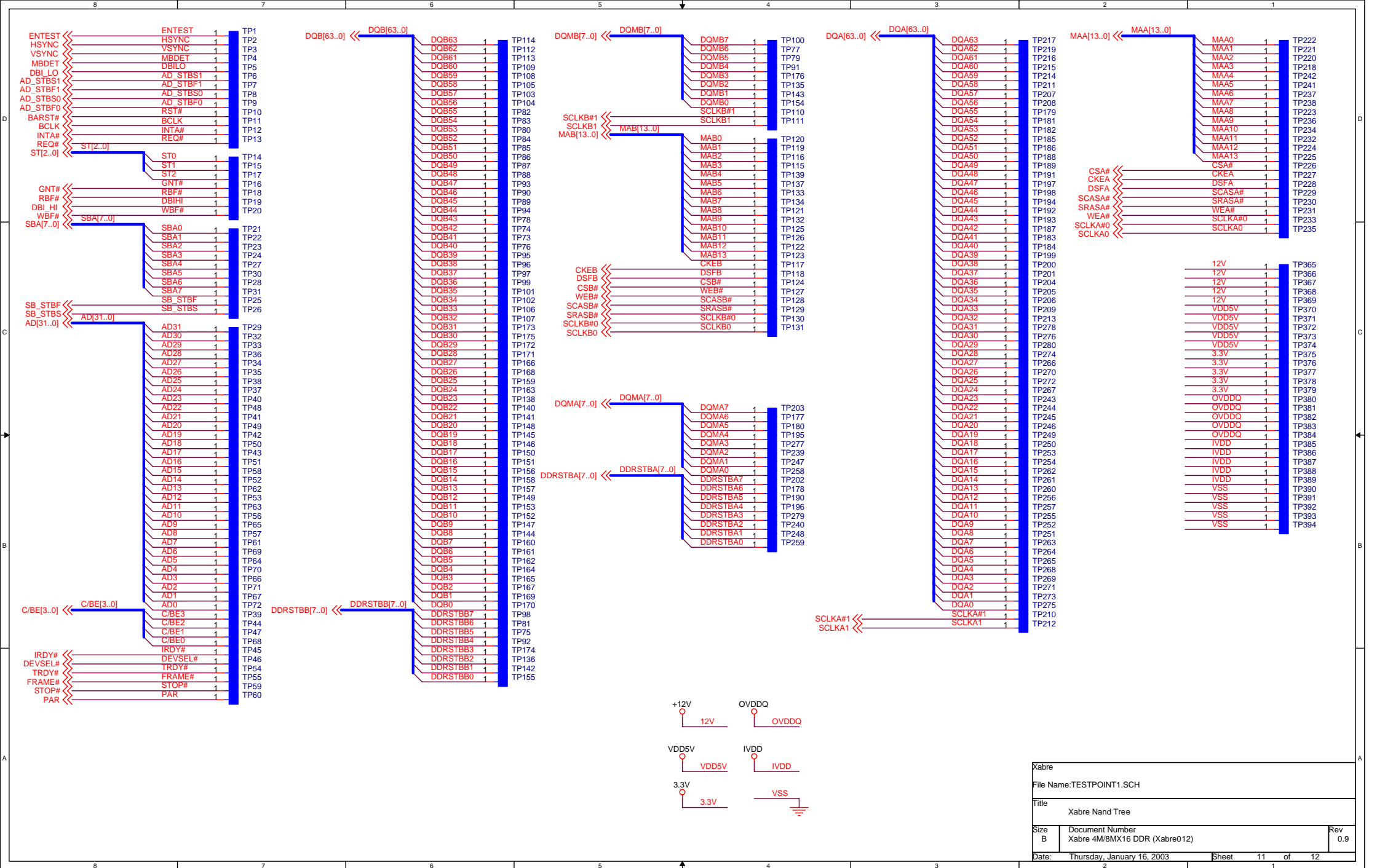
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File Name:ROM.SCH		
Title		
ROM/HW TRAPPING		
Size	Document Number	Rev
B	Xabre 4M/8MX16 DDR (Xabre012)	0.9
Date:	Thursday, January 16, 2003	Sheet 8 of 12

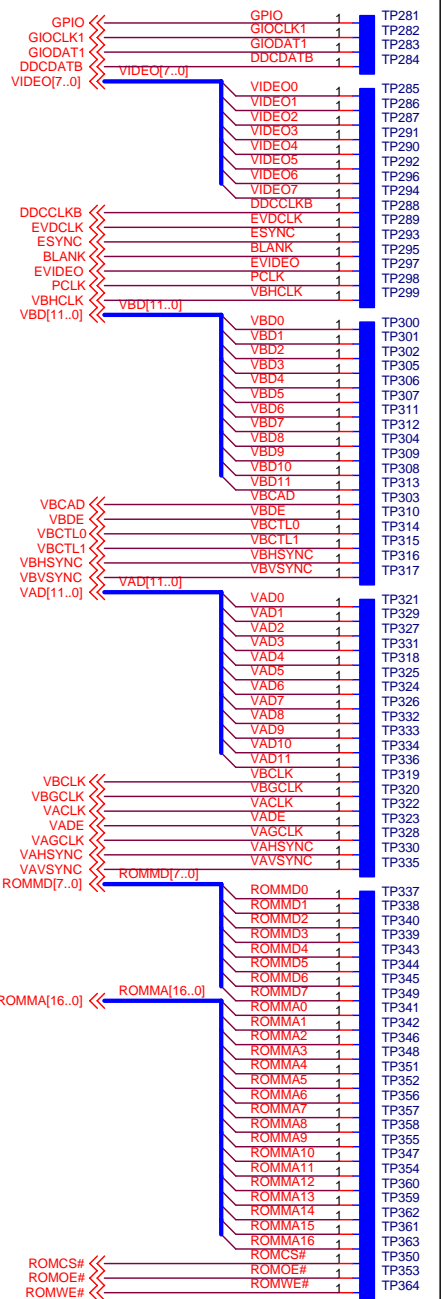


AVSS

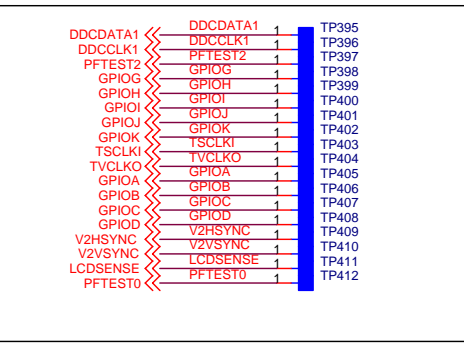
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File Name:OUTPUT.SCH		
Title		
OUTPUTS		
Size	Document Number	Rev
B	Xabre 4M/8MX16 DDR (Xabre012)	0.9
Date:	Thursday, January 16, 2003	Sheet 9 of 12



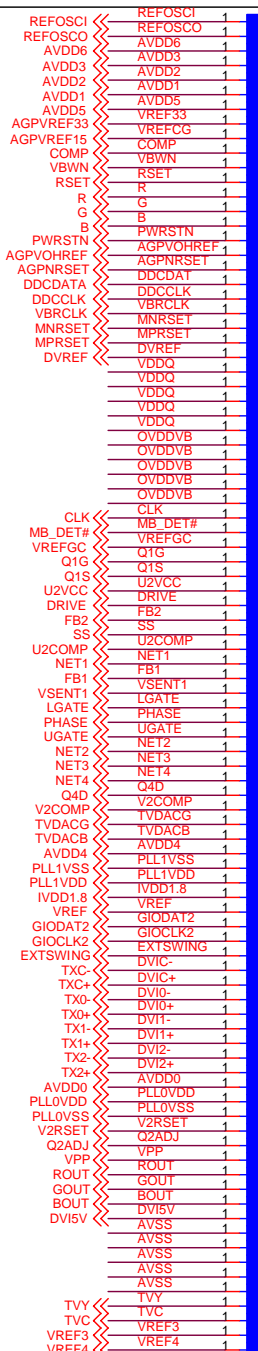
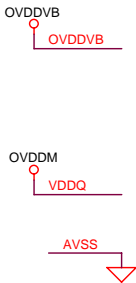




Xabre Nand Tree



SiS301C Supplement Nand Tree



Others Test Points

Xabre		
File Name:TESTPOINT2.SCH		
Title Xabre Nand Tree & Others		
Size B	Document Number Xabre 4M/8MX16 DDR (Xabre012)	Rev 0.9
Date: Thursday, January 16, 2003	Sheet 12	of 12