

G94-P562-A00 - GDDR3, DVI/VGA + DVI/VGA

REV	VARIANT	NVPN	ASSEMBLY
0	BASE	600-10562-0000-000	PS47 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU000	600-10562-0000-000	G94-400 625MHz/900MHz 512MB 18Mb/s2 BGA136 GDDR3, DVI-I-DL+DVI-I-DL
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REVISION HISTORY:

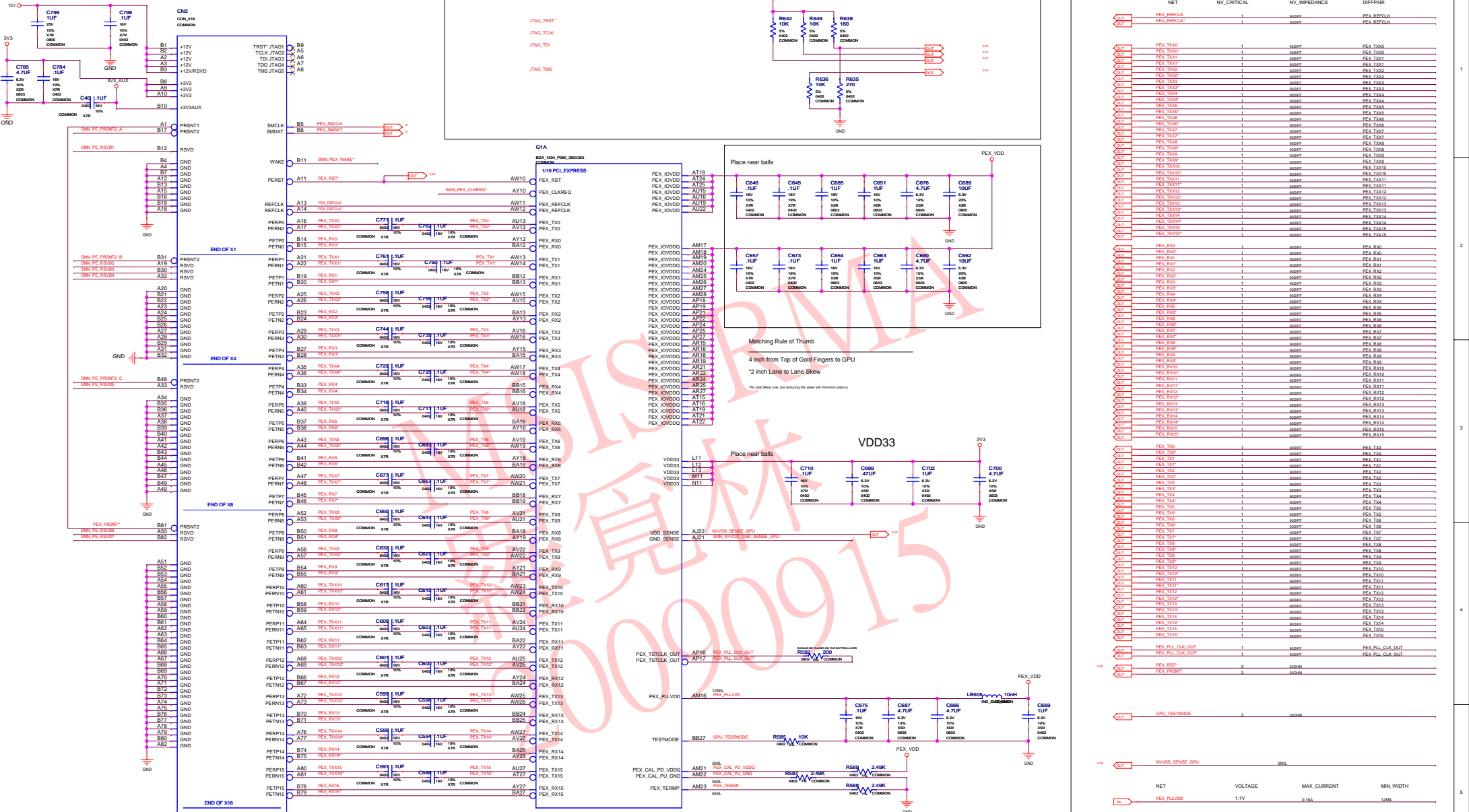
- 2.0 Base on 1.0 modify to four layer
Page 2: Remove J501

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PAGE DETAIL	Overview


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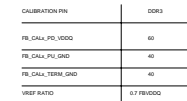


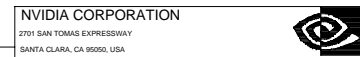
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PAGE DETAIL	PCI Express

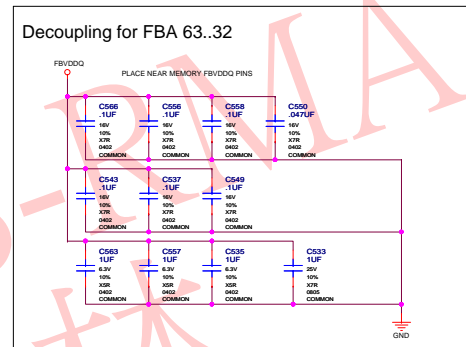
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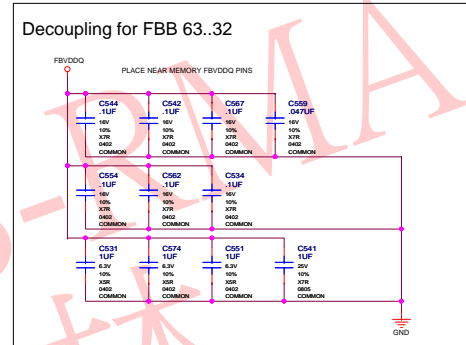
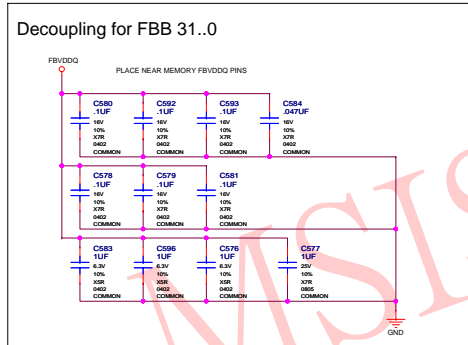







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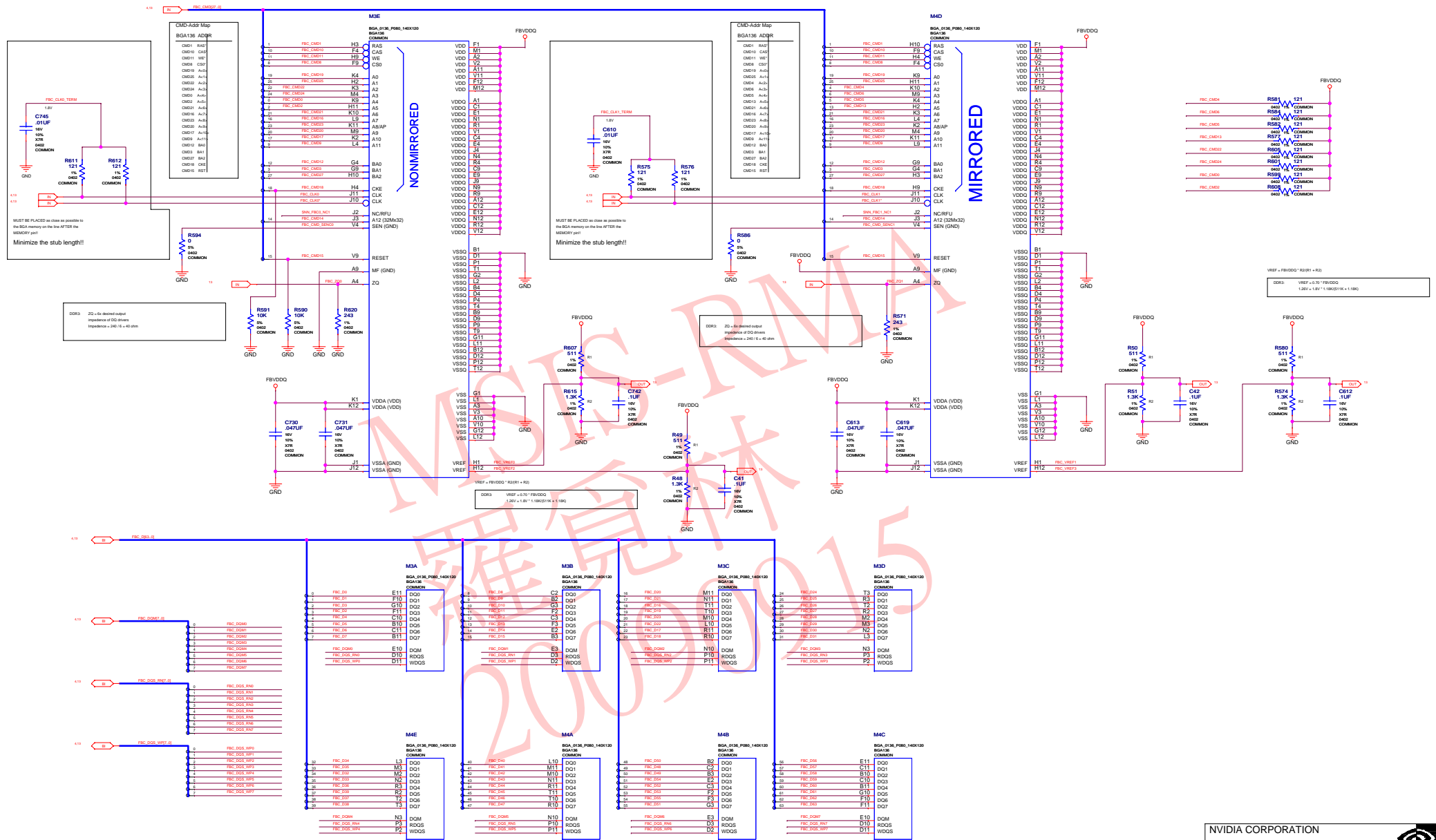


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PAGE DETAIL	FBB Partion Decoupling

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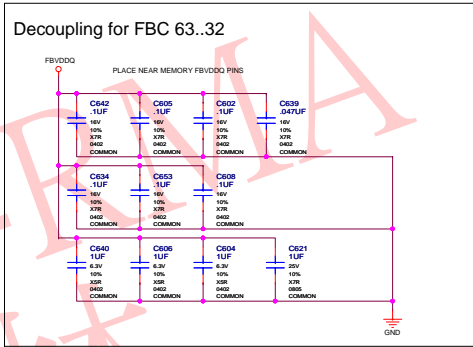
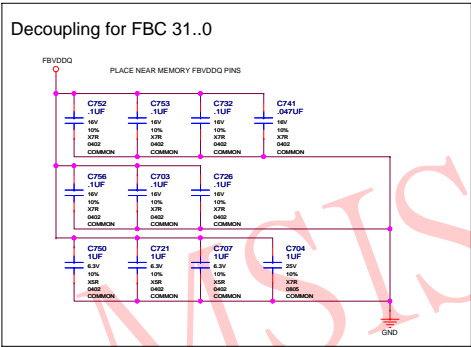


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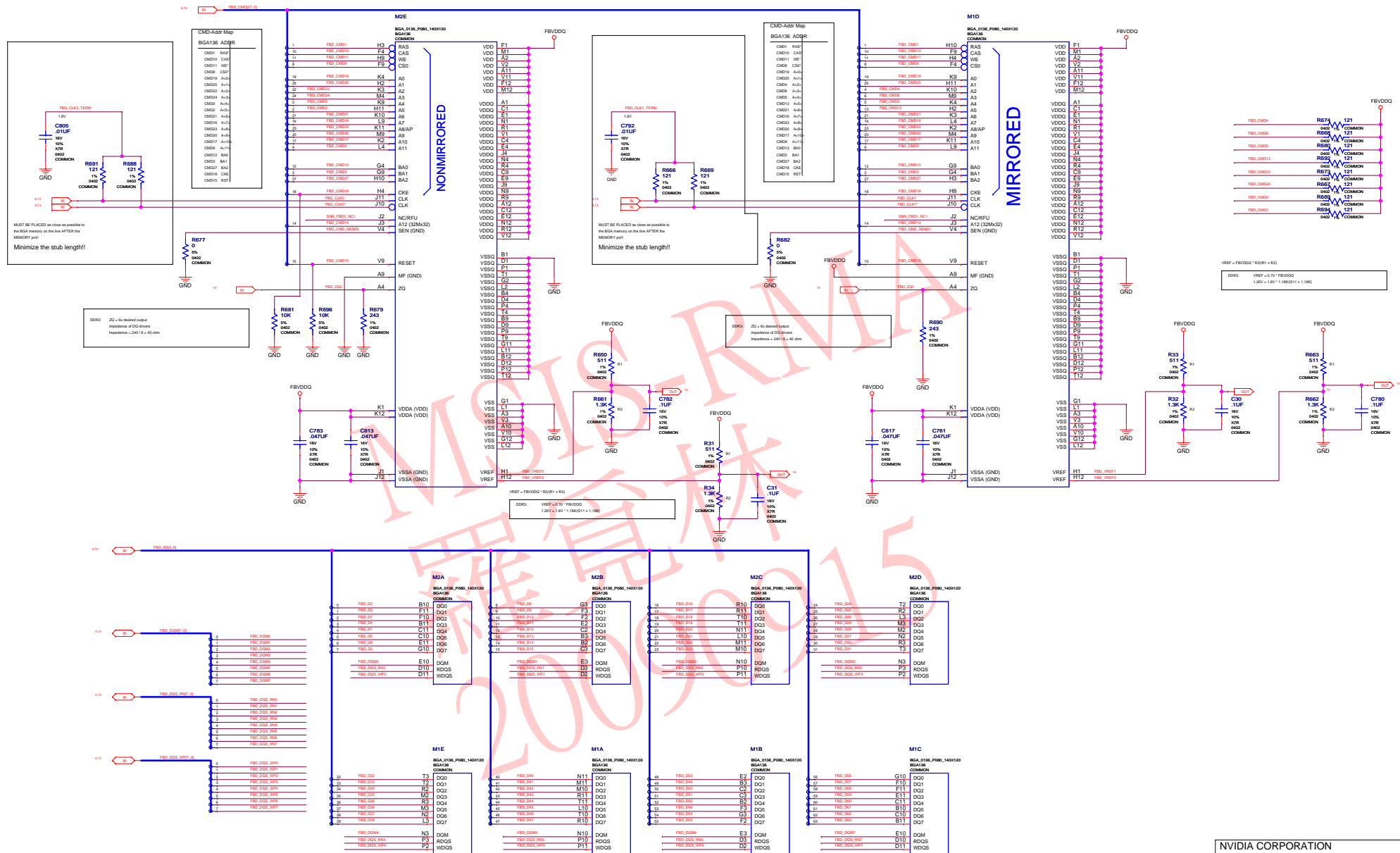
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PAGE DETAIL	FBC Partition



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PAGE DETAIL	FBC Partition Decoupling

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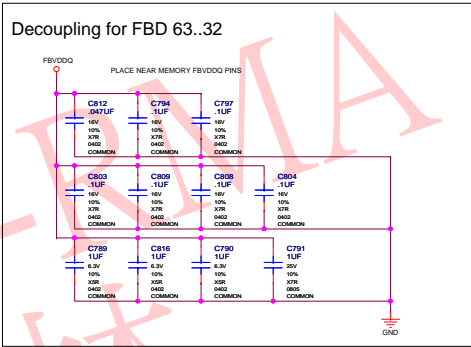
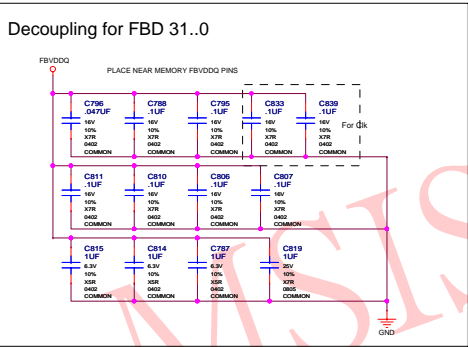
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PAGE DETAIL	FBD Partition

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FBD F

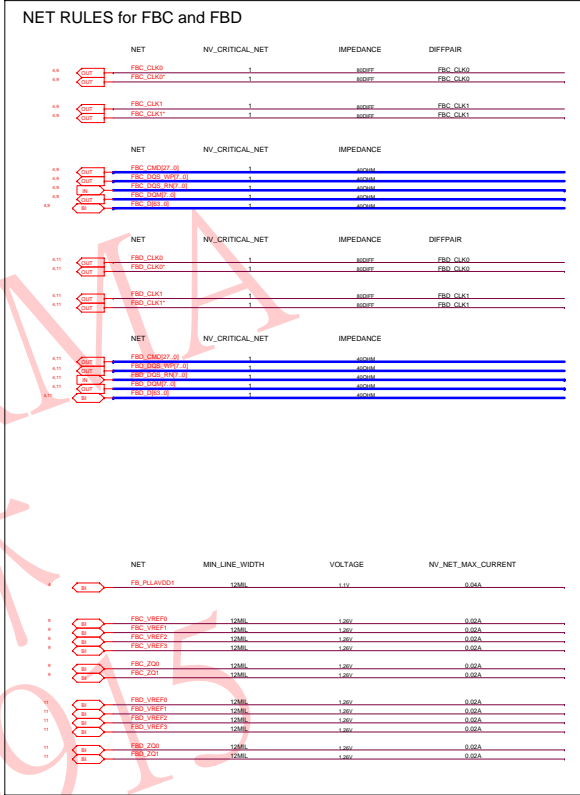
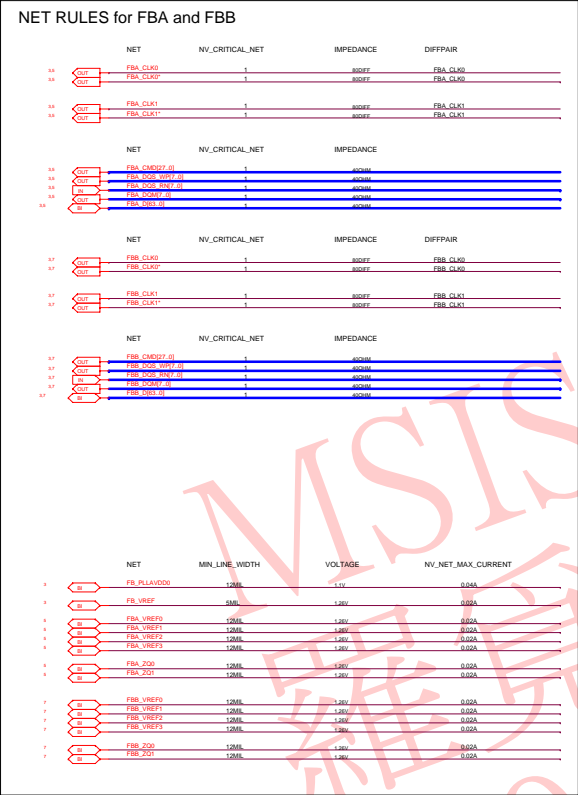


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PAGE DETAIL	FBD Partition Decoupling

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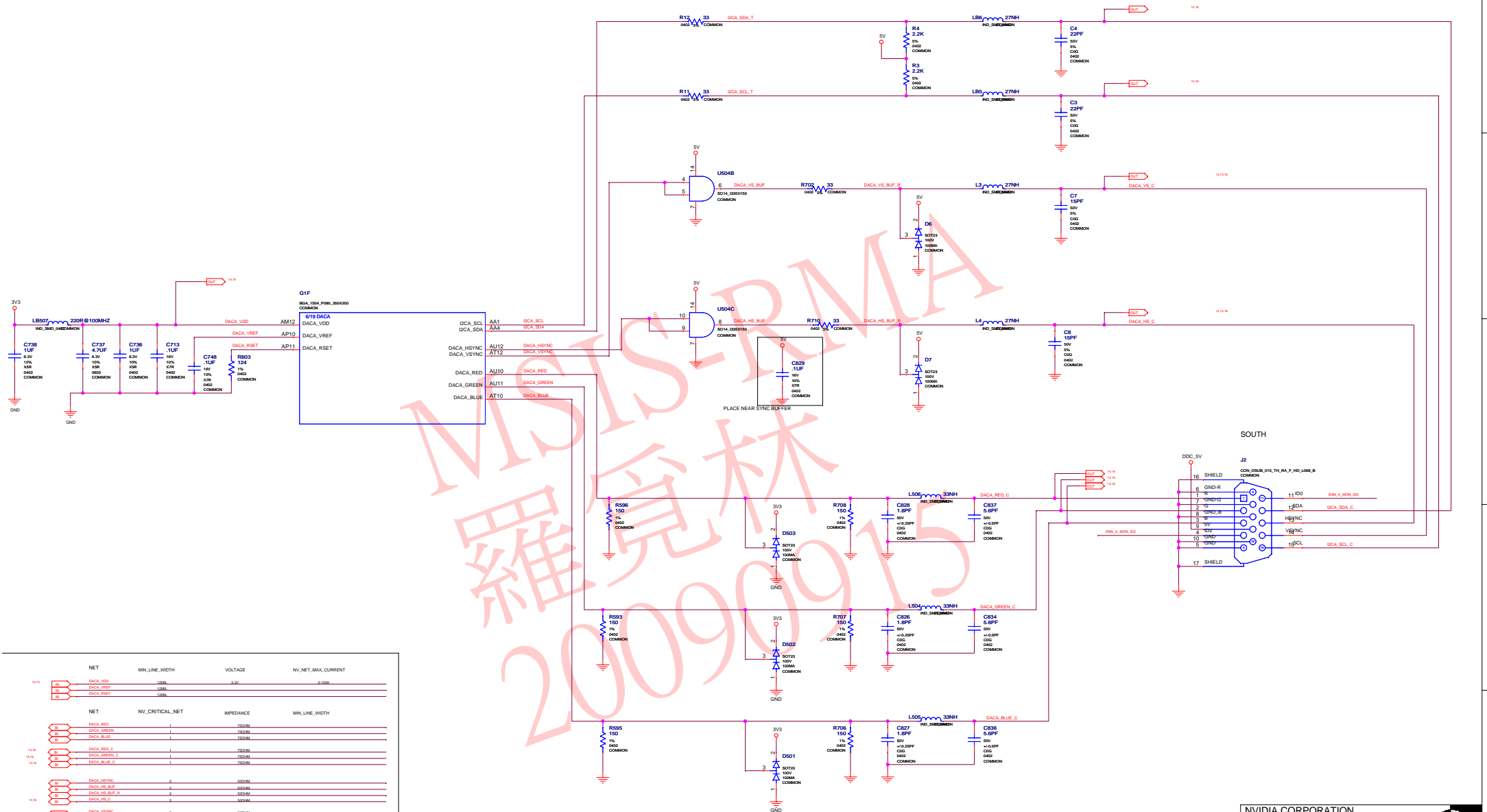


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PAGE DETAIL	FB Net Properties

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DACA RGB-FILTER



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PAGE DETAIL	DACA Interface

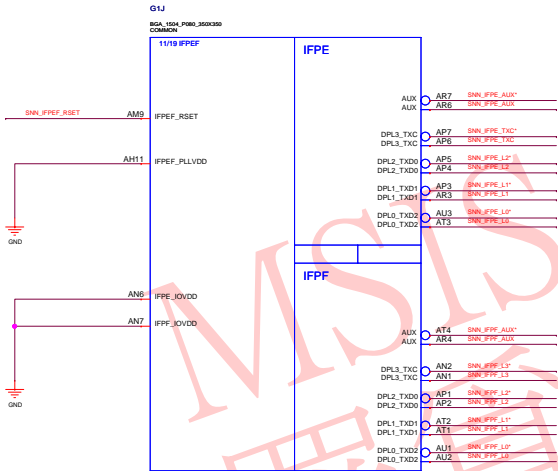
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NET	MIN_LINE_WIDTH	VOLTAGE	NV_NET_MAX_CURRENT

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PAGE DETAIL	IFPE/F Interface -- Unused

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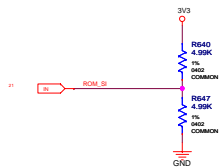
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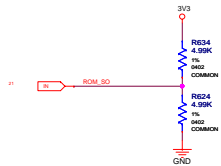


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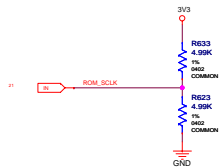


STRAP PIN	STRAP NAME
ROM_SI	PCI_DEVID_EXT

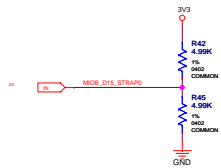
3V3	GND
5K	1 0



STRAP PIN	STRAP NAME
ROM_SO	SLOT_CLK_CFG



STRAP PIN	STRAP NAME
ROM_SCLK	PCI_DEVID[3]



STRAP PIN	STRAP NAME
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STRAP1	RAMCFG1
STRAP2	RAMCFG2

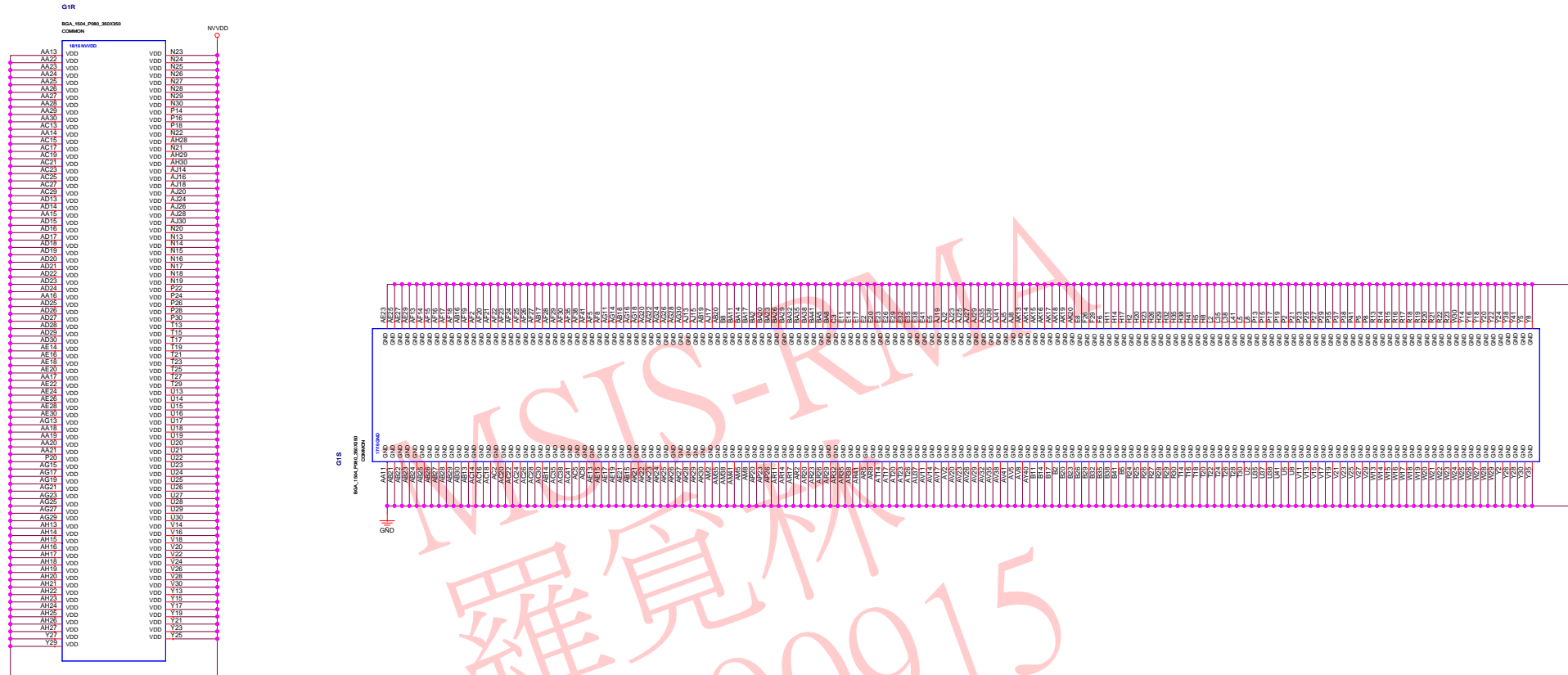
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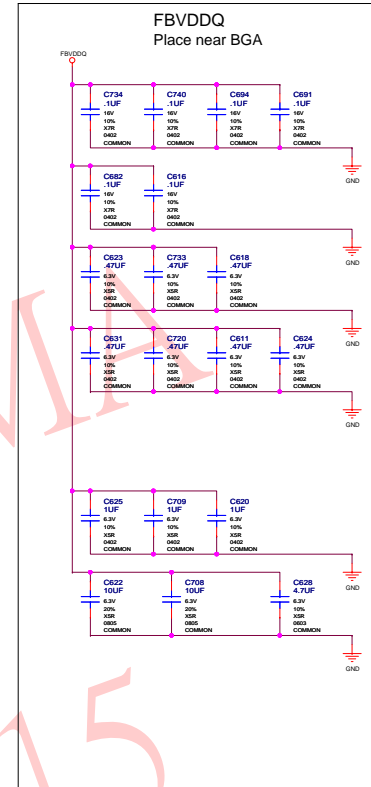
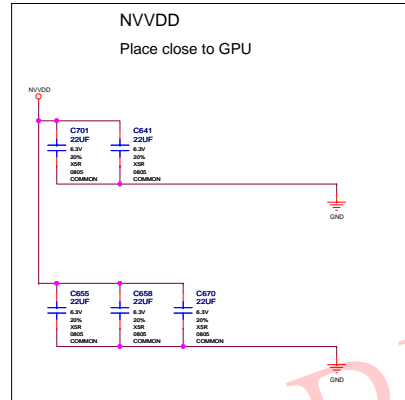
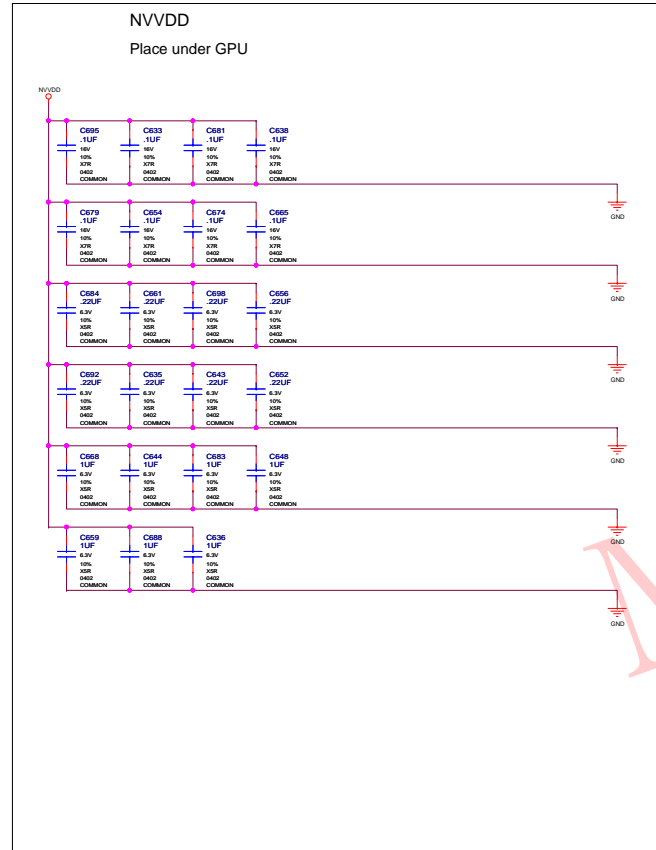
256MB (8Mx32)
101 --- 256-bit Gmenda
110 --- 256-bit Hynix
111 --- 256-bit Samsung
* VBIOS will be defined on a per SKU basis.

512MB (16Mx32)
001 --- 256-bit Gmenda
010 --- 256-bit Hynix
011 --- 256-bit Samsung

1024MB (32Mx32)
101 --- 256-bit Hynix
110 --- 256-bit Hynix
111 --- 256-bit Samsung


GPIO	GPIO
GPIO1	GPIO1
GPIO2	GPIO2
GPIO3	GPIO3
GPIO4	GPIO4
GPIO5	GPIO5
GPIO6	GPIO6
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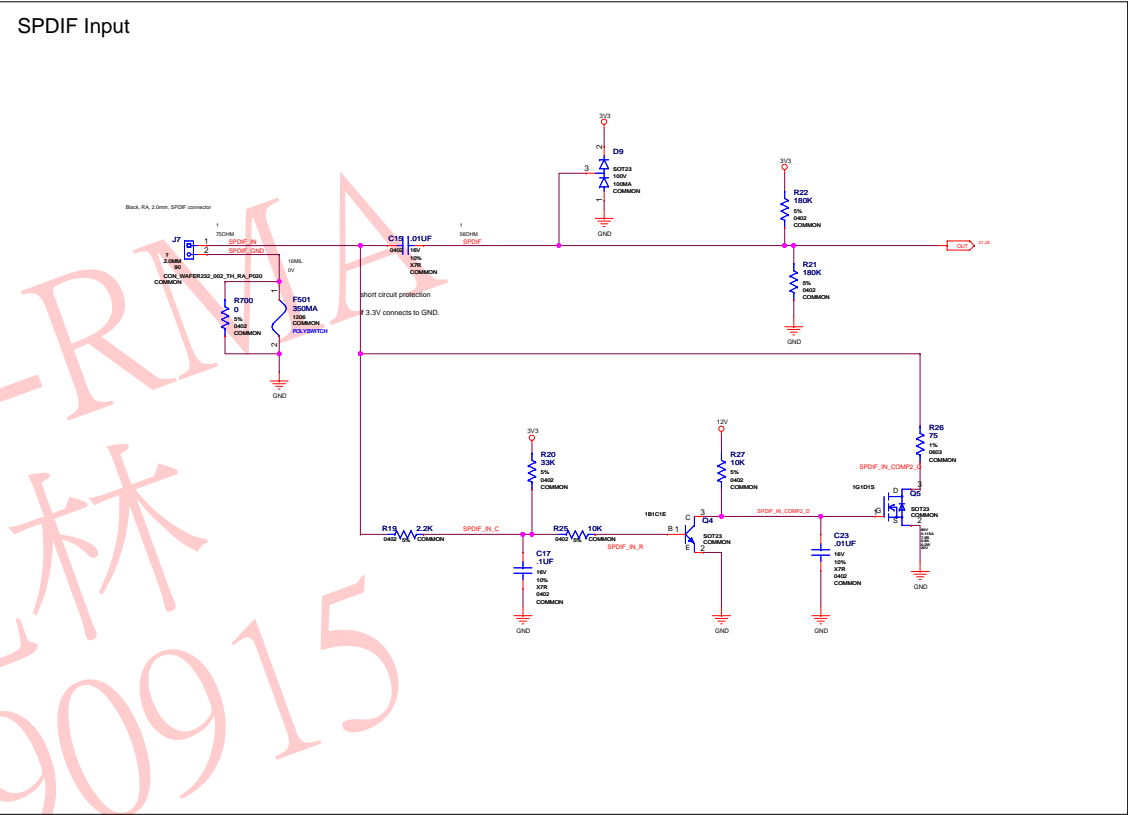
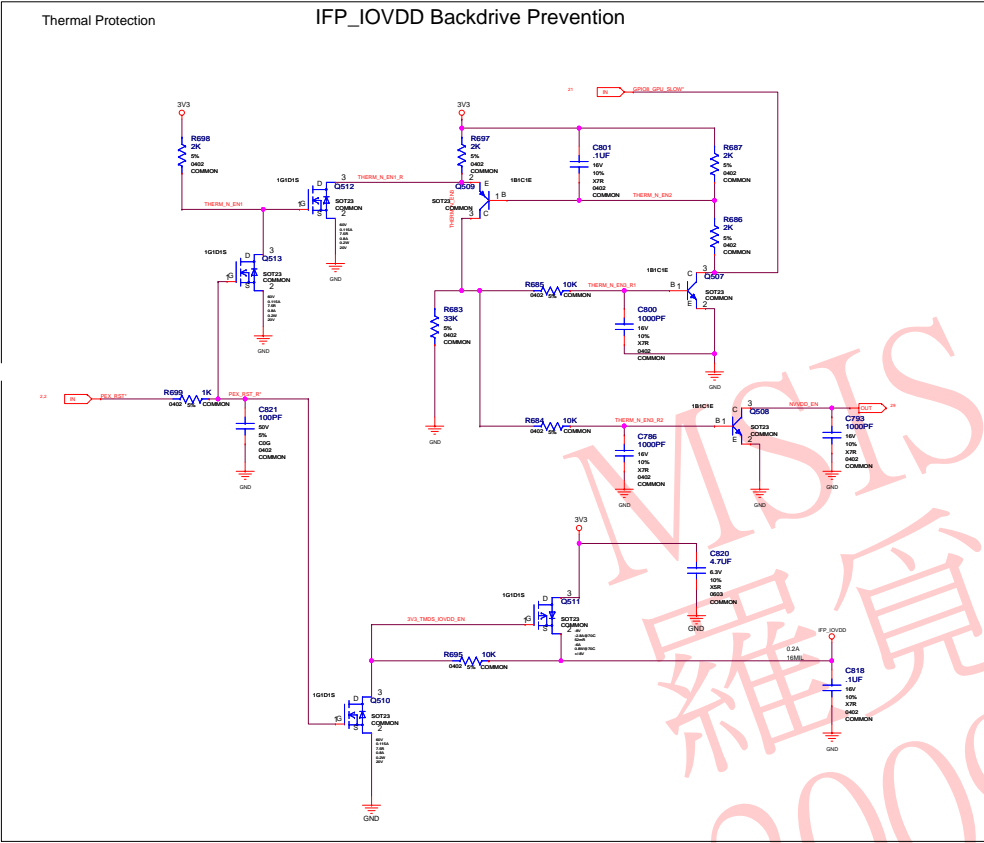
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PAGE DETAIL	NVVDD and FBVDDQ Decoupling

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NET	IMPEDANCE	NV_CRITICAL_NET
R1	SPDIF_IN_C	50OHM
R2	SPDIF_IN_R	50OHM
R3	SPDIF_IN_COMP2_D	50OHM
R4	SPDIF_IN_COMP2_S	50OHM
R5	SPDIF_IN_COMP2_S	50OHM
R6	SPDIF_IN	50OHM
R7	SPDIF	50OHM



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ID

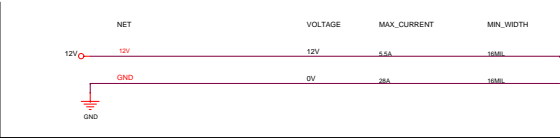
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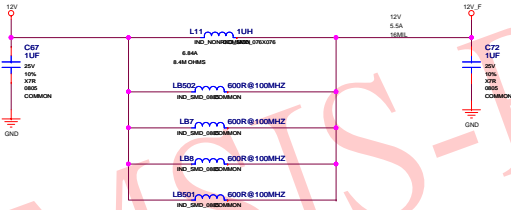
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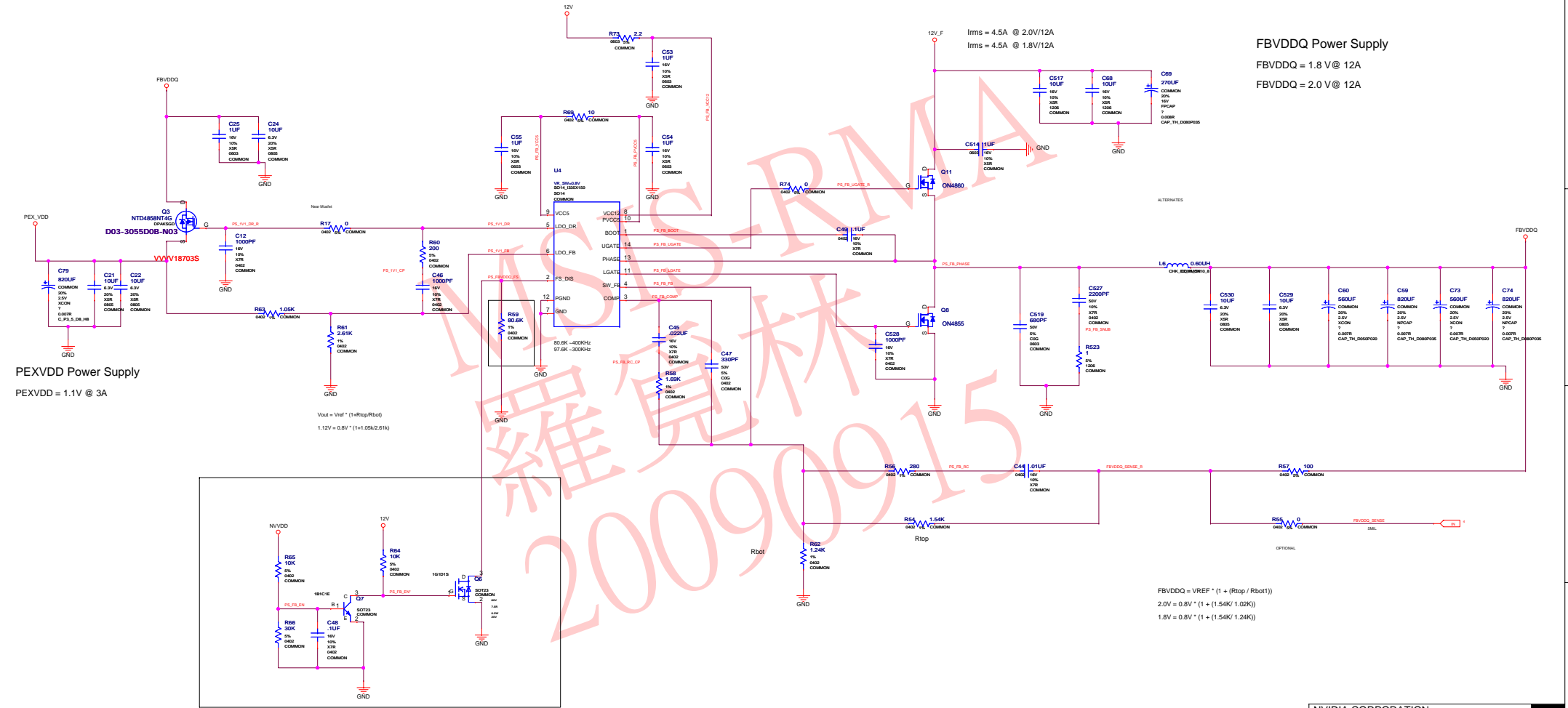
12V Power Supply Filter

12V_F = 12V @ 5.5A



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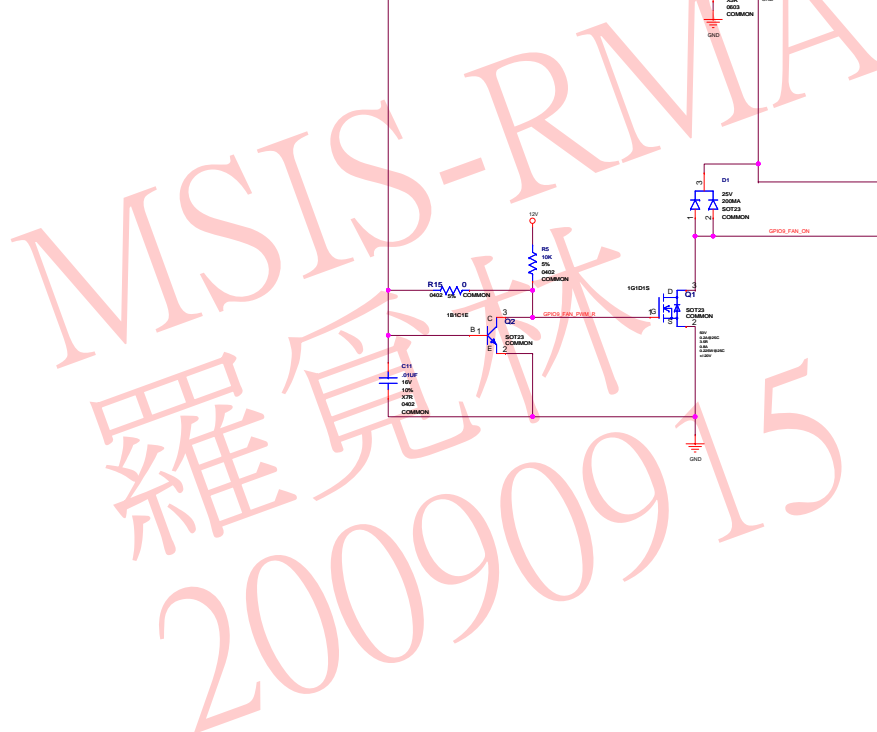
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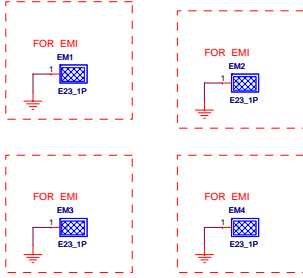
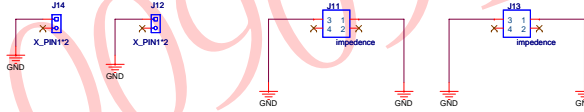
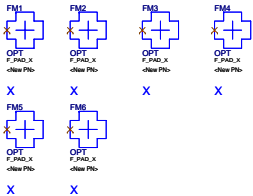
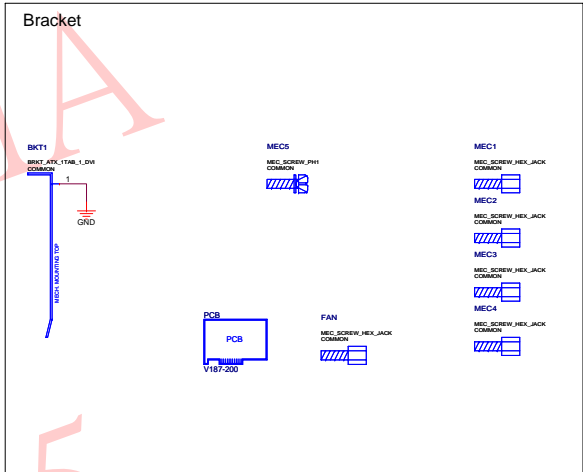
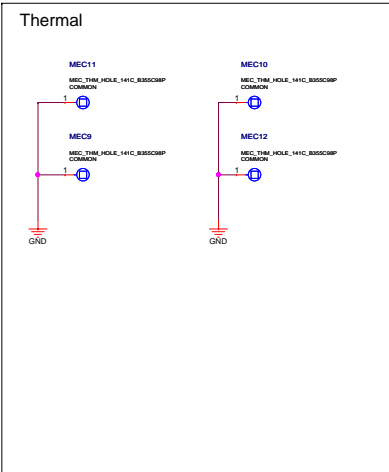


ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Fan Connector



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ASSEMBLY	PS47 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Thermal, Mechanical, and Bracket

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NAME		DATE	30 DEC 2008

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Design: p1052_A00		PEX_RK14* 2:30>2:58		PEX_TX04* 2:10>2:38		FBA_D4e5 3:18>5:40		FBB_CMD4> 3:20>7:1E 7:1G		FBB_D0M47> 3:4F 7:48 7:5E	
Date: Dec 24 11:53:24 2008		PEX_RK15 2:30>2:58		PEX_TX05 2:10>2:38		FBA_D4e6 3:18>5:40		FBB_CMD5> 3:20>7:1E 7:1G		FBB_D0S_RN4> 3:4F 7:48 7:4C	
Page wise report of nets and their base nets		PEX_RK15* 2:30>2:58		PEX_TX05* 2:10>2:38		FBA_D4e7 3:18>5:40		FBB_CMD6> 3:30>7:1E 7:1G		FBB_D0S_RN47> 3:4F 7:48 7:4C	
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PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e57 3:28>5:40		FBB_CMD57> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e58 3:28>5:40		FBB_CMD58> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e59 3:28>5:40		FBB_CMD59> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e60 3:28>5:40		FBB_CMD60> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e61 3:28>5:40		FBB_CMD61> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e62 3:28>5:40		FBB_CMD62> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e63 3:28>5:40		FBB_CMD63> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e64 3:28>5:40		FBB_CMD64> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e65 3:28>5:40		FBB_CMD65> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e66 3:28>5:40		FBB_CMD66> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e67 3:28>5:40		FBB_CMD67> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e68 3:28>5:40		FBB_CMD68> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e69 3:28>5:40		FBB_CMD69> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e70 3:28>5:40		FBB_CMD70> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e71 3:28>5:40		FBB_CMD71> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e72 3:28>5:40		FBB_CMD72> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e73 3:28>5:40		FBB_CMD73> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e74 3:28>5:40		FBB_CMD74> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e75 3:28>5:40		FBB_CMD75> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e76 3:28>5:40		FBB_CMD76> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e77 3:28>5:40		FBB_CMD77> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e78 3:28>5:40		FBB_CMD78> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e79 3:28>5:40		FBB_CMD79> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:20>2:48		FBA_D4e80 3:28>5:40		FBB_CMD80> 3:30>7:1E 7:1E		FBB_D0S_VP4> 3:4F 7:58 7:5C	
PEX_CAL_PD_VDDQ 2:5D		PEX_TX2 2:2C 2:30>		PEX_TX09 2:2							

