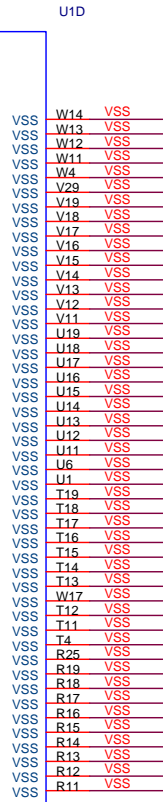
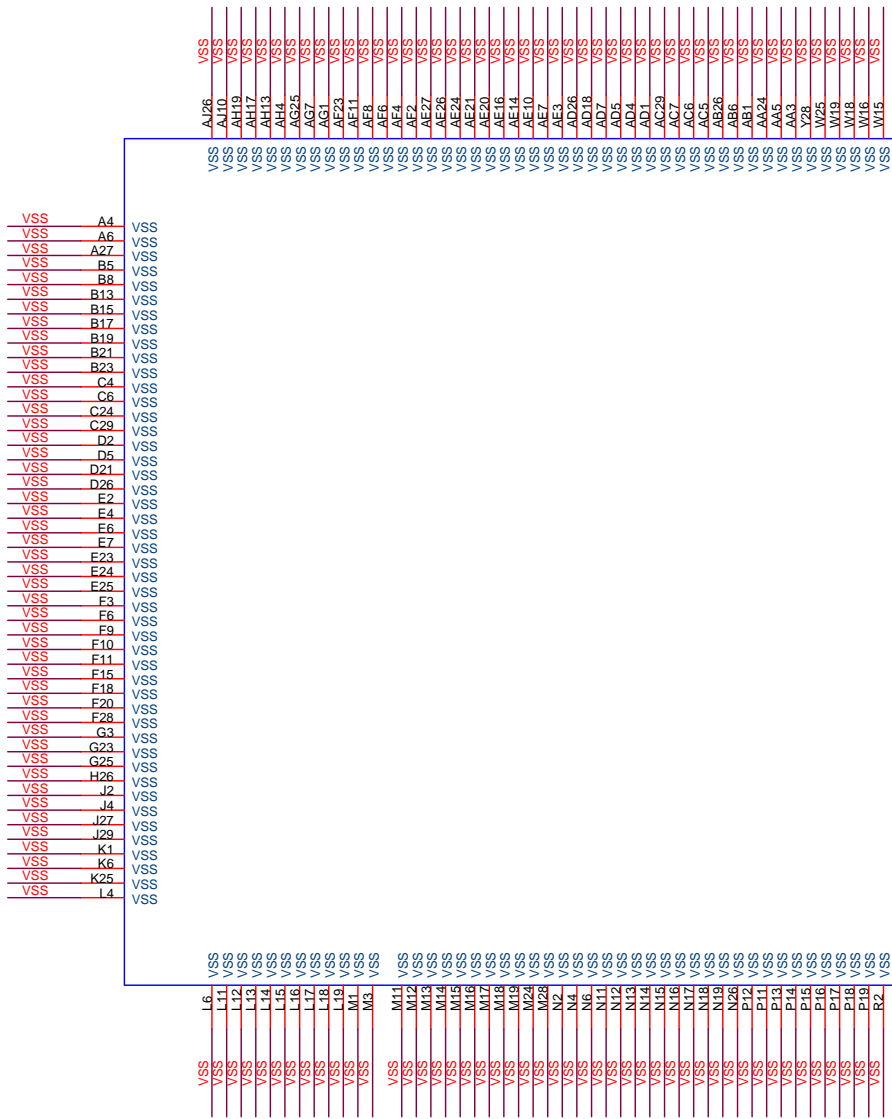
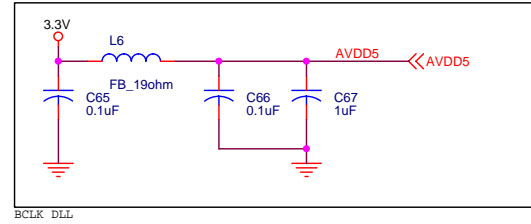
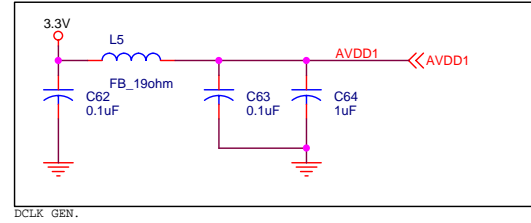
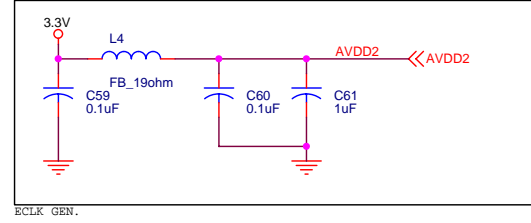
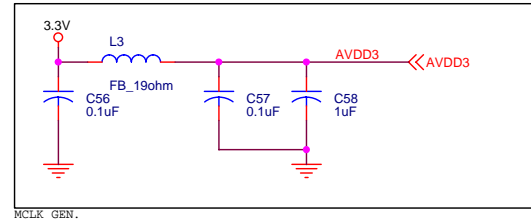


Bypass capacitors for Xabre (solder side)

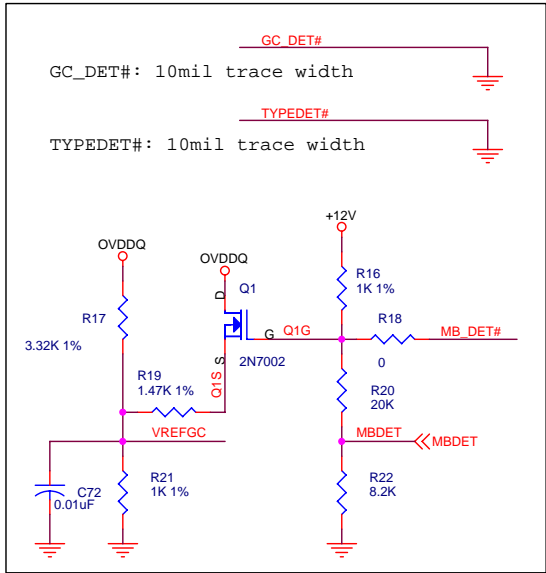
Xabre		
File Name:XABRE_3.SCH		
Title		
Xabre_C		
Size	Document Number	Rev
B	Xabre 4M/8MX16 DDR (Xabre011)	0.9
Date:	Wednesday, January 08, 2003	Sheet 3 of 12



Xabre400/200



Xabre		
File Name:XABRE_4.SCH		
Title		
Xabre_D		
Size	Document Number	Rev
B	Xabre 4M/8MX16 DDR (Xabre011)	0.9
Date:	Wednesday, January 08, 2003	Sheet 4 of 12

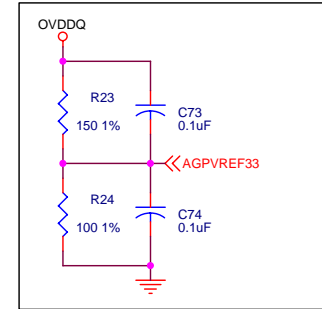
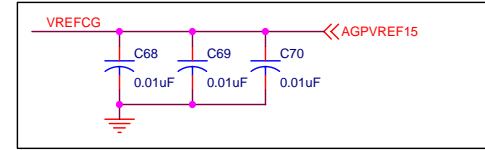
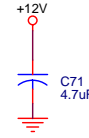


1.5V level for AGP version 2.0 and 3.0

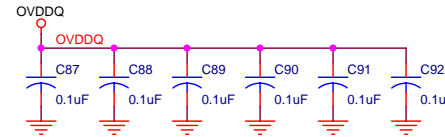
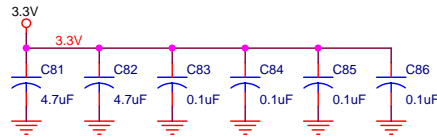
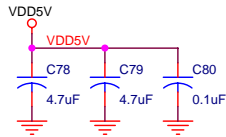
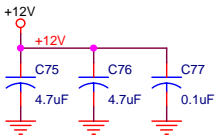
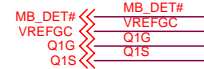
VDD5V	B01	OVRCONT	A01	+12V
VDD5V	B02	TYPEDET#	A02	TYPEDET#
B03	B03	GC_DET#	A03	GC_DET#
B04	B04	USB+	A04	VSS
VSS	B05	GND	A05	INTA#
B06	B06	INTB#	A06	BARST#
CLK	B07	CLK	A07	GNT#
REQ#	B08	REQ	A08	3.3V
3.3V	B09	VCC3.3	A09	ST1
ST0	B10	ST1	A10	MB_DET#
ST2	B11	ST2	A11	DBI_HI
RBF#	B12	RBF	A12	VSS
VSS	B13	DBI_LO	A13	WBFB#
DBI_LO	B14	GND	A14	SBA1
SBA0	B15	WBFB	A15	3.3V
3.3V	B16	SBA0#	A16	SBA3
SBA2	B17	VCC3.3	A17	SBSTB#
SBSTB	B18	SBA2#	A18	VSS
VSS	B19	SB_STBF	A19	SBA5
SBA4	B20	GND	A20	SBA7
SBA6	B21	SBA4#	A21	RESERVED
B22	B22	SBA7#	A22	GND
B23	B23	RESERVED	A23	GND
B24	B24	GND	A24	VCC3.3
B25	B25	3.3AUX	A25	AD30
AD31	B26	VCC3.3	A26	AD28
AD29	B27	AD31	A27	3.3V
3.3V	B28	AD29	A28	AD26
AD27	B29	VCC3.3	A29	AD24
AD25	B30	AD27	A30	VSS
VSS	B31	AD25	A31	ADSTB1#
ADSTB1	B32	GND	A32	C#BE3
AD23	B33	AD_STF1	A33	OVDDQ
OVDDQ	B34	AD23	A34	VDDQ1.5
AD21	B35	VDDQ1.5	A35	AD22
AD19	B36	AD21	A36	AD20
VSS	B37	AD19	A37	VSS
AD17	B38	GND	A38	AD18
C#BE2	B39	AD17	A39	AD16
OVDDQ	B40	C#BE2	A40	OVDDQ
IRDY#	B41	VDDQ1.5	A41	FRAME#
B42	B42	FRAME	A42	TRDY#
B43	B43	RES/KEY	A43	STOP#
B44	B44	GND/KEY	A44	VSS
B45	B45	RES/KEY	A45	PAR
DEVSEL#	B46	3.3KEY	A46	AD15
OVDDQ	B47	TRDY	A47	OVDDQ
VSS	B48	STOP	A48	AD13
B49	B49	PERR	A49	VSS
C#BE1	B50	GND	A50	AD9
OVDDQ	B51	SERR	A51	C#BE0
AD14	B52	C#BE1	A52	OVDDQ
AD12	B53	VDDQ1.5	A53	AD15
VSS	B54	AD14	A54	AD11
AD10	B55	AD12	A55	VSS
AD8	B56	GND	A56	AD9
OVDDQ	B57	AD10	A57	C#BE0
ADSTB0	B58	AD8	A58	OVDDQ
AD7	B59	VDDQ1.5	A59	ADSTB0#
VSS	B60	AD_STF0	A60	AD6
AD5	B61	GND	A61	VSS
OVDDQ	B62	AD5	A62	AD4
AD3	B63	AD3	A63	OVDDQ
AD1	B64	VDDQ1.5	A64	AD0
VREFCG	B65	AD1	A65	VREFCG
B66	B66	AGPVREFCG	A66	

AGP3.0-B

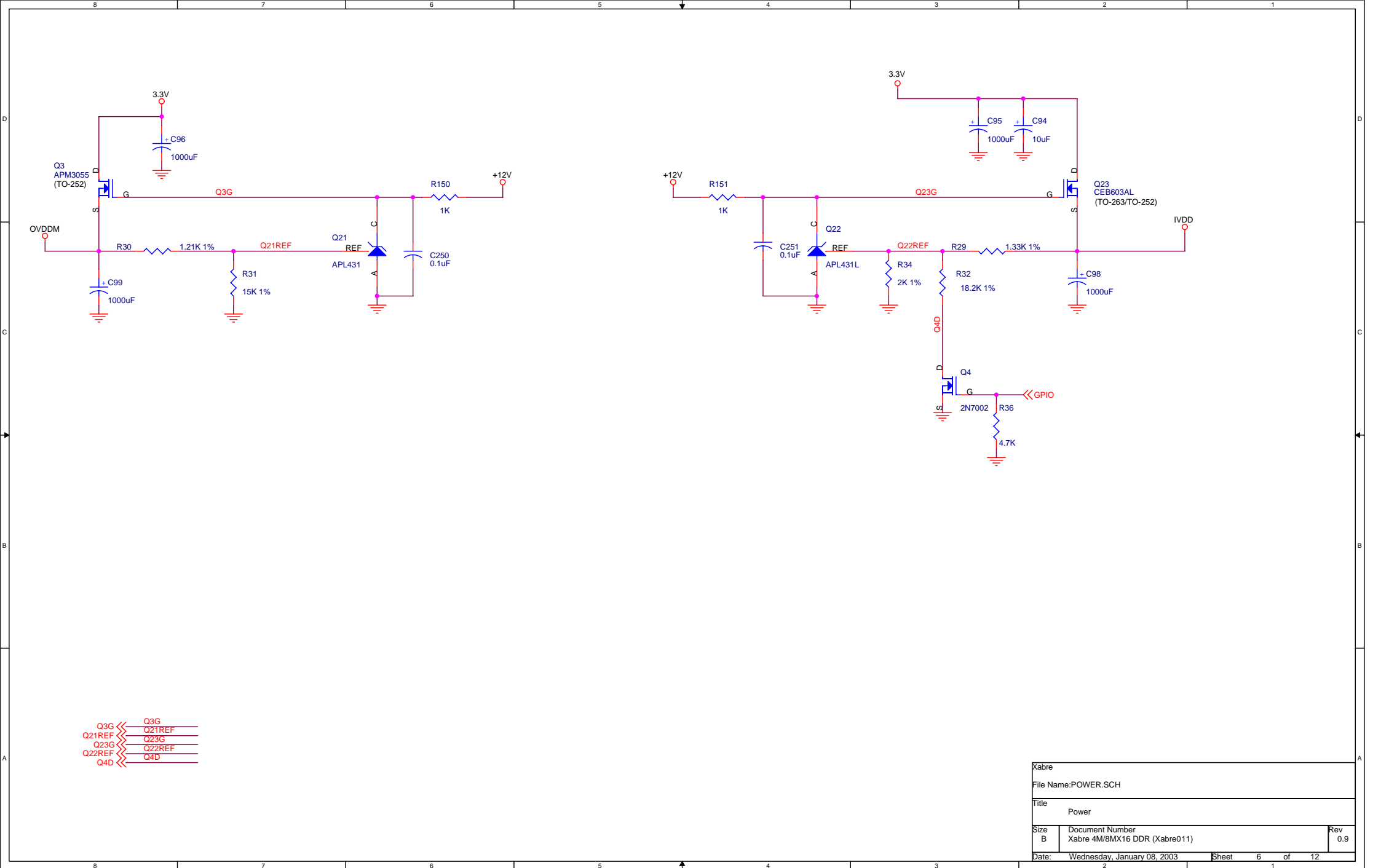
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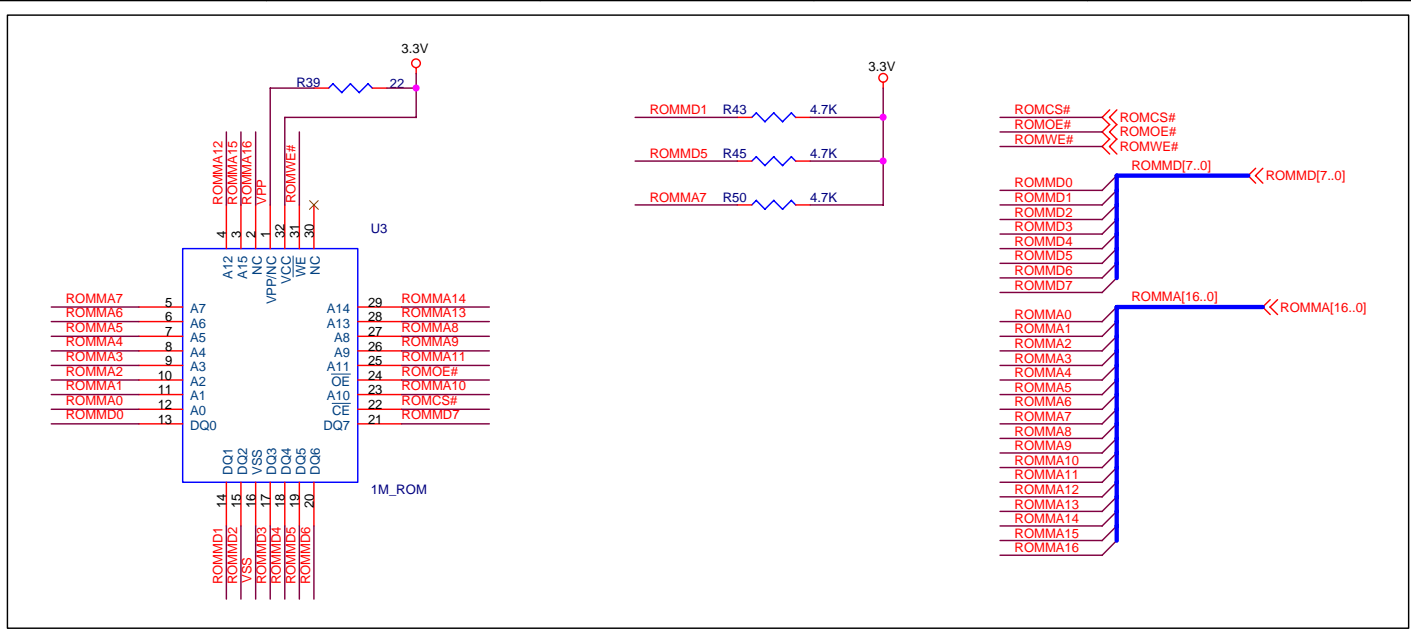


3.3V level for AGP version 1.0

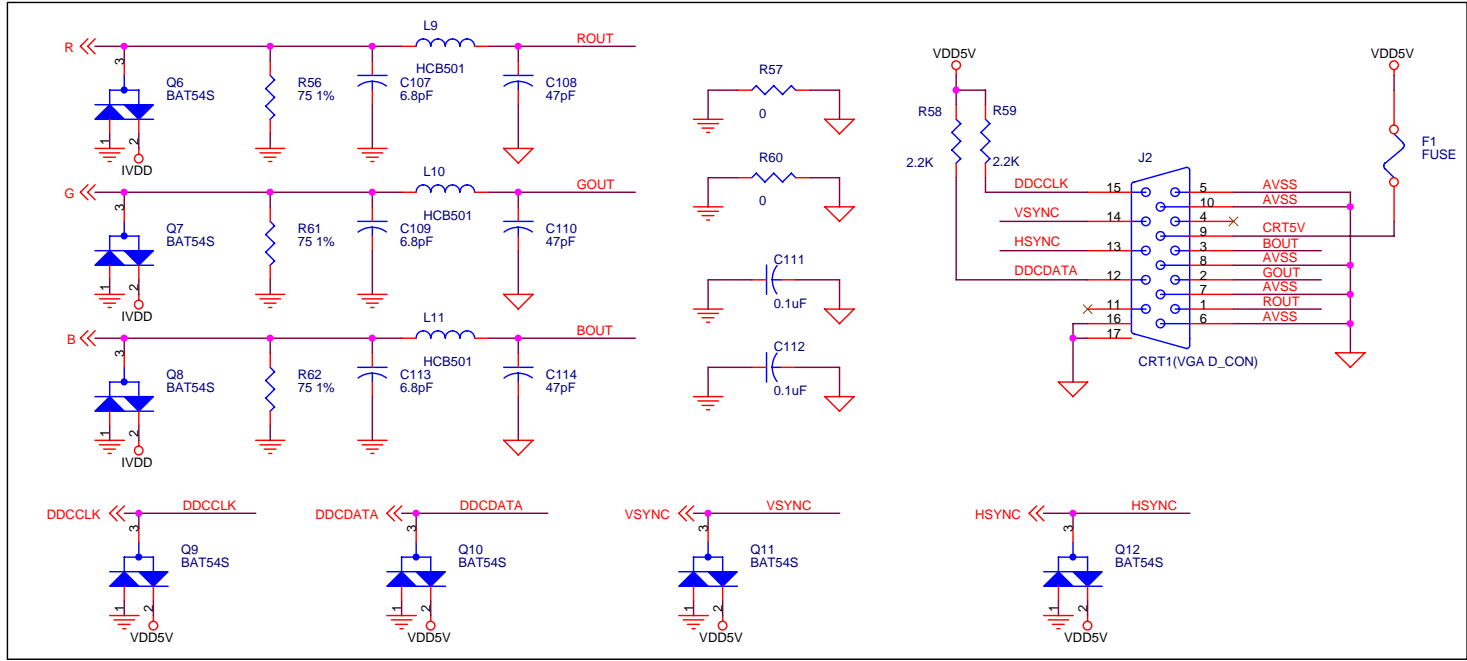


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Title		
AGP		
Size	Document Number	Rev
B	Xabre 4M/8MX16 DDR (Xabre011)	0.9
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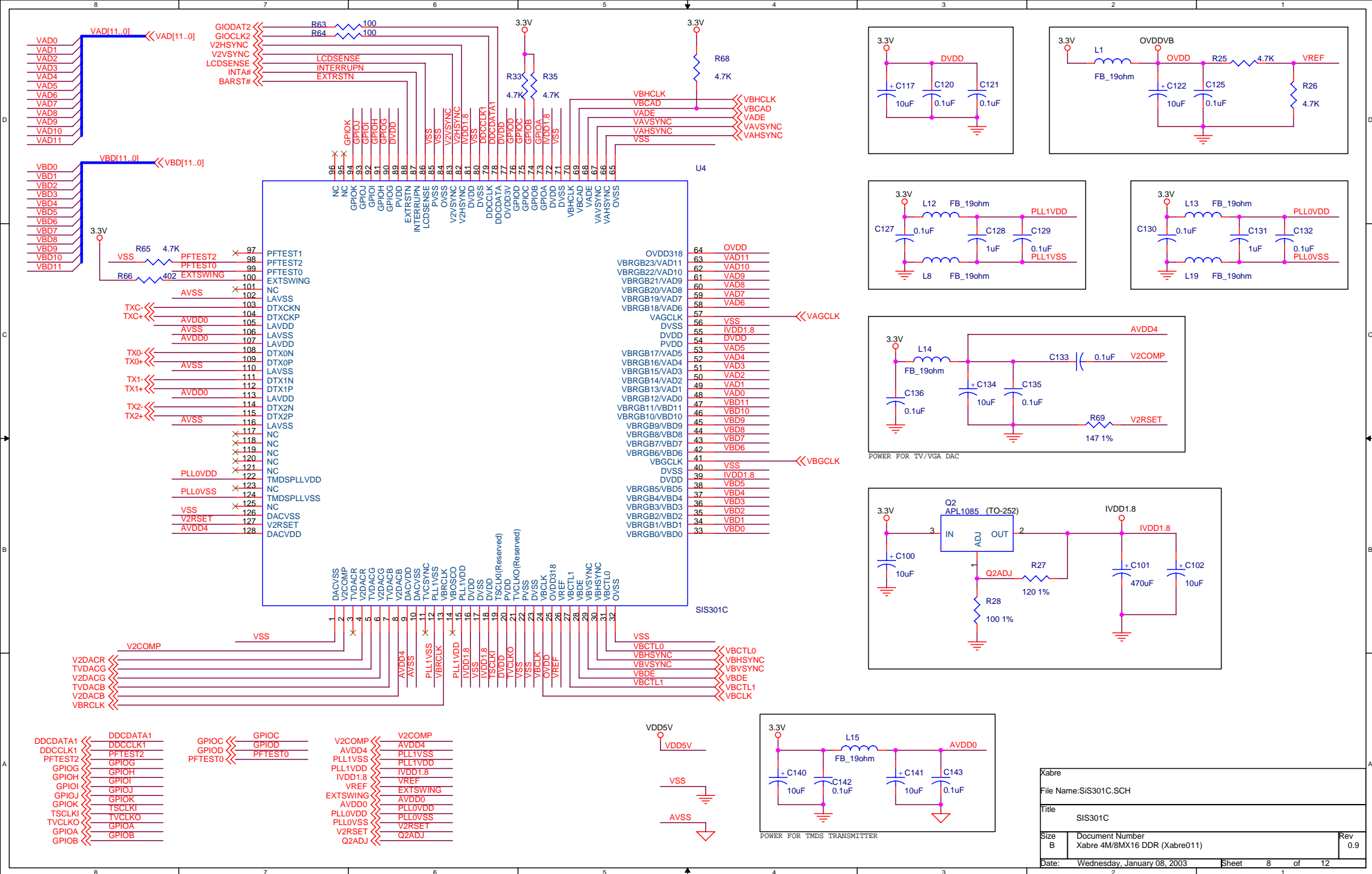


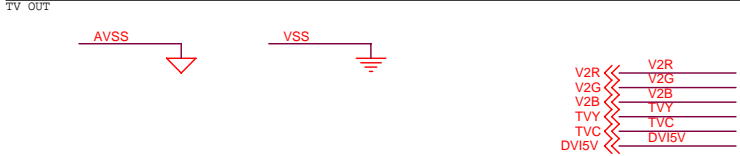
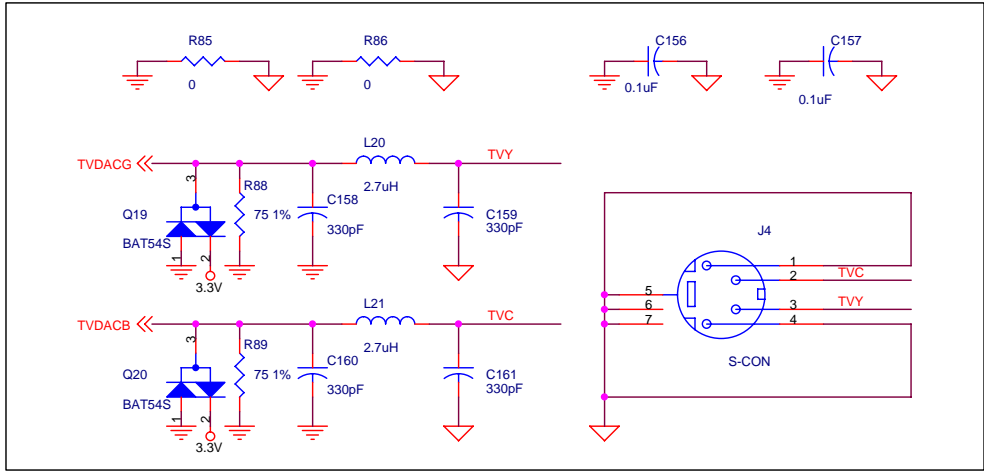
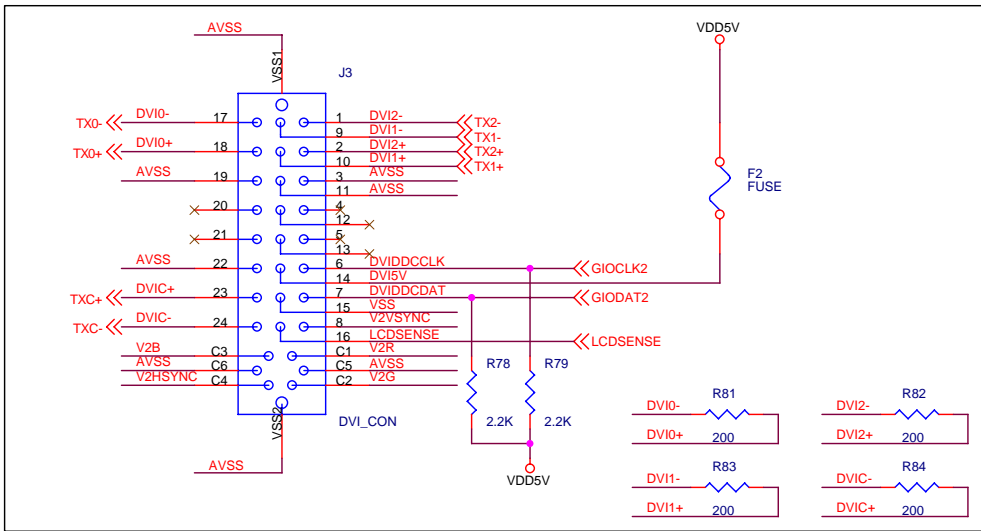
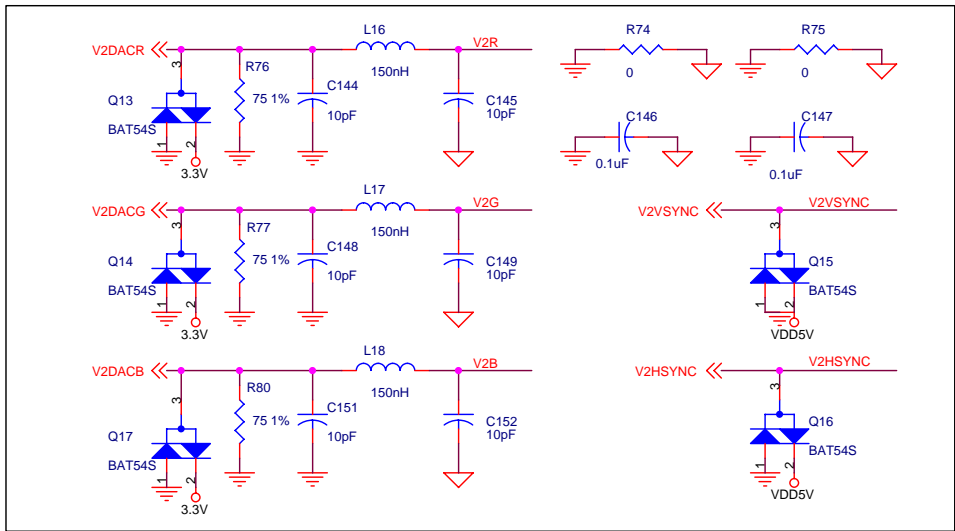
ROM & HW Trapping



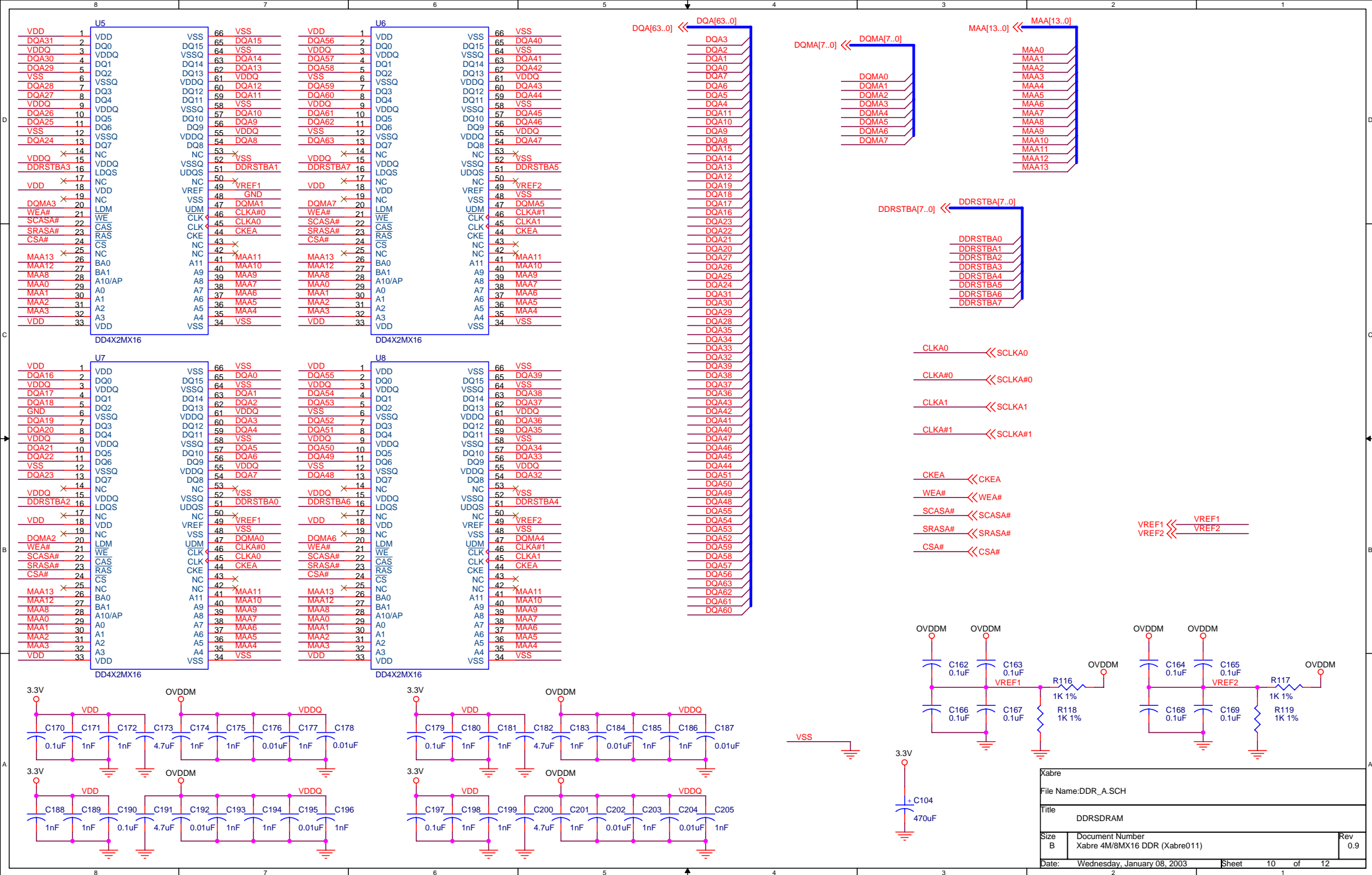
VGA CRT1

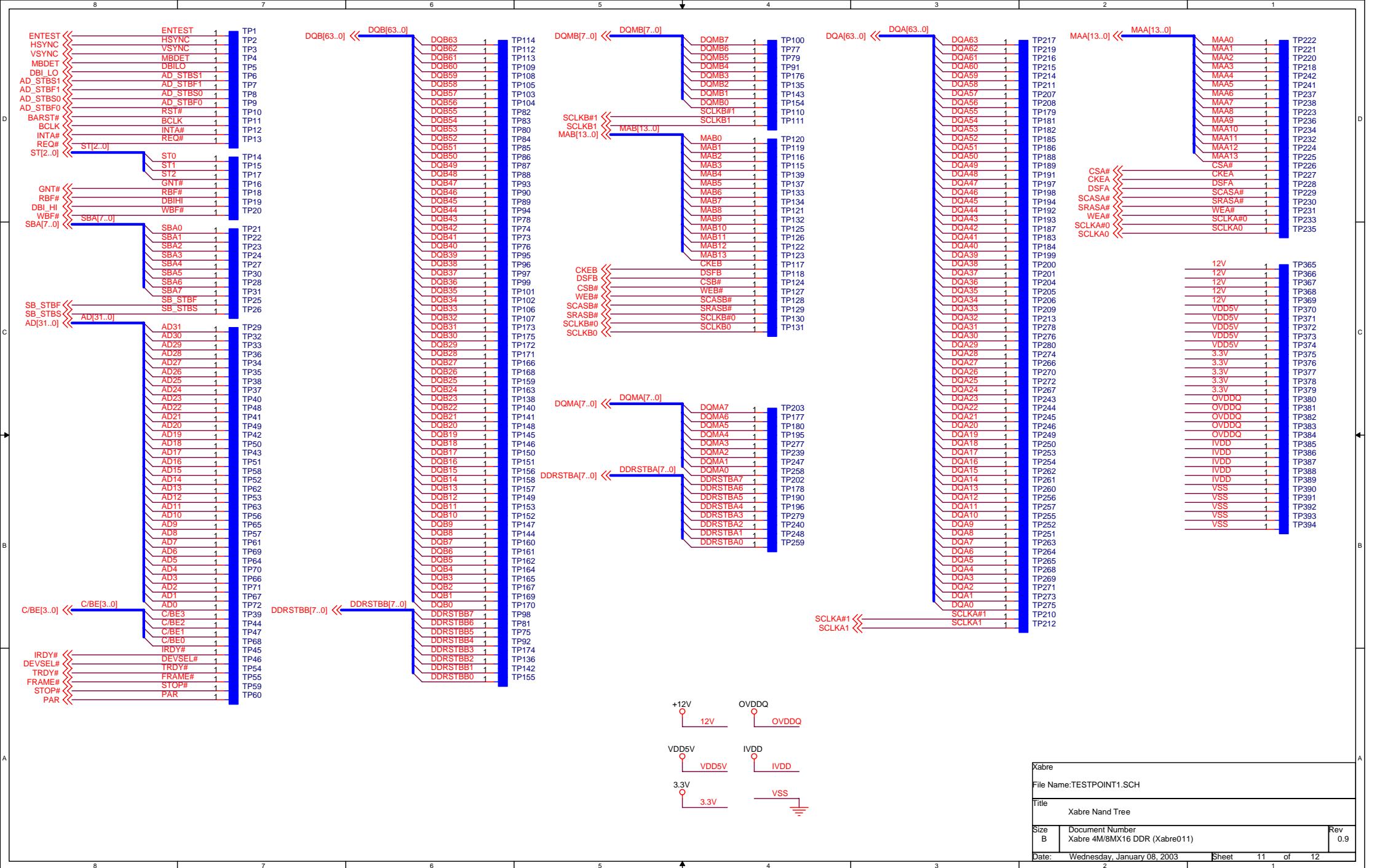
Xabre		
File Name: CRT1.SCH		
Title		
CRT1 & ROM		
Size	Document Number	Rev
B	Xabre 4M/8MX16 DDR (Xabre011)	0.9
Date:	Wednesday, January 08, 2003	Sheet 7 of 12

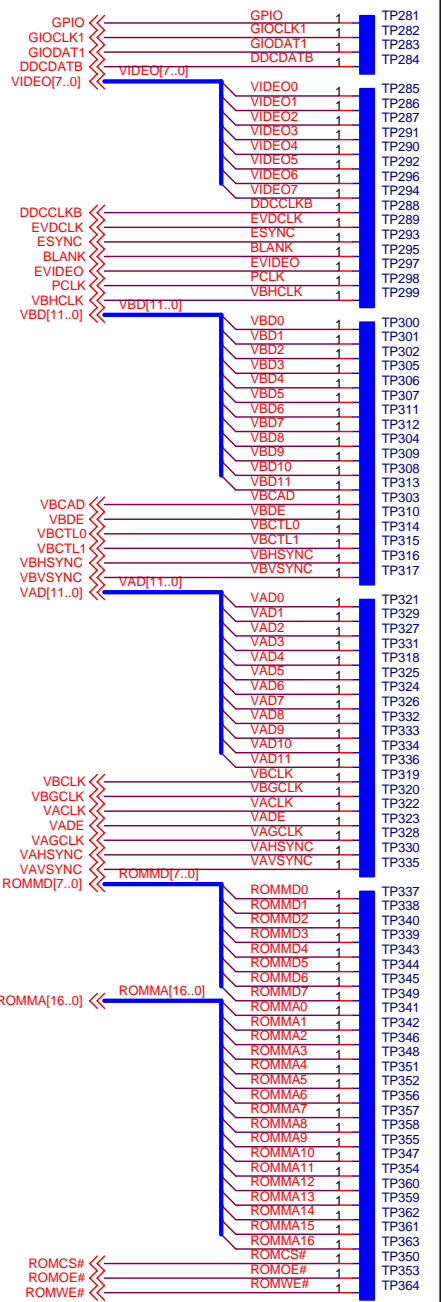




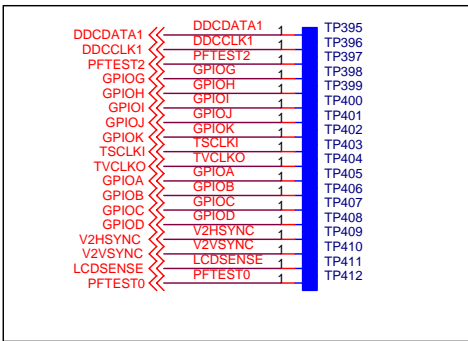
Xabre		
File Name: CRT2.SCH		
Title VGA CRT2/TV OUT/LCD OUTPUT		
Size B	Document Number Xabre 4M/8MX16 DDR (Xabre011)	Rev 0.9
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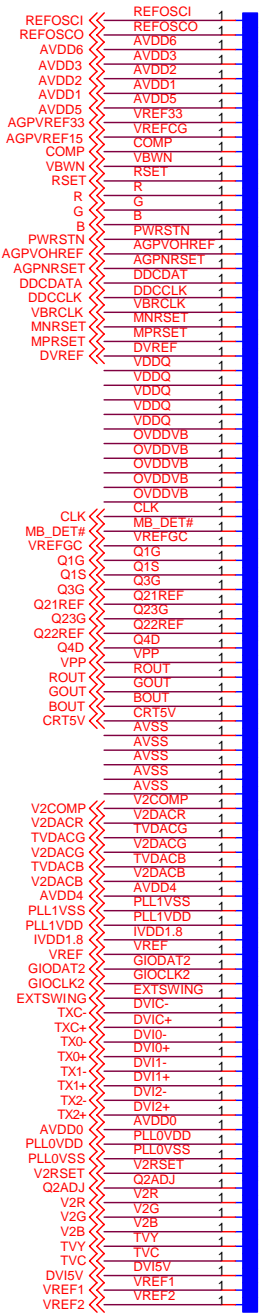




Xabre Nand Tree



SiS301C Supplement Nand Tree



Others Test Points

Xabre		
File Name:TESTPOINT2.SCH		
Title		
Xabre Nand Tree & Others		
Size	Document Number	Rev
B	Xabre 4M/8MX16 DDR (Xabre011)	0.9
Date:	Wednesday, January 08, 2003	Sheet 12 of 12