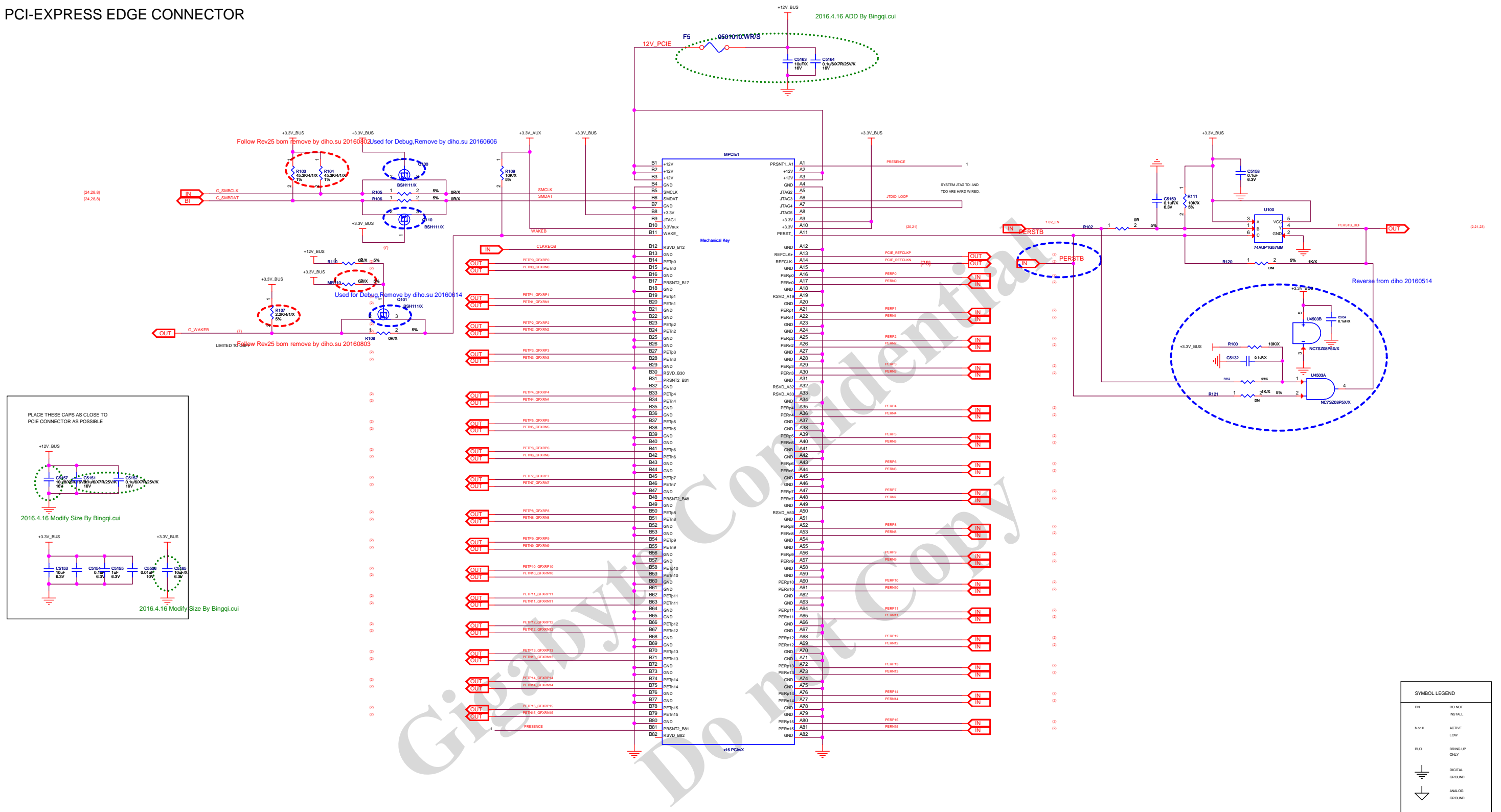
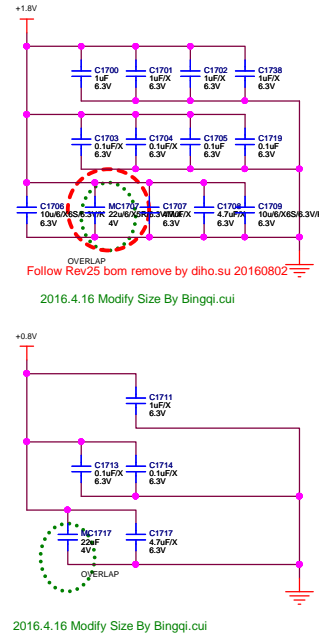


## (1) PCI-EXPRESS EDGE CONNECTOR

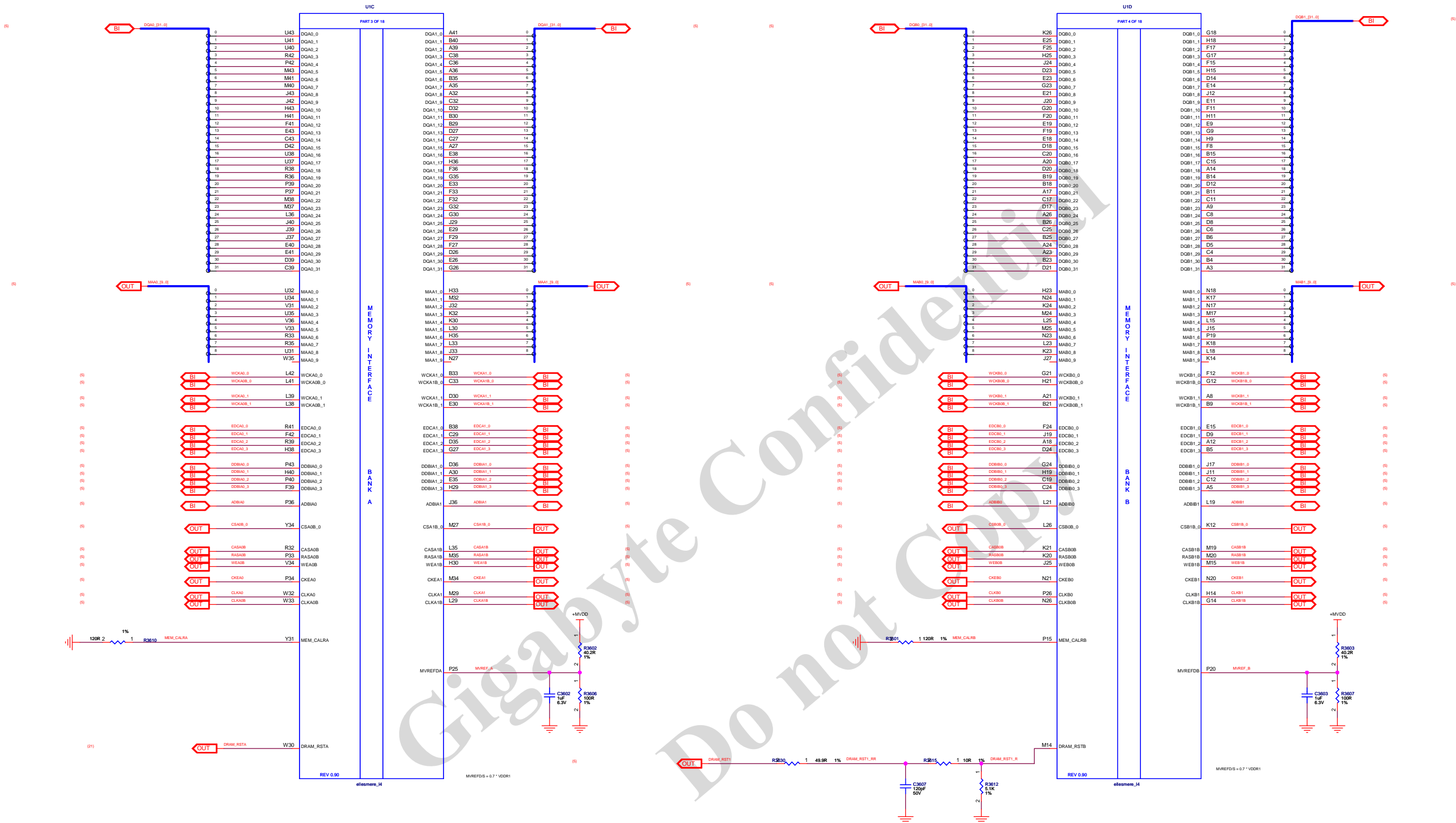


<b>GIGABYTE</b>			
Title <b>PCI Express</b>			
Size	Document Number		
Custom	Rev      1.1		
<b>GV-RX480G1 GAMING-4GD</b>			
Date:	Tuesday, October 18, 2016	Sheet	1 of 29

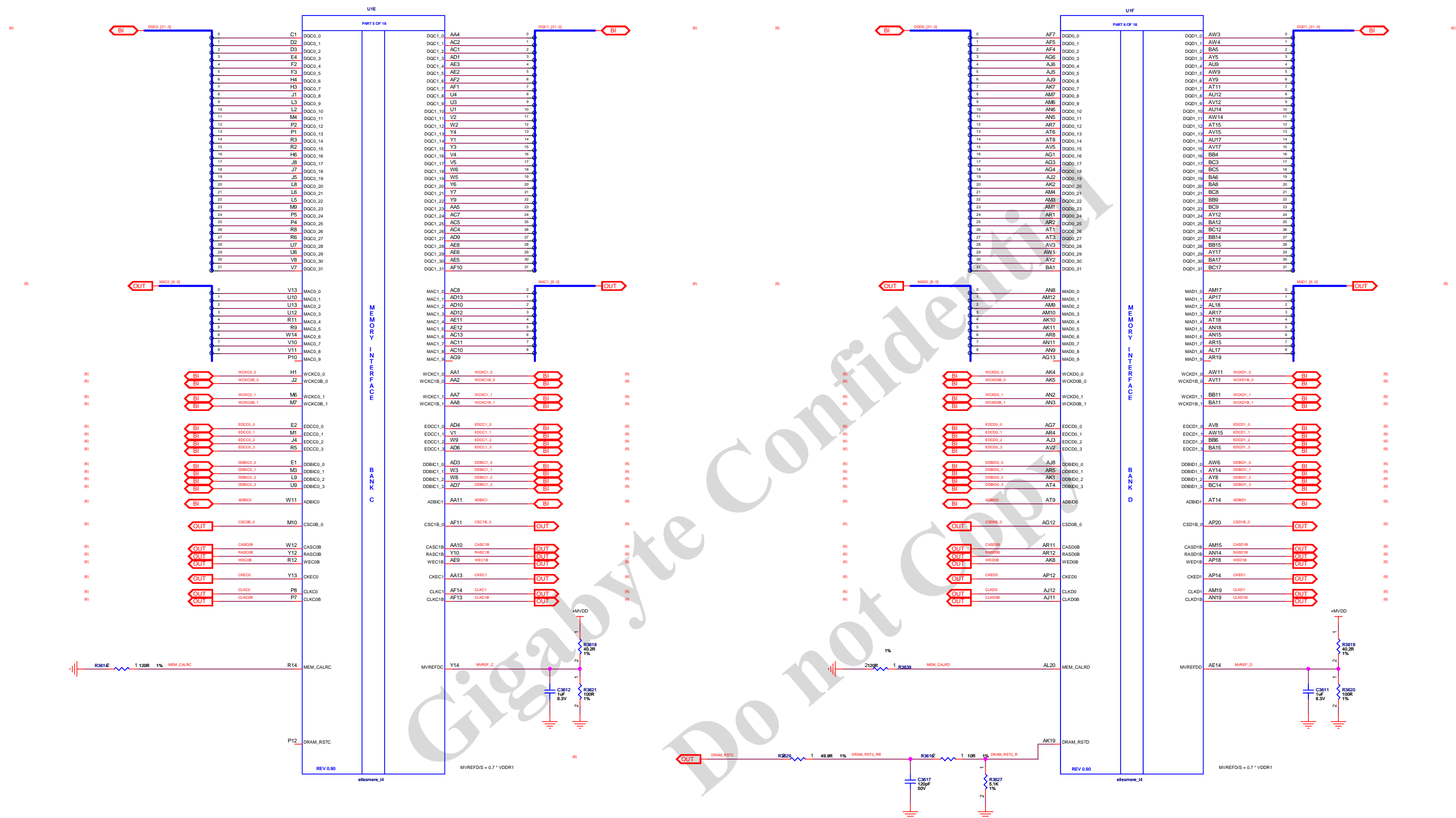
## (2) ELLESMERE PCIE INTERFACE



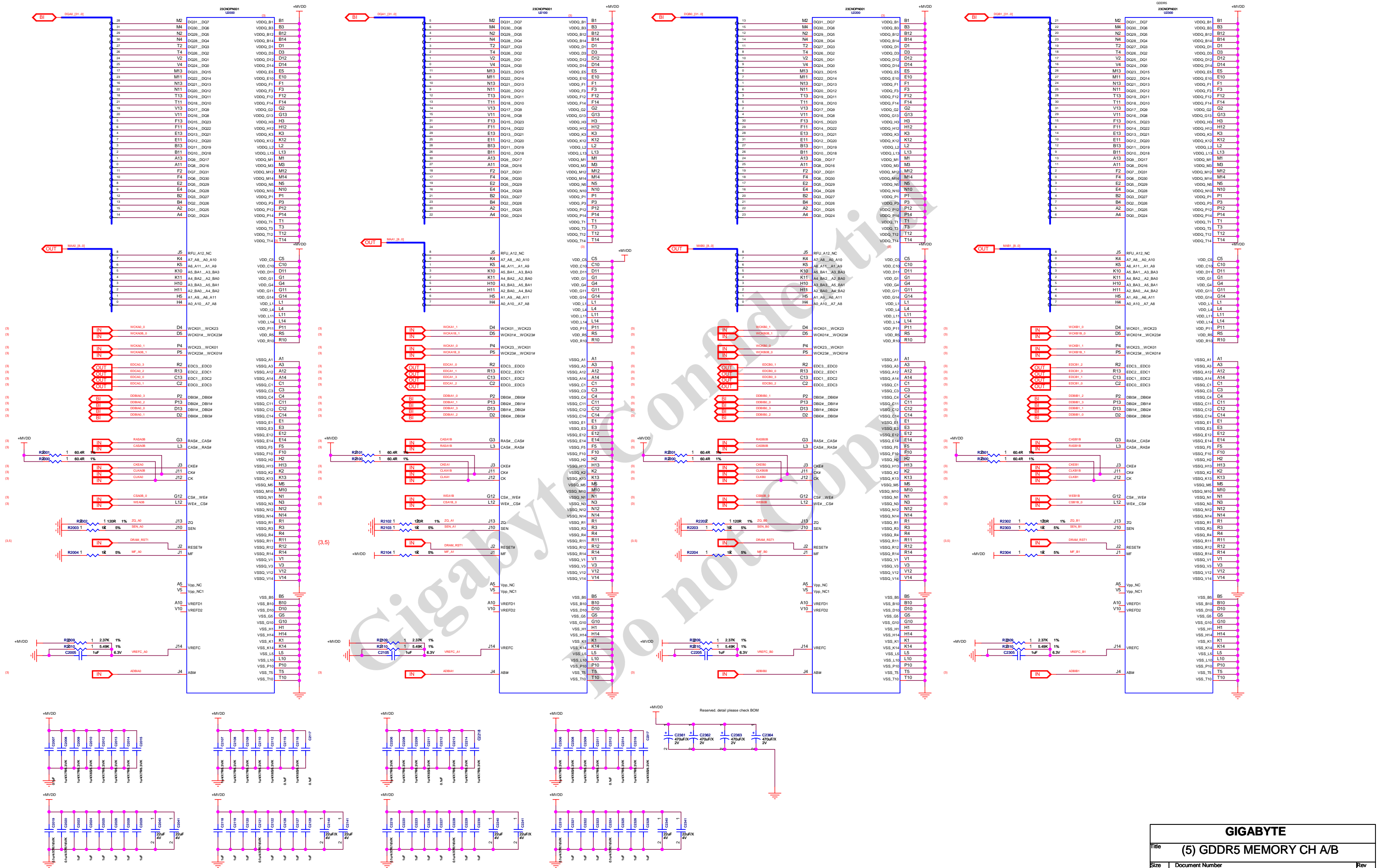
### (3) ELLESMERE MEM INTERFACE CH A/B



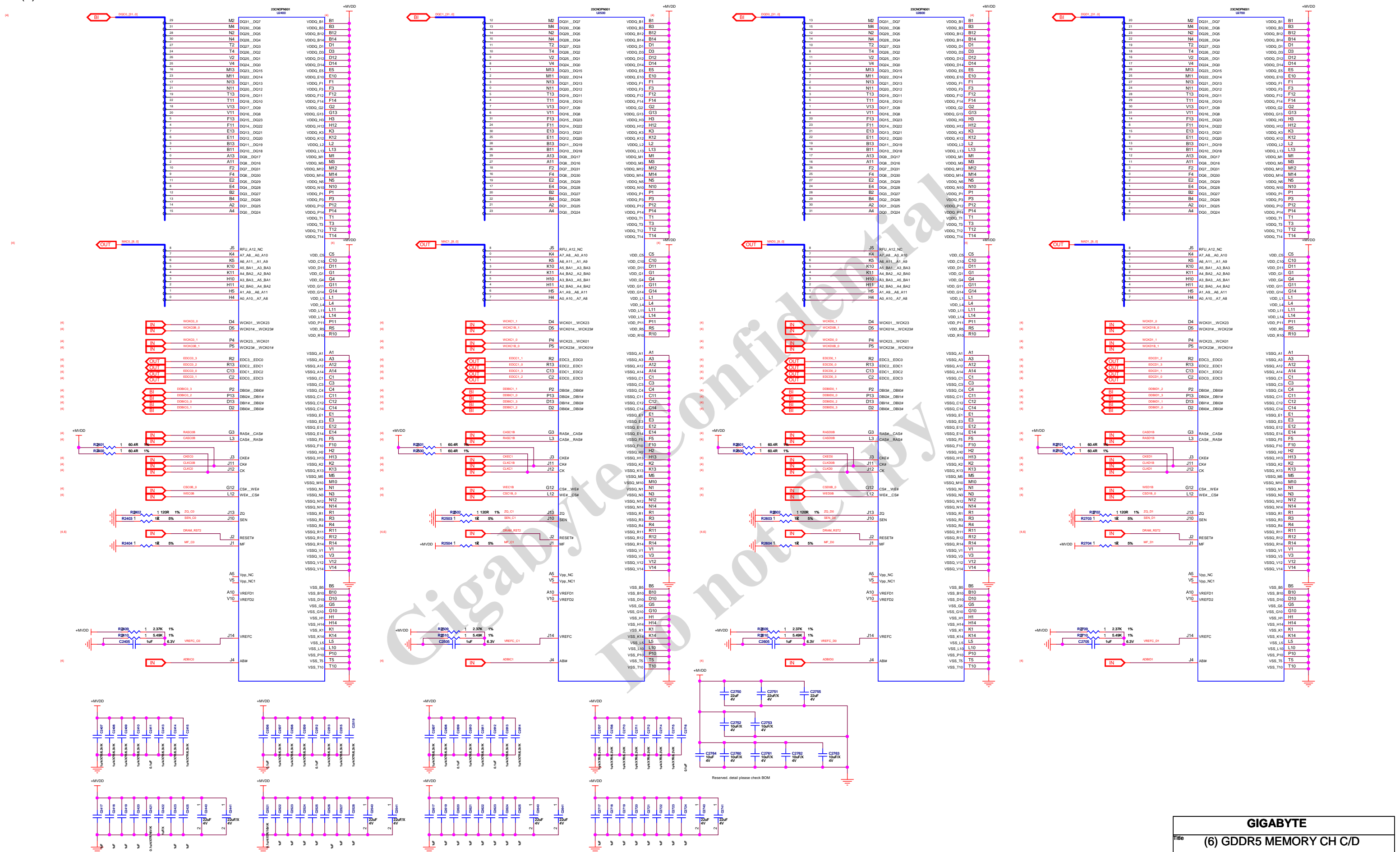
## (4) ELLESMERE MEM INTERFACE CH C/D



(5) GDDR5 MEMORY CH A/B



## (6) GDDR5 MEMORY CH C/D



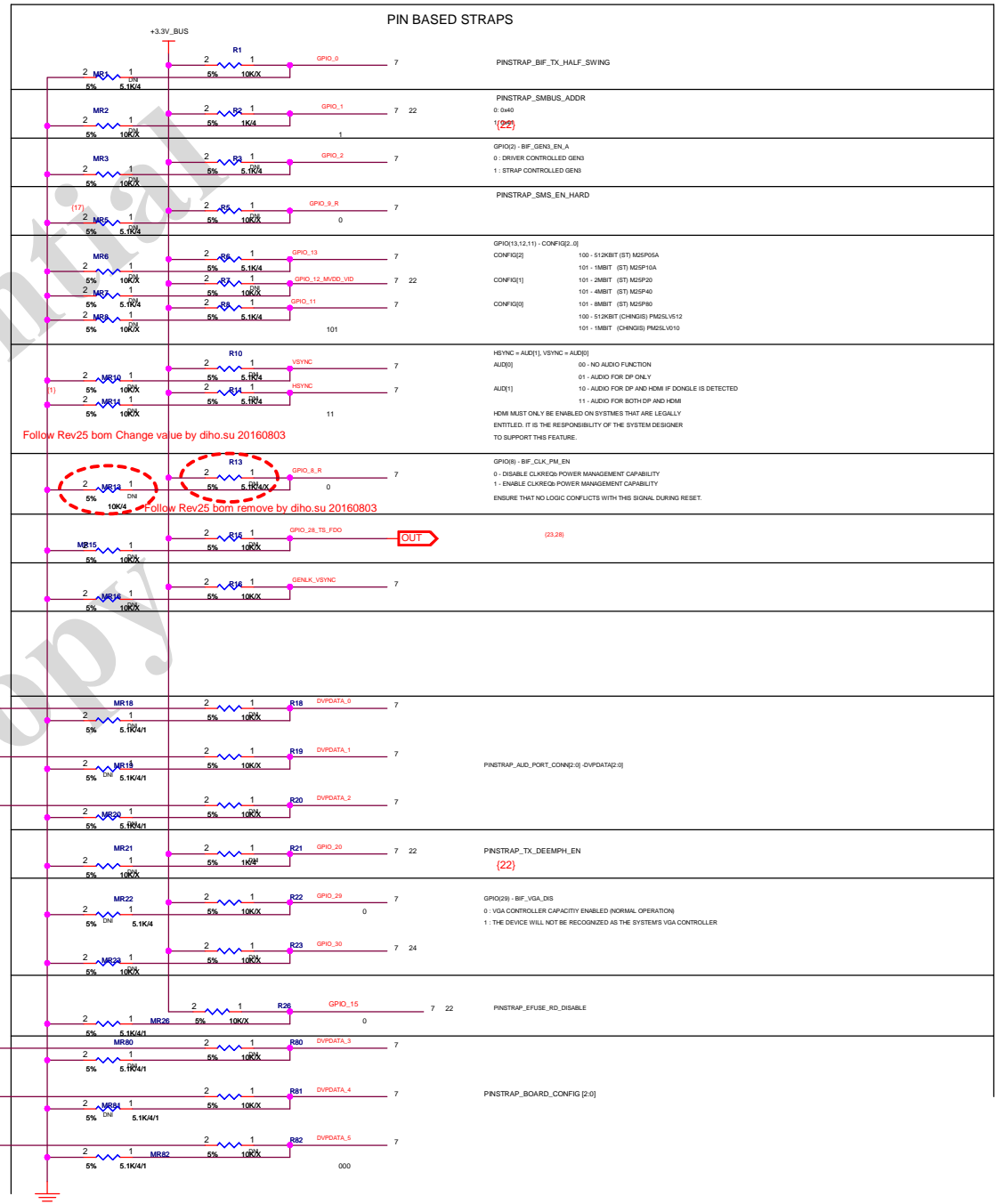
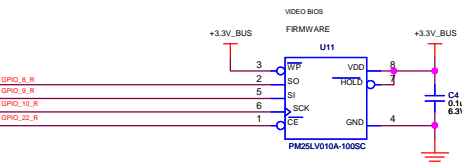
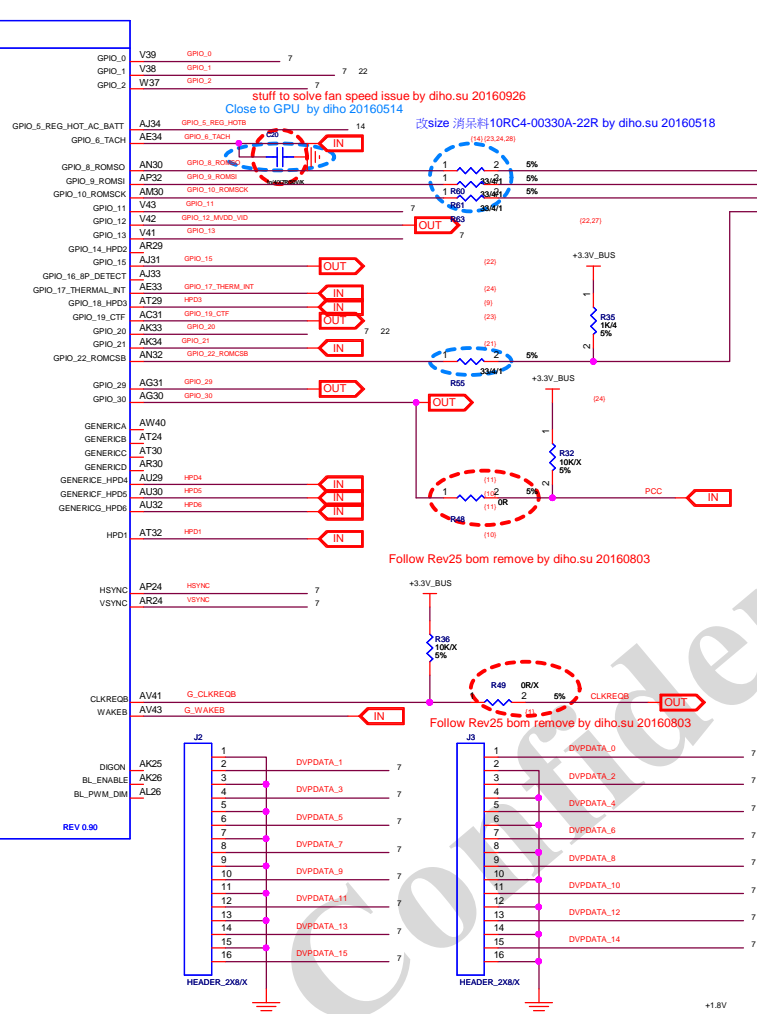
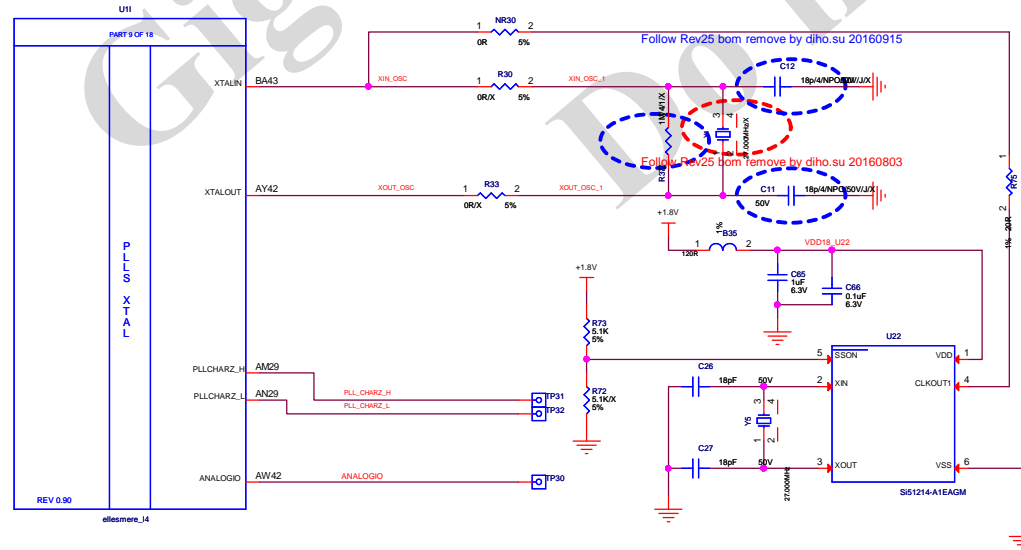
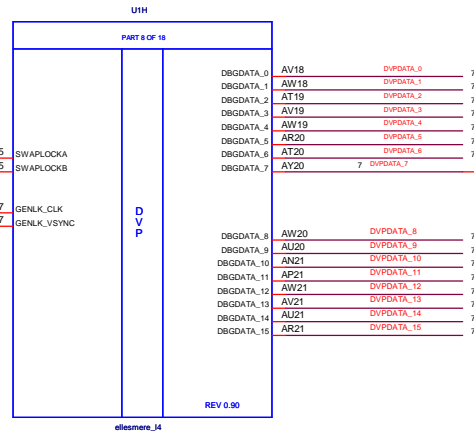
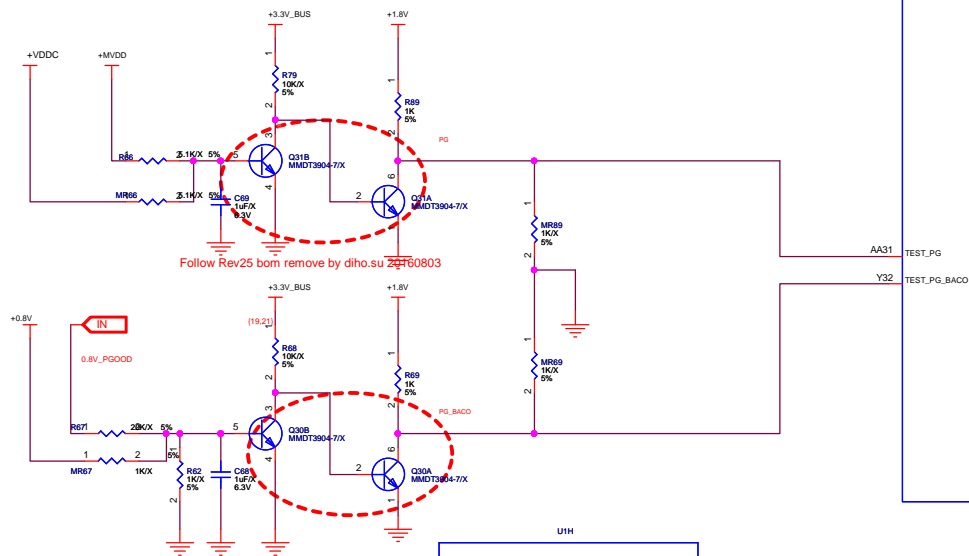


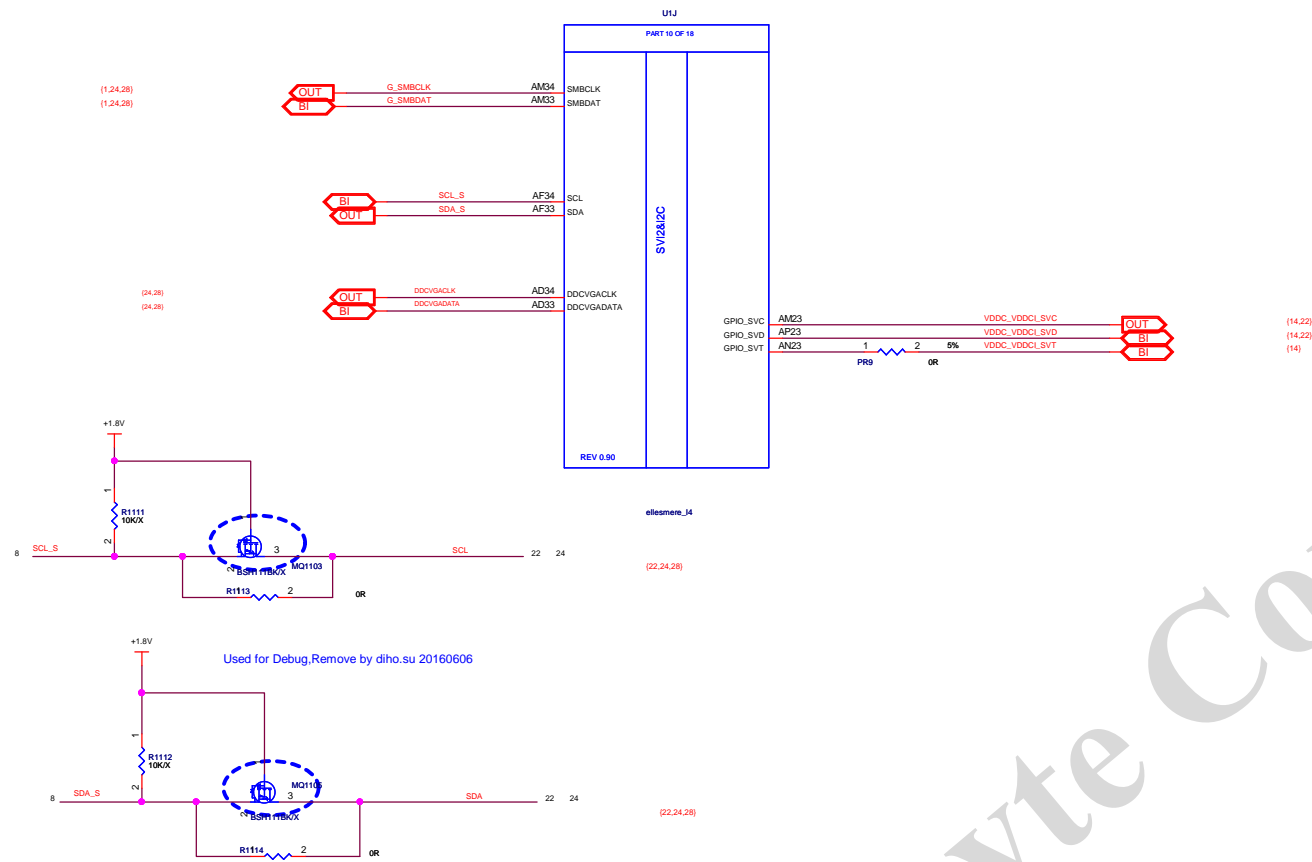
(7) ELLESMERE GPIO STRAP CF XTAL

I2C ADDRESS	FUNCTION	DEVICE

D0C1VGA BUS:

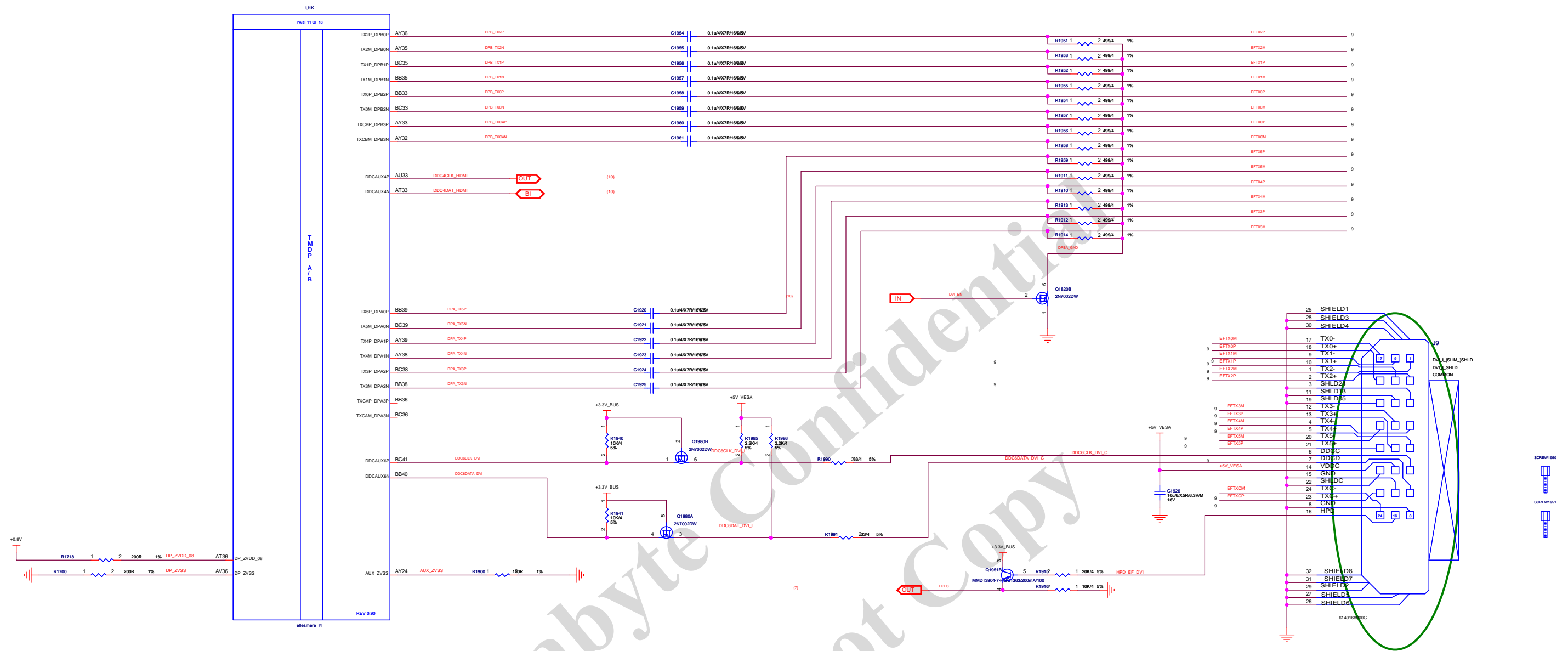
I2C ADDRESS	FUNCTION	DEVICE
0x68	EXT TEMP SENSOR	LM96063



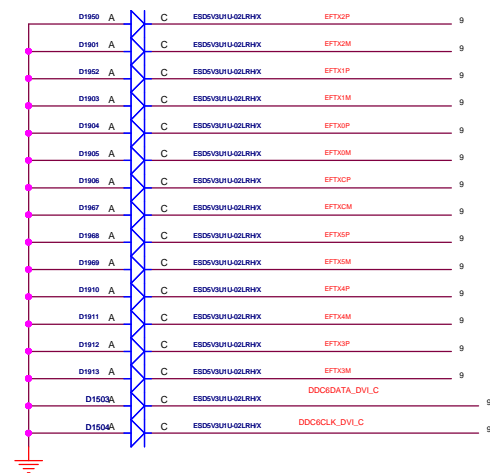




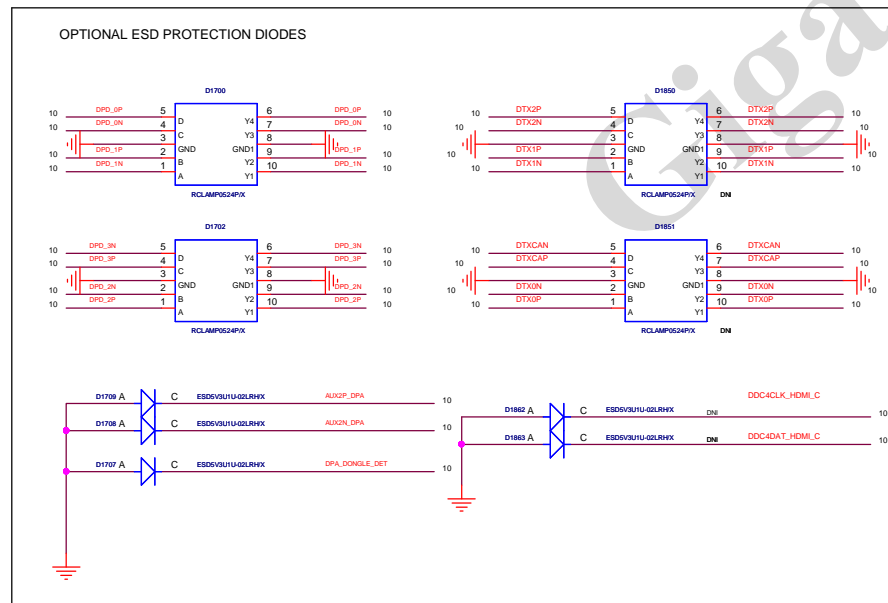
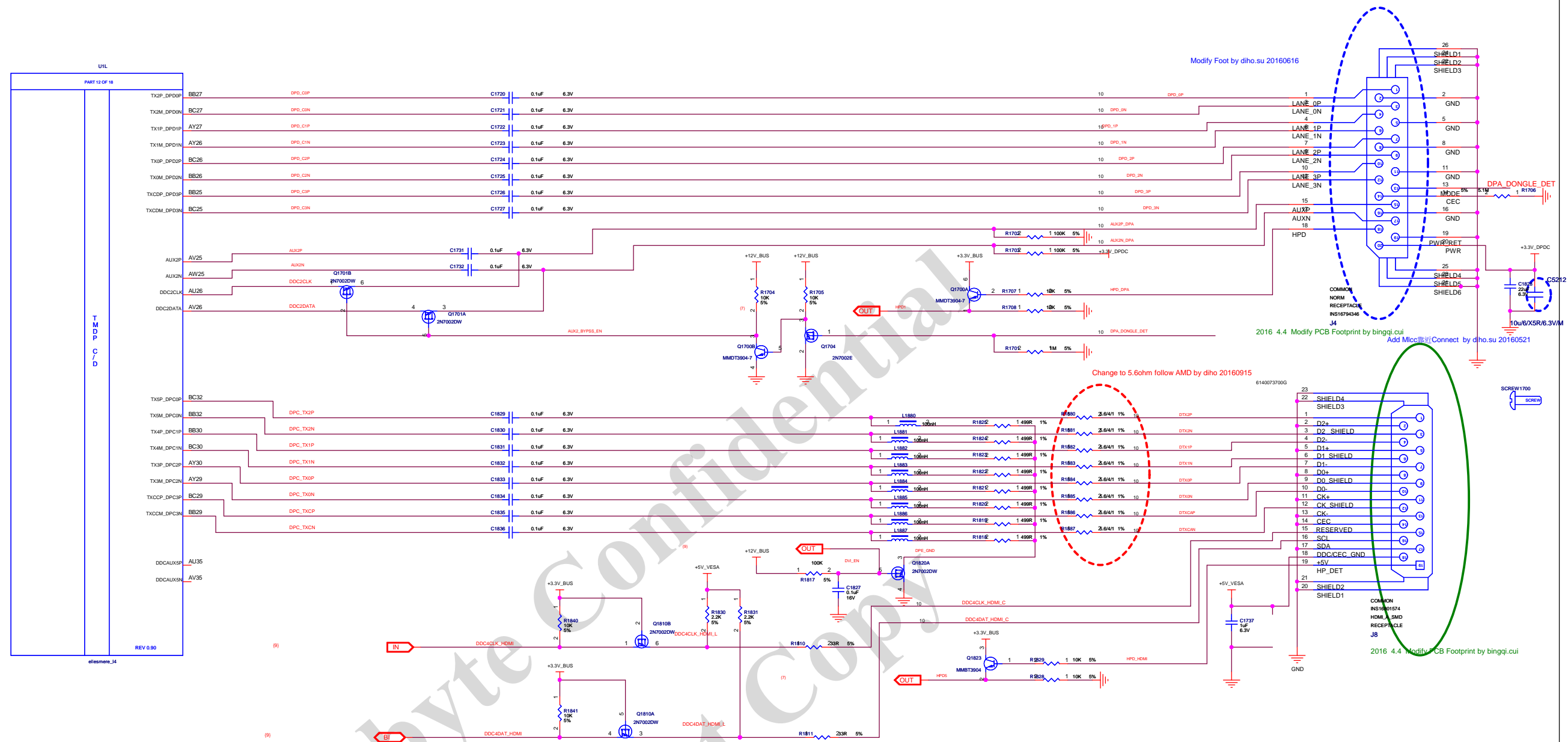
(9) ELLESMERE TMDP A/B

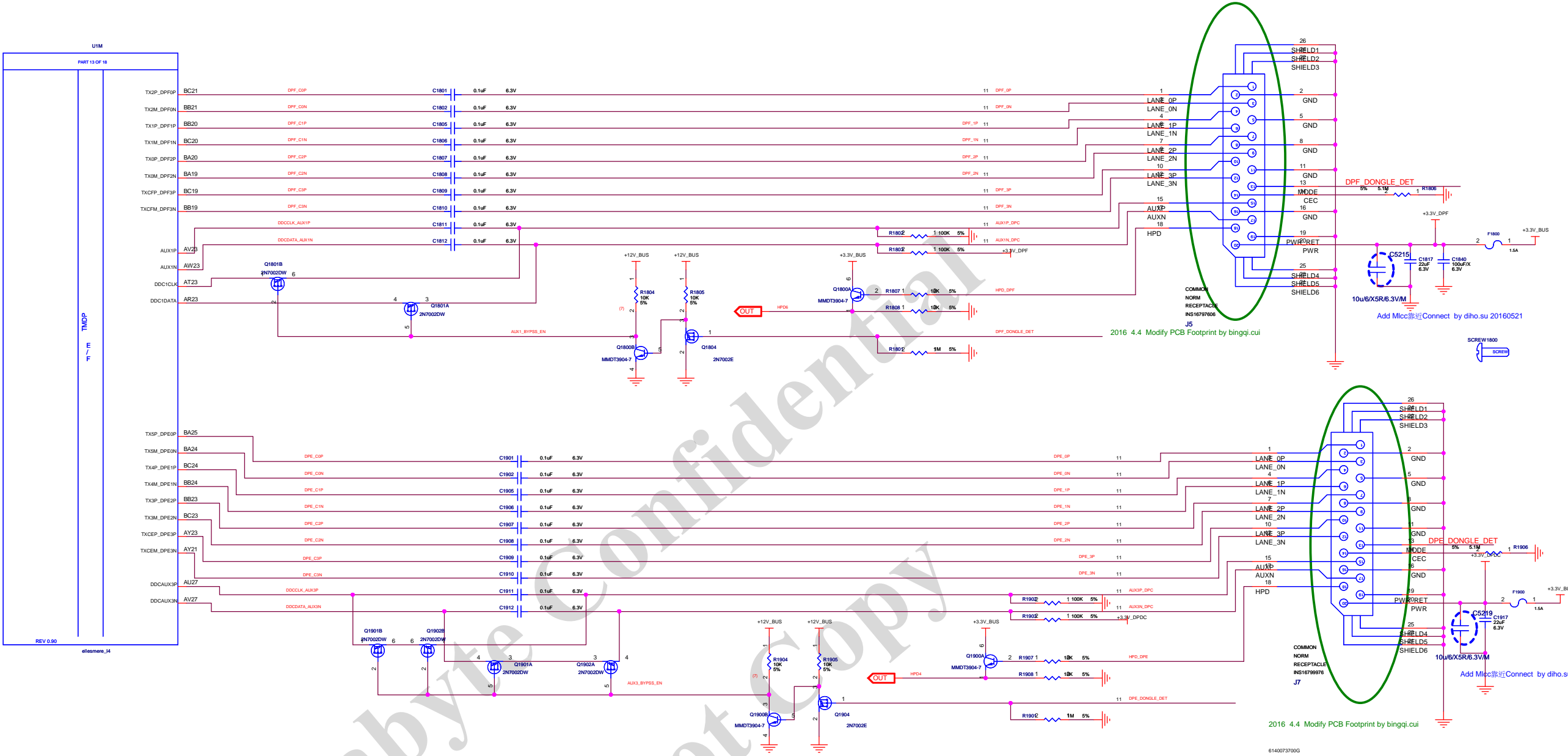


### OPTIONAL ESD PROTECTION DIODES

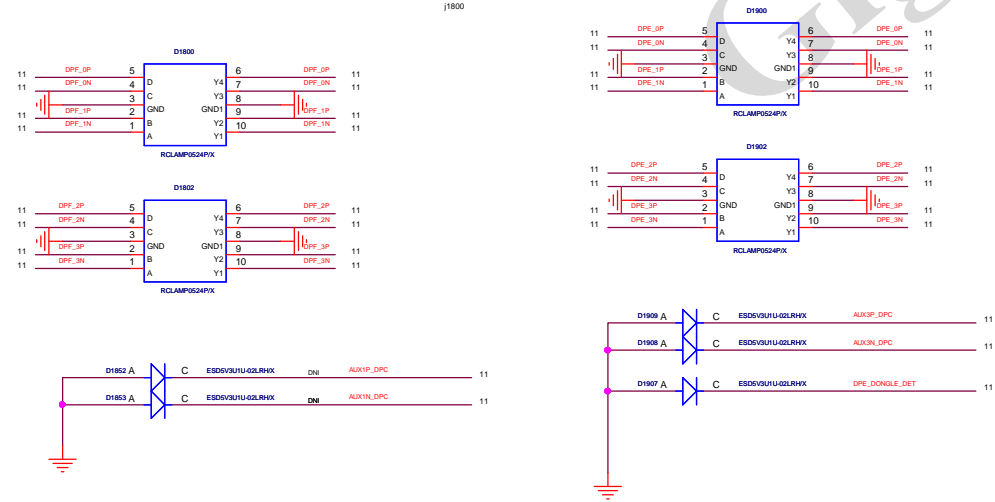


(10) ELLESMERE TMDP C/D



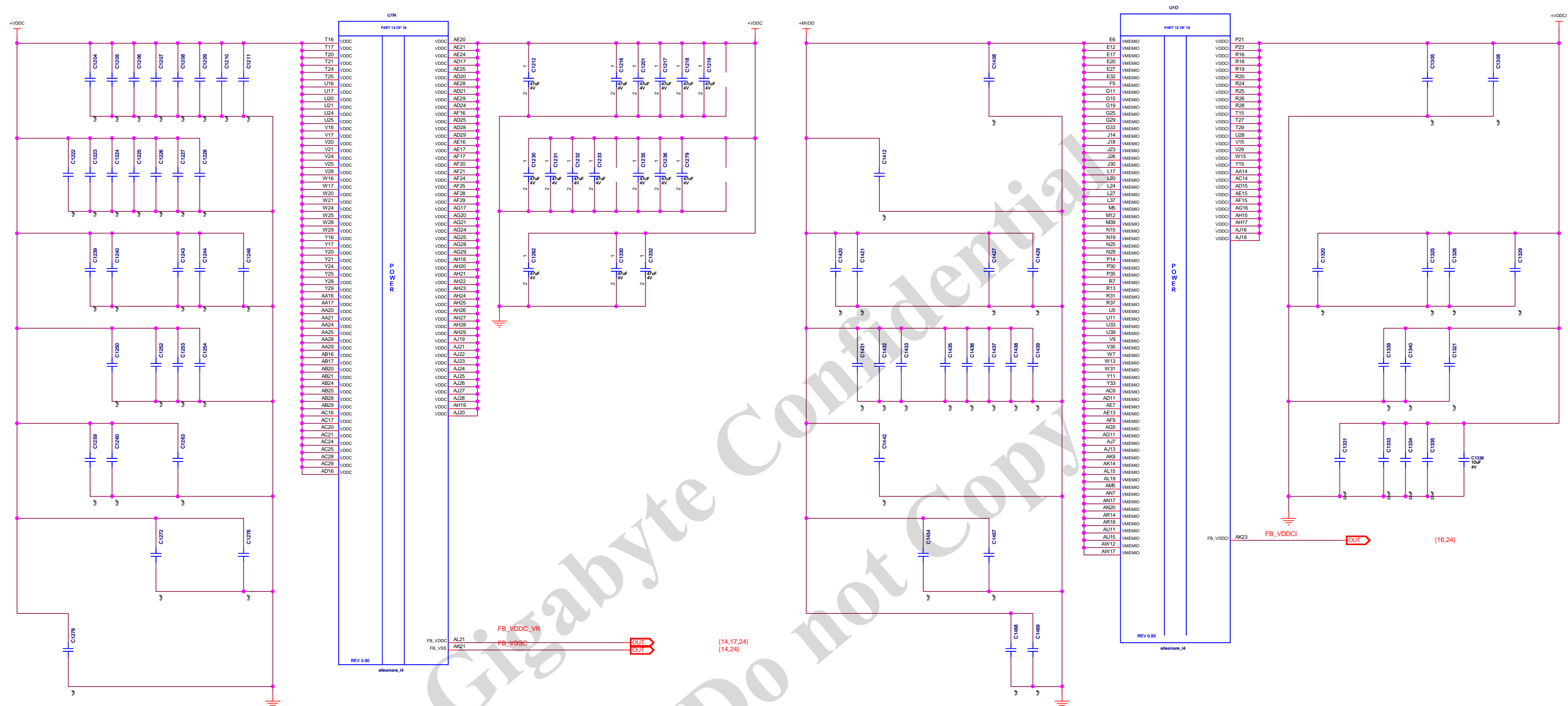


OPTIONAL ESD PROTECTION DIODES

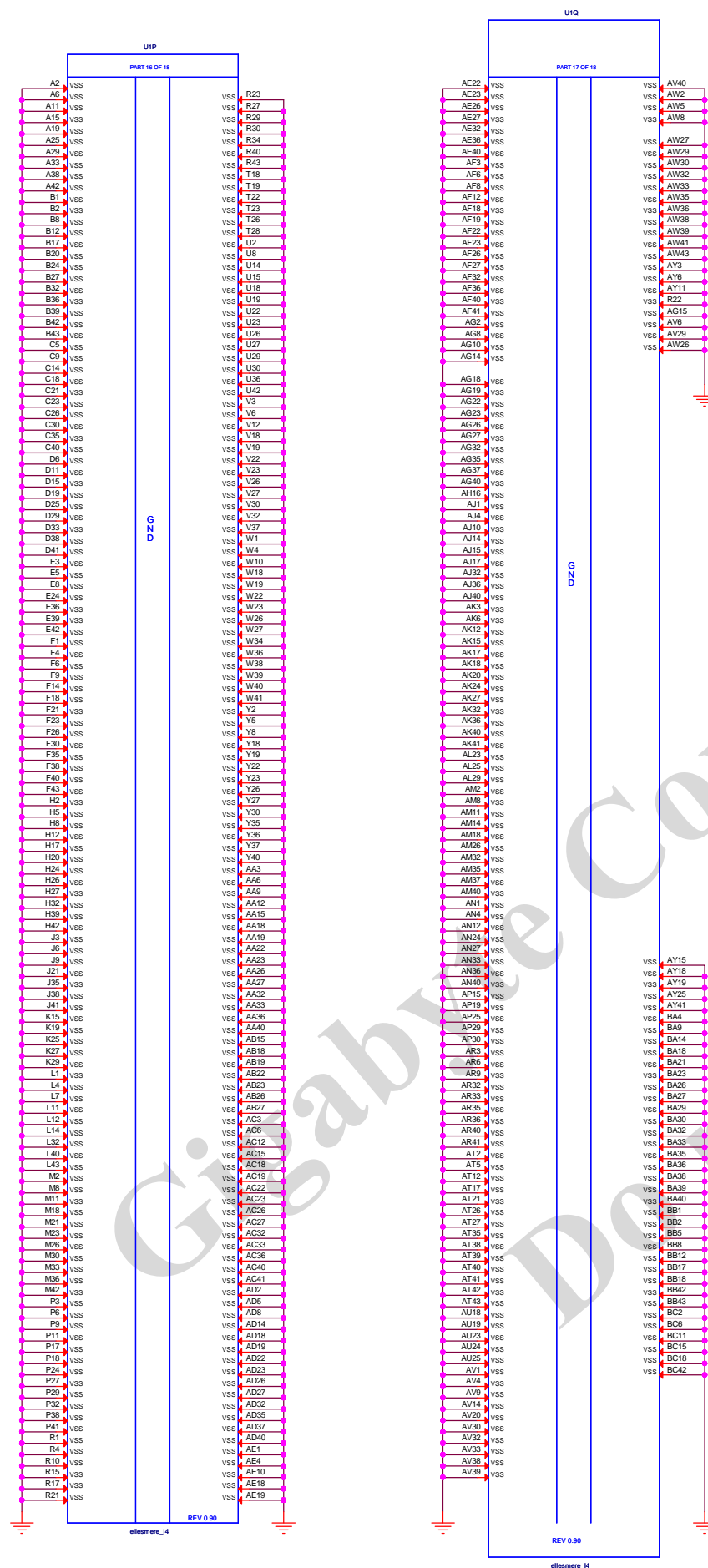


GIGABYTE			
Title ELLESMERE LVTMDP E/F			
Size Custom	Document Number GV-RX480G1 GAMING-4GD	Rev 1.1	
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(12) ELLESMERE POWER



(13) ELLESMERE GROUND



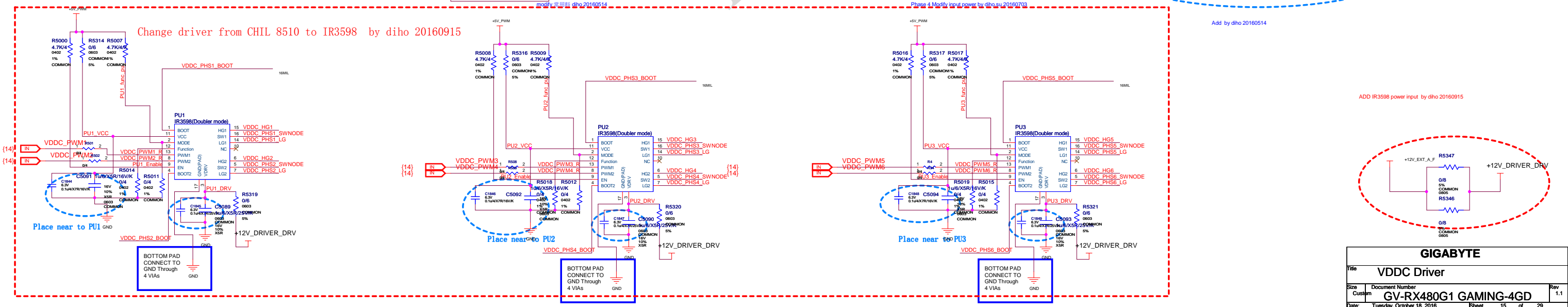
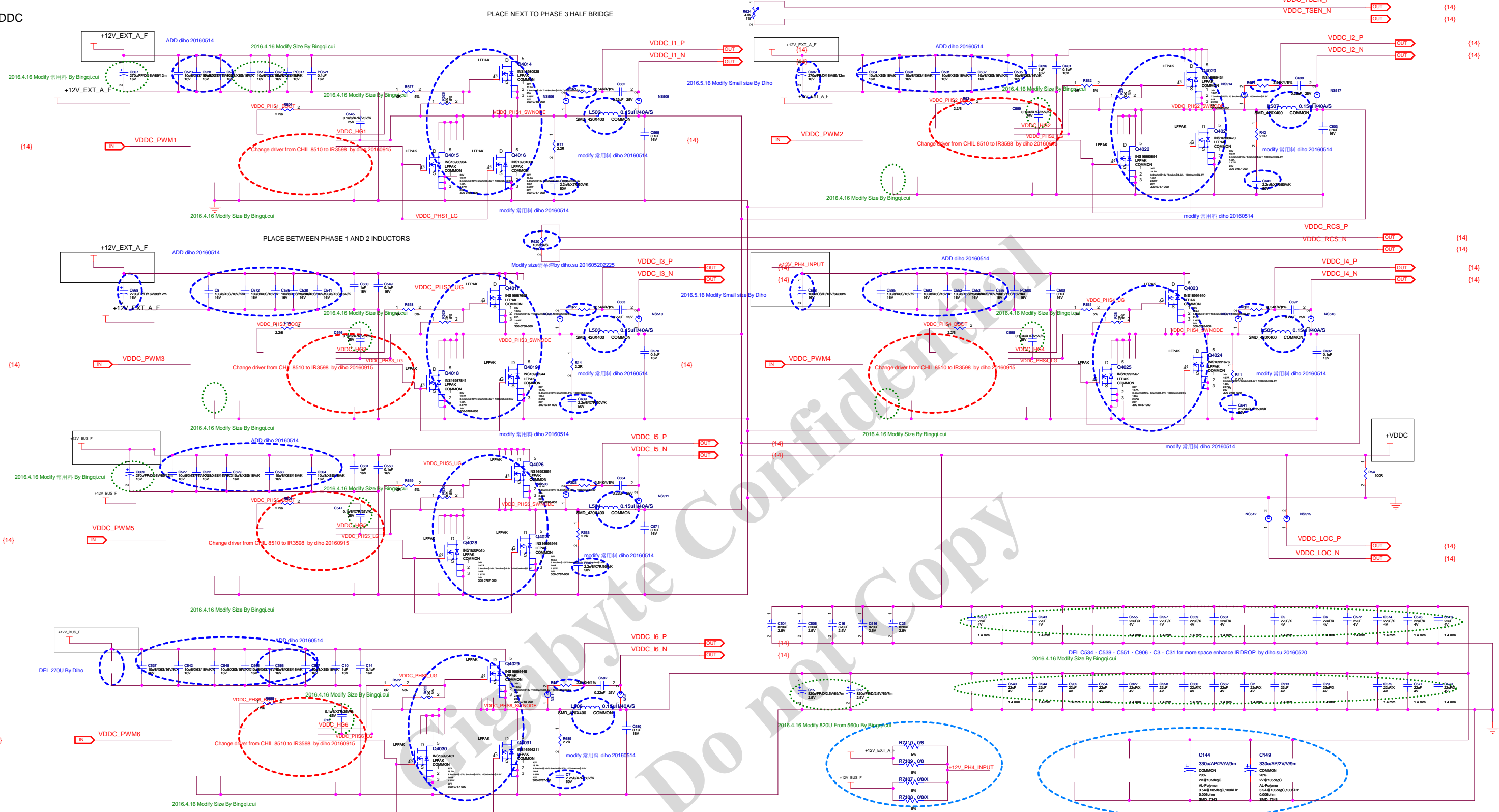
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Title <b>ELLESMERE GND</b>			
Size	Document Number		Rev
Custom	<b>GV-RX480G1 GAMING-4GD</b>		<b>1.1</b>
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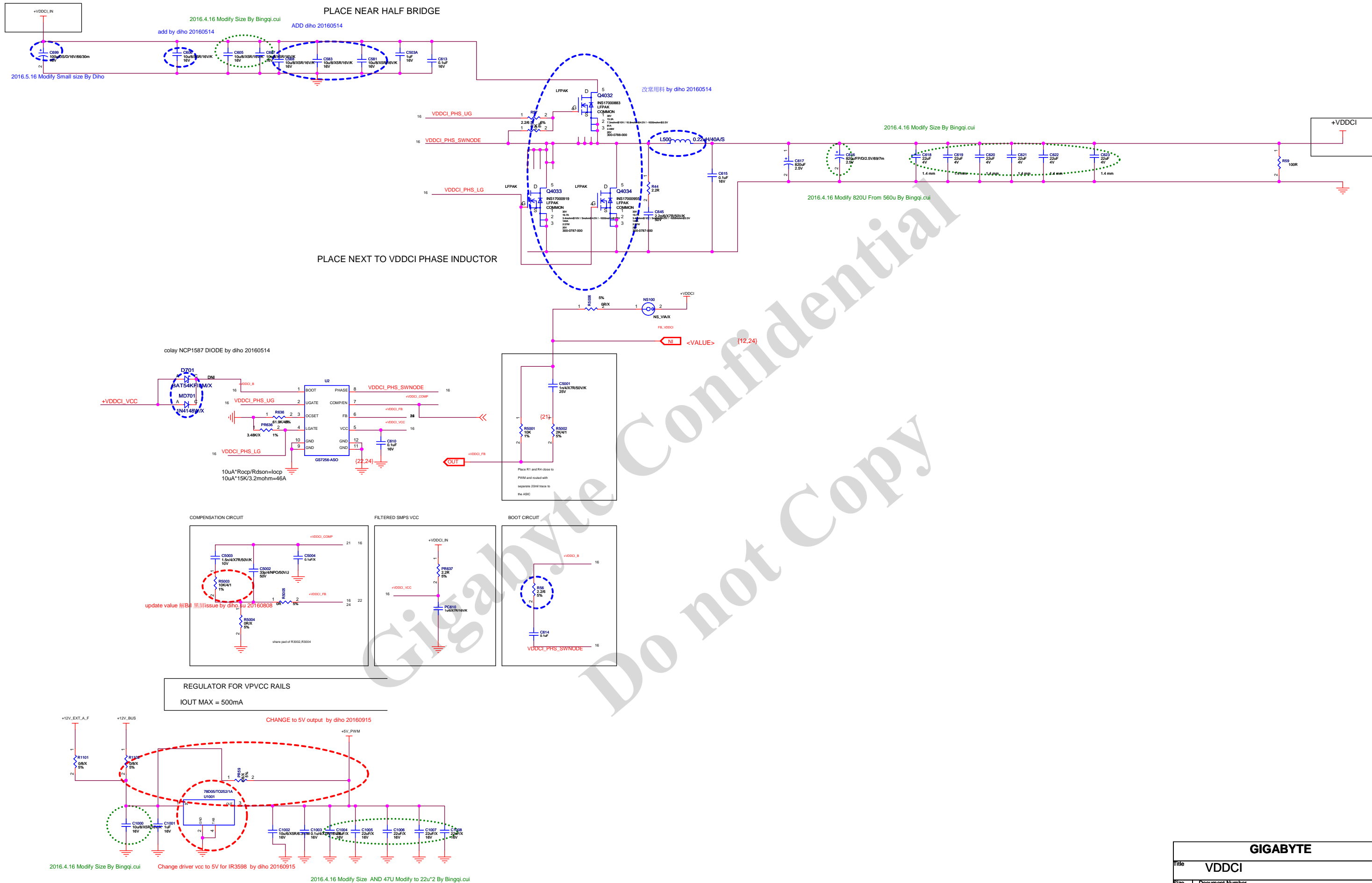




VDDC

PLACE NEXT TO PHASE 3 HALF BRIDGE

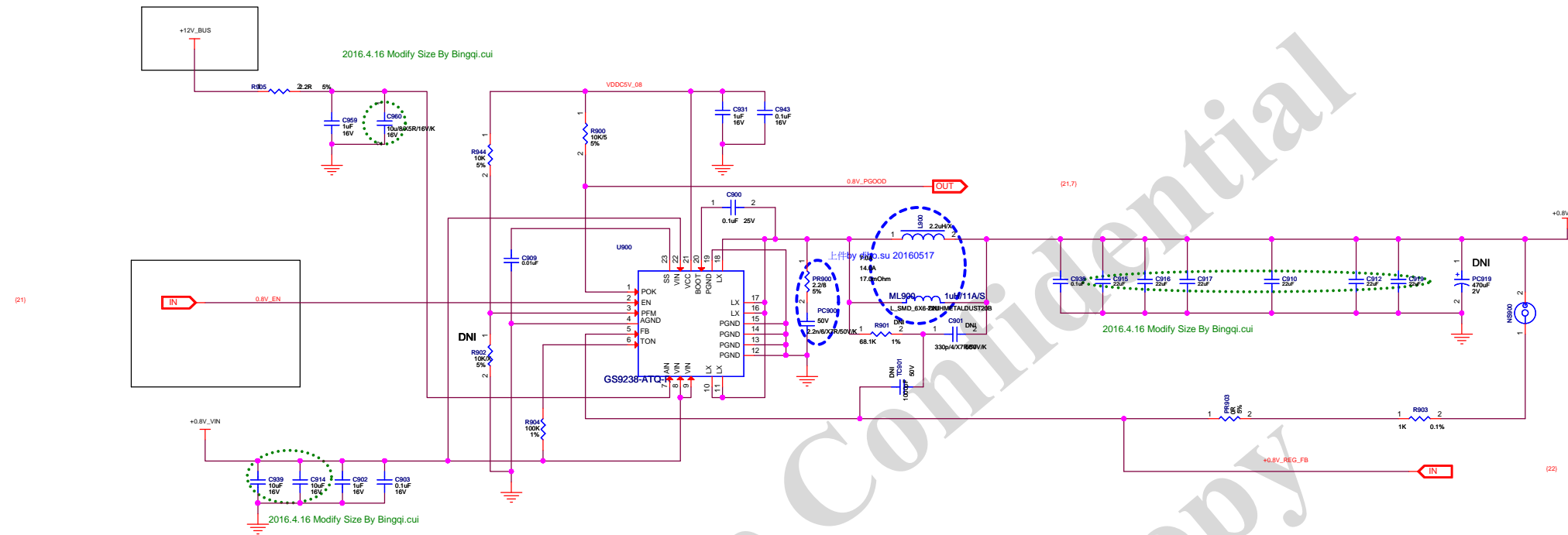


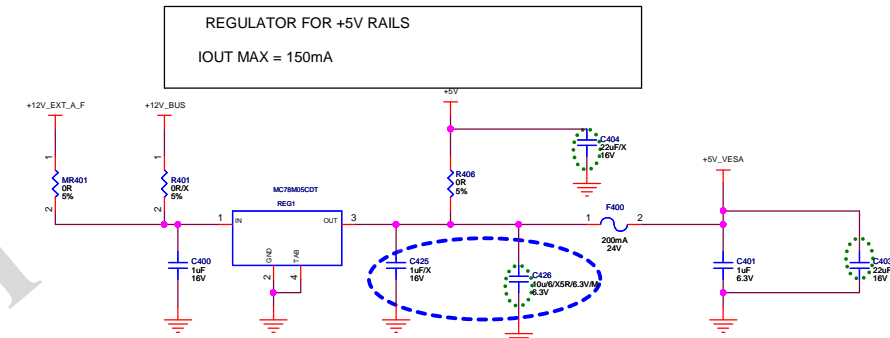


GIGABYTE			
Title		VDDCI	
Size	Document Number	Rev	
Custom	GV-RX480G1.GAMING-4GD	1.1	
Date:	Tuesday, October 18, 2016	Sheet	16 of 29



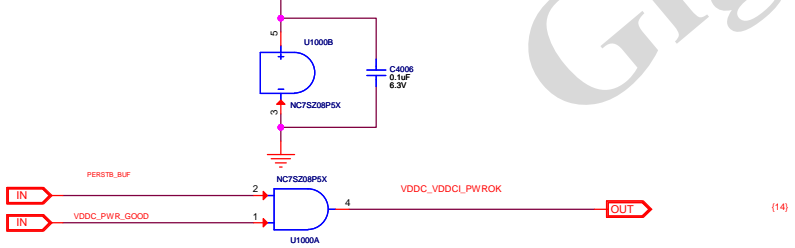
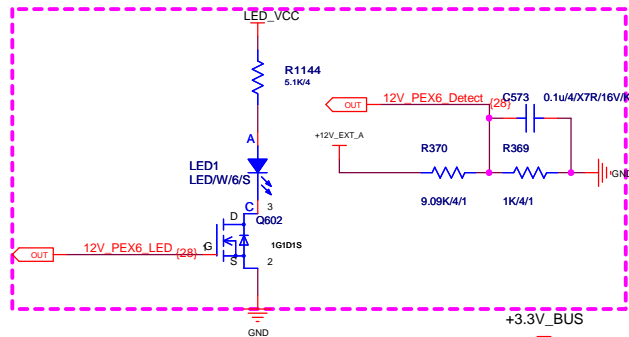
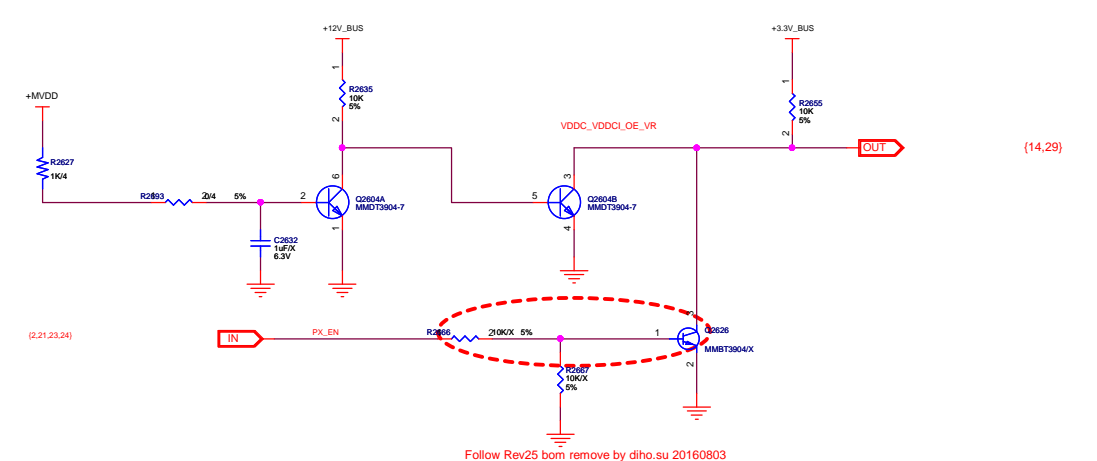
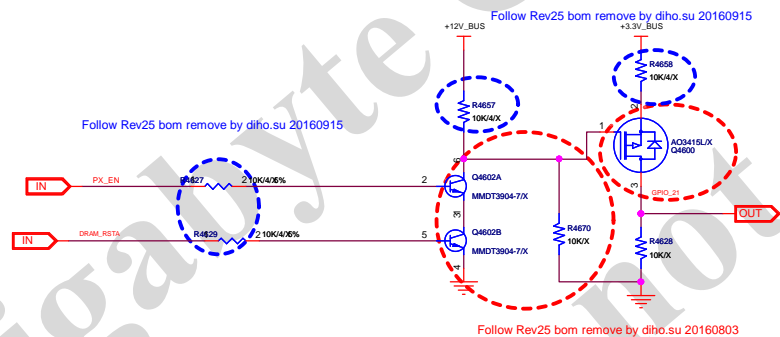
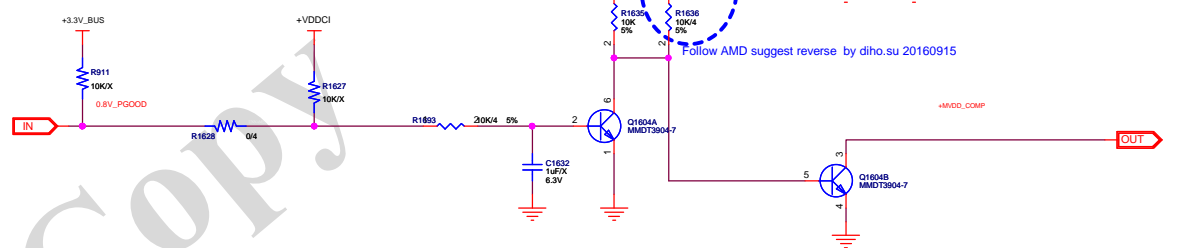
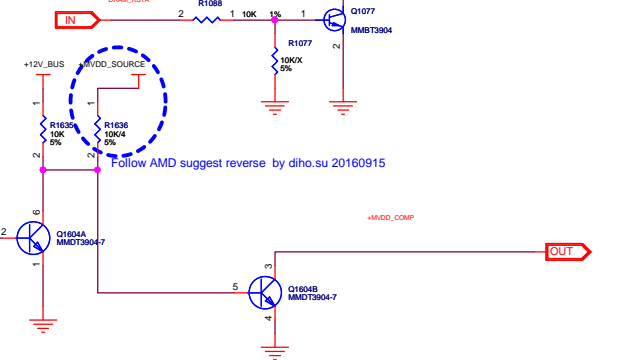
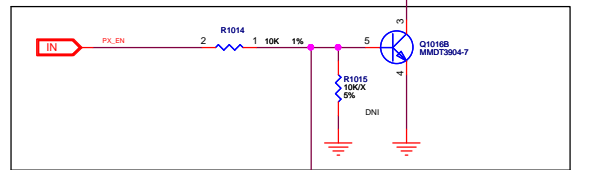
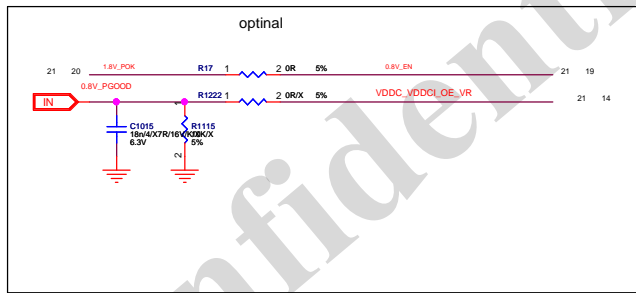
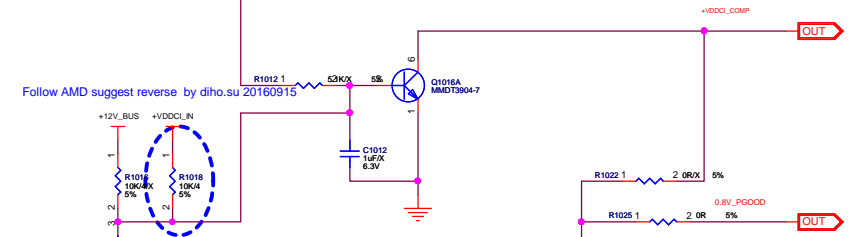
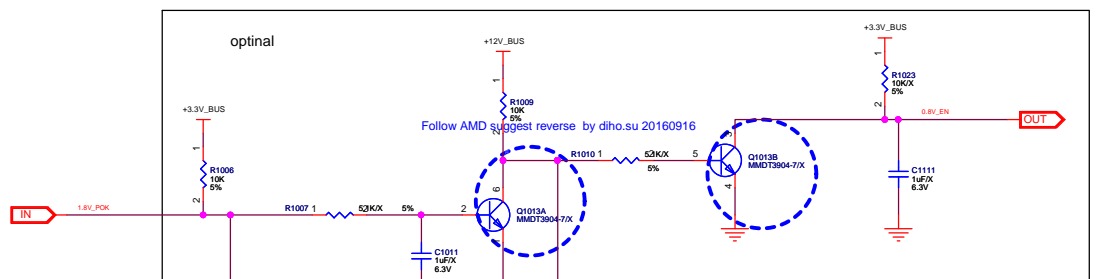
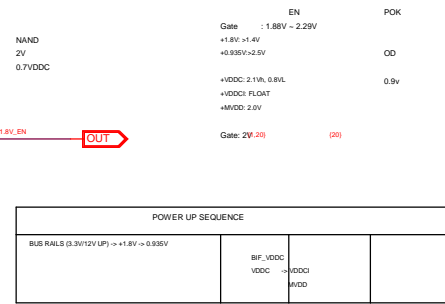
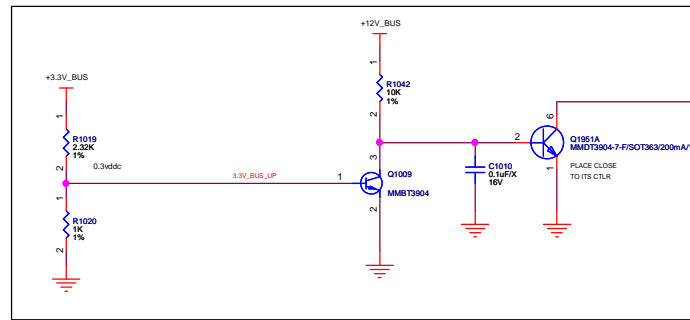
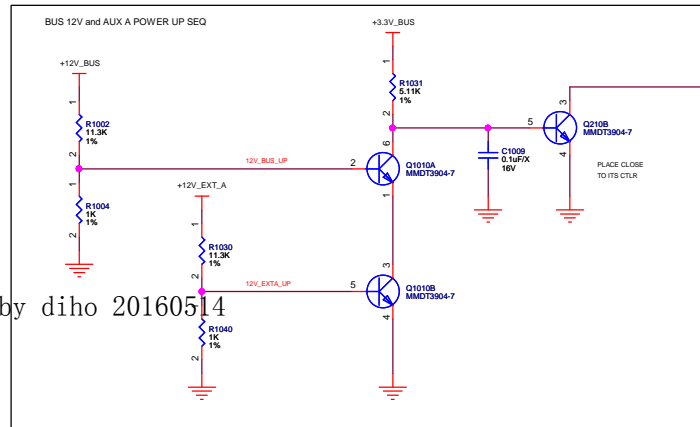
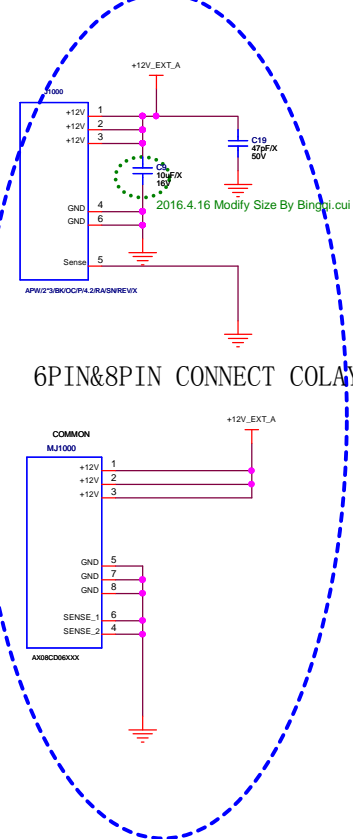




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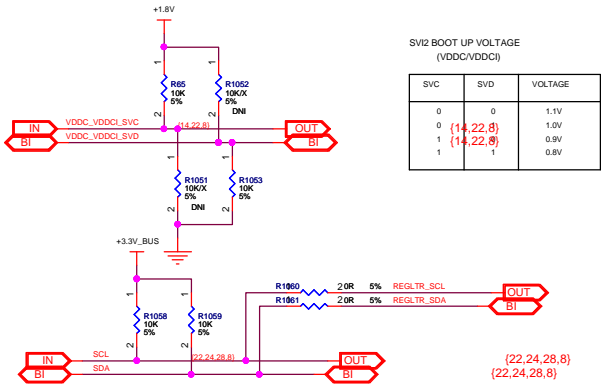


(19) POWER MANAGEMENT



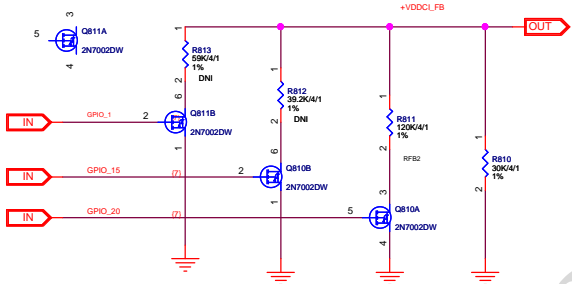
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(22,24,28,8)

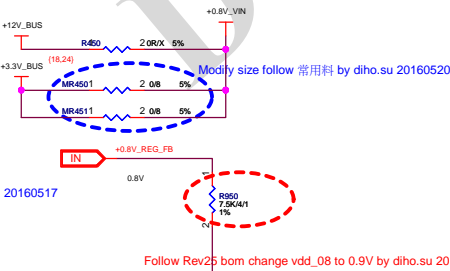
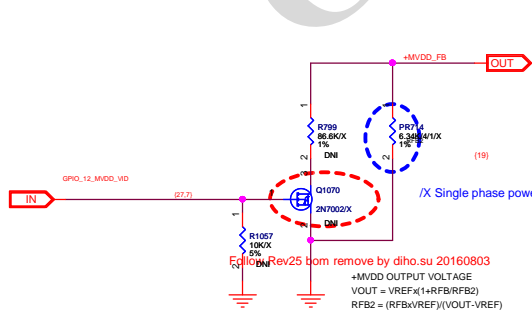


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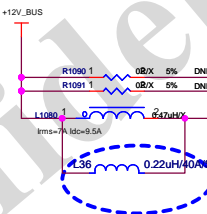
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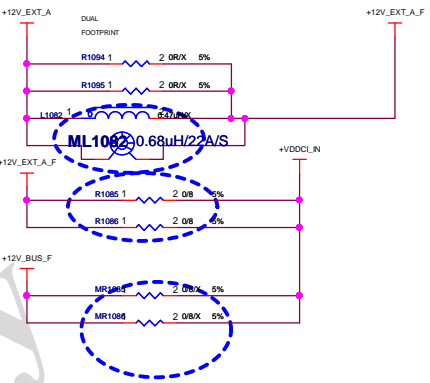
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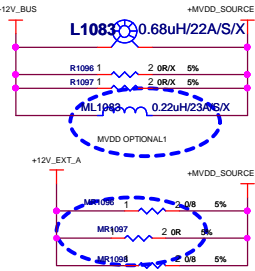
+12V\_BUS INPUT



+12V\_EXT\_A INPUT

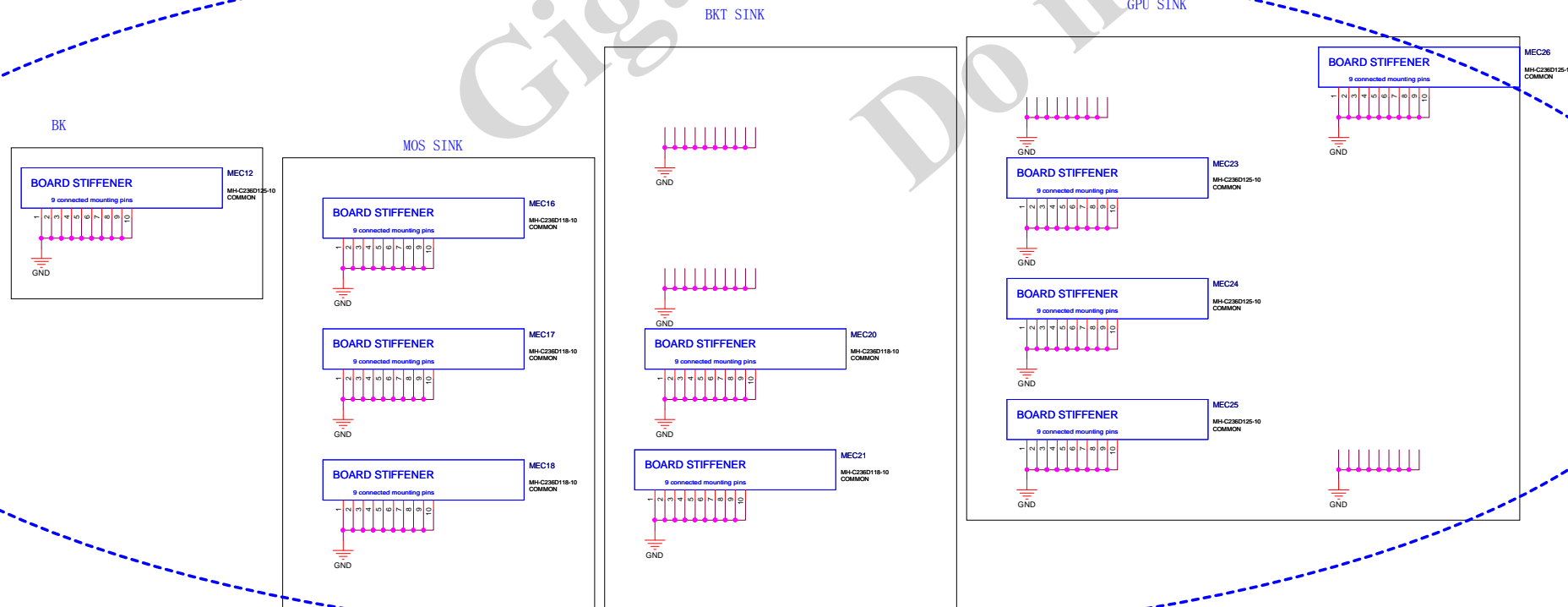
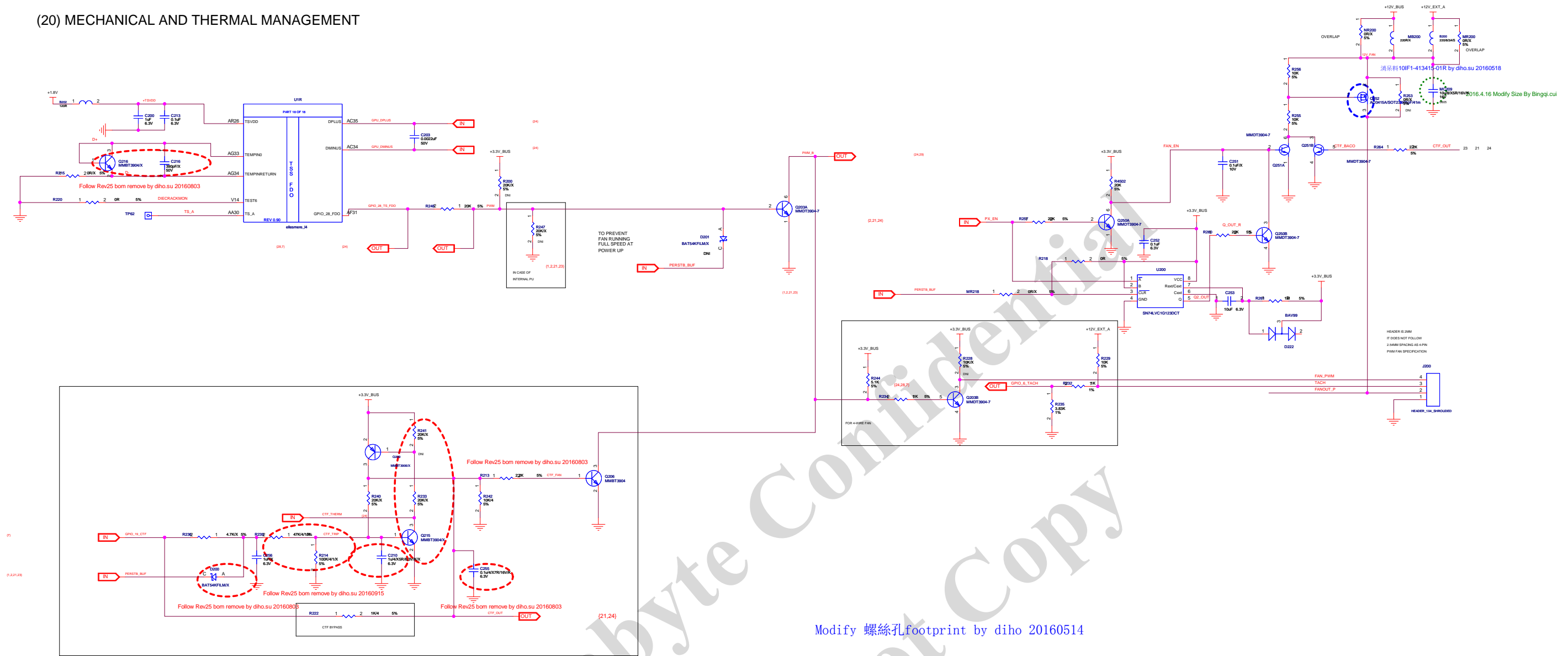


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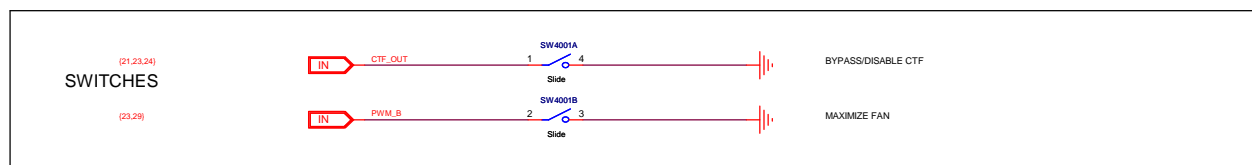
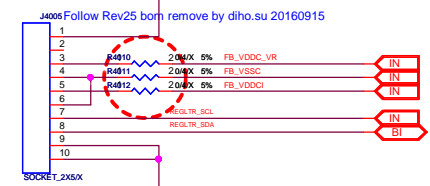
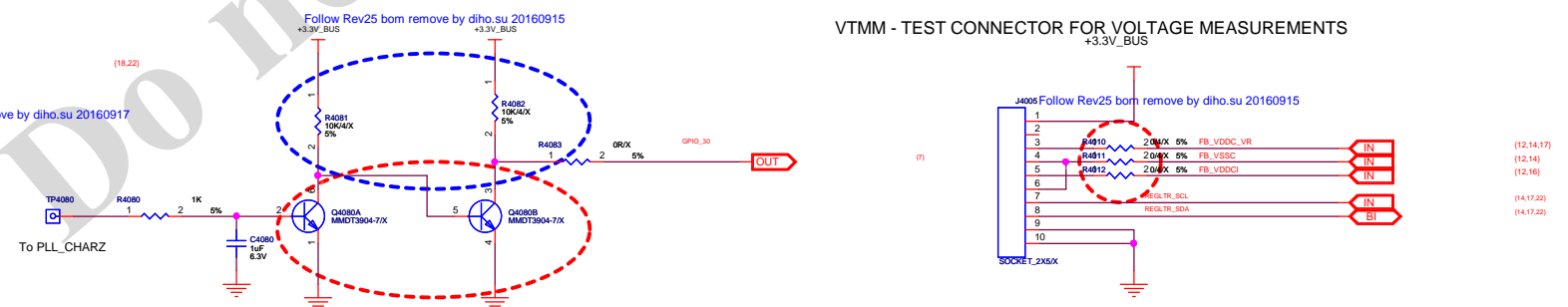
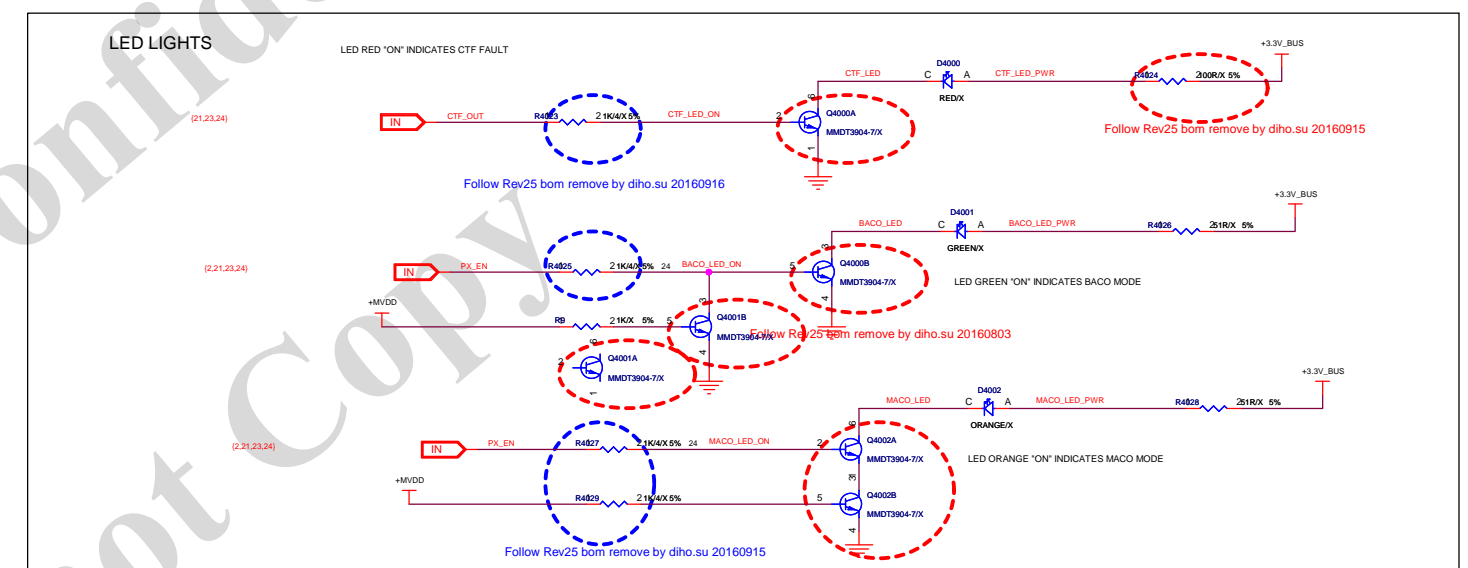
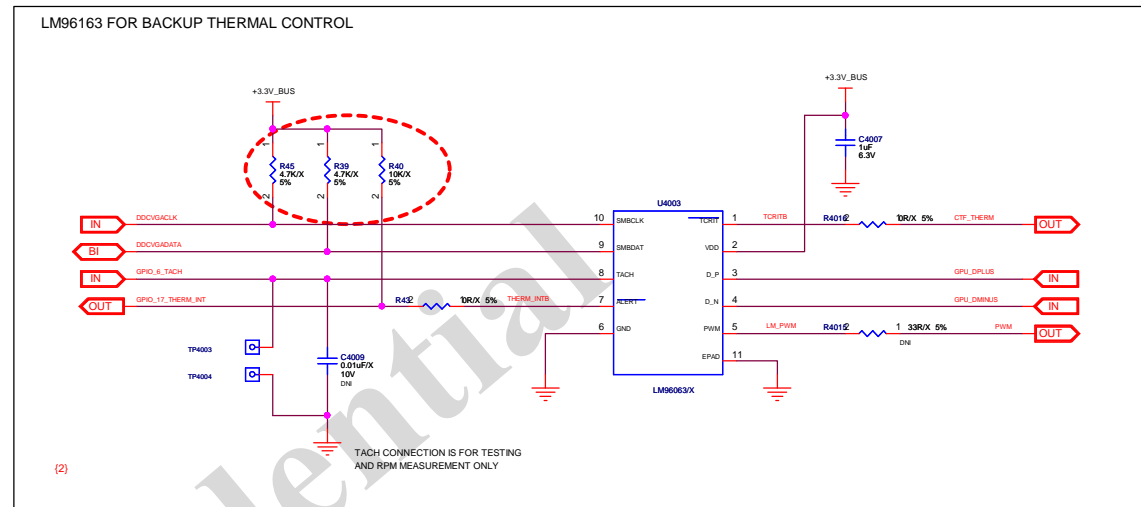
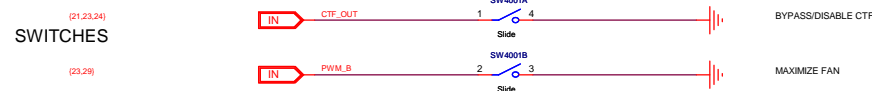
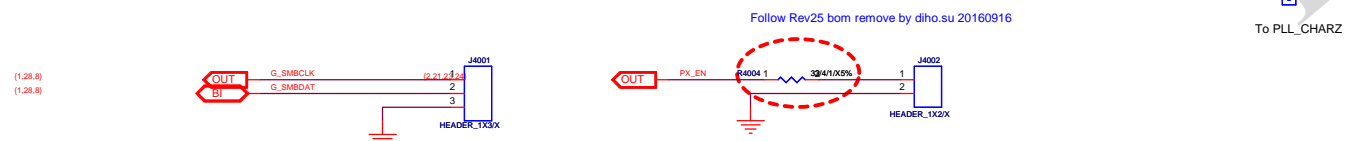
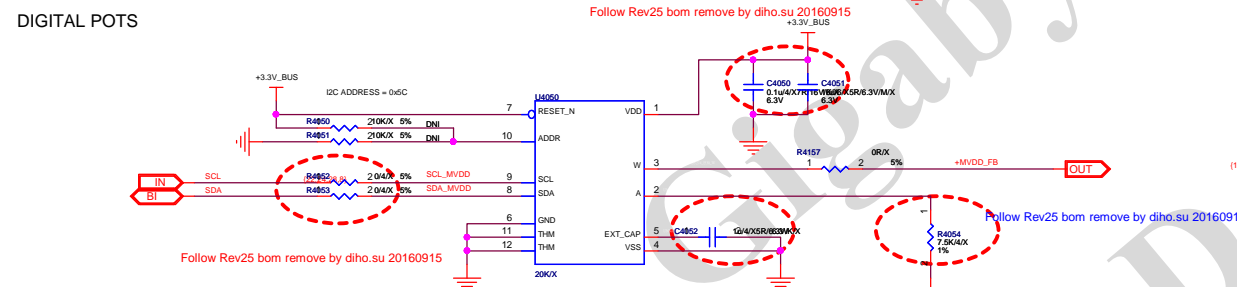
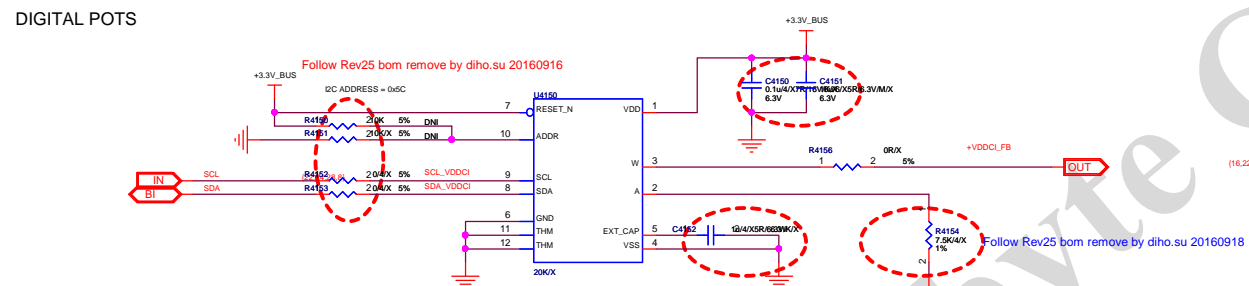
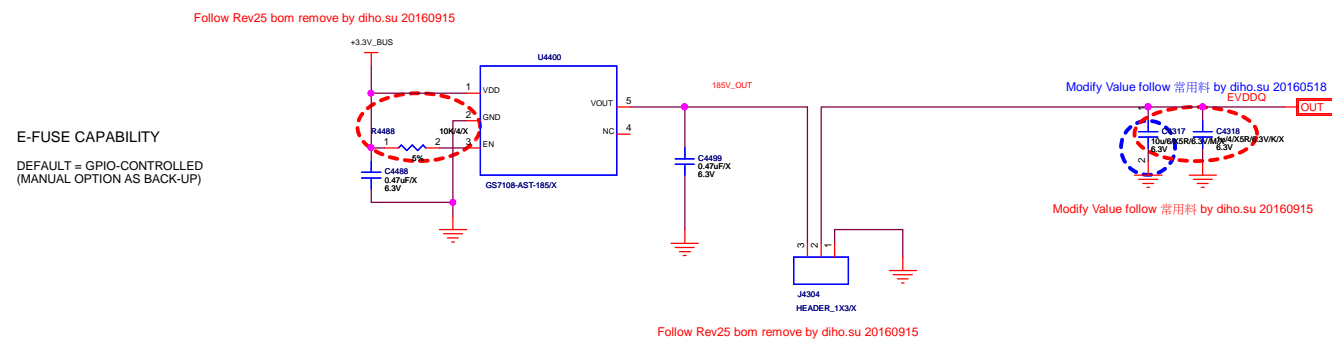
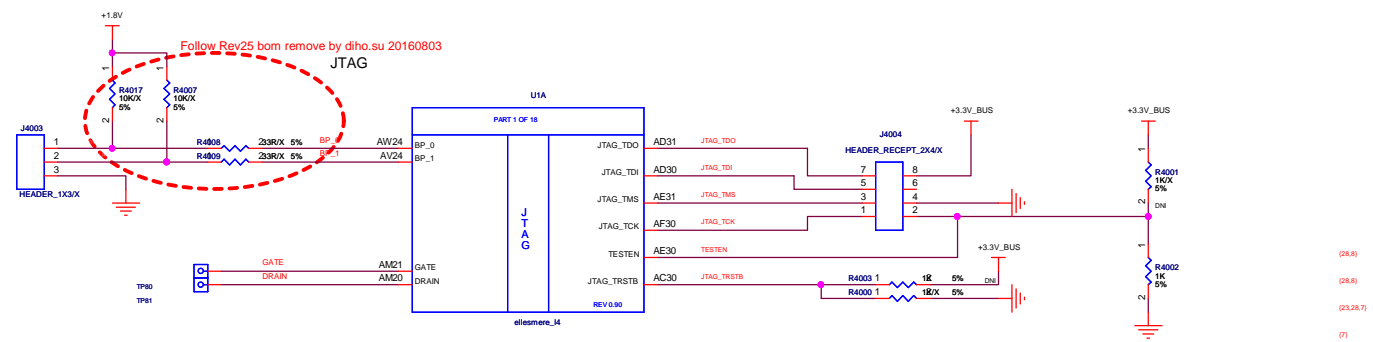


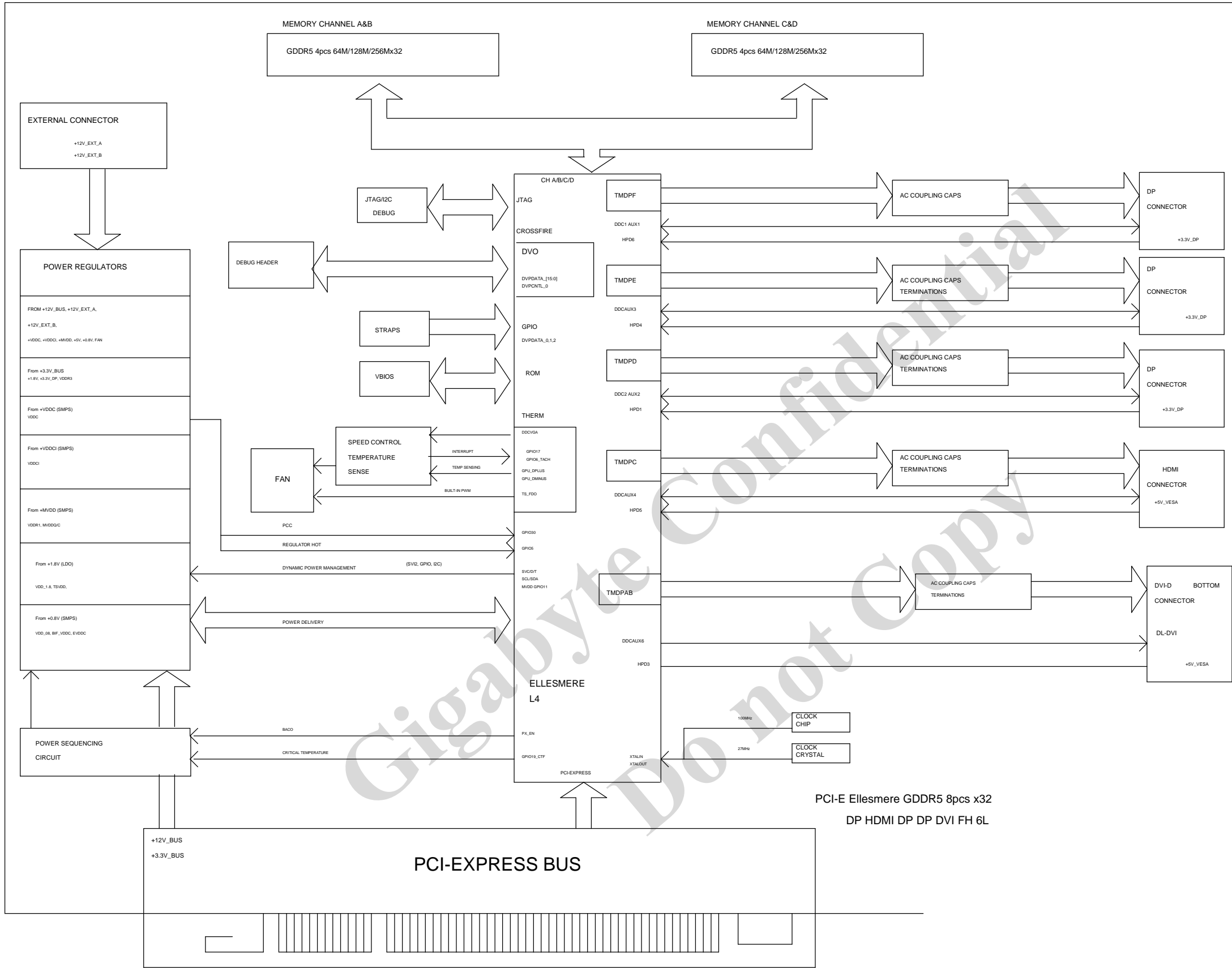
Modify size follow 常用料 by diho.su 20160518

## (20) MECHANICAL AND THERMAL MANAGEMENT



## (21) DEBUG CIRCUITS





0	00A	08/28/2016	
1	00B	10/18/2016	1. Add GPIO1 for dynamic VDDCI 2. update VDDCI driver sequence
2	00C	03/22/2016	HDMI: - add series resistor R1880 ~R1887 - add pull down inductor L1880 ~L1887  remove C405, C403, VR400,C410,C414,R405
3	00D	04/18/2016	Modify GBT Design

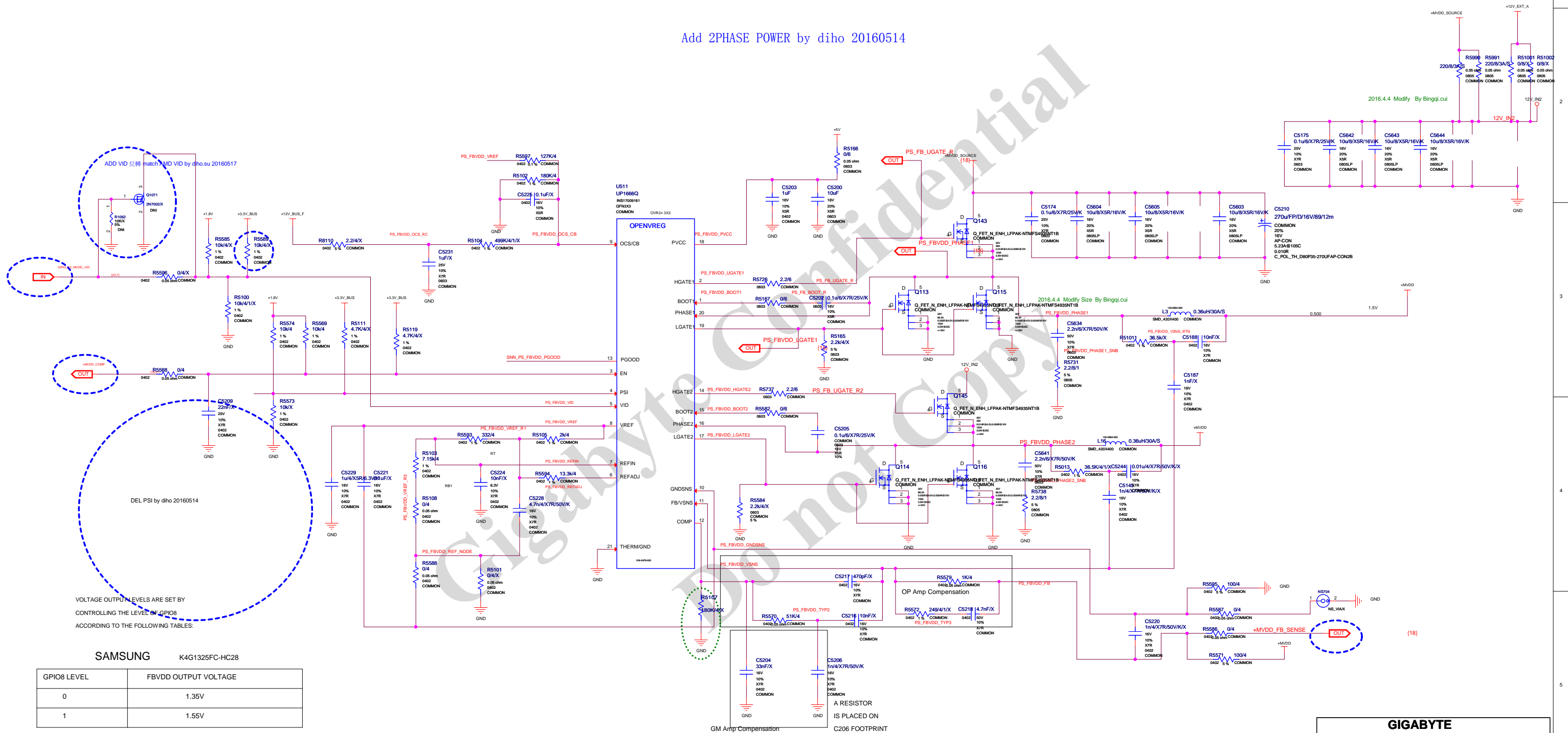
Gigabyte Confidential  
Do not Copy

GIGABYTE			
Title			
BLOCK DIAGRAM			
Size	Document Number		Rev
Custom	GV-RX480G1 GAMING-4GD		1.1
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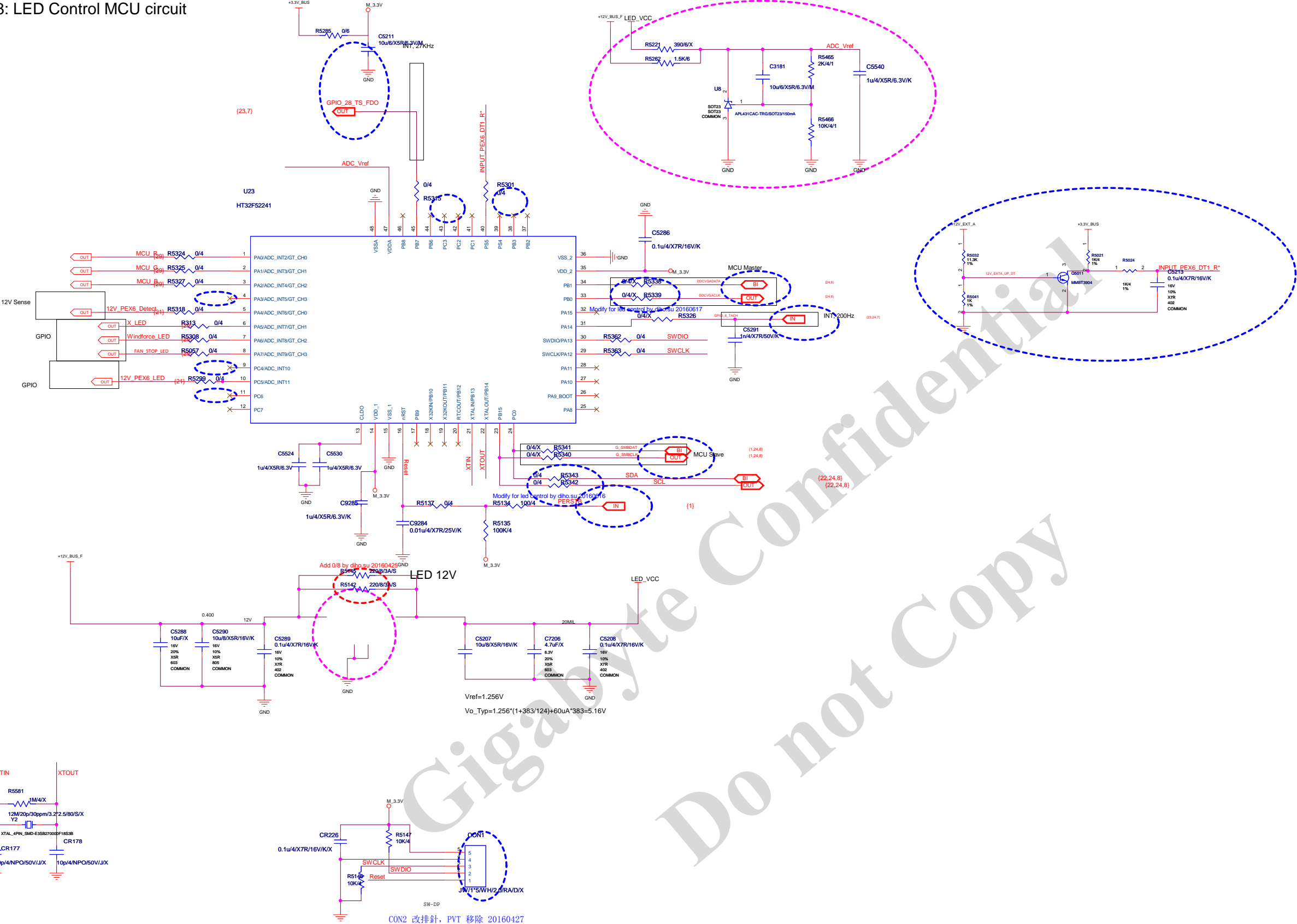


GPIO8 LEVEL	FBVDD OUTPUT VOLTAGE
0	1.35V
1	1.55V

ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: FBVDD



<b>GIGABYTE</b>			
Title FBVDD			
Size	Document Number		Rev
Custom	<b>GV-RX480G1 GAMING-4GD</b>		1.1
Date:	Tuesday, October 18, 2016	Sheet	27 of 29



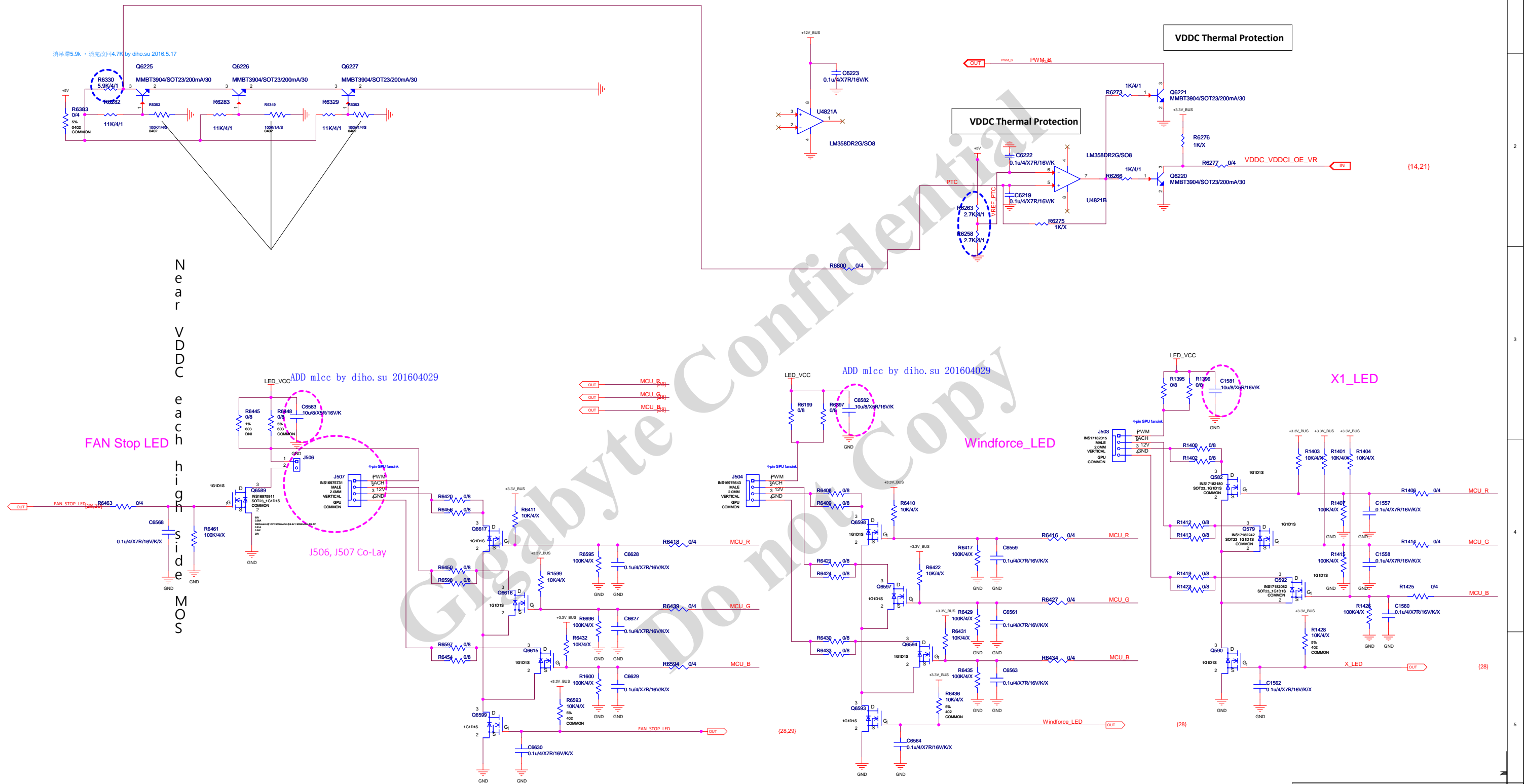
Vout = 8 ~ 7.5V

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ASSEMBLY PAGE DETAIL  
MECH: Bracket/Thermal

Title			Rev 1.1
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### Add MOS VRHOT CIRCUIT



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PAGE DETAIL

MECH: Bracket/Thermal

Title										
Size	Document Number									Rev
Custom	FAN GV-RX480G1 GAMING-4GD									1.1
Date:	Tuesday, October 18, 2016				Sheet	29	of	29		