

PG411 A01


GP104 - 8GB GDDR5, 256b, 256Mx32
Tall DVI-D + DP + DP + HDMI + DP

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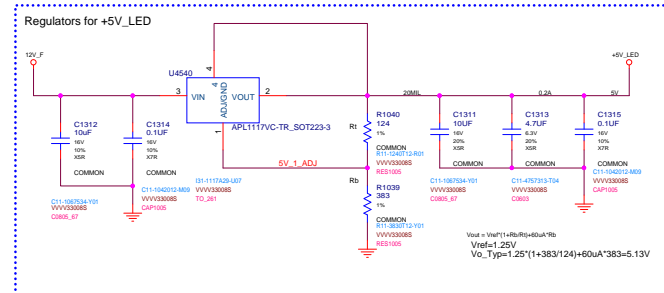
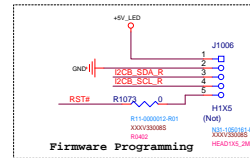
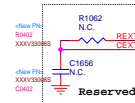
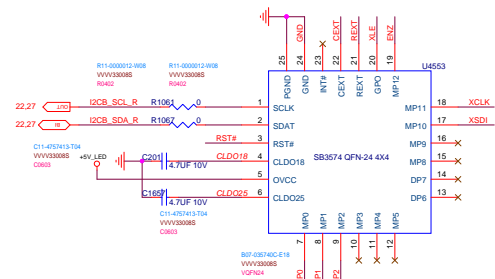
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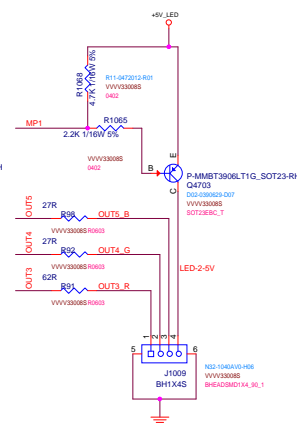
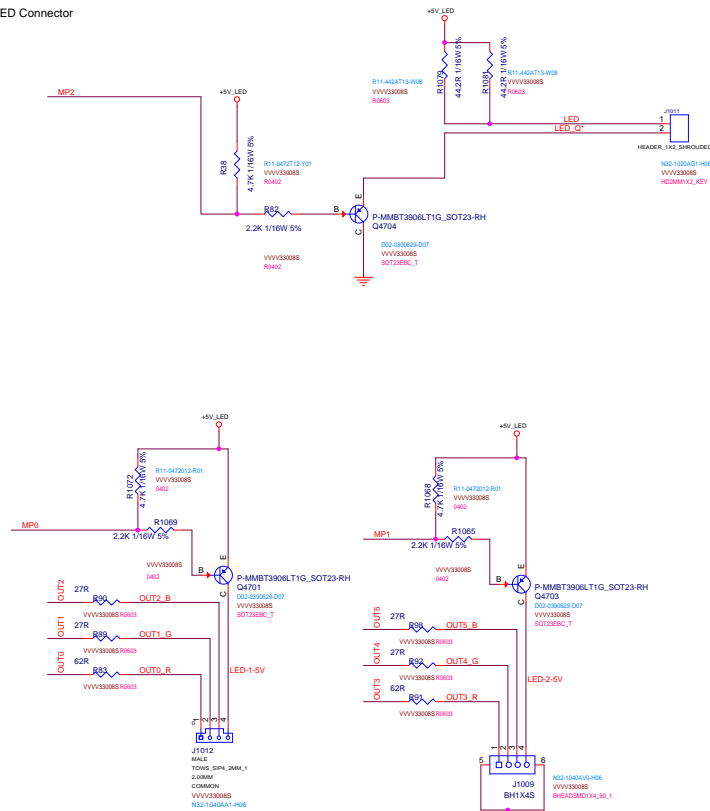
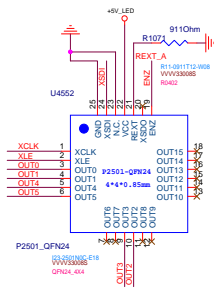
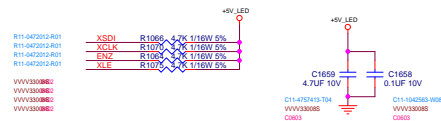
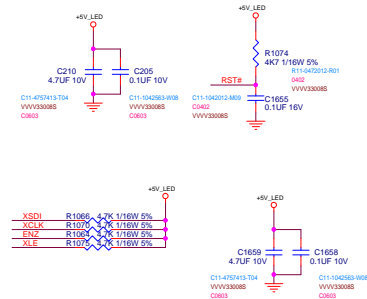
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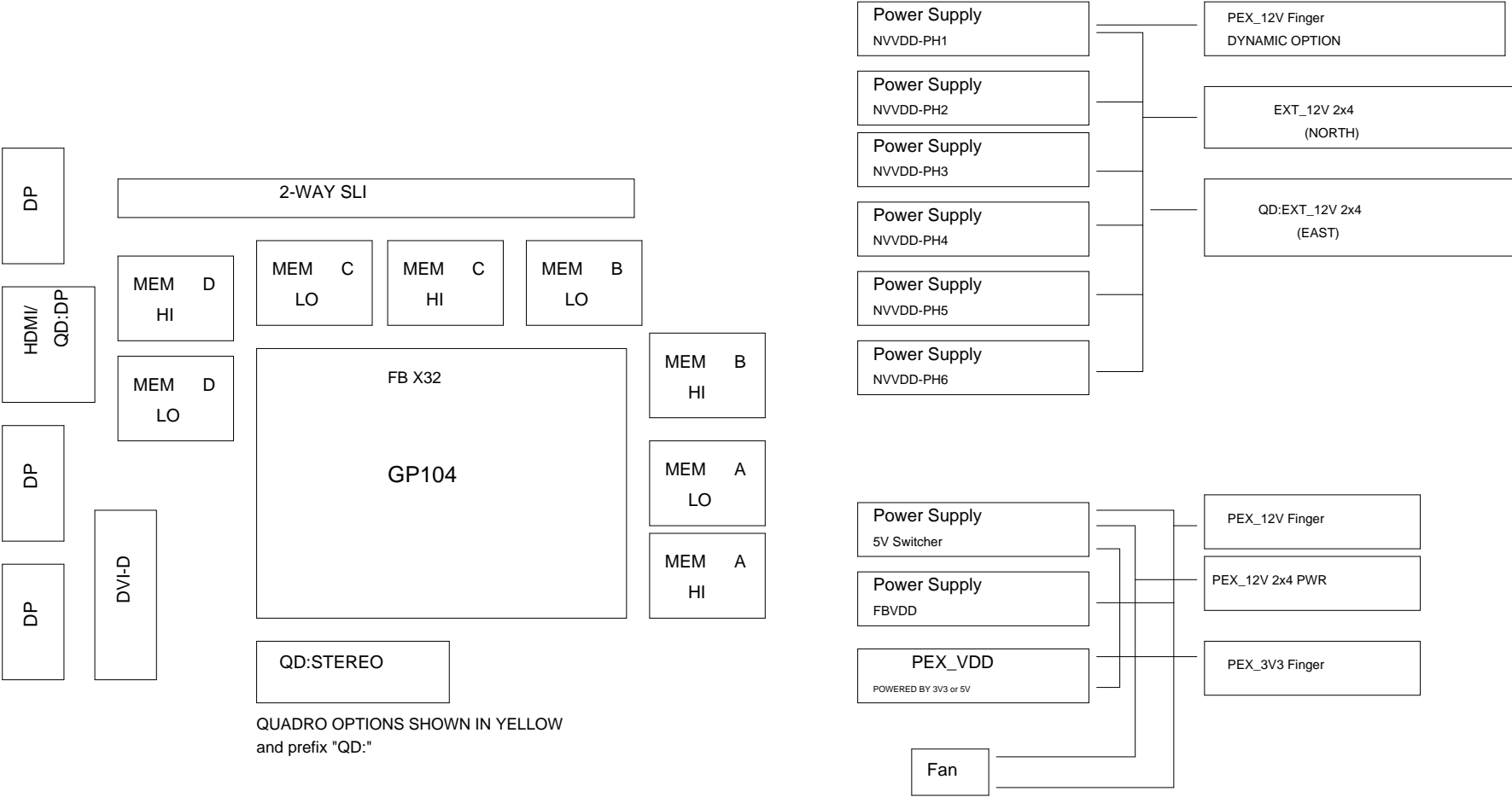
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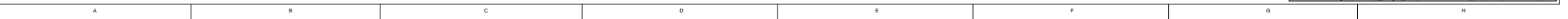
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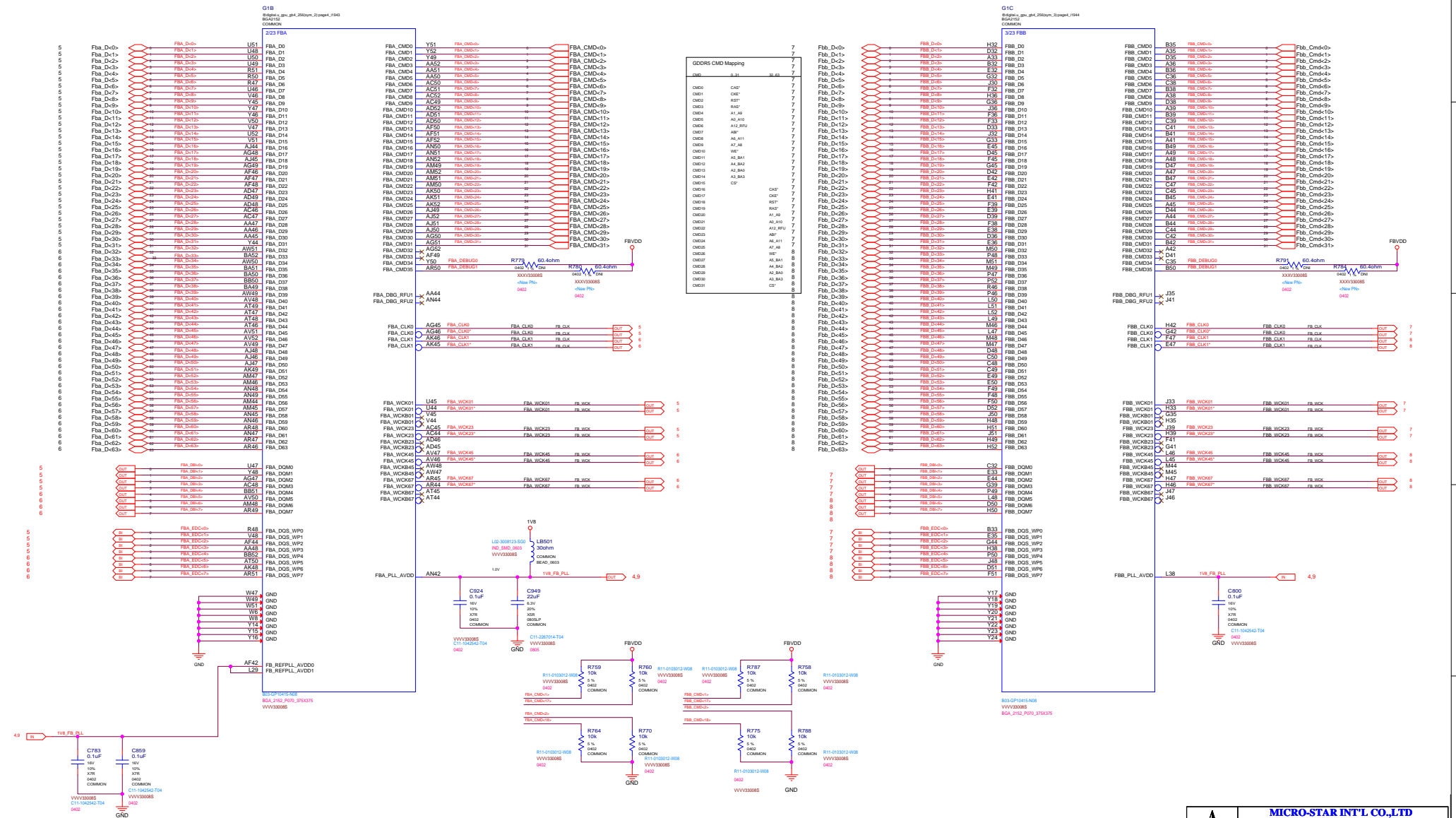


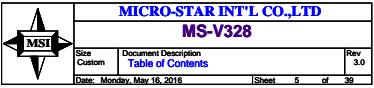


QUADRO OPTIONS SHOWN IN YELLOW
and prefix "QD:"

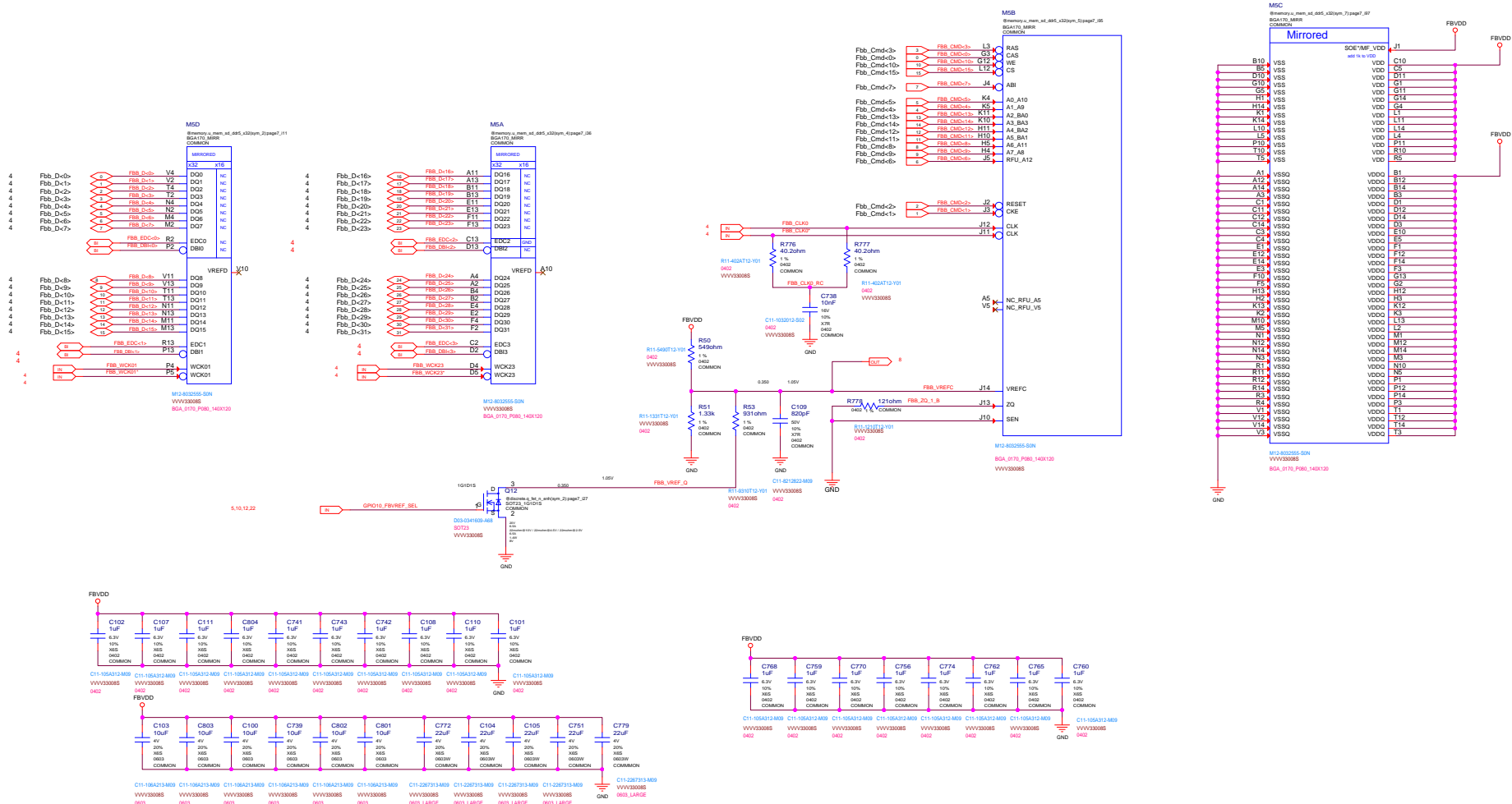
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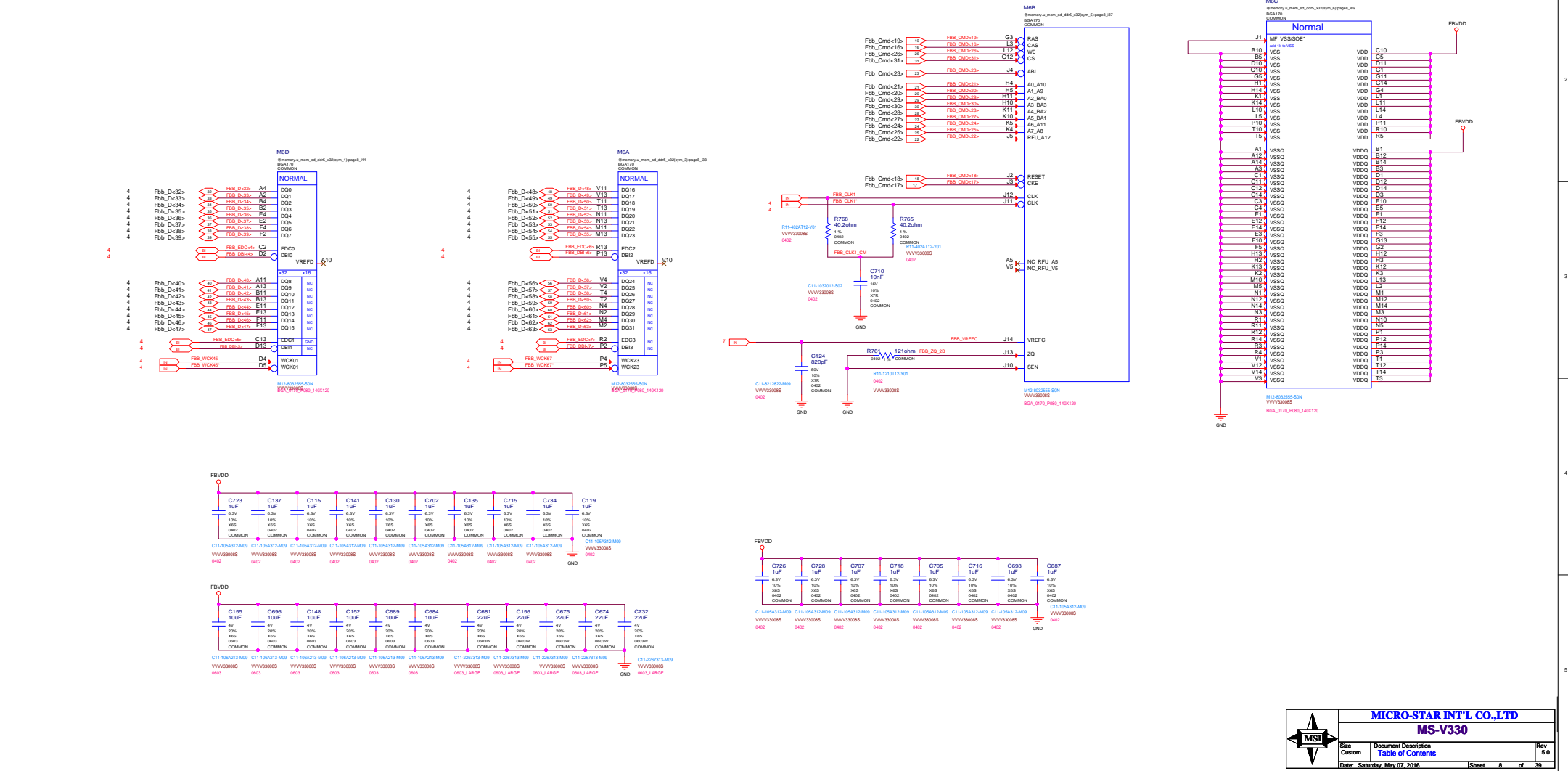


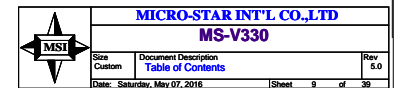
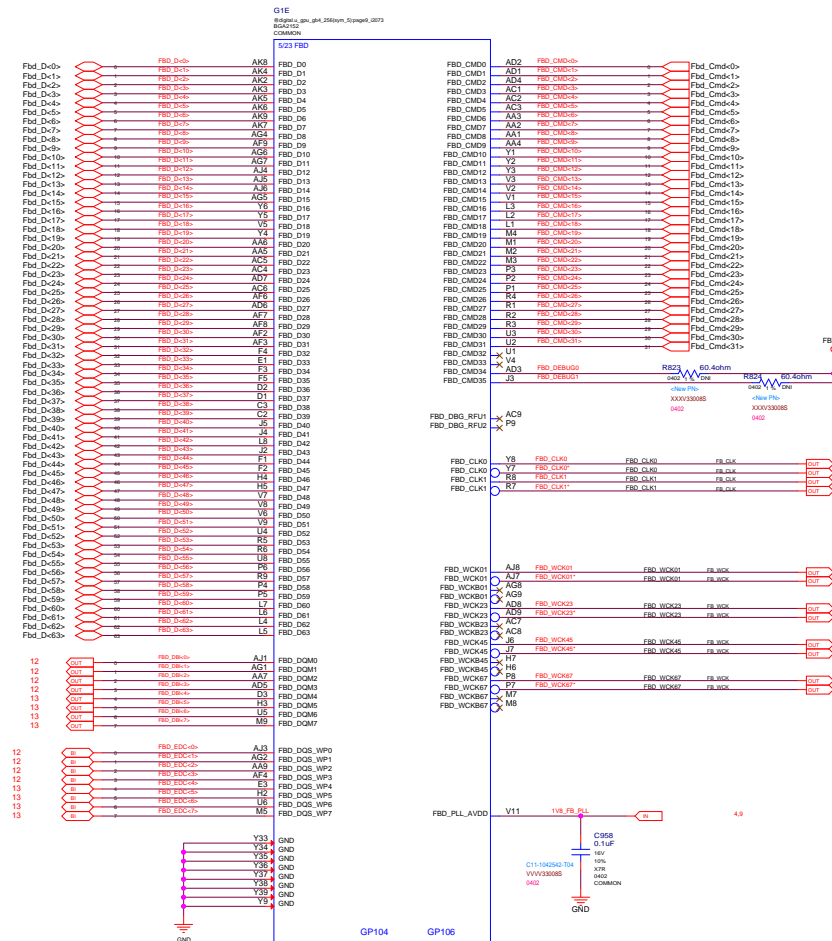
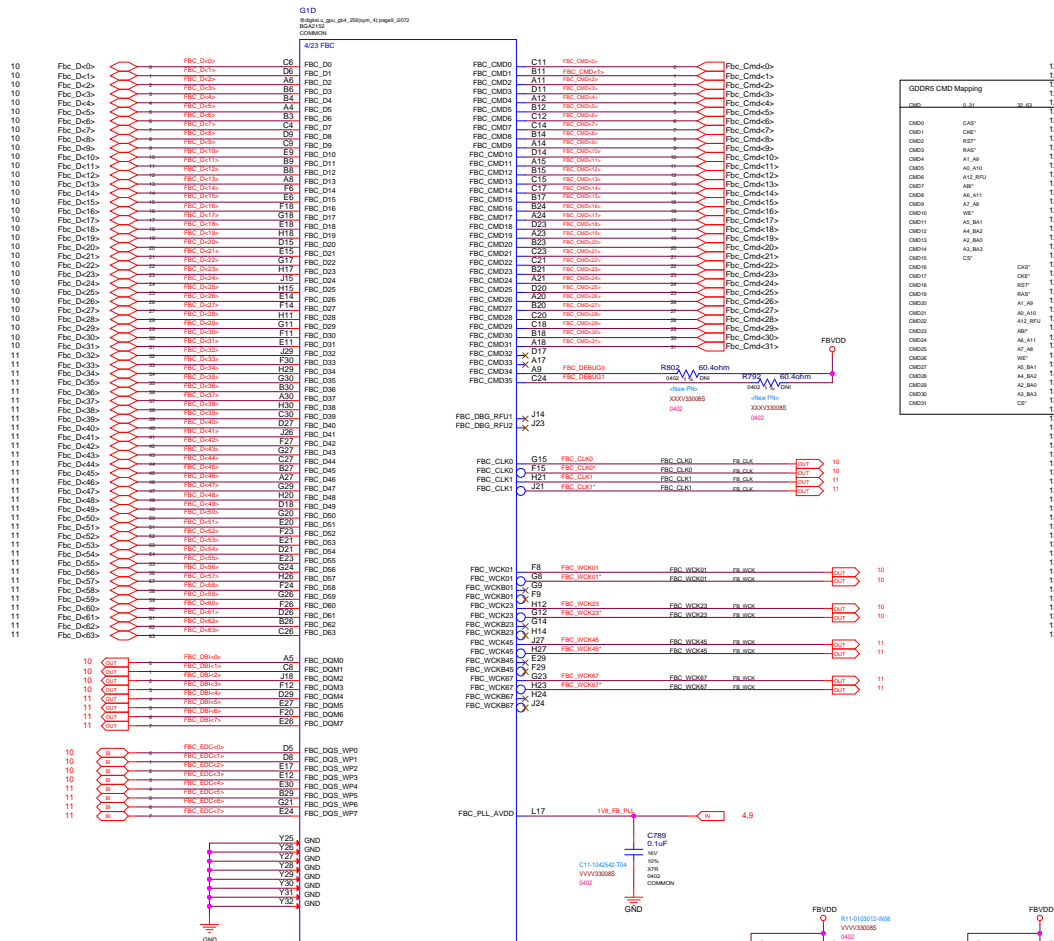


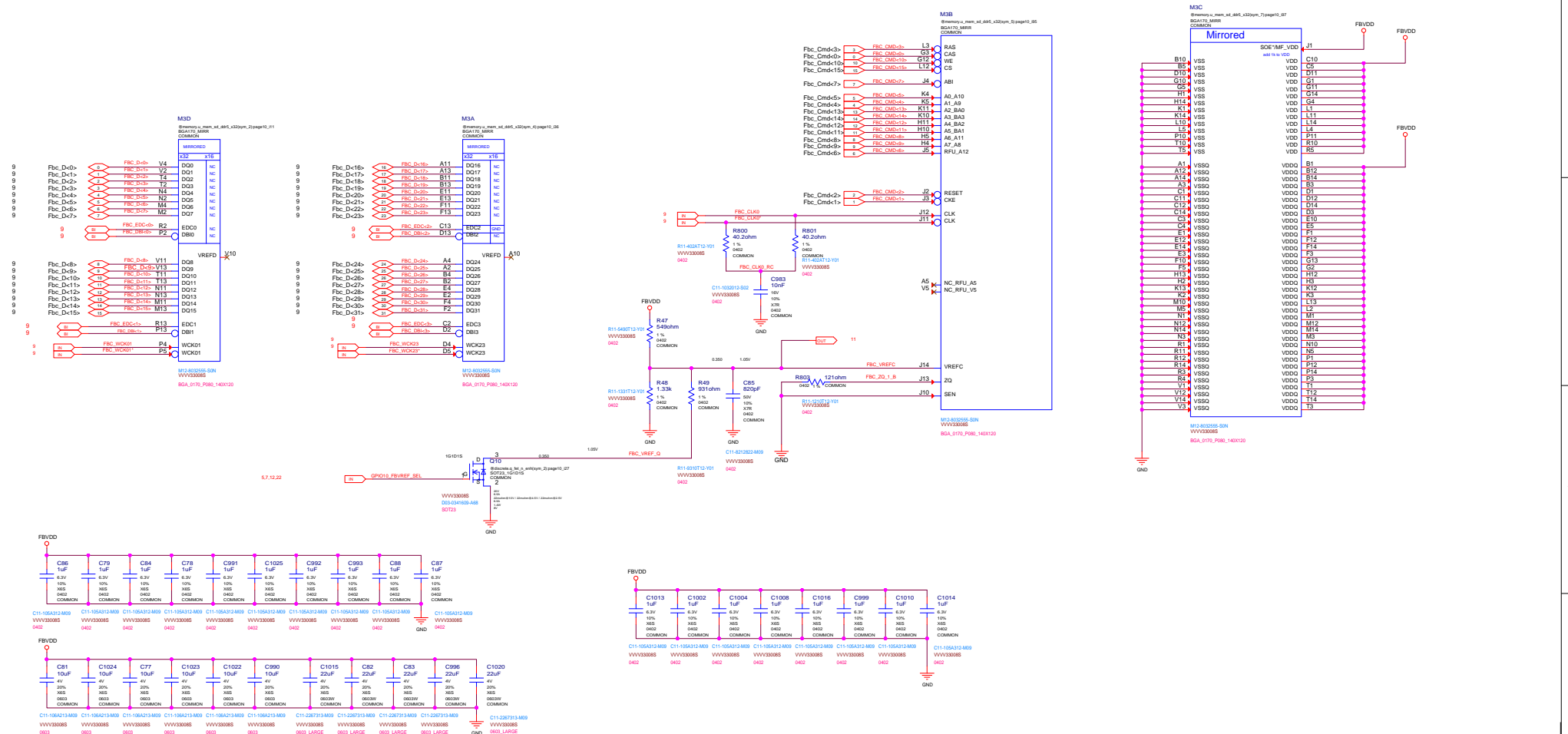


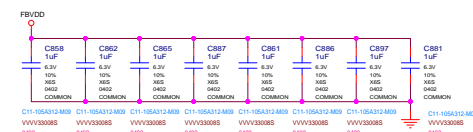
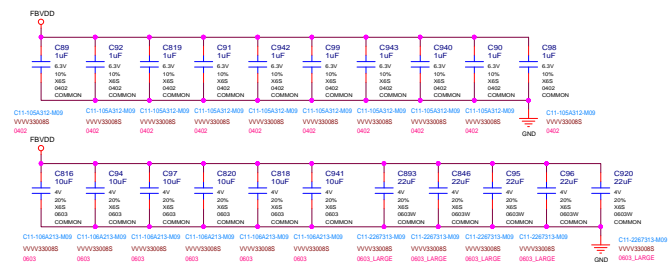
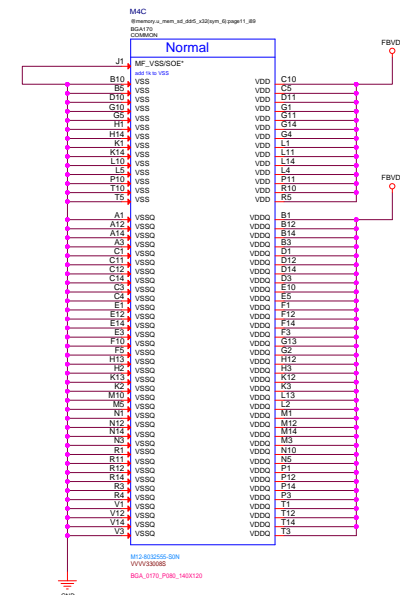
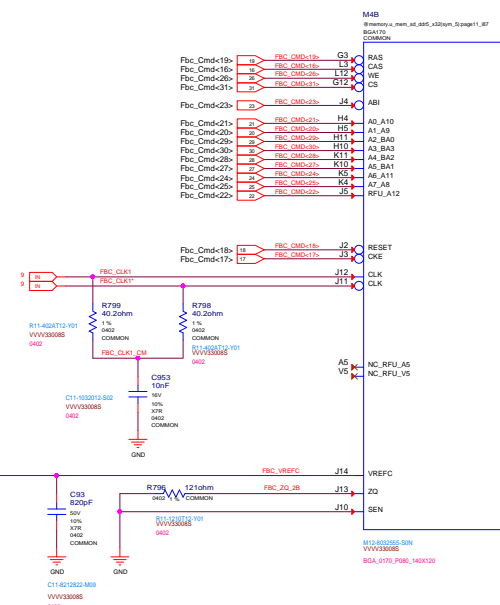
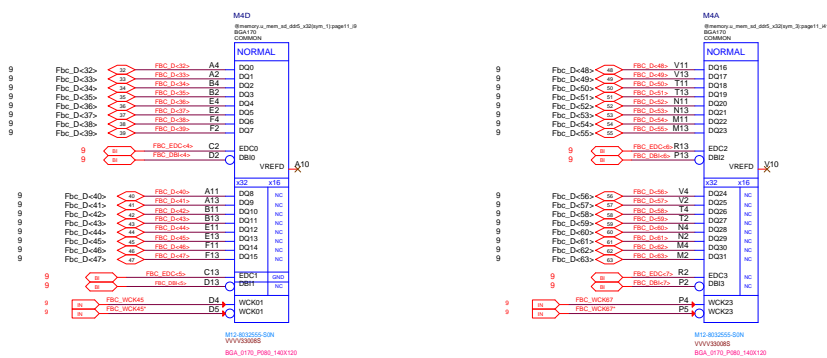


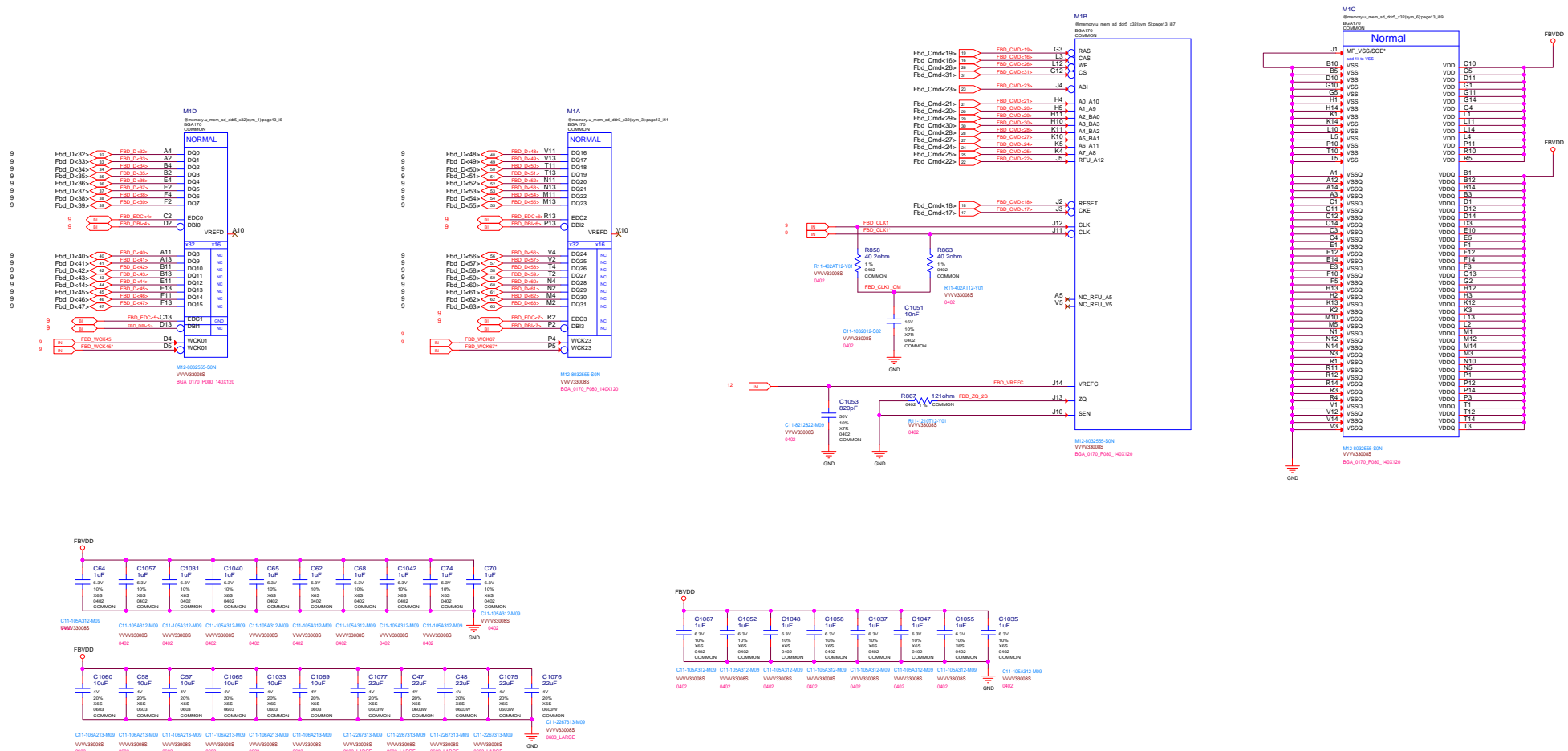


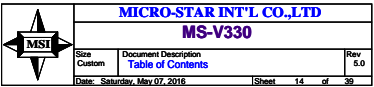


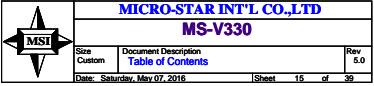




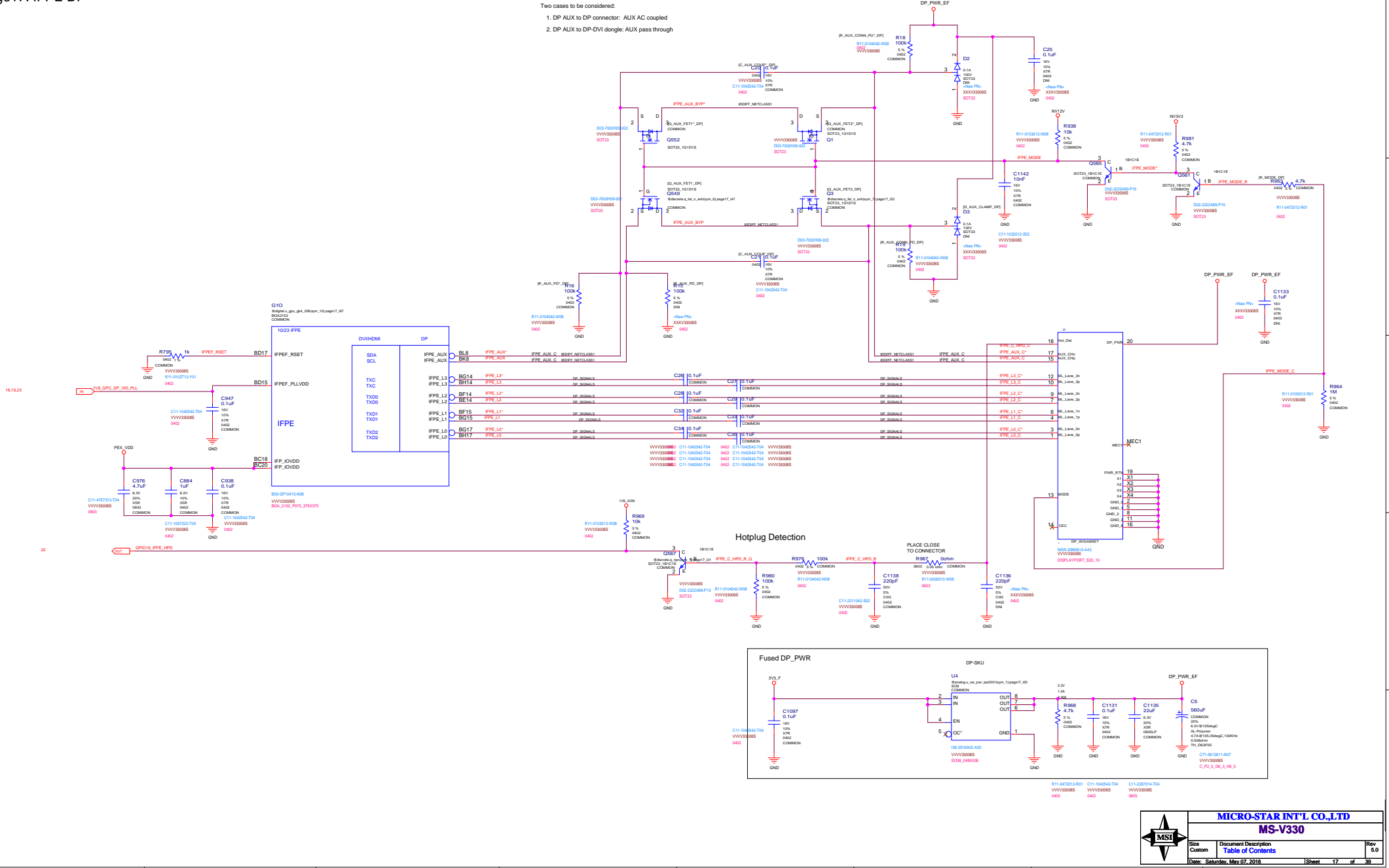


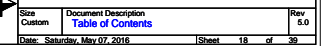


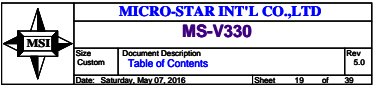
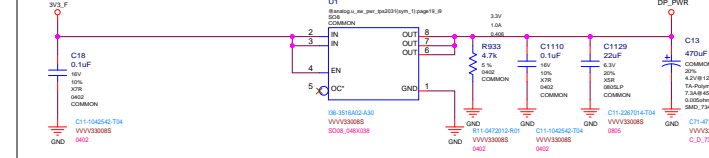


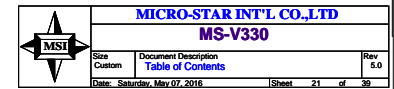


- Two cases to be considered:
1. DP AUX to DP connector: AUX AC coupled
 2. DP AUX to DP-DVI dongle: AUX pass through





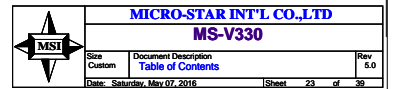


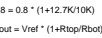



H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V

```
1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER

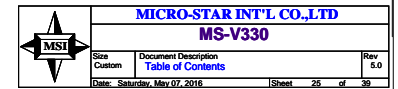
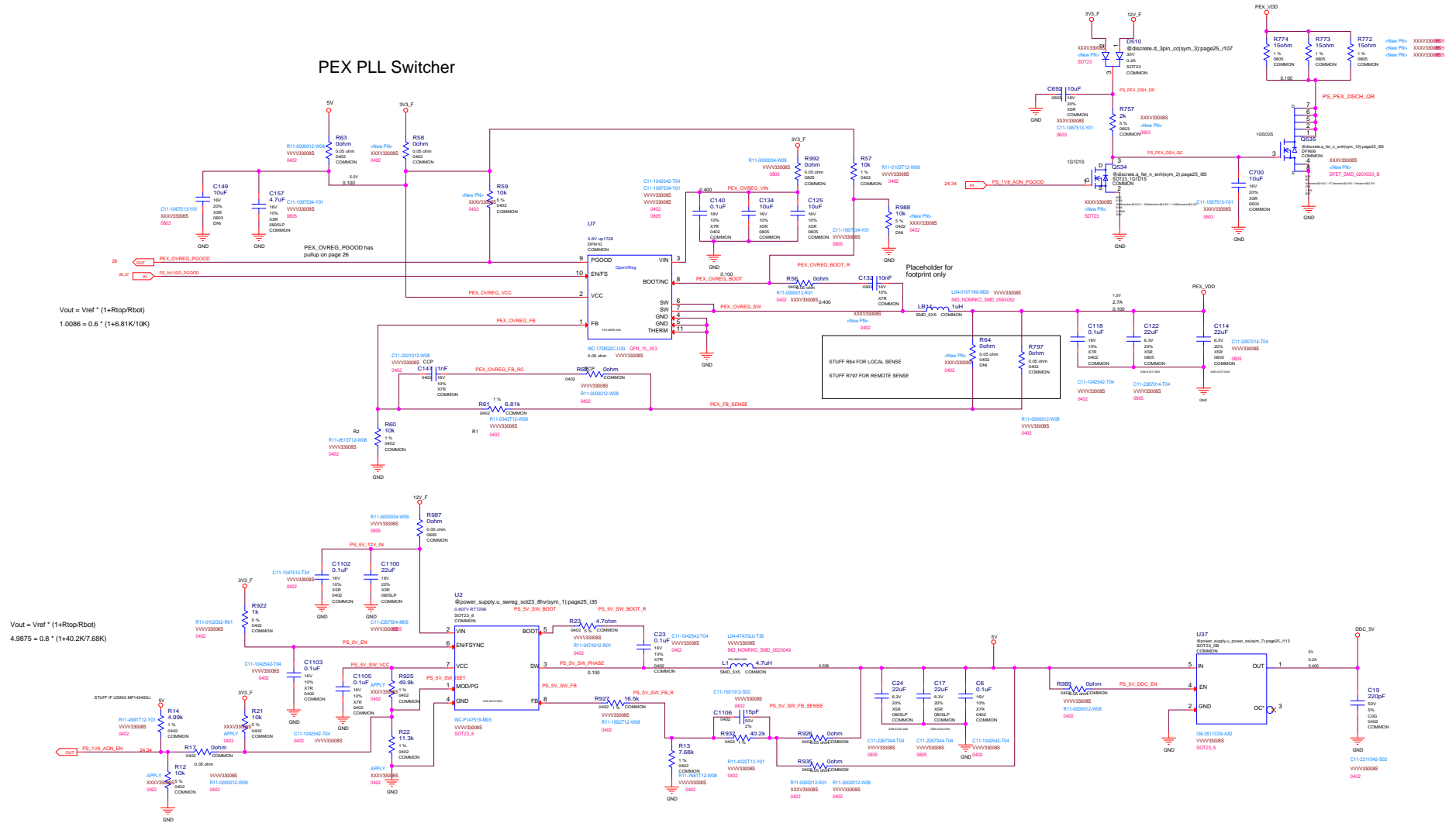
1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE
```

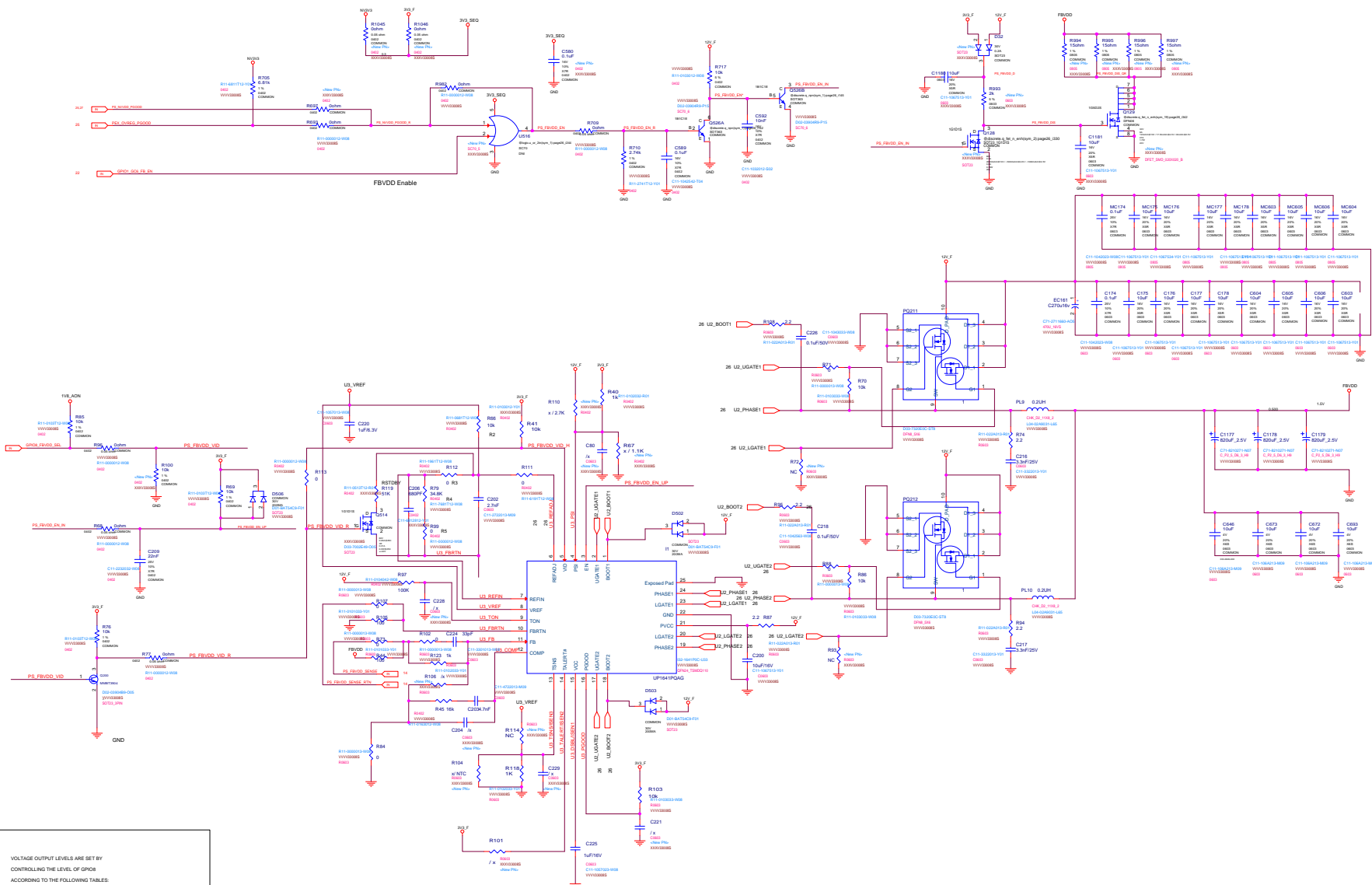




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PEX PLL Switcher



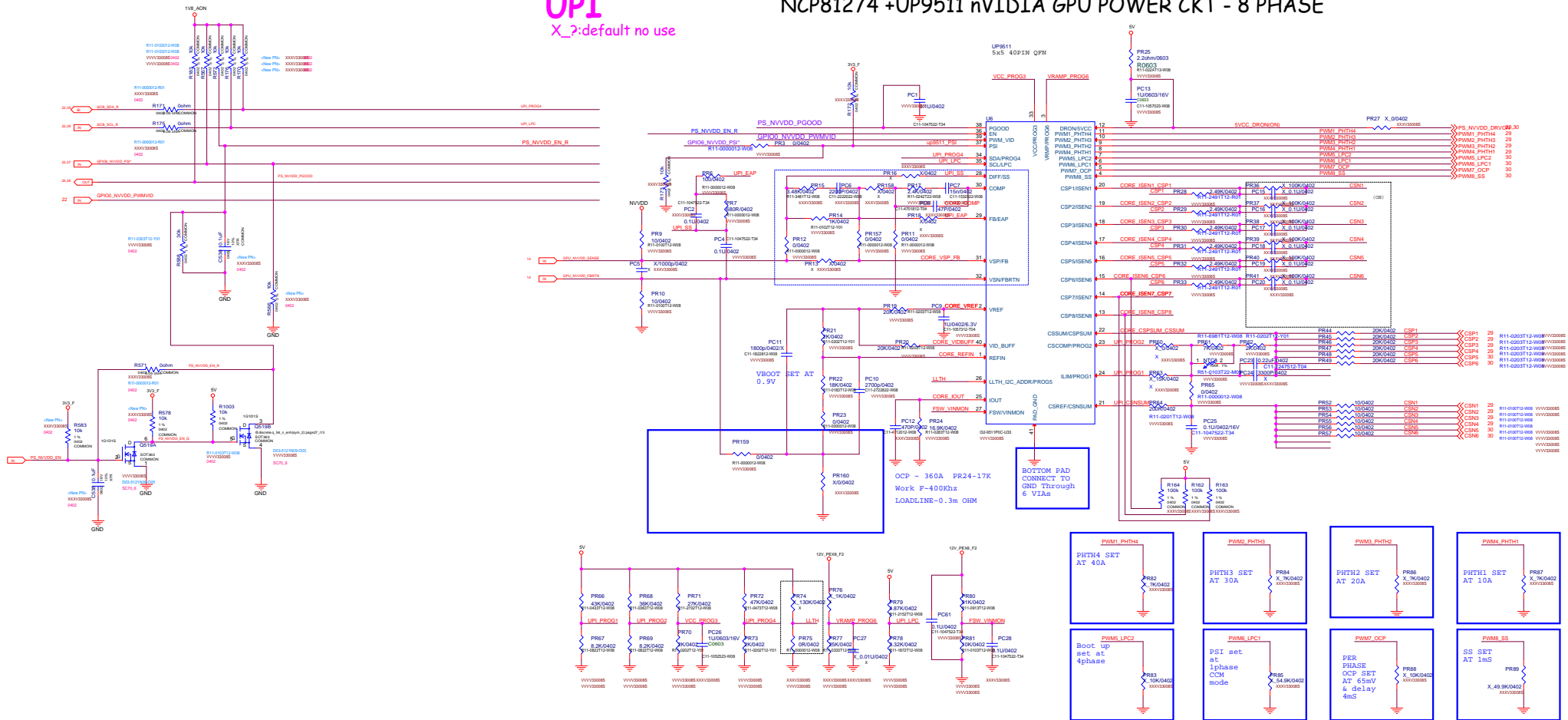


VOLTAGE OUTPUT LEVELS ARE SET BY
CONTROLLING THE LEVEL OF GPIO8
ACCORDING TO THE FOLLOWING TABLES:

SAMSUNG K4G1325FC-HC28

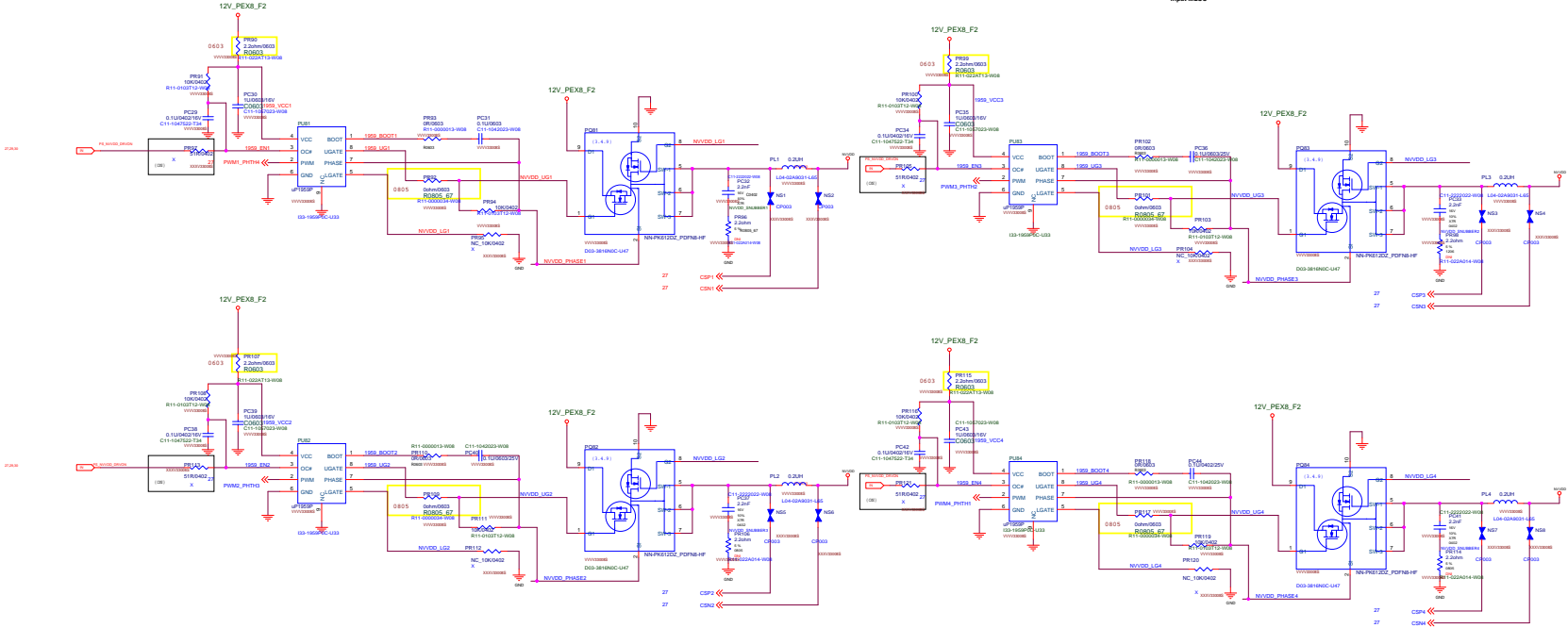
GPIO8 LEVEL	FBVDD OUTPUT VOLTAGE
0	1.35V
1	1.55V

NCP81274 +UP9511 nVIDIA GPU POWER CKT - 8 PHASE

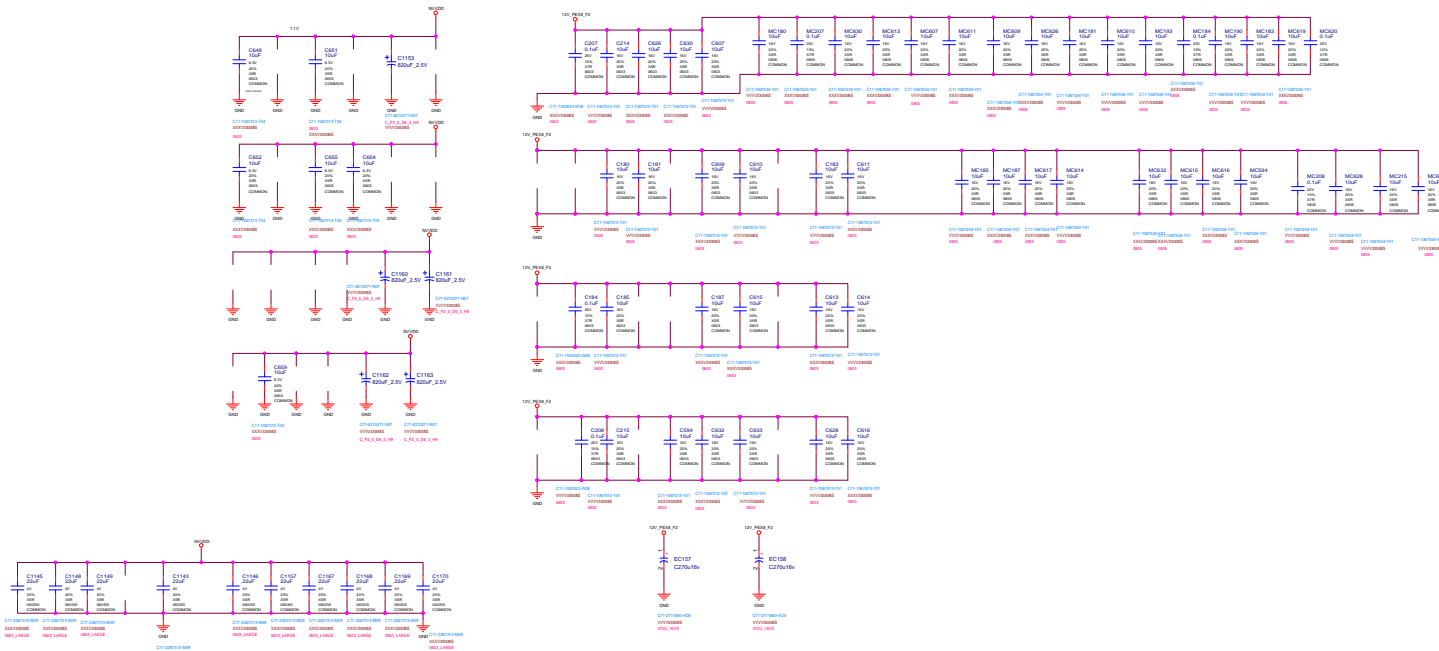


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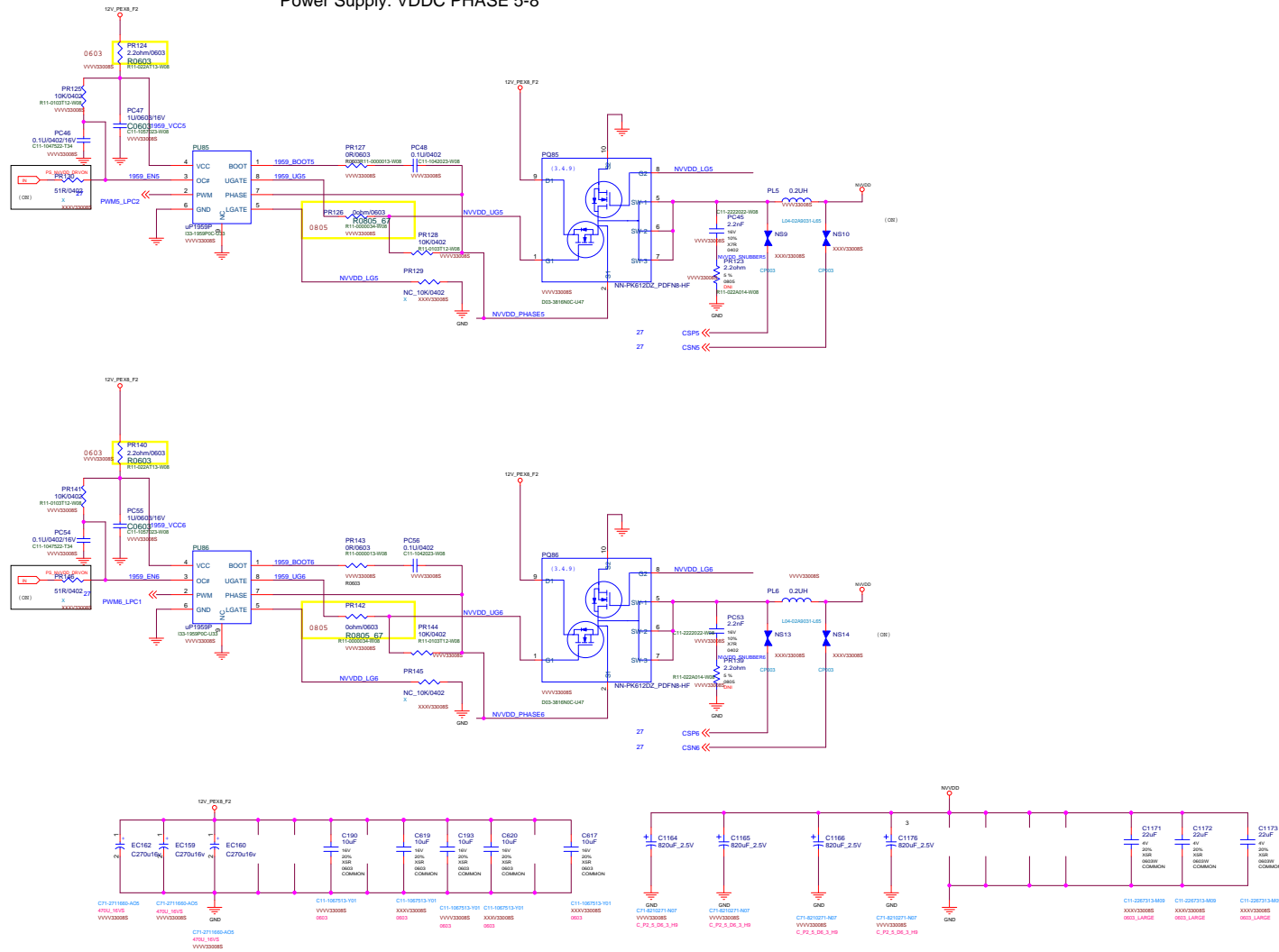
Power Supply: NVVDD PHASE 1~4




5V POWER SUPPLY FOR DRIVER VCC



Power Supply: VDDC PHASE 5-8







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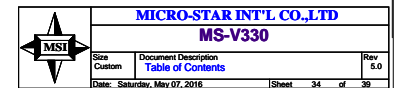
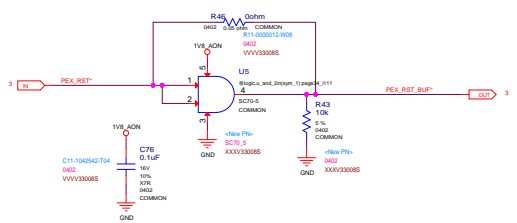
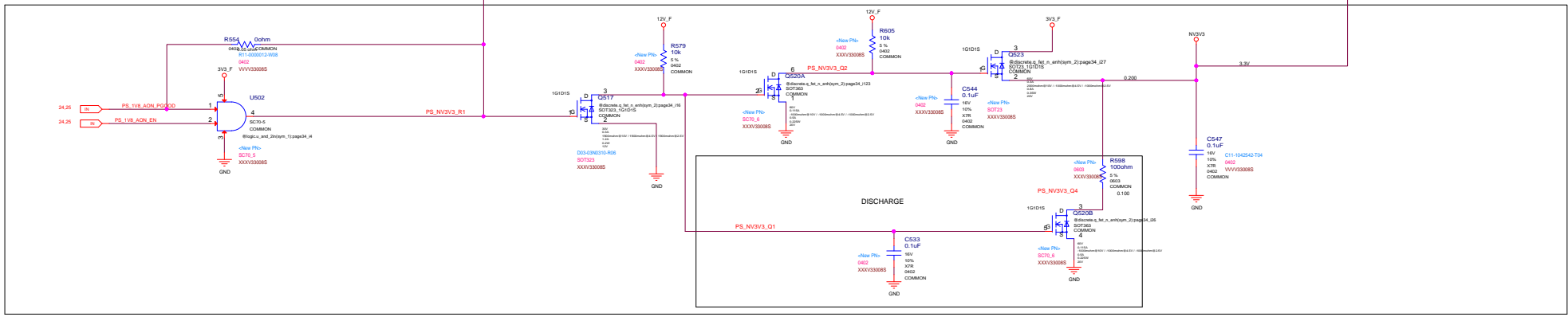
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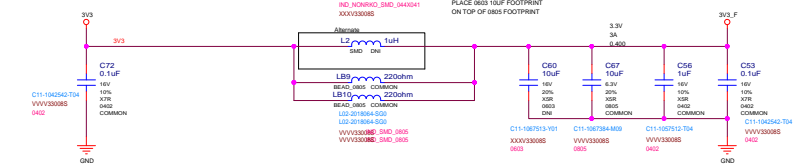
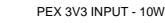
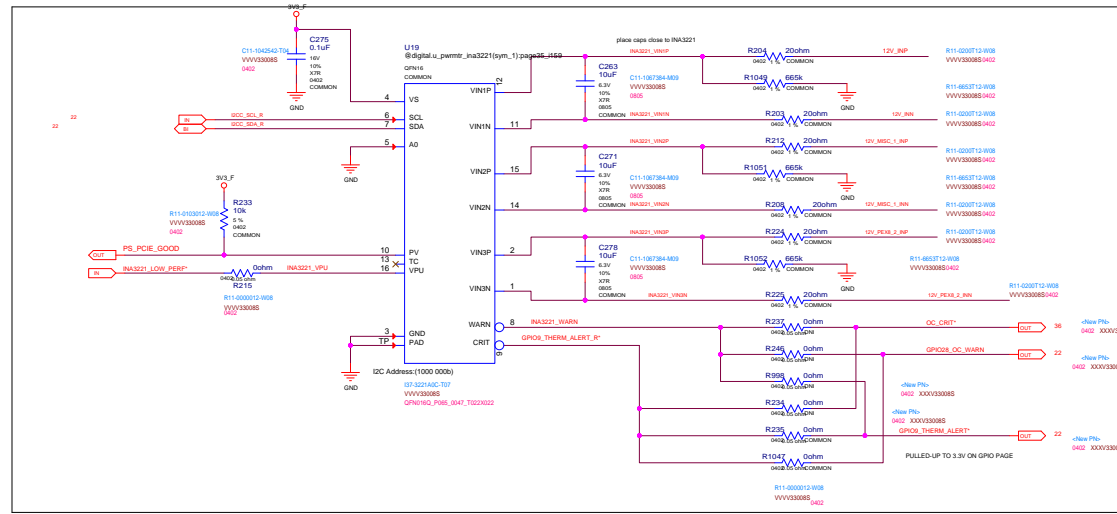


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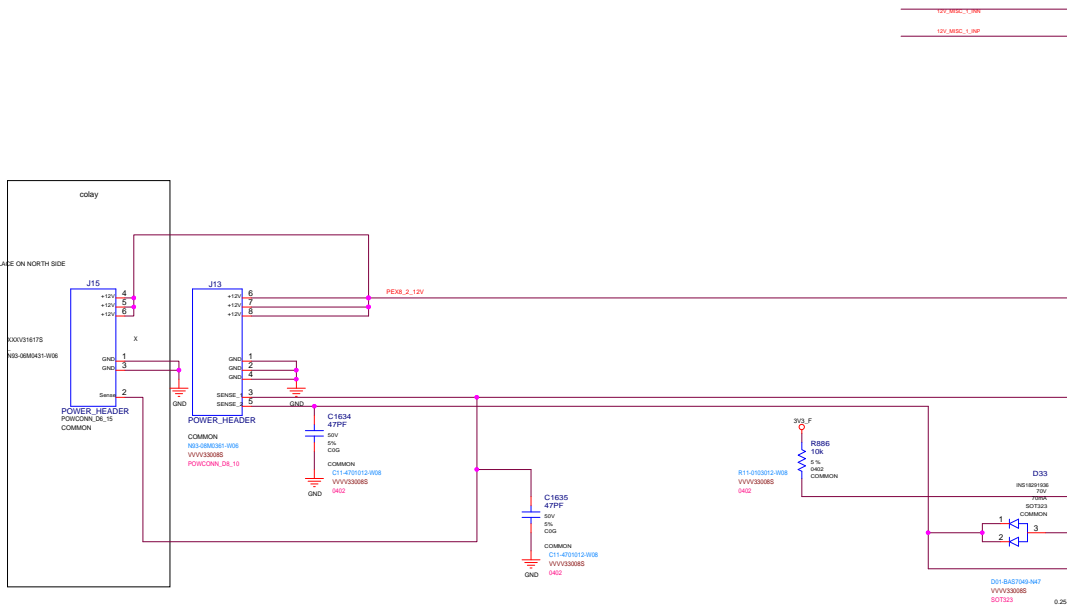
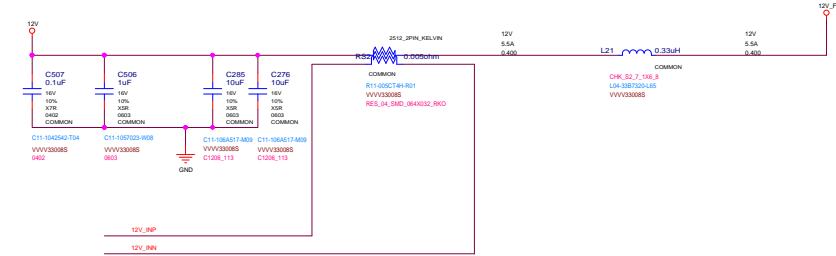


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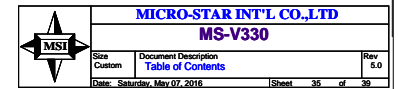
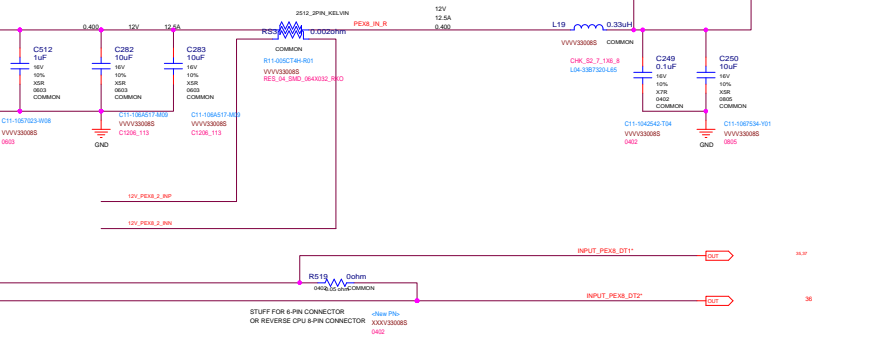


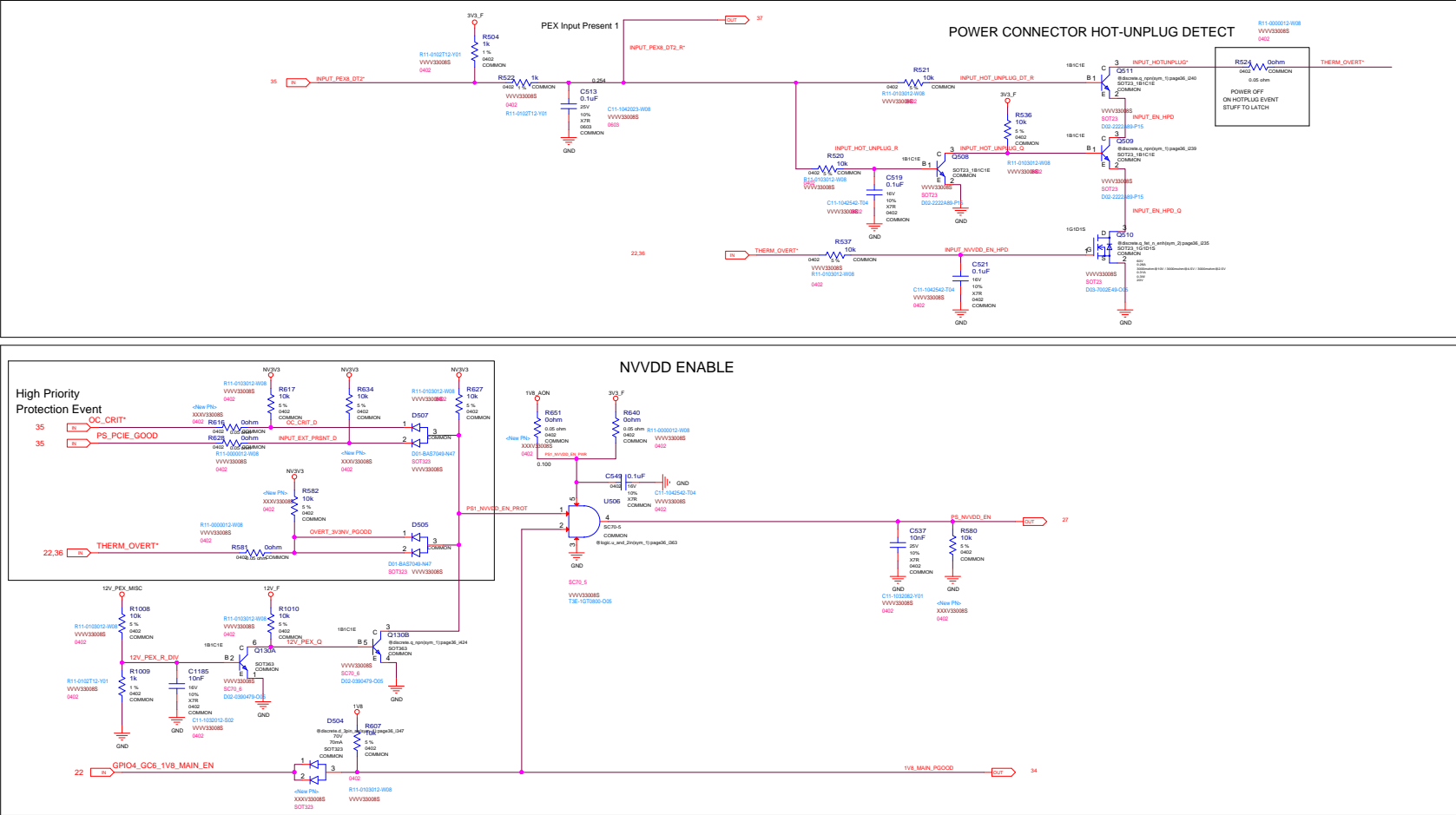


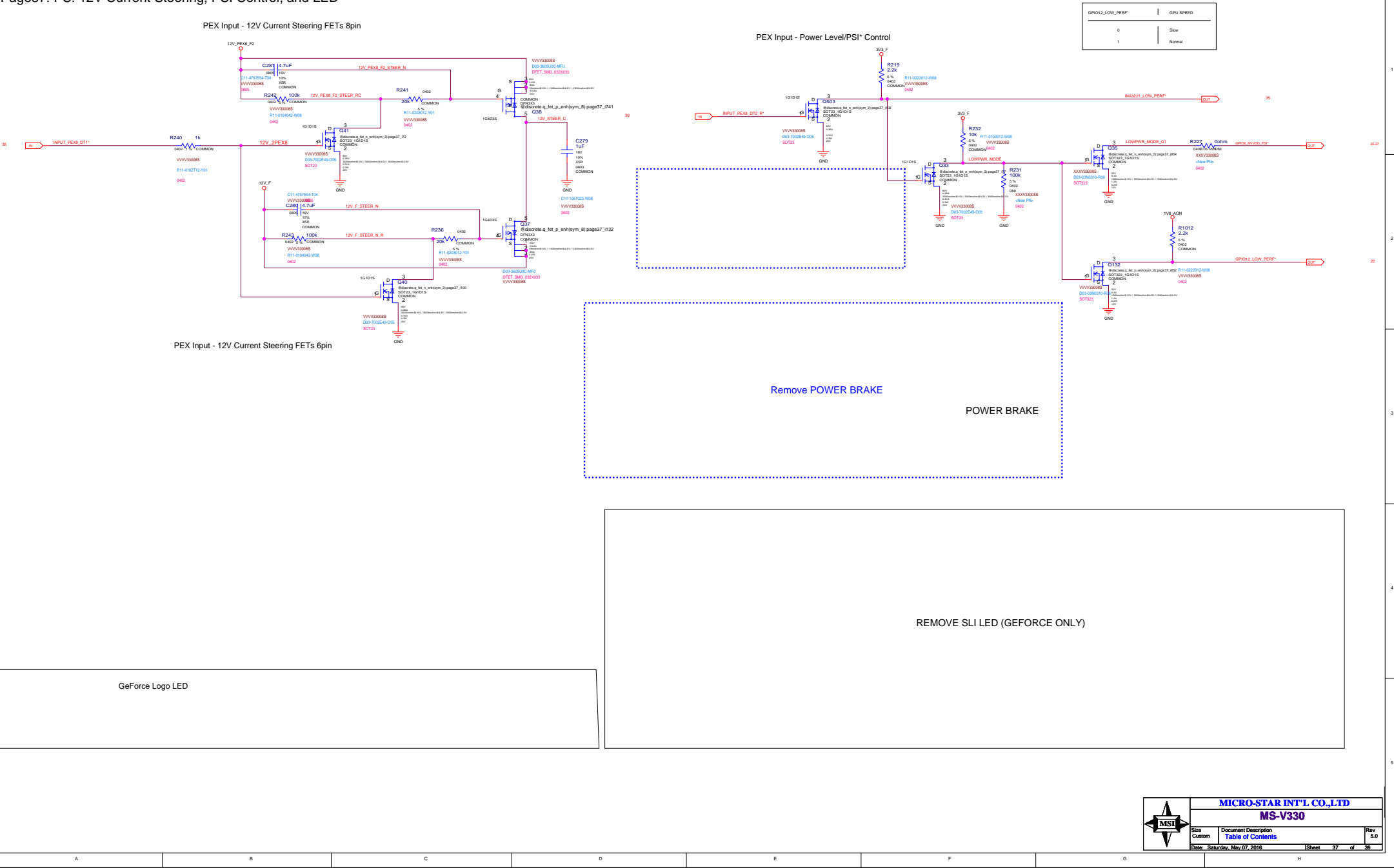
PEX_12V INPUT - 66W



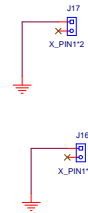
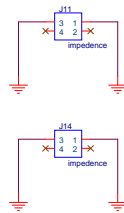
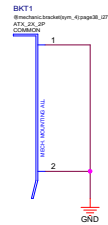
PEX8 INPUT 2 - 2x4 PCIE CON 150W







Brackets:



HDMI_FEE
HDMI
\$\$\$\$\$\$
HDMI_FEE
COMMON
WWW33098
Y01-RHCM903-000

