NV20, 4MX16 DDR, RGB, EXTERNAL DVI-I, TV-DOWN, TV IF , AGP4X

PCI DEVICE ID 0X0=0X200 FOR NV20.

NVDD SET TO 1.60 FOR -VP CHIP

FBVDD SET TO: 3.47V

FBVDDQ SET TO: 2.59V

HISTORY REVISION:

X00: Based on P50-A06
- See change list in 149- file.
- Set FBVDDQ=2.59V

P50-A07-X01:

- Changed all memory clk/clk# diff pair resistors to 68R 5% (from 47R)

P50-A08:

X04: - Delay PLL_VDD to come up after NVVDD.

X05: - Added 1UF accross R257.

X06: - Removed X04-5 above, added a switcher generated PLL delay option.
- SSENA cap for 2nd SW changed to 1UF.
- A05 Si, NVVDD=1.52V

P50-A09:

X02: - Changed PLL VDD and DAC VDD to be gated by Fet controlled by FBVDD power good signal.

X03: - Added option to pull up power good to 12V

ECO1235: - Changed R841 PU to 10K (from 4.7K)

ECO1373: - Moved 75 ohm termination closer to filter for noise level reduction. Replaced C303,306,309 with 75ohm and no stuffed R208-210.

1. GOLD FINGER
2. HOST
3. DAC/TV/TMDS
4. DECOUPING
5. STRAPS
6. CRT
7. FBA/FBB
8. DECOUPING
9. 2*32 DDR
10. 2*32 DDR
11. 1M FLASH ROM 3.3V
12. TV PH/7108
13. POWER
14. HARDWARE MONITOR

{Item}\t{Quantity}\t{Reference}\t{Source Package} {USR DFINE} {Value}\t{Assembly} {Assembly}{Source Package} {Value}{PN}{AVL1}{AVL2}{AVL3}{AVL4}{AVL5}

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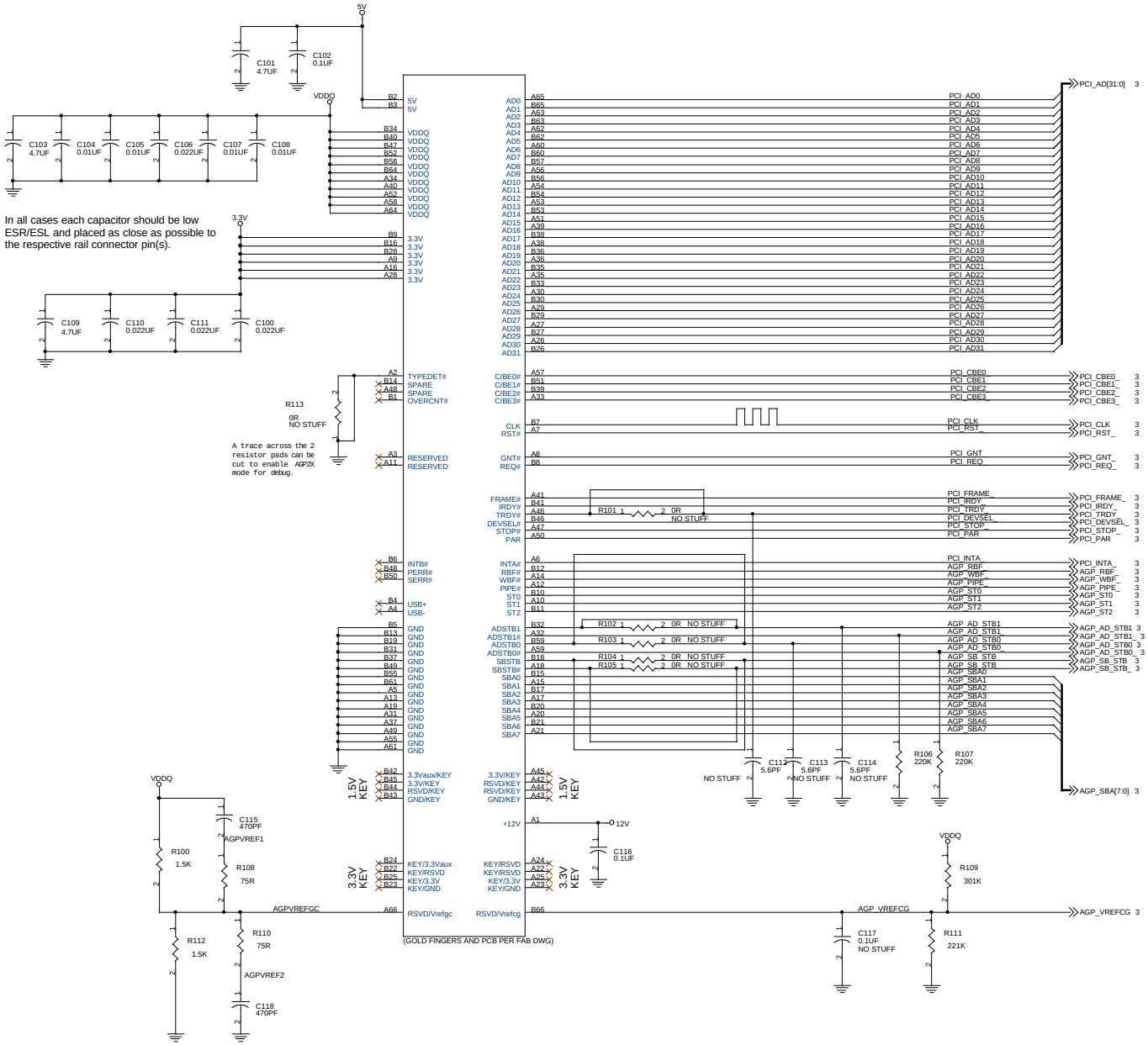
MS-8850 based on P50A09

Size	Document Number	Rev
C	TOP PAGE	1.1A

Date: Monday, November 05, 2001 Sheet 1 of 15



MSI	Micro-Star International Co., LTD.	
	MS-8850 based on P50A09	
Size: C	Document Number: BUS I/O	Rev: 1.1A
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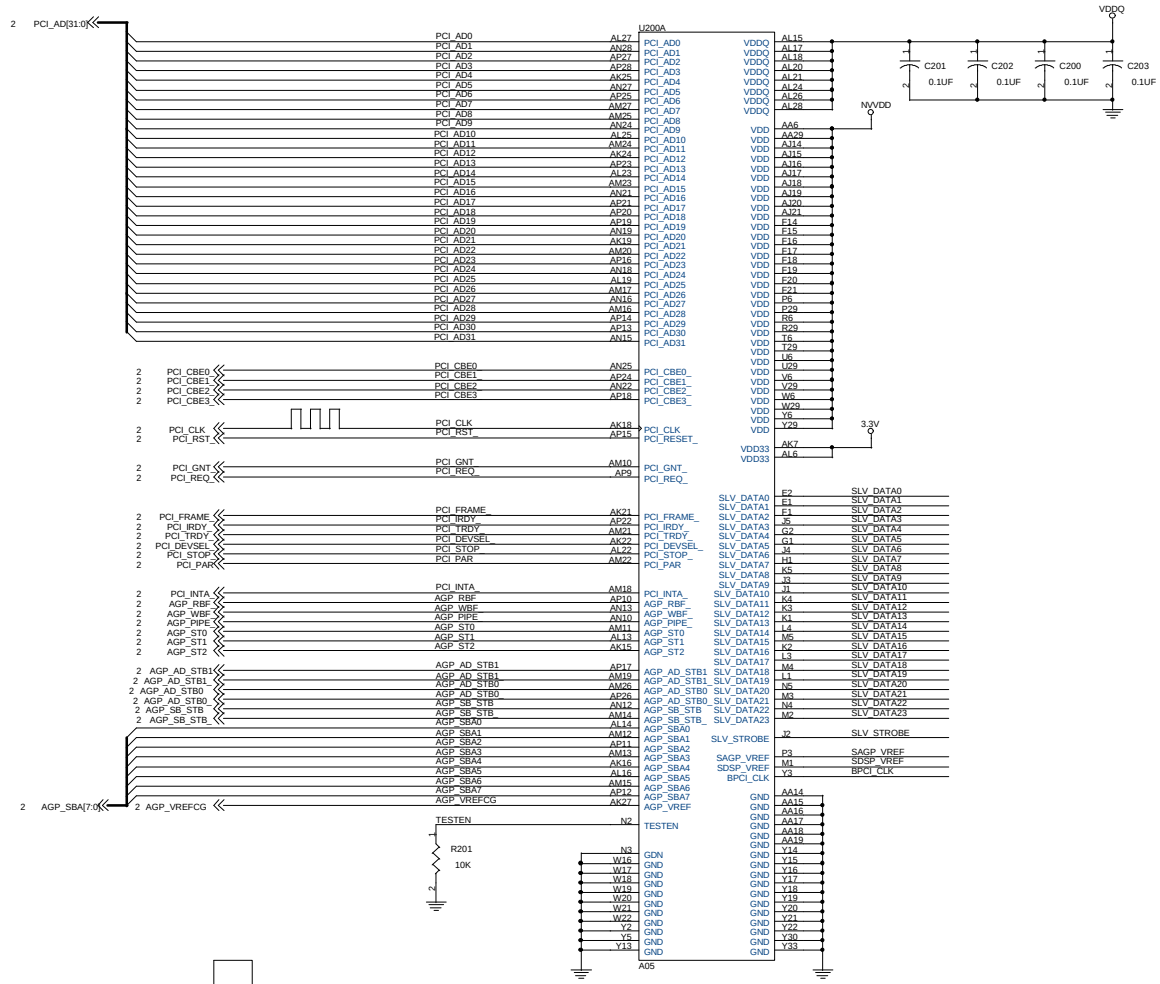

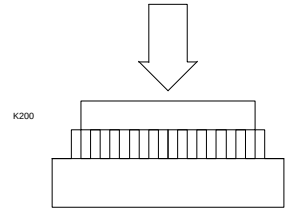

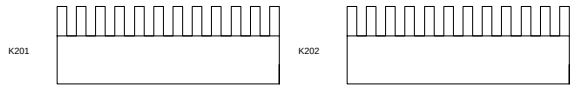

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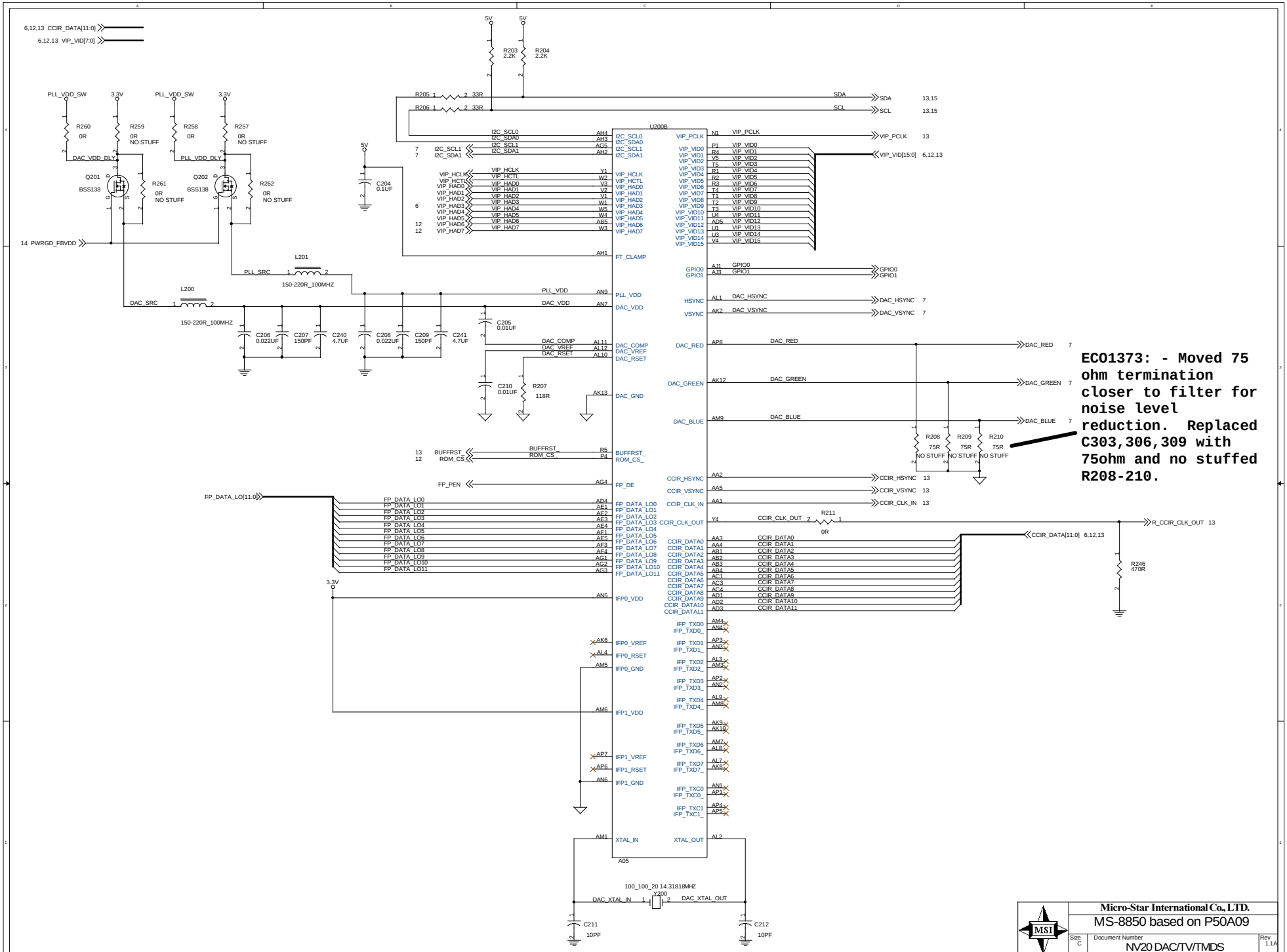

ECO1373: - Moved 75 ohm termination closer to filter for noise level reduction. Replaced C303,306,309 with 75ohm and no stuffed R208-210.

Micro-Star International Co., LTD.	
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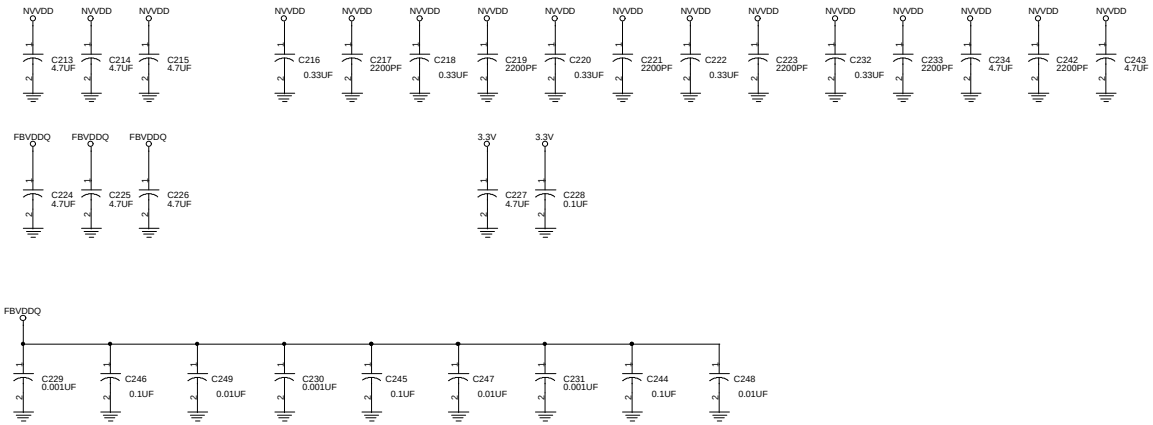
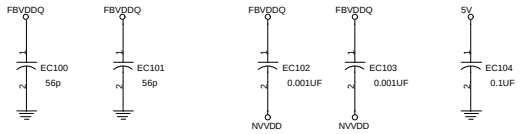
Use thick (non-impedance controlled) traces on XTALIN/OUT



For EMI Solution



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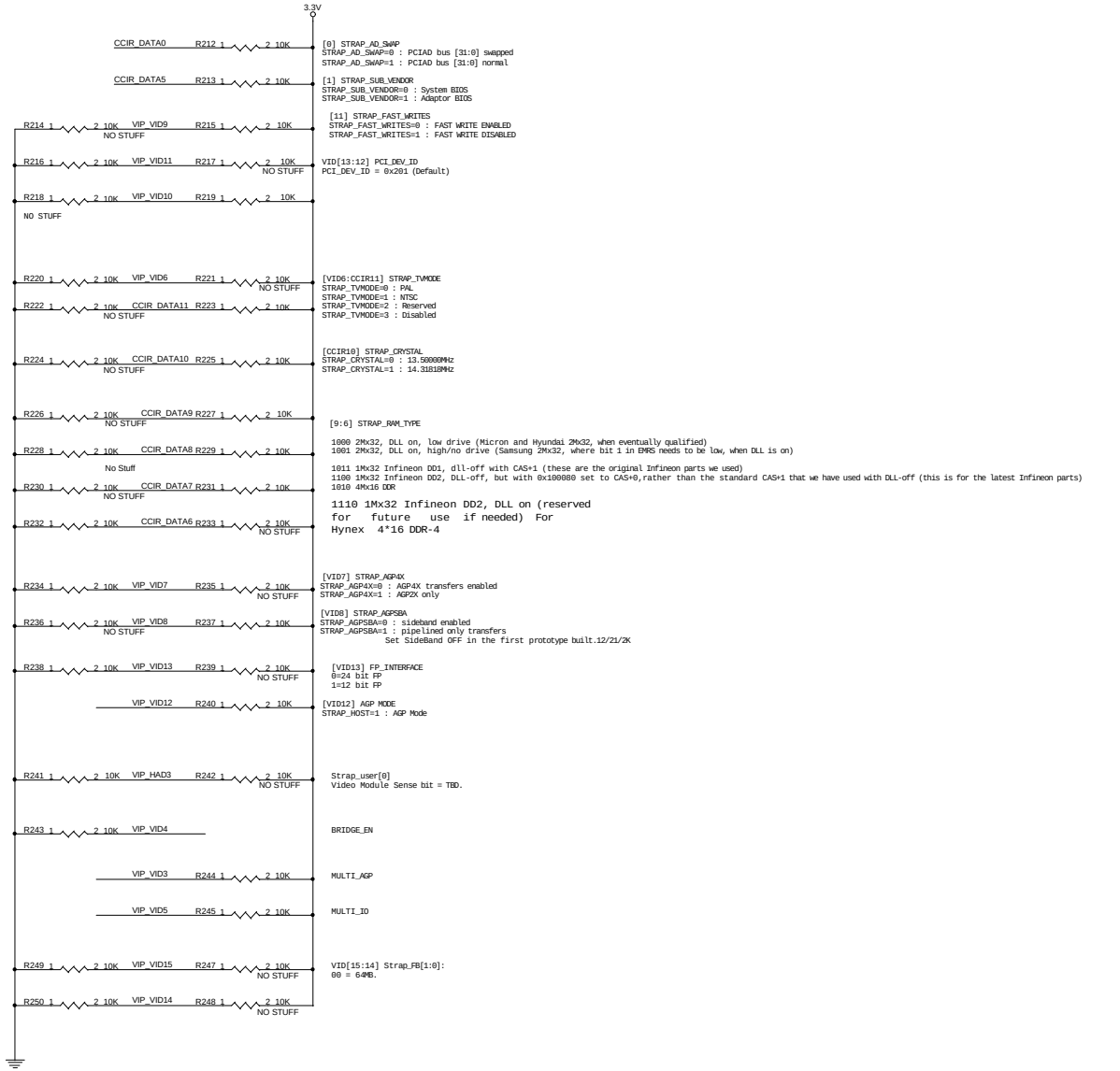
4,12,13 CCIR_DATA[11:0]
4,12,13 VIP_VID[15:0]
4 FP_DATA_LO[11:0]

4 VIP_HAD0
4 VIP_HAD1
4 VIP_HAD2
4 VIP_HAD3
4 VIP_HAD4
4 VIP_HAD5
4,12 VIP_HAD6
4,12 VIP_HAD7



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Size: C	Document Number: NV20 HOST STRAPS	Rev: 1.1A
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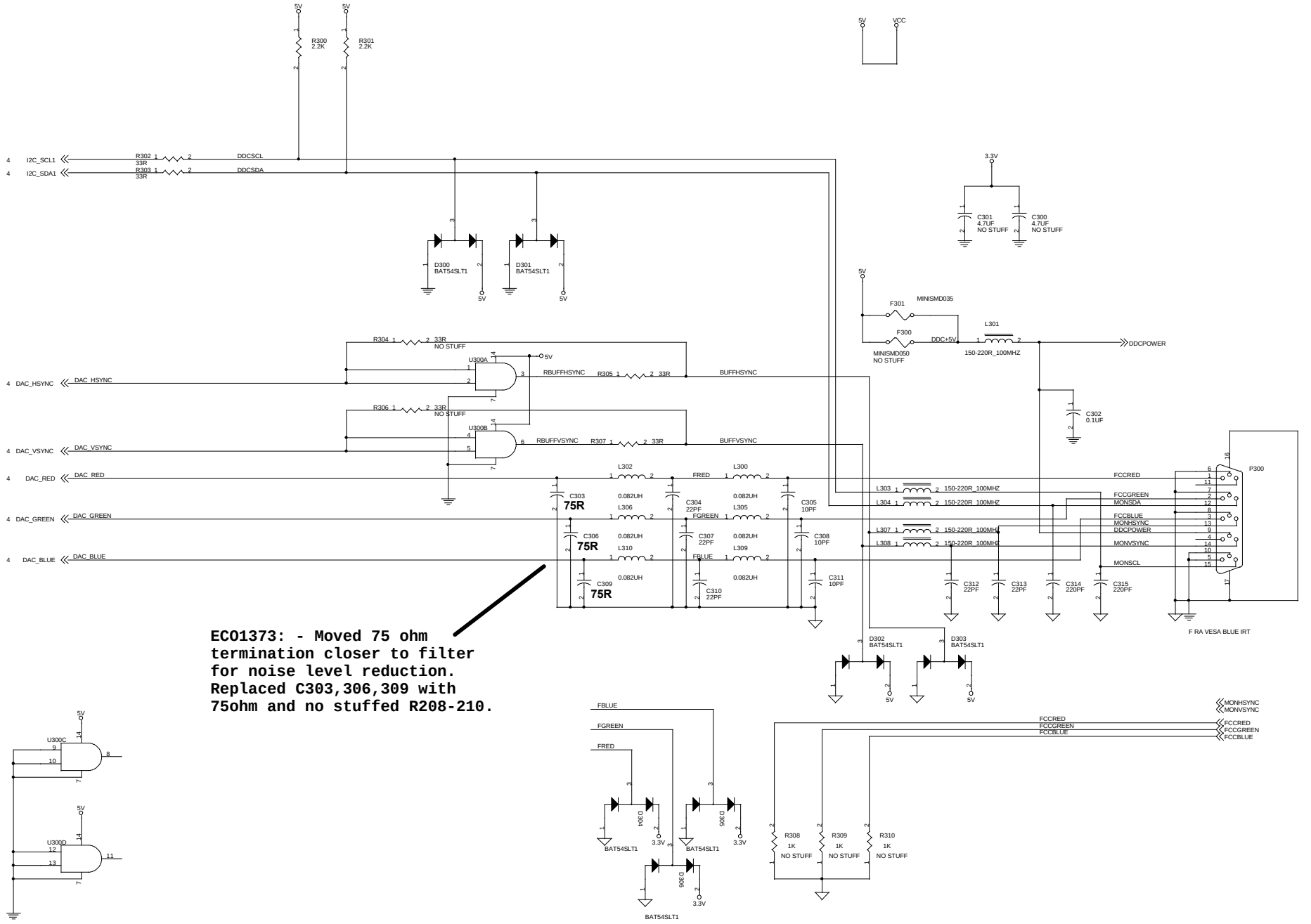

ECO1373: - Moved 75 ohm termination closer to filter for noise level reduction. Replaced C303,306,309 with 75ohm and no stuffed R208-210.

Micro-Star International Co., LTD.	
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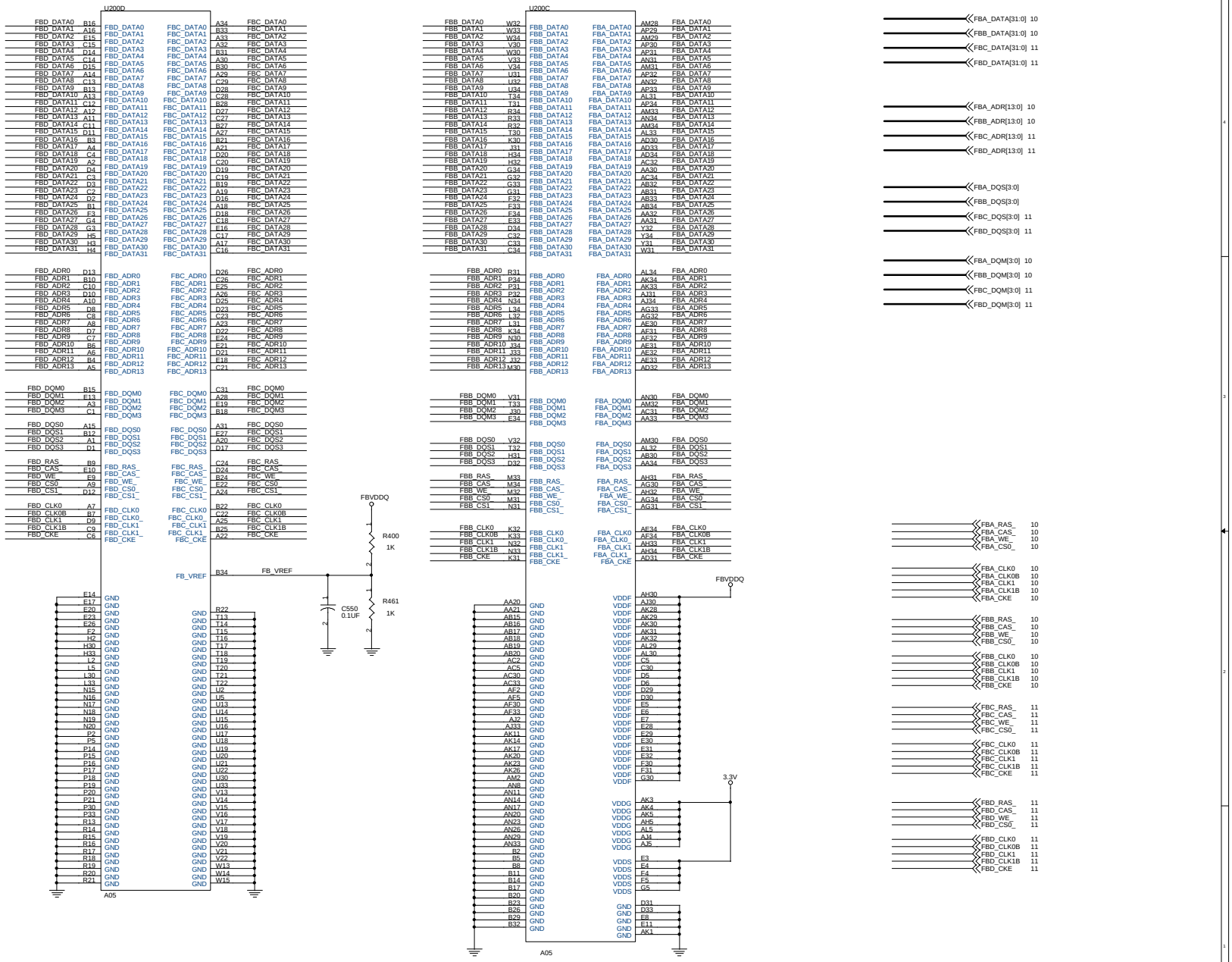

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MS-8850 based on P50A09	
Size: C	Document Number: NV20 FBA/B/C/D
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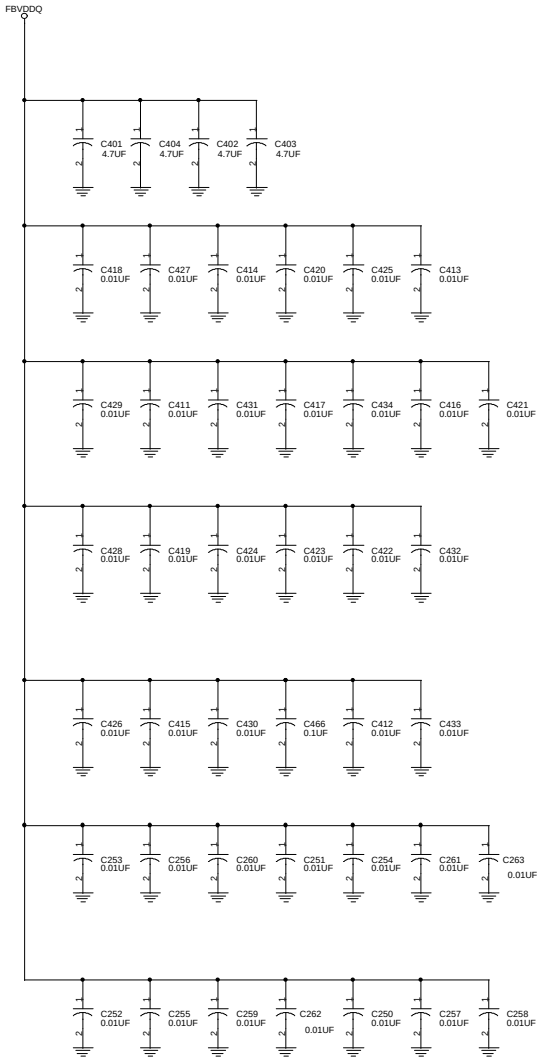

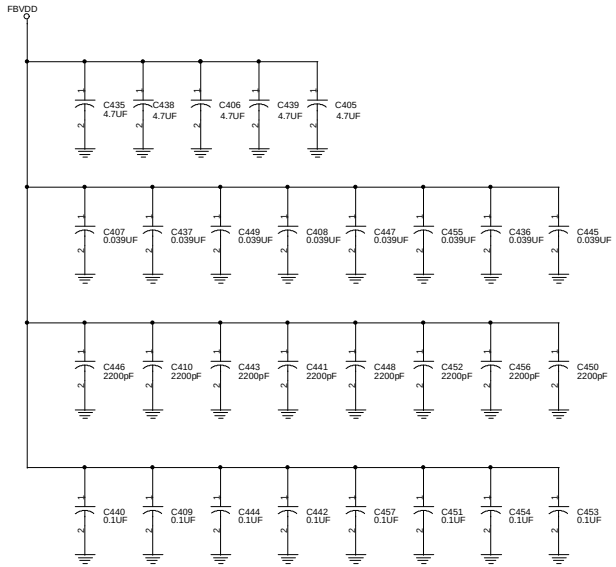

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	MS-8850 based on P50A09	
Size C	Document Number: FB DECOUPLING 4MX16 SDRAM	Rev 1.1A
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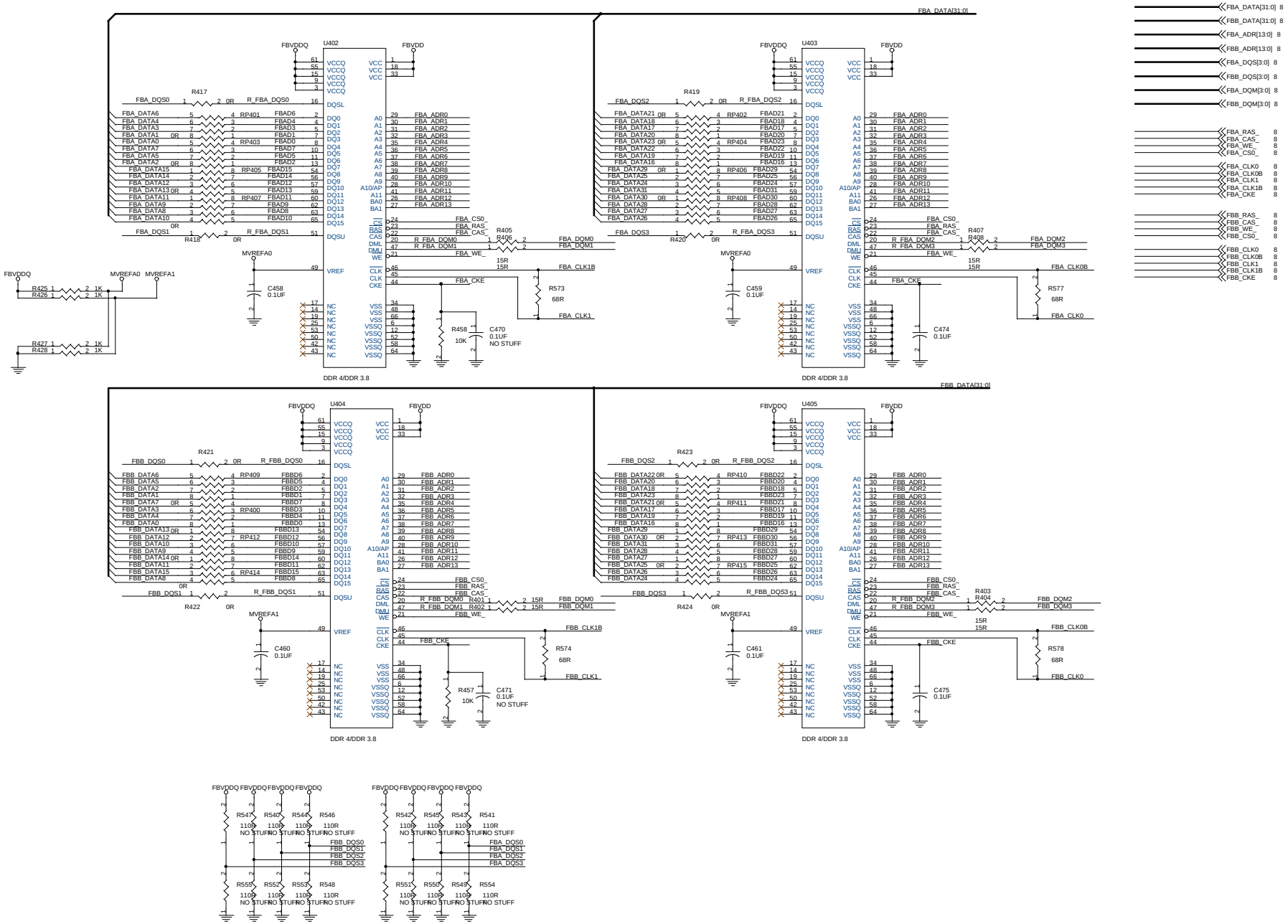

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MS-8850 based on P50A09			
Size: C	Document Number: FBA/B DDR 4MX16 SDRAM		Rev: 1.1A
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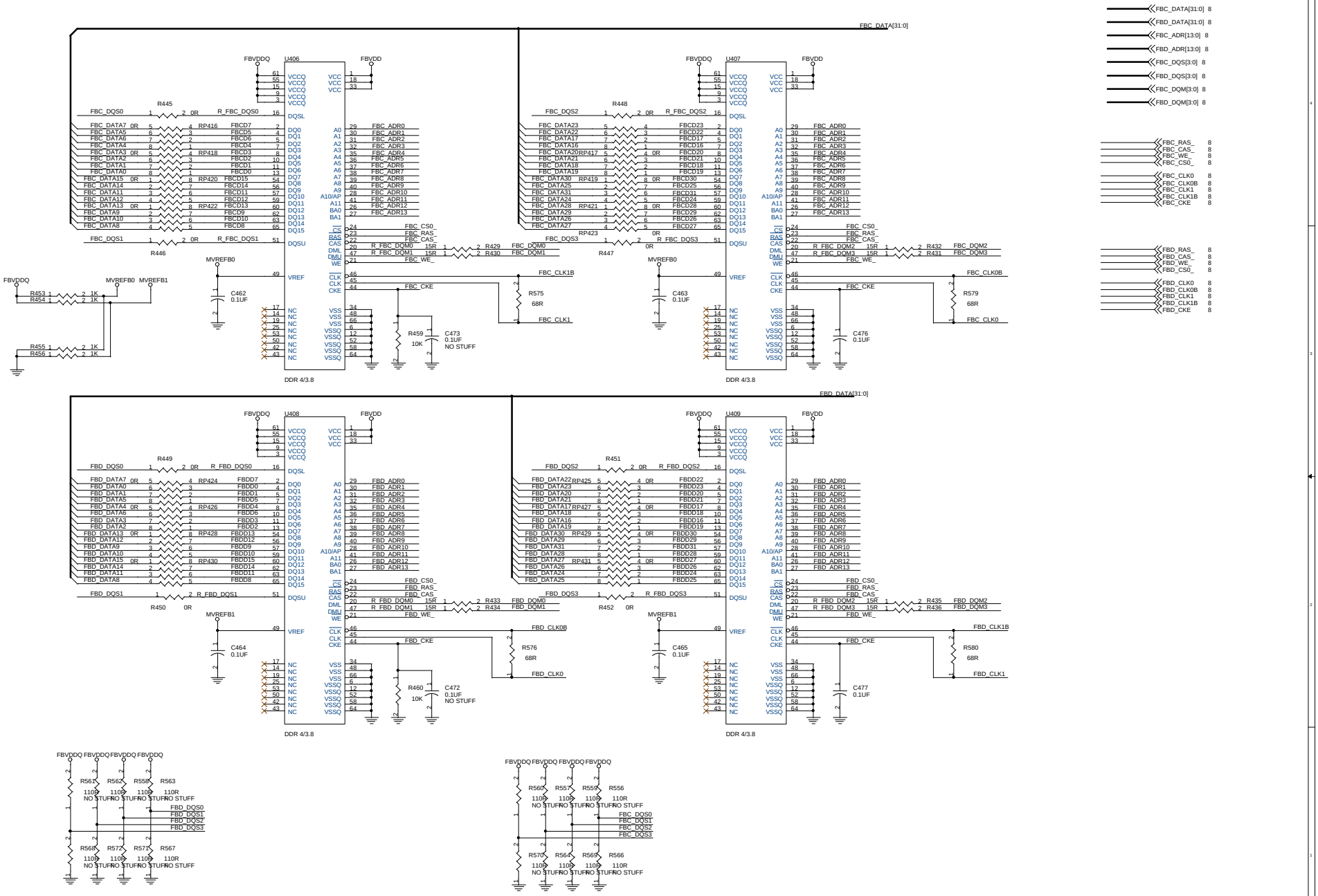

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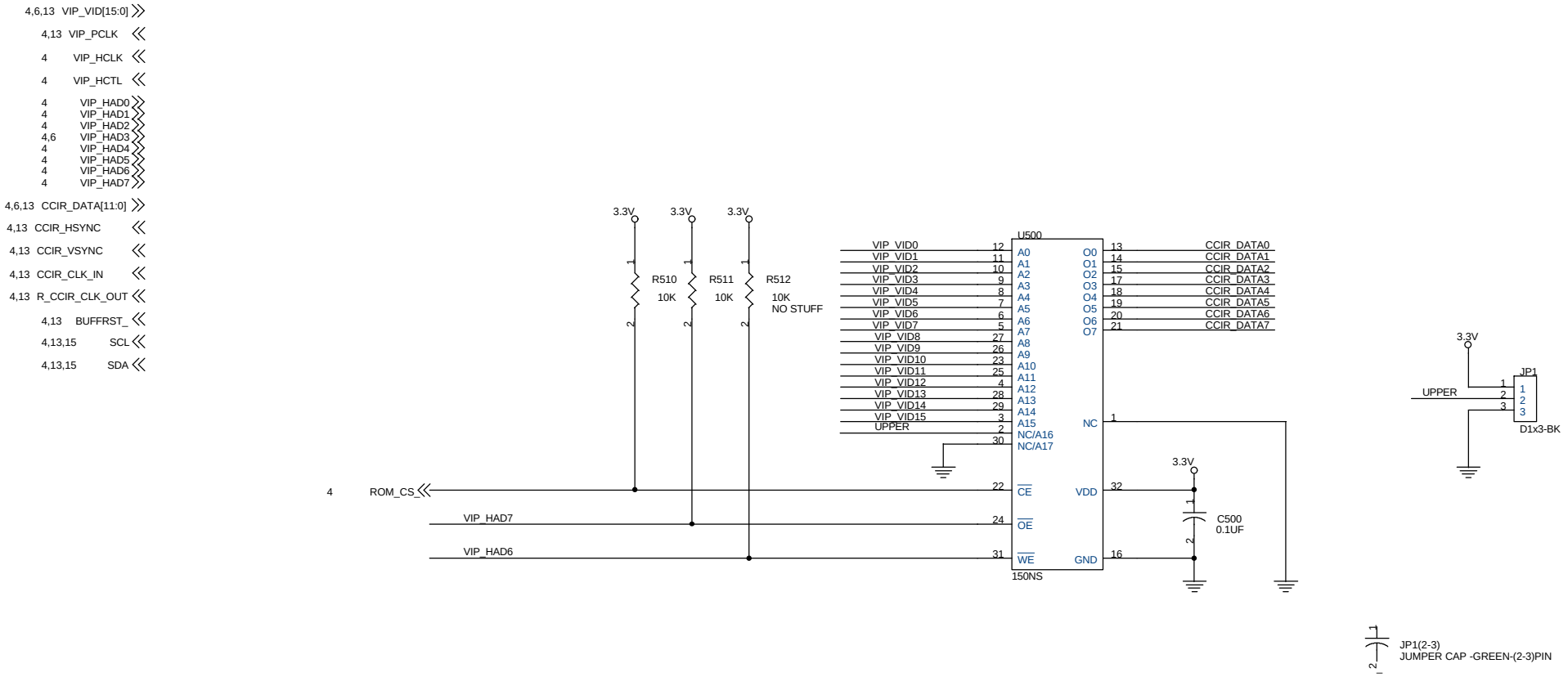

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MS-8850 based on P50A09		
Size: B	Document Number: ROM VIP & VIDEO IF	Rev: 1.1A
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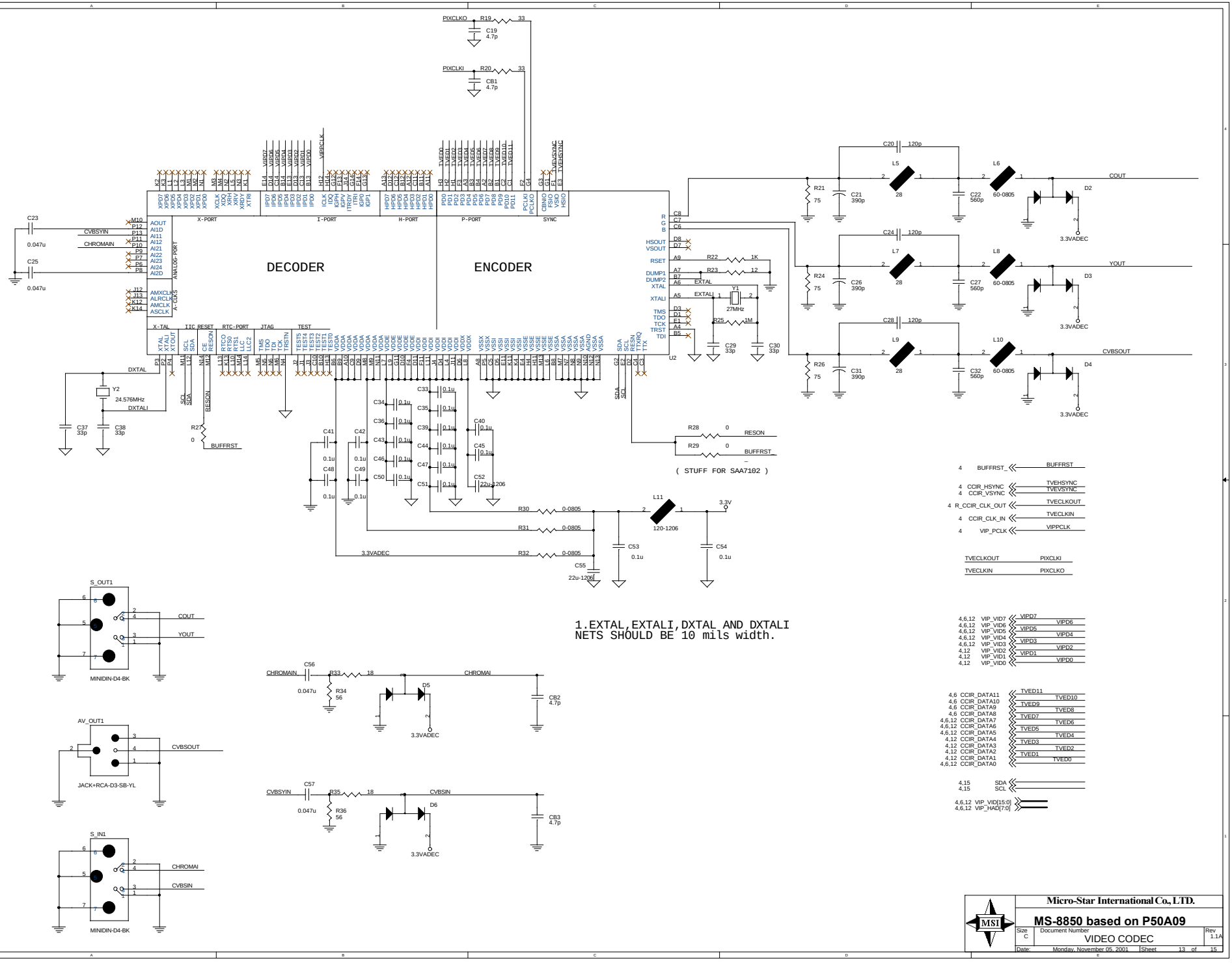
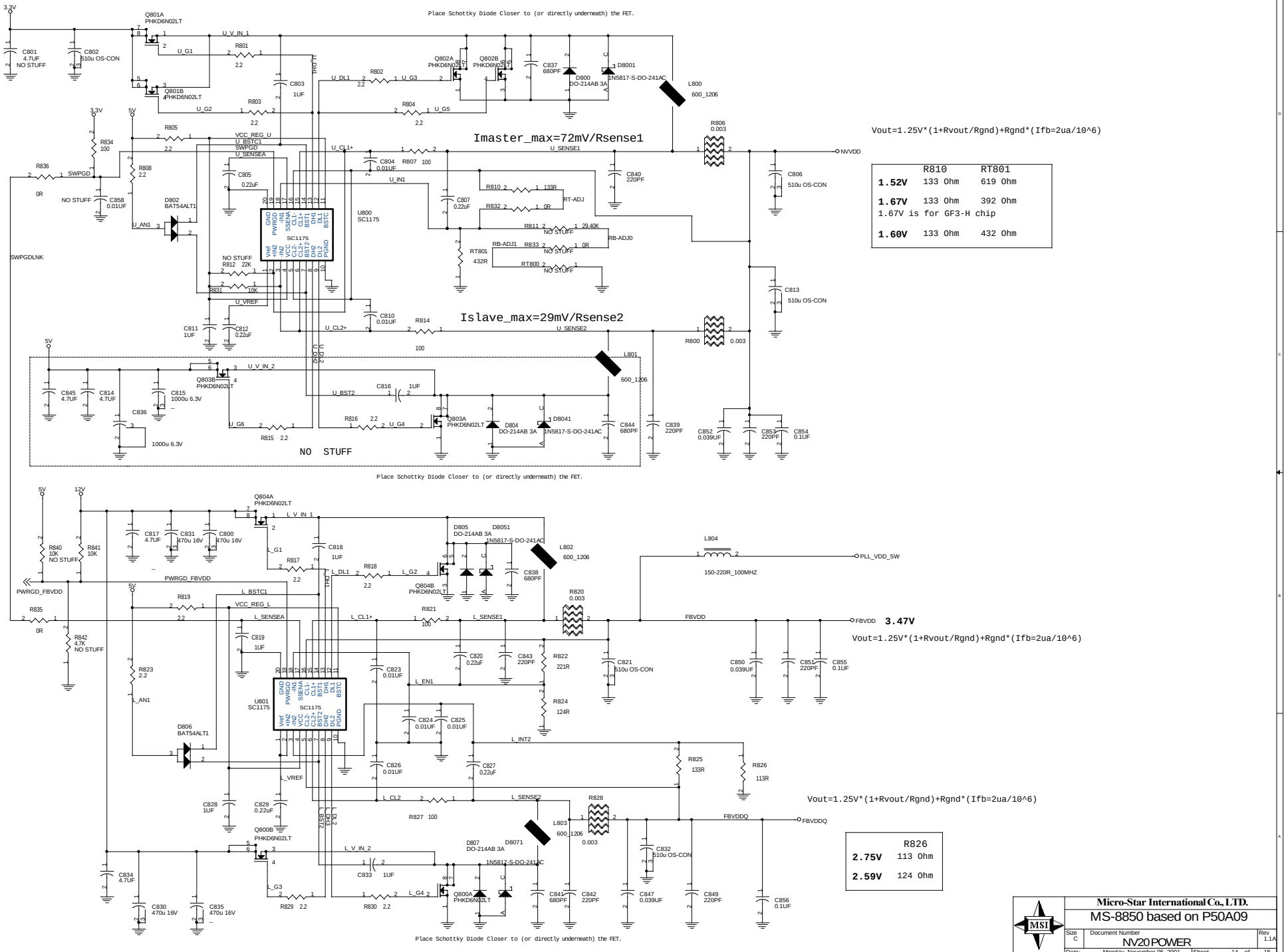

DECODER
ENCODER
X-PORT
I-PORT
H-PORT
P-PORT
SYNC
AMALOG-PORT
A-CLKS
X-TAL
IIC_RESET
RTC-PORT
JTAG
TEST
U2
PIXCLKO
PIXCLKI
DXTAL
DXTALI
CVBSYIN
CHROMAIN
CHROMAI
CVBSIN
COUT
YOUT
CVBSOUT
BUFFRST
RESON
3.3VADEC
27MHz
(STUFF FOR SAA7102)
1.EXTAL,EXTALI,DXTAL AND DXTALI
NETS SHOULD BE 10 mils width.
S_OUT1
AV_OUT1
S_IN1
MINIDIN-D4-BK
JACK+RCA-D3-SB-YL
TVECLKOUT
TVECLKIN
PIXCLKI
PIXCLKO
Micro-Star International Co., LTD.
MS-8850 based on P50A09
VIDEO CODEC
Monday, November 05, 2001



	R810	RT801
1.52V	133 Ohm	619 Ohm
1.67V	133 Ohm	392 Ohm
1.67V is for GF3-H chip		
1.60V	133 Ohm	432 Ohm

	R826
2.75V	113 Ohm
2.59V	124 Ohm

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MS-8850 based on P50A09

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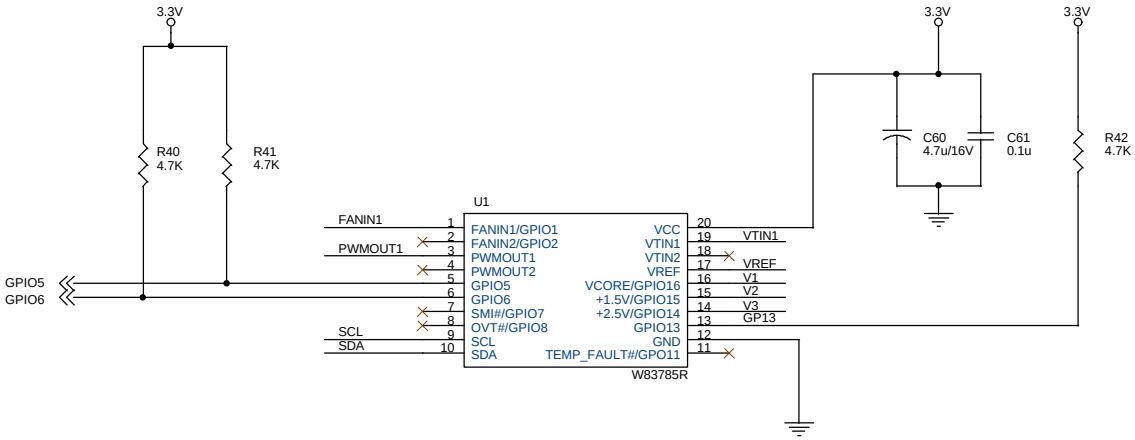
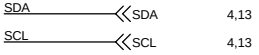
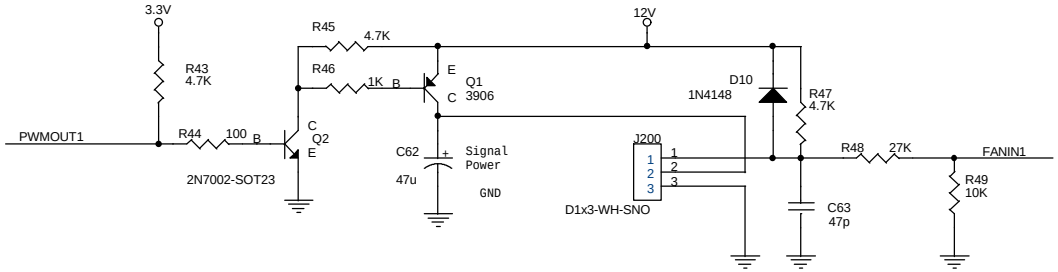
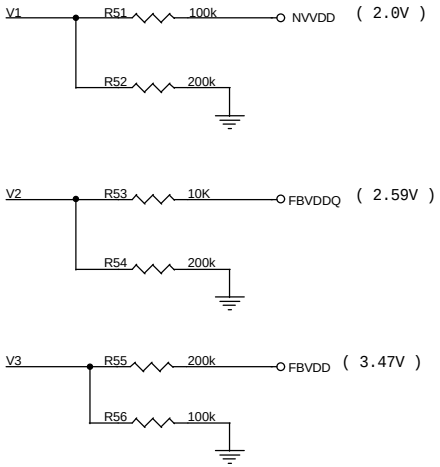




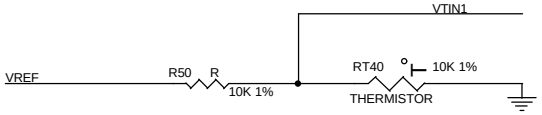


PWM1 Circuit for FAN1 speed Control

FOR 12V FAN



VOLTAGE SENSORING CIRCUIT



TEMPERATURE SENSORING CIRCUIT

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P300-1
SCREW

P300-2
SCREW

BRACKET
BRACKET

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Size: A	Document Number: <Doc>	Rev: 1.1A
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