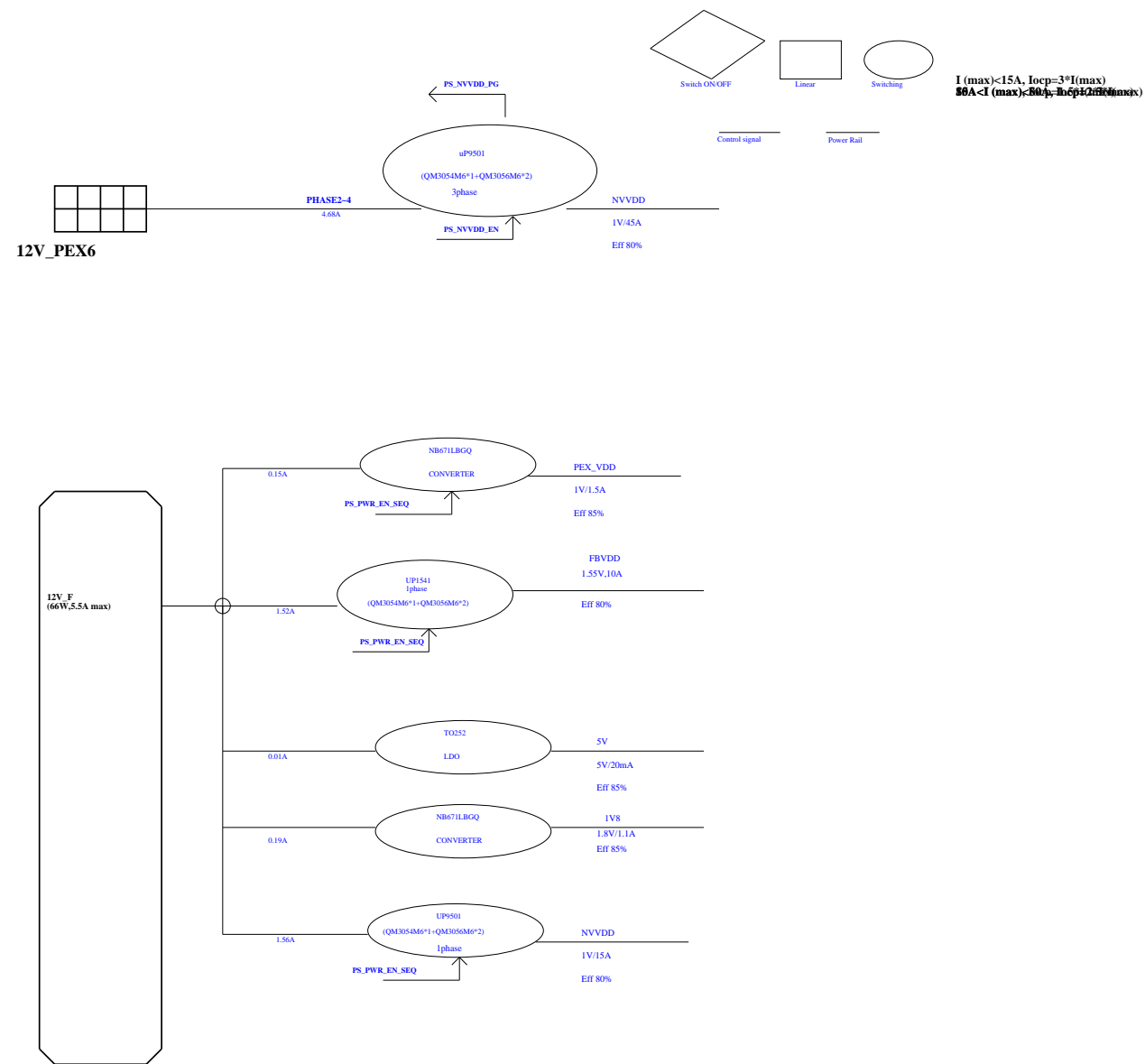


PG210 B00									
GP107 128b GDDR5, 75W , PCB 5.7									
DVI-D_DL (DP + DP) + HDMI (DP) + DP									
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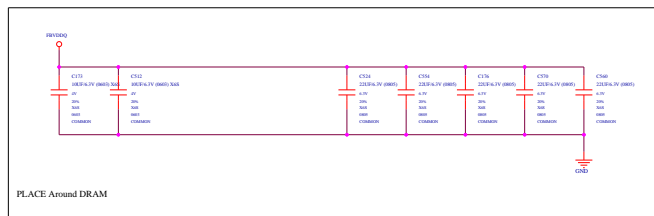
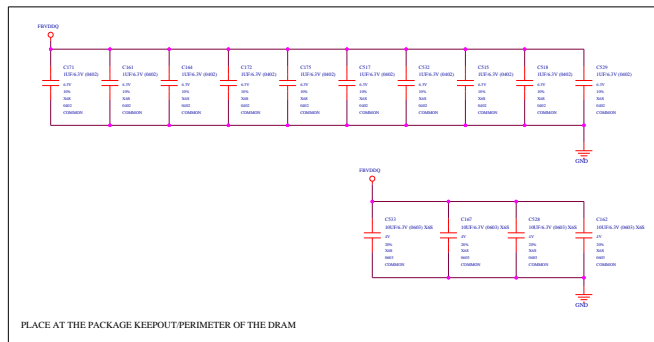
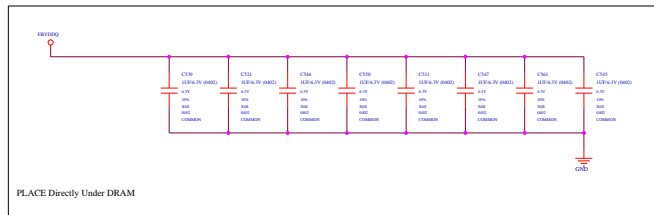
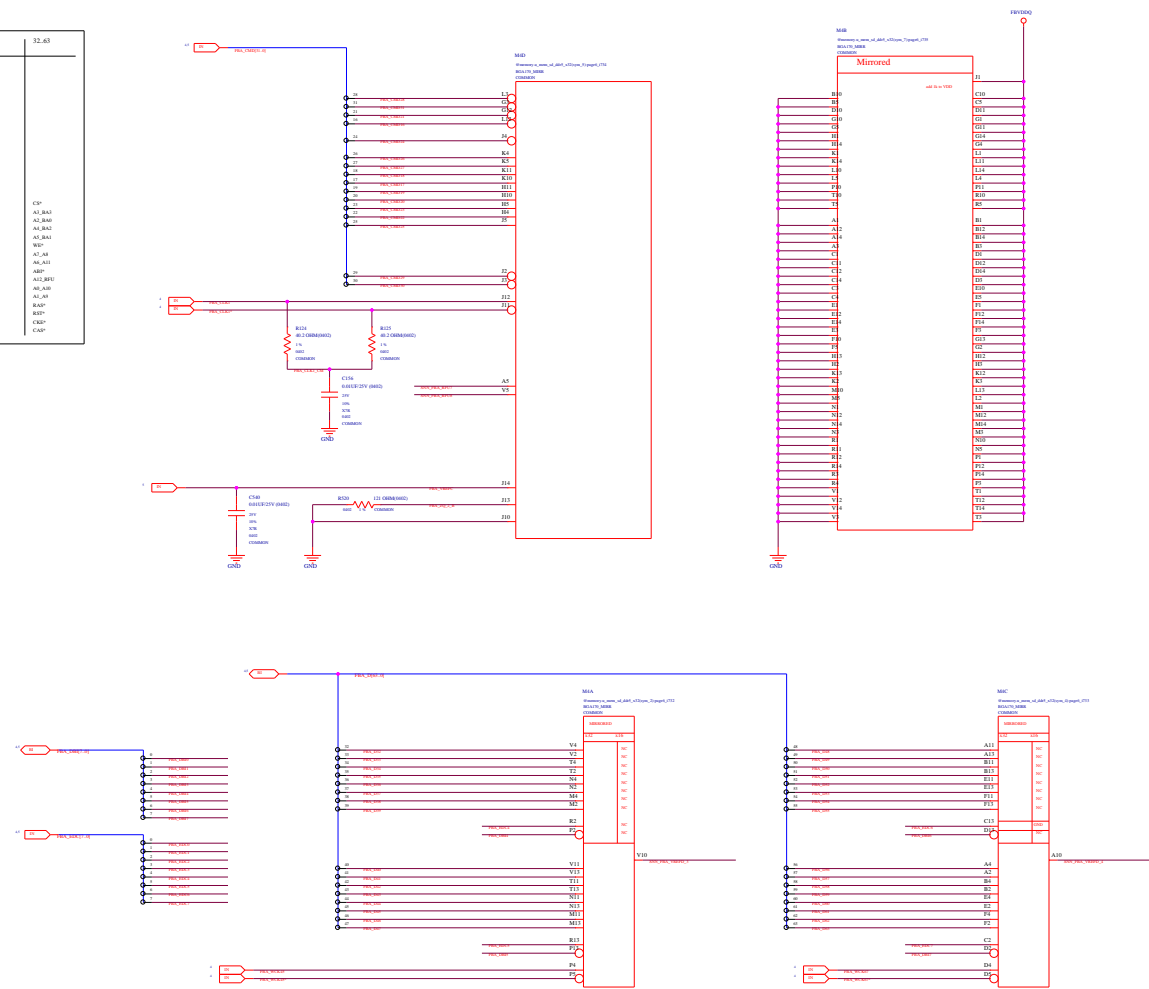


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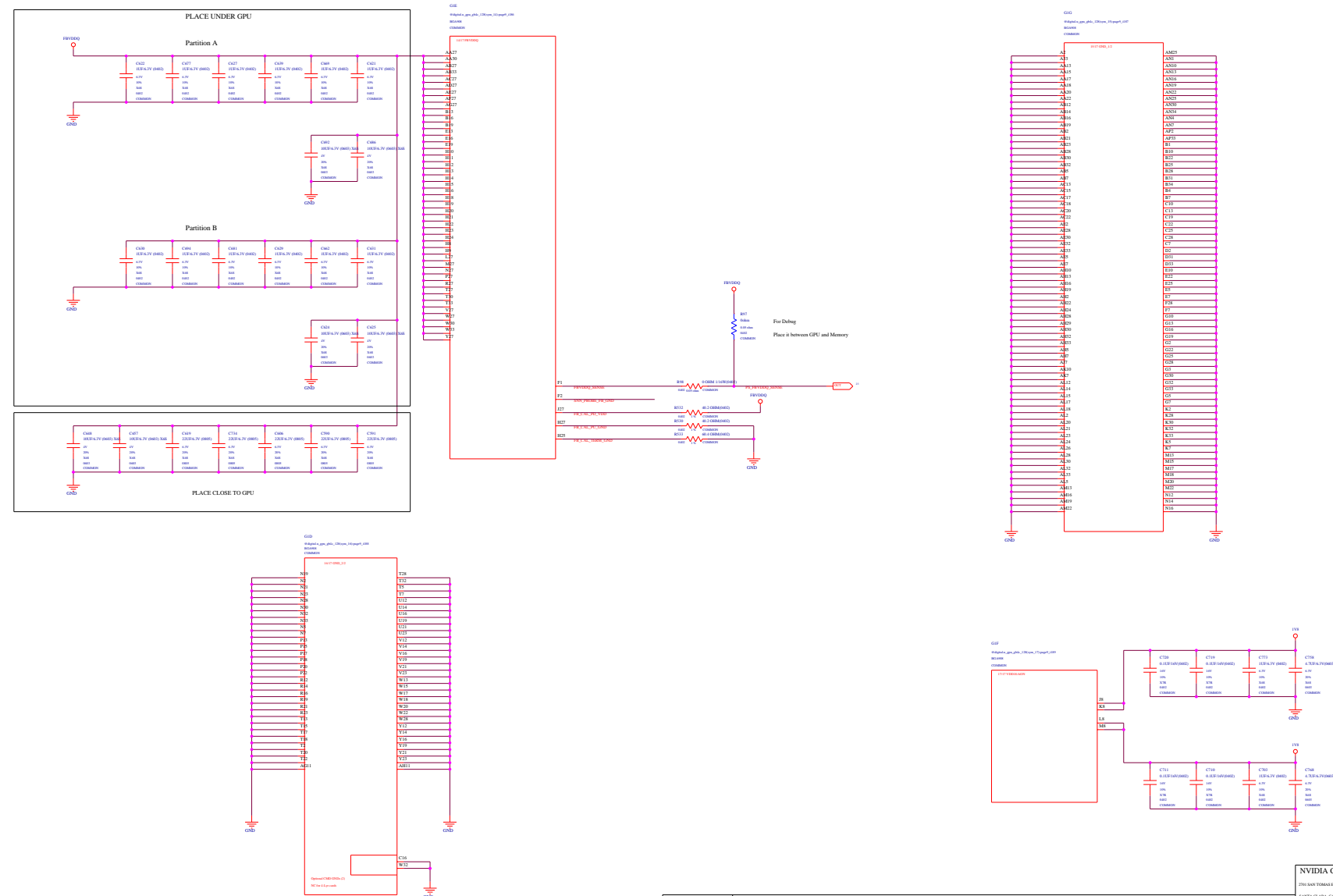
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CMD0	CS*	
CMD1	AL_BA2	
CMD2	AL_BA0	
CMD3	AL_BA3	
CMD4	AL_BA1	
CMD5	WE*	
CMD6	AL_A8	
CMD7	AL_A11	
CMD8	WE*	
CMD9	AL_A10	
CMD10	AL_A9	
CMD11	AL_A7	
CMD12	AL_A6	
CMD13	AL_A5	
CMD14	AL_A4	
CMD15	AL_A3	
CMD16	AL_A2	
CMD17	AL_A1	
CMD18	AL_A0	
CMD19	AL_A15	
CMD20	AL_A14	
CMD21	AL_A13	
CMD22	AL_A12	
CMD23	AL_A11	
CMD24	AL_A10	
CMD25	AL_A9	
CMD26	AL_A8	
CMD27	AL_A7	
CMD28	AL_A6	
CMD29	AL_A5	
CMD30	AL_A4	
CMD31	AL_A3	
CMD32	AL_A2	
CMD33	AL_A1	
CMD34	AL_A0	
CMD35	AL_A15	
CMD36	AL_A14	
CMD37	AL_A13	
CMD38	AL_A12	
CMD39	AL_A11	
CMD40	AL_A10	
CMD41	AL_A9	
CMD42	AL_A8	
CMD43	AL_A7	
CMD44	AL_A6	
CMD45	AL_A5	
CMD46	AL_A4	
CMD47	AL_A3	
CMD48	AL_A2	
CMD49	AL_A1	
CMD50	AL_A0	
CMD51	AL_A15	
CMD52	AL_A14	
CMD53	AL_A13	
CMD54	AL_A12	
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CMD57	AL_A9	
CMD58	AL_A8	
CMD59	AL_A7	
CMD60	AL_A6	
CMD61	AL_A5	
CMD62	AL_A4	
CMD63	AL_A3	
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CMD65	AL_A1	
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CMD67	AL_A15	
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CMD86	AL_A12	
CMD87	AL_A11	
CMD88	AL_A10	
CMD89	AL_A9	
CMD90	AL_A8	
CMD91	AL_A7	
CMD92	AL_A6	
CMD93	AL_A5	
CMD94	AL_A4	
CMD95	AL_A3	
CMD96	AL_A2	
CMD97	AL_A1	
CMD98	AL_A0	
CMD99	AL_A15	
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CMD107	AL_A7	
CMD108	AL_A6	
CMD109	AL_A5	
CMD110	AL_A4	
CMD111	AL_A3	
CMD112	AL_A2	
CMD113	AL_A1	
CMD114	AL_A0	
CMD115	AL_A15	
CMD116	AL_A14	
CMD117	AL_A13	
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CMD119	AL_A11	
CMD120	AL_A10	
CMD121	AL_A9	
CMD122	AL_A8	
CMD123	AL_A7	
CMD124	AL_A6	
CMD125	AL_A5	
CMD126	AL_A4	
CMD127	AL_A3	
CMD128	AL_A2	
CMD129	AL_A1	
CMD130	AL_A0	
CMD131	AL_A15	
CMD132	AL_A14	
CMD133	AL_A13	
CMD134	AL_A12	
CMD135	AL_A11	
CMD136	AL_A10	
CMD137	AL_A9	
CMD138	AL_A8	
CMD139	AL_A7	
CMD140	AL_A6	
CMD141	AL_A5	
CMD142	AL_A4	
CMD143	AL_A3	
CMD144	AL_A2	
CMD145	AL_A1	
CMD146	AL_A0	
CMD147	AL_A15	
CMD148	AL_A14	
CMD149	AL_A13	
CMD150	AL_A12	
CMD151	AL_A11	
CMD152	AL_A10	
CMD153	AL_A9	
CMD154	AL_A8	
CMD155	AL_A7	
CMD156	AL_A6	
CMD157	AL_A5	
CMD158	AL_A4	
CMD159	AL_A3	
CMD160	AL_A2	
CMD161	AL_A1	
CMD162	AL_A0	
CMD163	AL_A15	
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CMD197	AL_A13	
CMD198	AL_A12	
CMD199	AL_A11	
CMD200	AL_A10	
CMD201	AL_A9	
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CMD208	AL_A2	
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CMD210	AL_A0	
CMD211	AL_A15	
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CMD216	AL_A10	
CMD217	AL_A9	
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CMD219	AL_A7	
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CMD236	AL_A6	
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CMD239	AL_A3	
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CMD482	AL_A0	
CMD483	AL_A15	
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CMD485	AL_A13	
CMD486	AL_A12	
CMD487	AL_A11	
CMD488		

MEMORY: FBA Partition 63..32

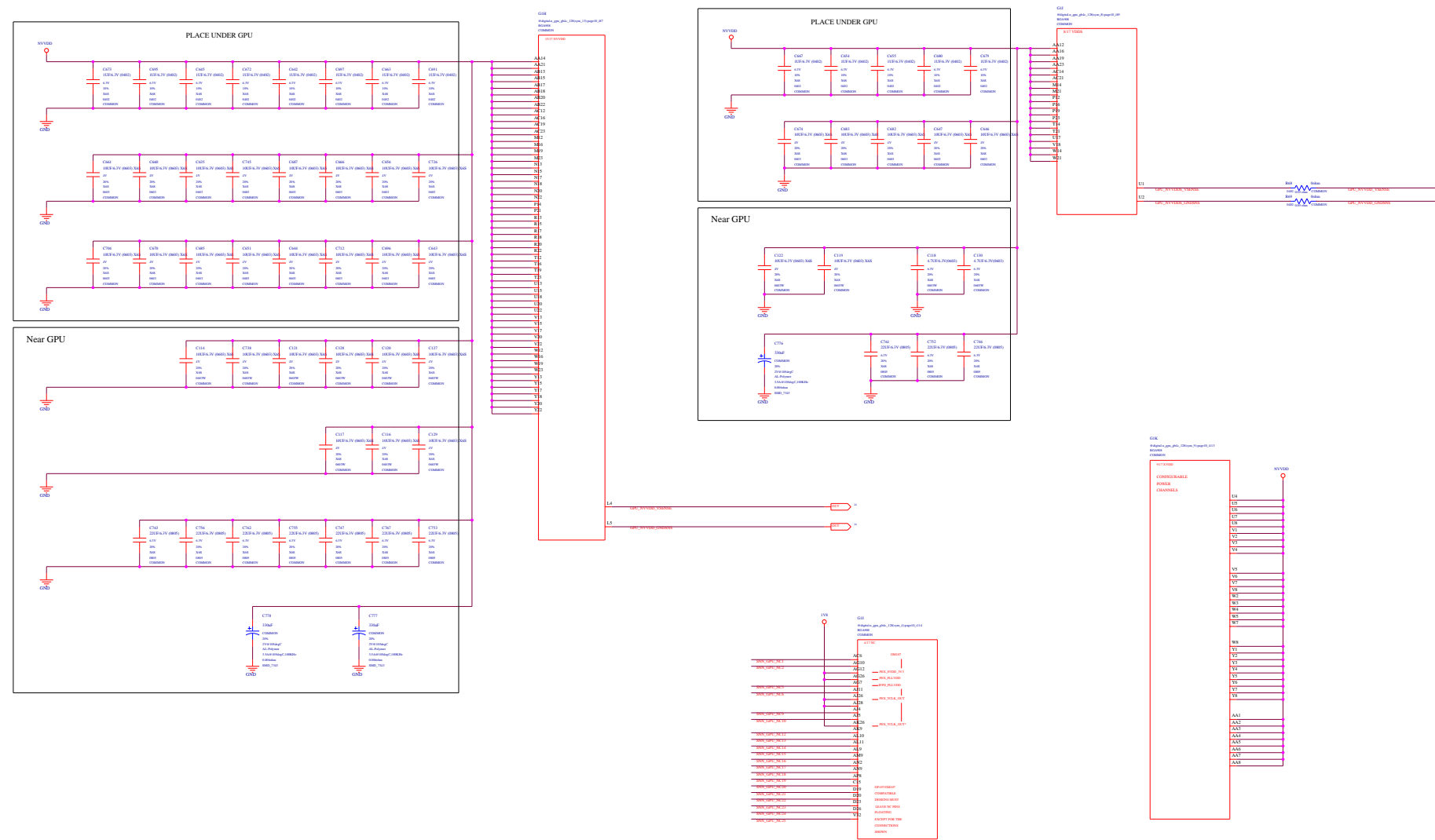
CMSD	B-31	32-60
CMS0	CSP	
CMS0	A1, B0A	
CMS2	A1, B0A	
CMS2	A1, B0A	
CMS3	A1, B0A	
CMS3	W07	
CMS4	A1, A0	
CMS4	A0, A1	
CMS9	AMP	
CMS9	A10, B0U	
CMS09	AMP	
CMS11	A1, A0	
CMS12	B0A0	
CMS13	B07*	
CMS14	CMS*	
CMS15	CMS*	
CMS16		CSP
CMS17	A1, B0A	
CMS18	A1, B0A	
CMS19	A1, B0A	
CMS20	W07	
CMS21	A1, A0	
CMS23	A0, A1	
CMS25	A10, B0U	
CMS26	AMP	
CMS28	A0, A1	
CMS29	B07	
CMS30	CMS*	
CMS31	CMS*	



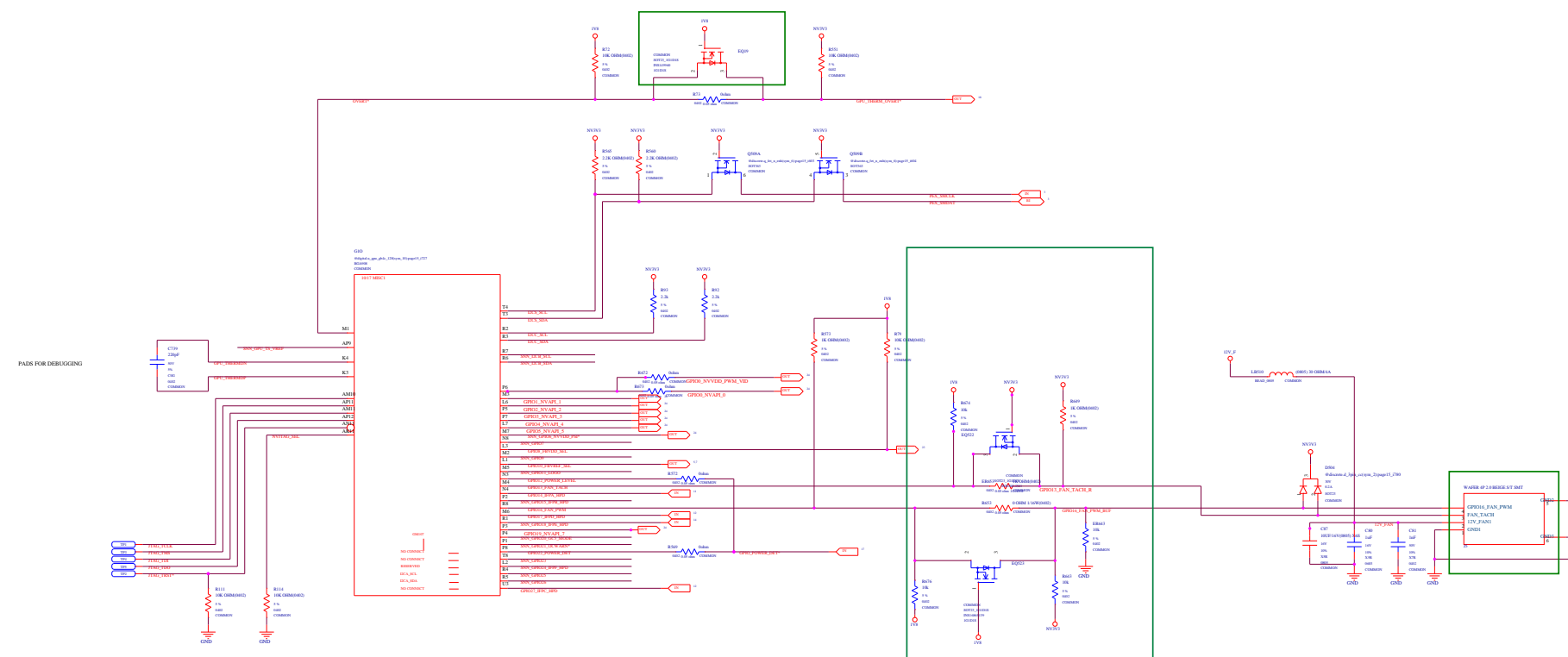
GPU FBVDDQ & 1V8 Decoupling



GPU NVVDD & NVVDDS Decoupling



MISC1: GPIO, Fan, Thermal, JTAG

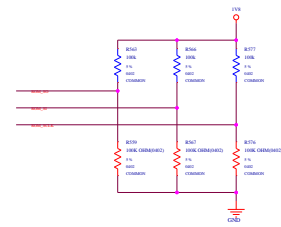


MISC2: ROM, XTAL, Straps

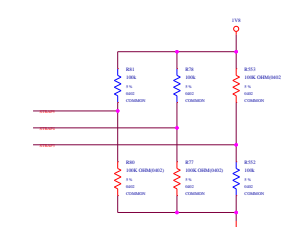
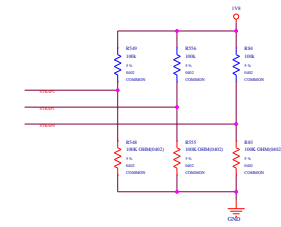
ROM_50	ROM_51	ROM_5CLK	SOR_EXPOSED[10]	1 ENABLE / 0 DISABLE
L	L	L	1111	Default
L	L	H	1110	
L	H	L	1101	
L	H	H	1100	
H	L	L	1011	
H	L	H	1010	
H	H	L	1001	
H	H	H	1000	
L	L	M	0111	
L	M	L	0110	
L	M	H	0101	
L	H	M	0100	
H	L	M	0011	
H	M	L	0010	
H	M	H	0001	
H	H	M	0000	

STRAP2	STRAP1	STRAP0	RAMTCG(4:0)	
L	L	L	0000	Summing B-Die 4GB 256Mb/32
L	L	H	0001	MicroB F-Die 4GB 256Mb/32
L	H	L	0010	Hybris M-Die 4GB 256Mb/32
L	H	H	0011	
H	H	L	0110	Hybris A-Die 4GB 128Mb/32
H	H	H	0111	Summing E-Die 4GB 128Mb/32
L	L	M	1000	MicroB B-Die 4GB 128Mb/32
L	M	L	1001	

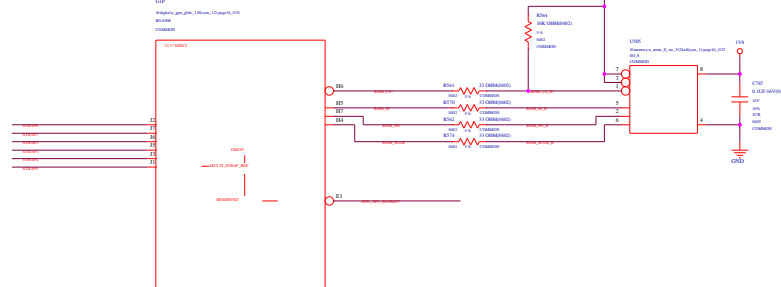
STRAP1	STRAP4	STRAP5	EMB_Alt_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1 DEFAULT
L	L	L	0	0	0	0



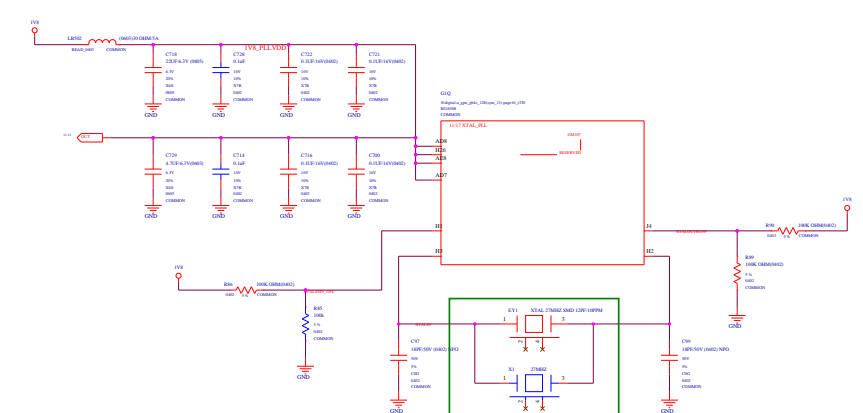
H=High (Tied to 1.8)
M=Middle/Tied to 0
L=Low (Tied to 0V)



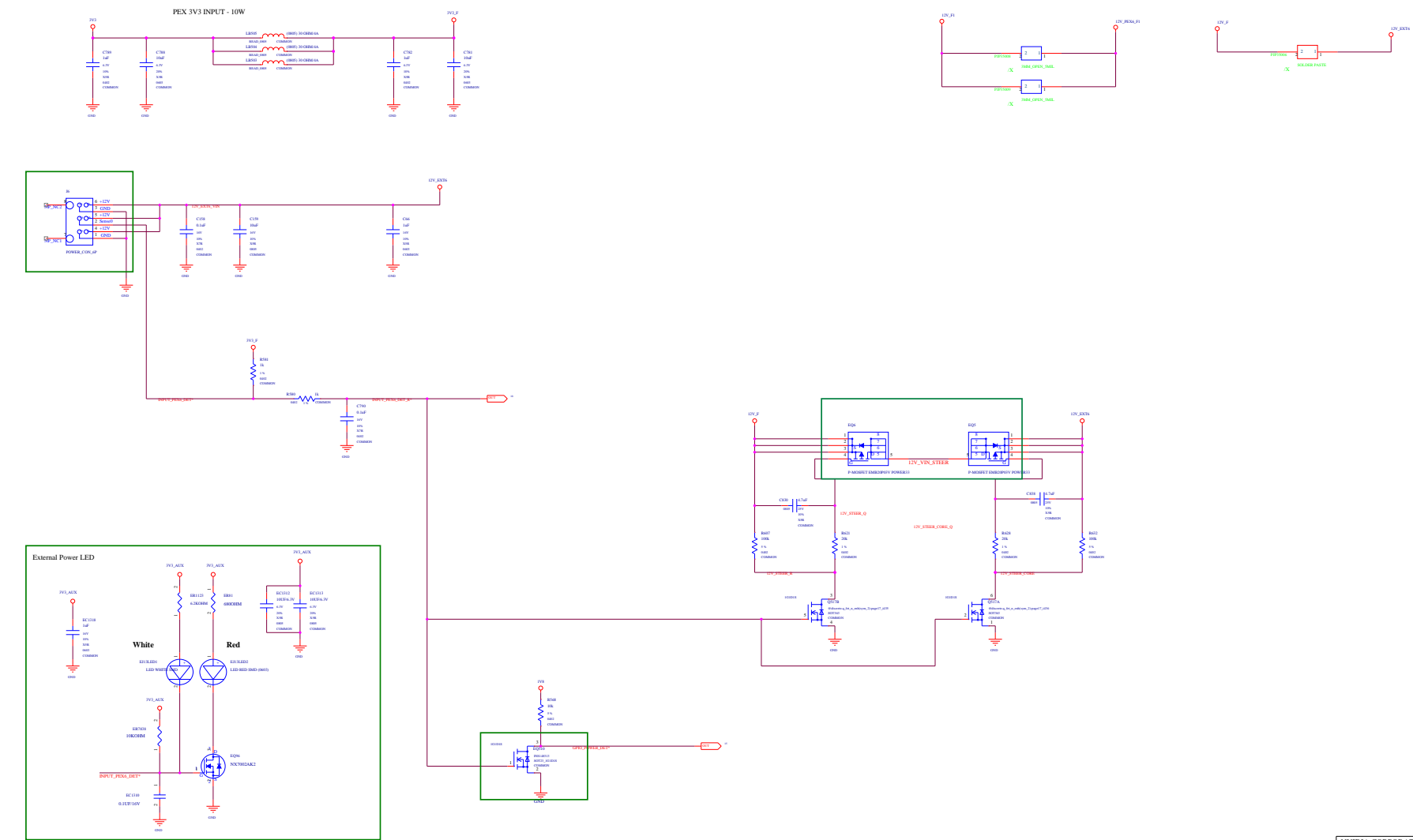
```
1:SMR_ALT_ADDR ENABLE
0:SMR_ALT_ADDR DISABLE
1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGINAL
1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER
1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE
```



Smart Fan			
XTALOUT/REF	Voltage	SmartFan PWM	GPIO PWM at the FAN
Low	0V	GPIO Disabled	100%
Middle	0.5V	66% PWM	33% PWM (Typical)
High	1.8V	33% PWM	66% PWM



Power Inputs and Filtering

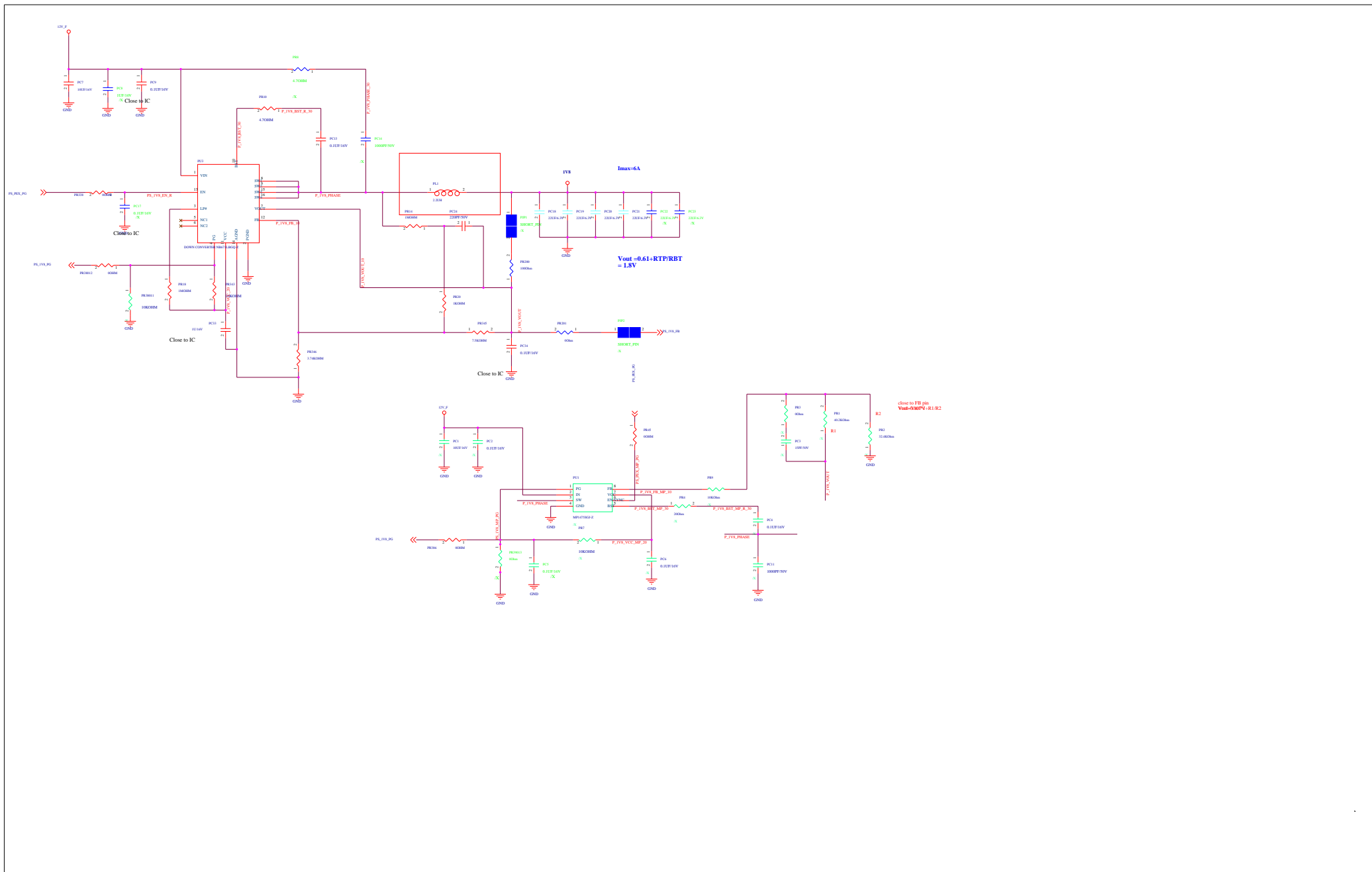


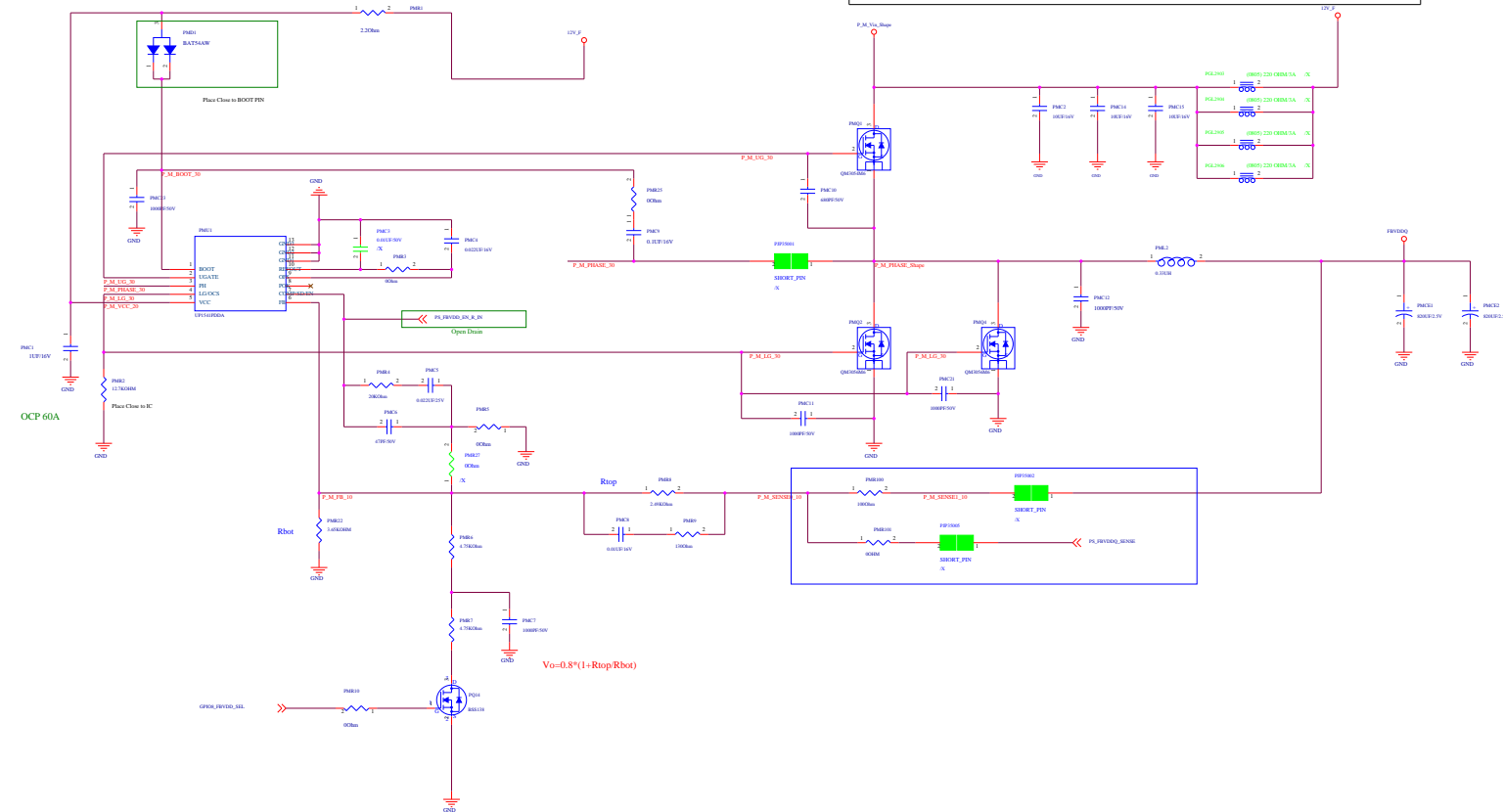
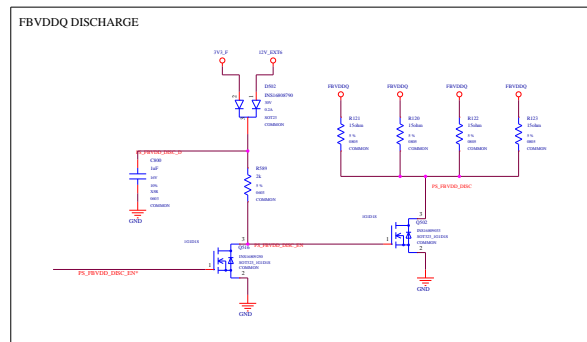
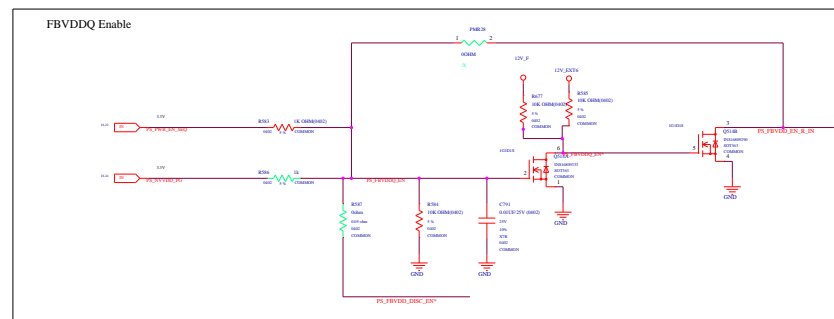
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SANTA CLARA, CA 95050, USA



PARTS LIST		REVISIONS	
REV	DATE	DESCRIPTION	BY
1.0	2010-01-01	Initial Release	1





GPIO LEVEL	FBVDDQ OUTPUT VOLTAGE
0	1.35V
1	1.55V

