

PG161-A00

TU106 6GB GDDR6, 192b, X16  
DVI-D/DP + DP + HDMI

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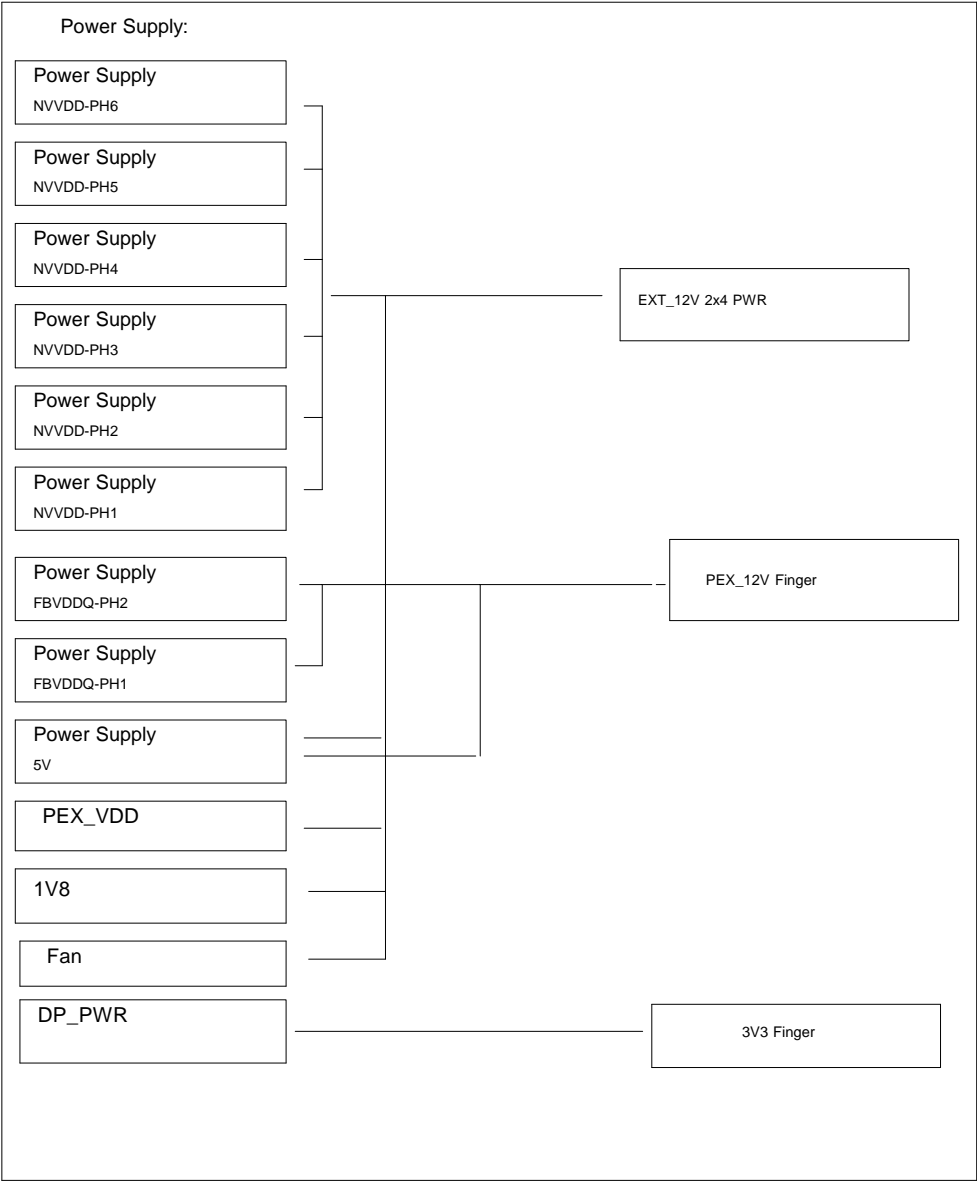
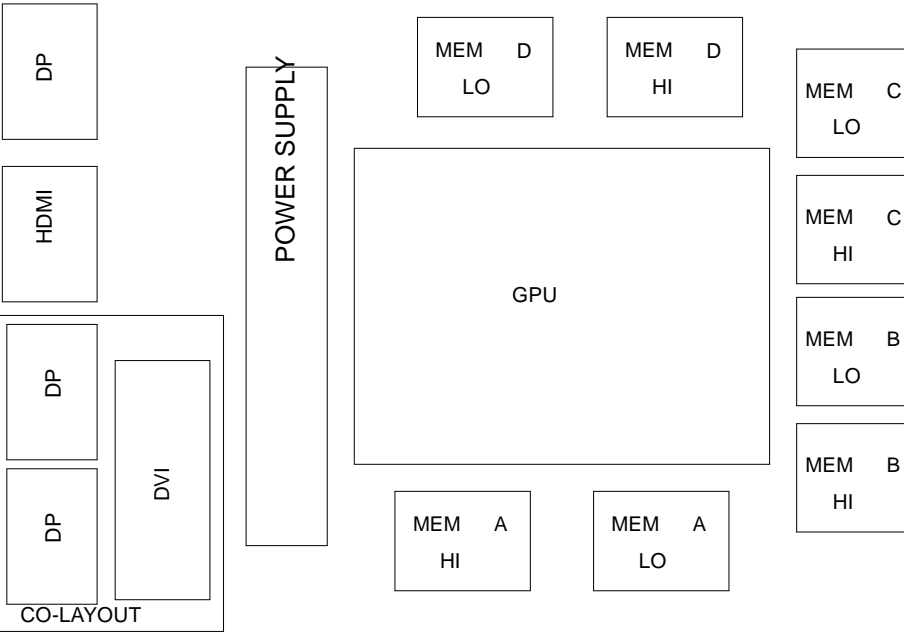
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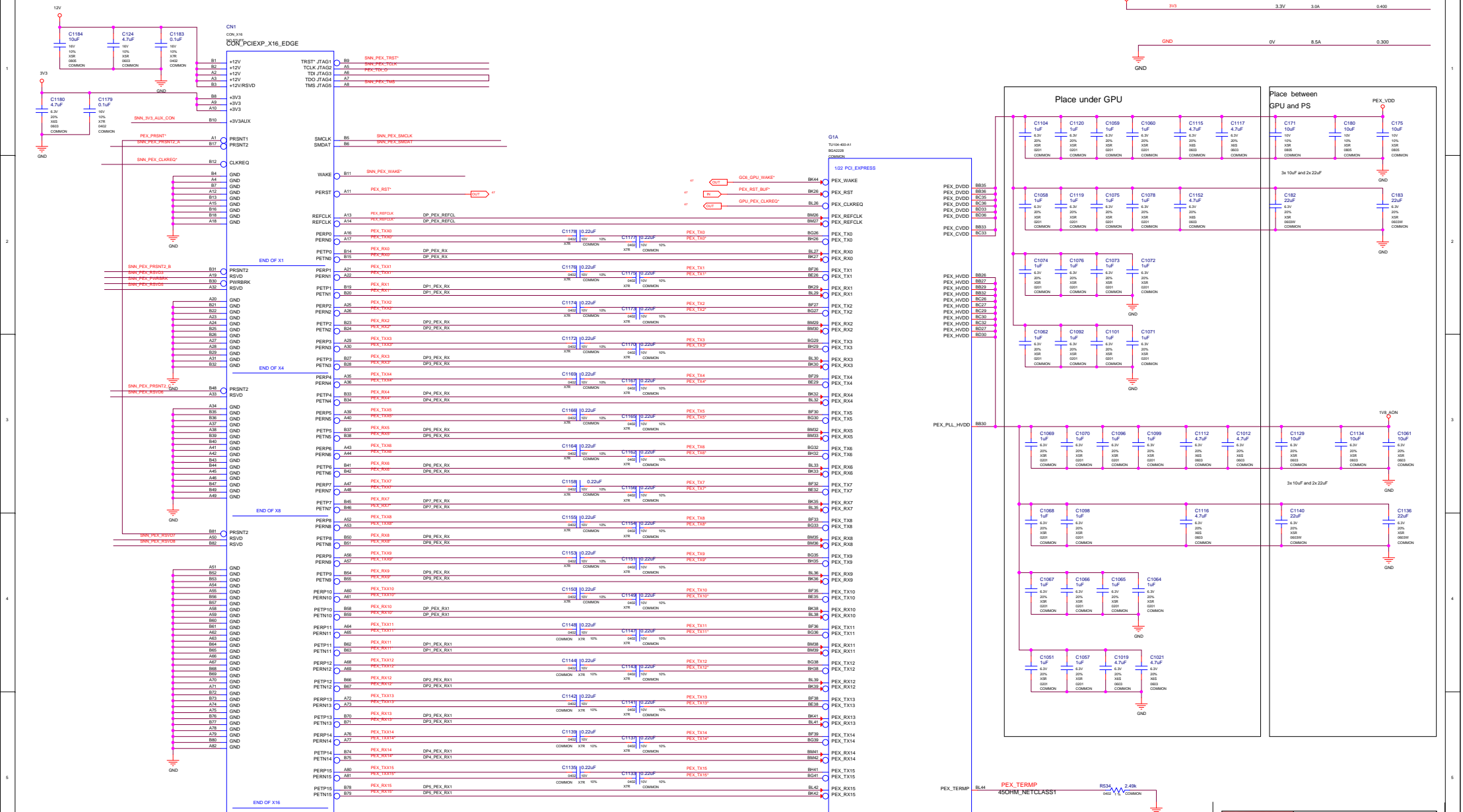
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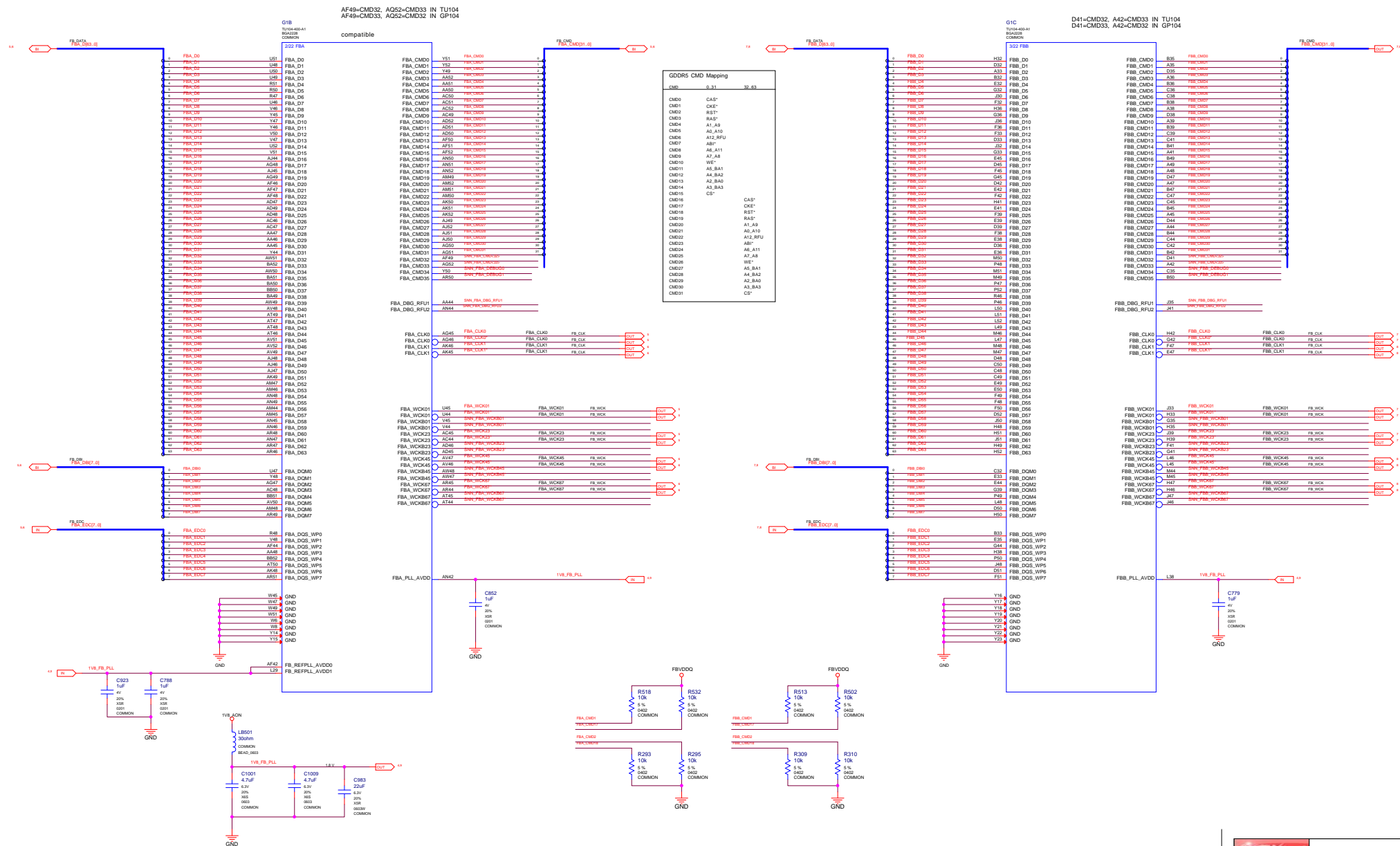
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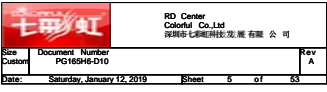


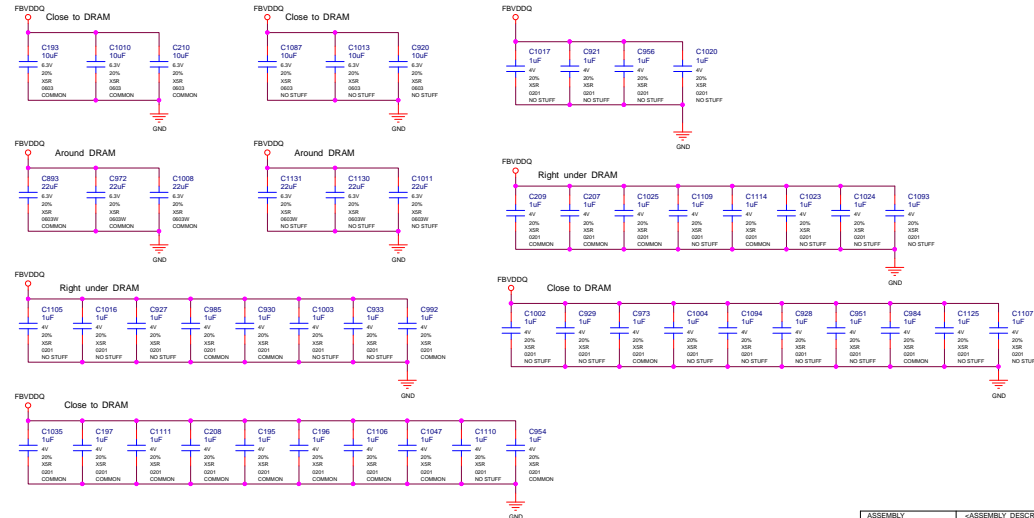
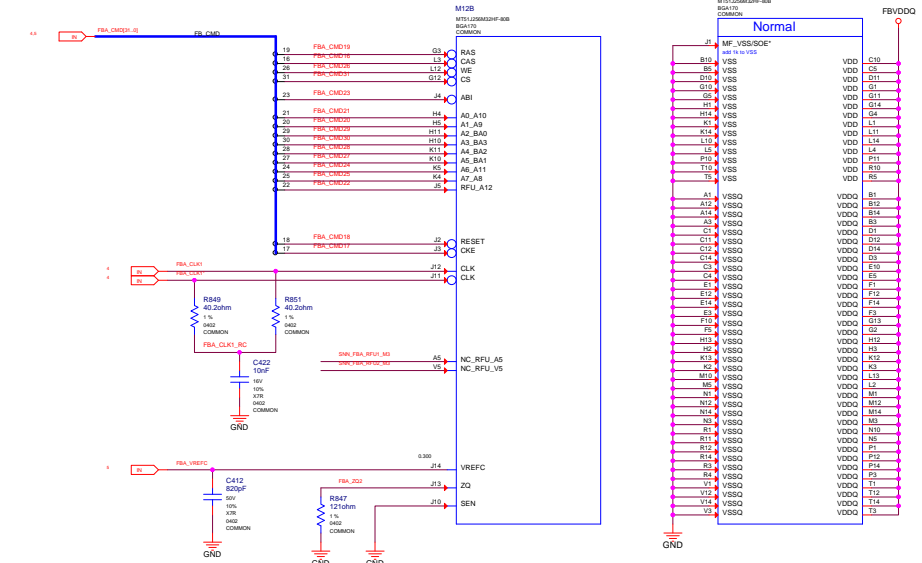
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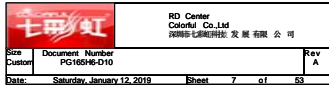


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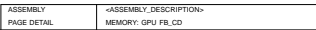
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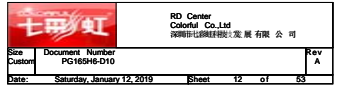


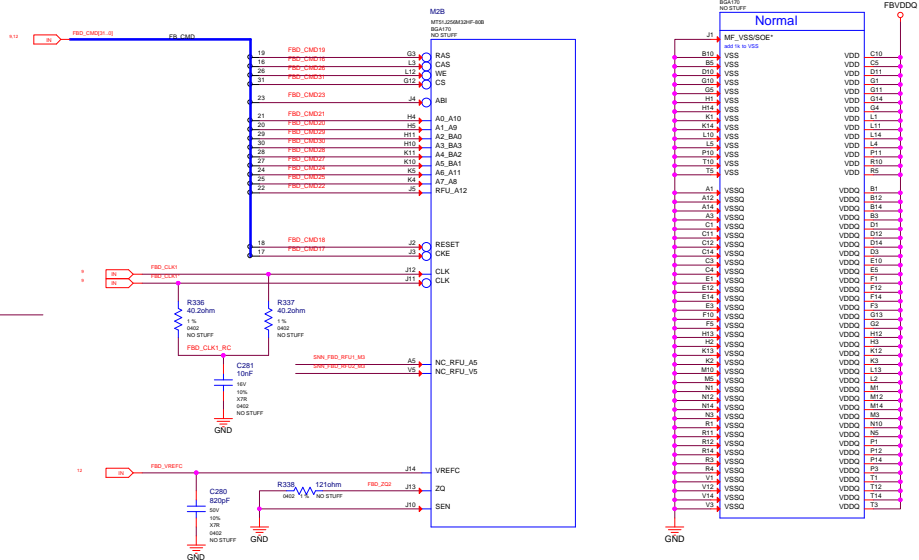














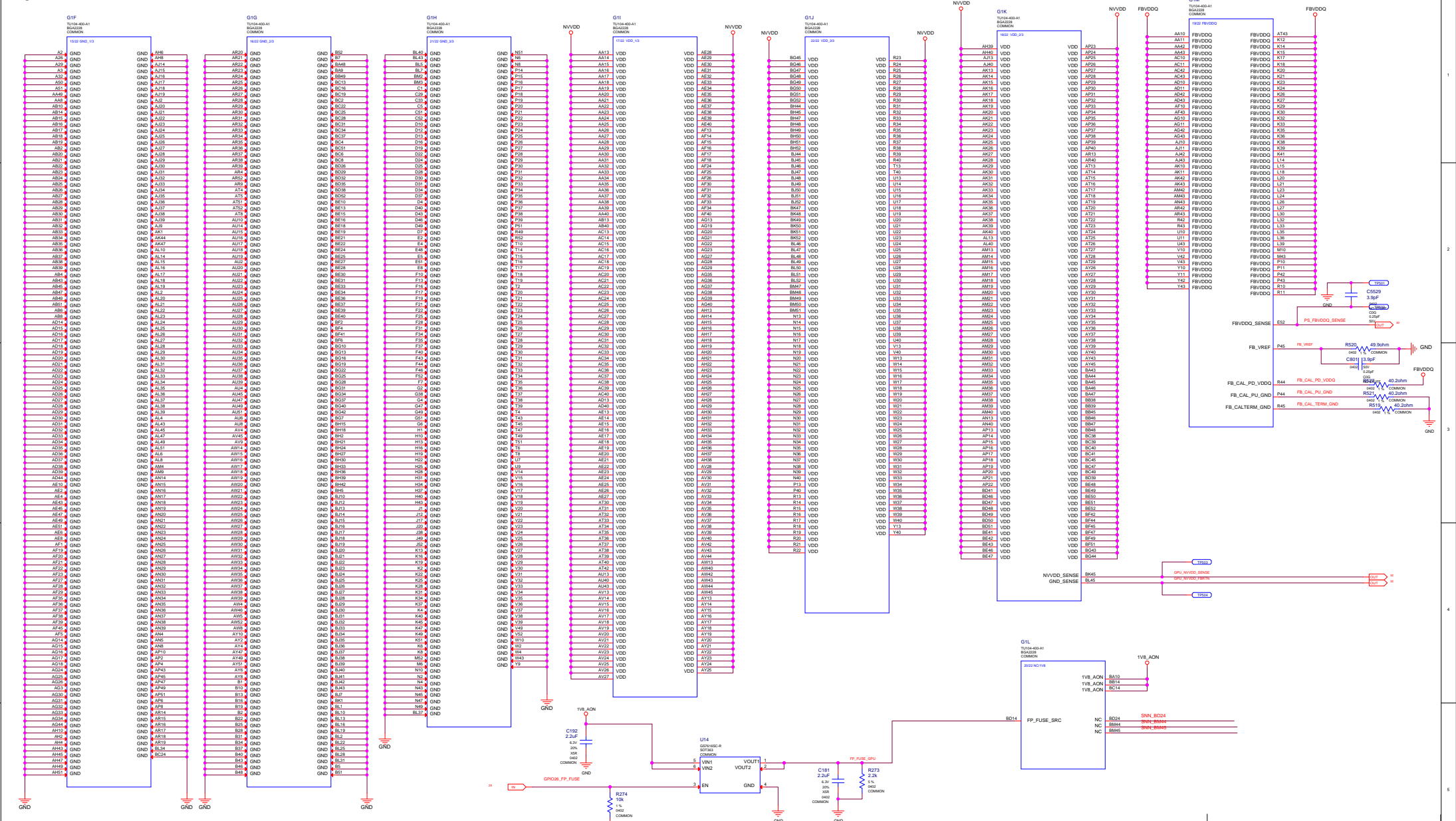
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NO PEX GEN4 SUPPORT  
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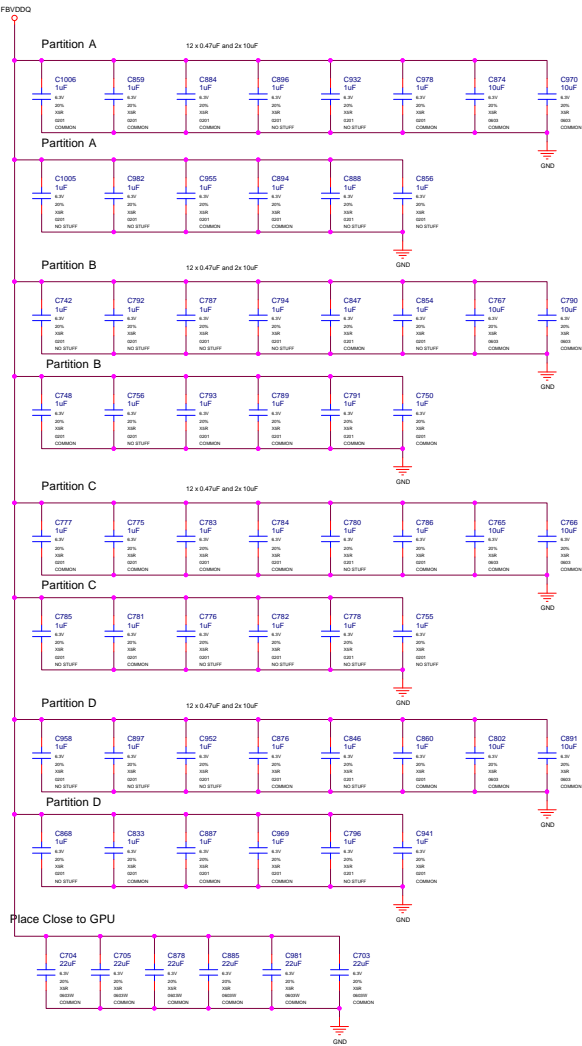
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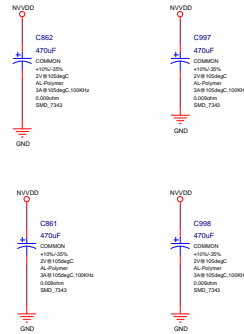
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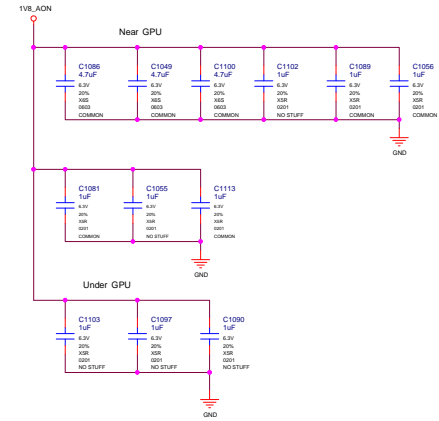
FBVDDQ



NVVDD



1V8\_AON



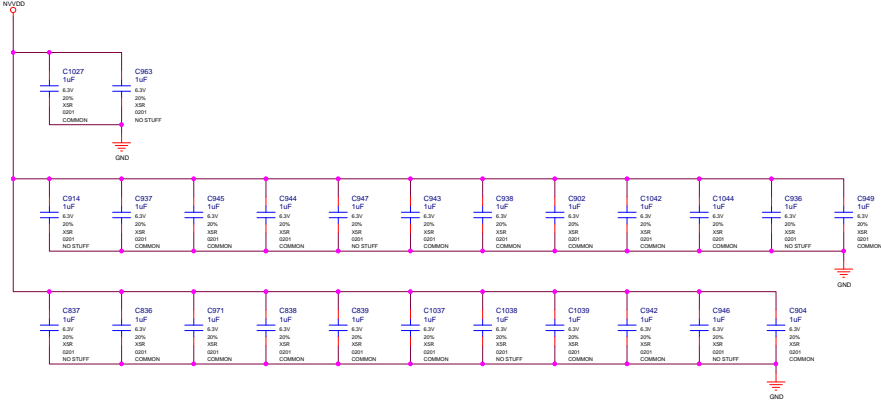
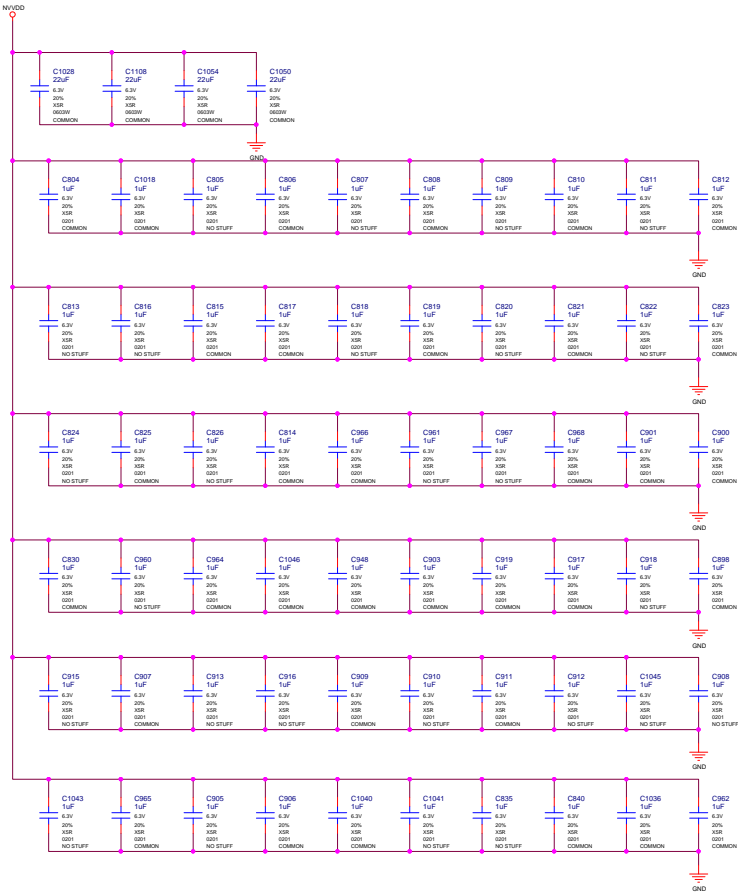
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NVDD

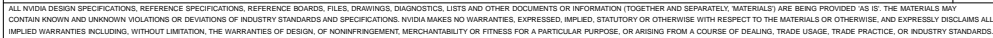



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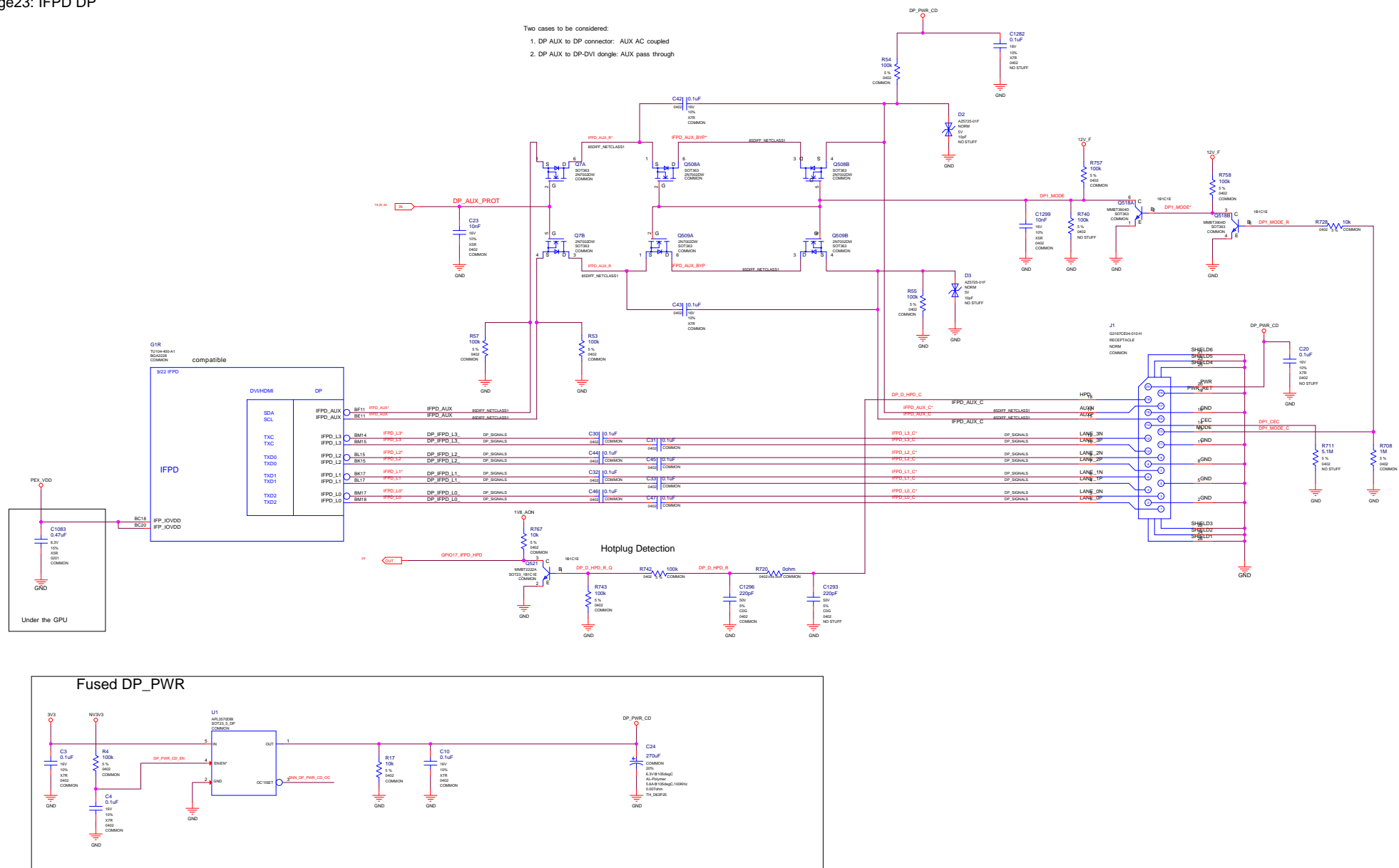


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STRAP2	STRAP1	STRAP0	RAMCFG[4:0]
L	L	L	00000
L	L	H	00001
L	H	L	00010
L	H	H	00011
H	H	L	00110
H	H	H	00111
L	L	M	01000

RAMCFG[4:0]	DENSITY	WIDTH	VENDOR	DIE
00000	8Gb	256-bit	Samsung	C
00001	8Gb	256-bit	Micron	A
00010	8Gb	256-bit	Hynix	M
00011	8Gb	256-bit	Samsung	C
00100	8Gb	256-bit	Micron	A
00101	8Gb	256-bit	Hynix	M
00110	16Gb	256-bit	Samsung	M
00111				

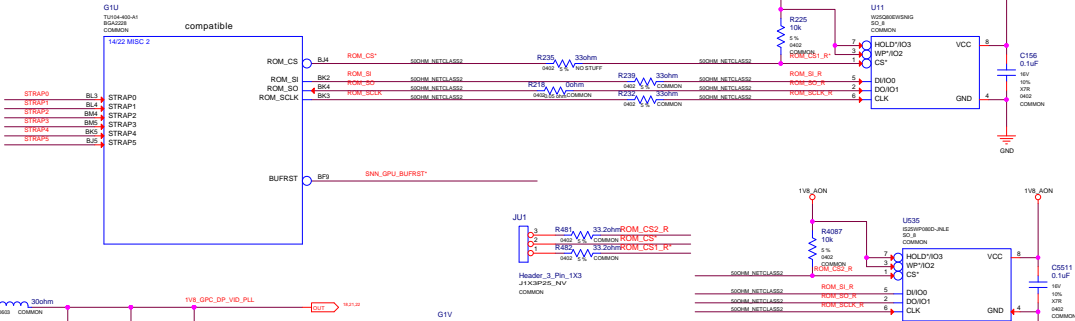
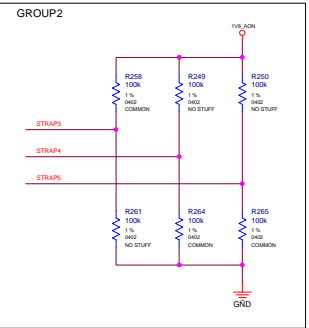
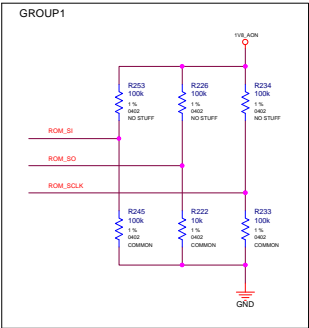
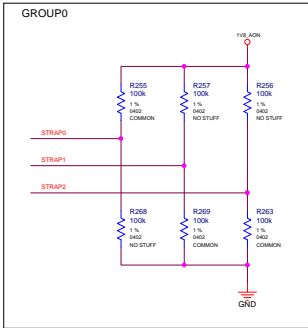
ROM_SO	ROM_SI	ROM_SCLK	DUMMY[2:0]_FS_OVERT	1:ENABLE 0:DISABLE	
L	L	L	XXX1	FS_OVERT ENABLE	DEFAULT
L	L	M	XXX0	FS_OVERT DISABLE	

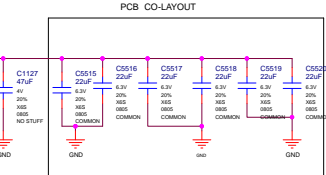
STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1 DEFAULT
L	L	L	0	0	0	0

H=High :Tied to 1.8V  
M=Middle:Tied to 0.9V  
L=Low :Tied to 0V

1:SMB\_ALT\_ADDR ENABLE  
0:SMB\_ALT\_ADDR DISABLE  
  
1:DEVID\_SEL REBRAND  
0:DEVID\_SEL ORIGINAL

1:PCIE\_CFG LOW POWER  
0:PCIE\_CFG HIGH POWER  
  
1:VGA\_DEVICE ENABLE  
0:VGA\_DEVICE DISABLE





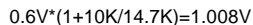
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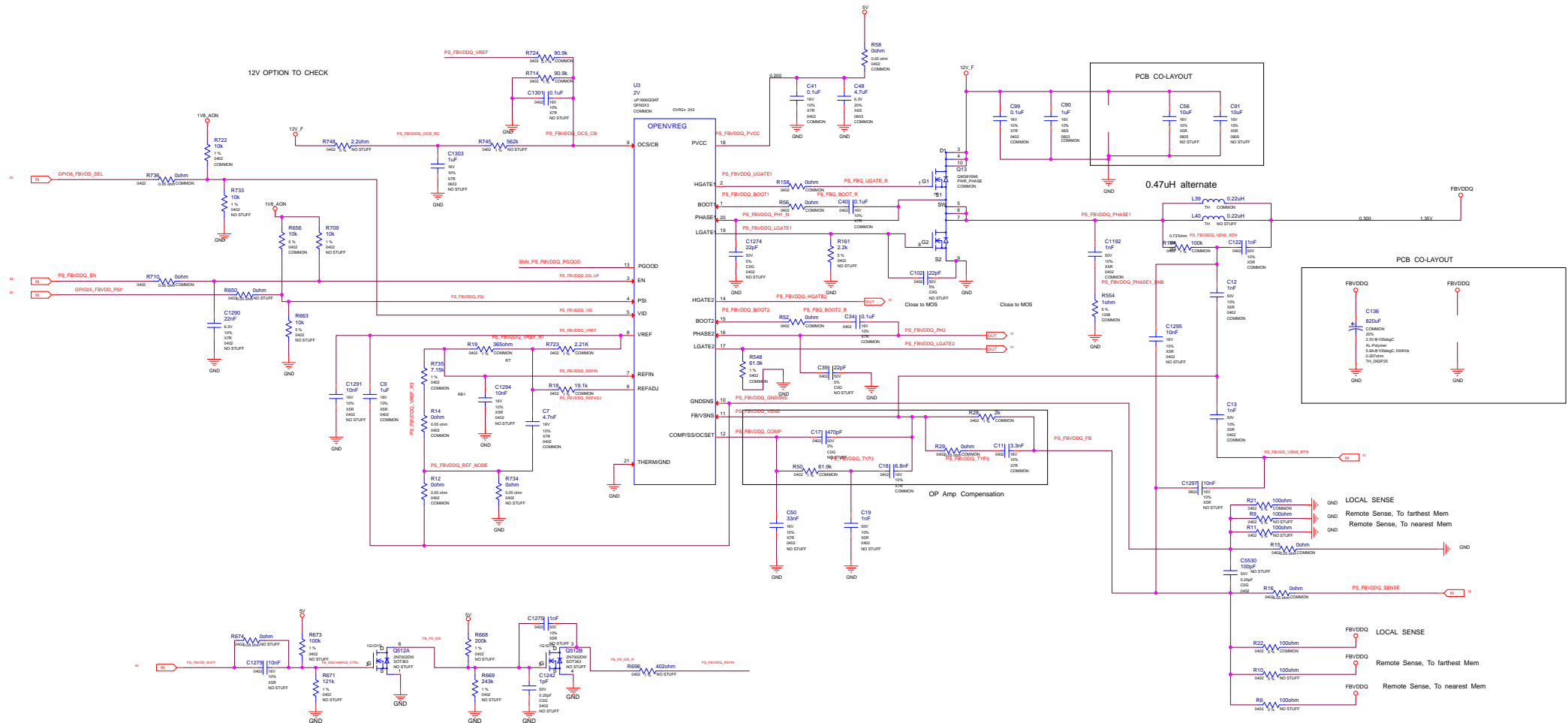
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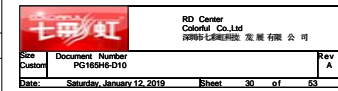
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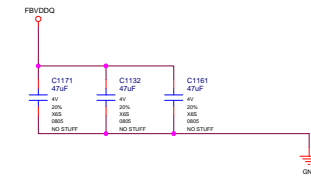
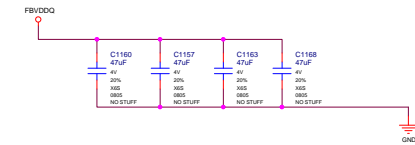
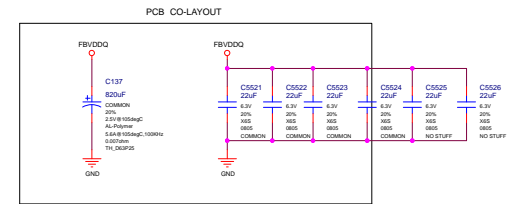
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ASSEMBLY	«ASSEMBLY_DESCRIPTION»
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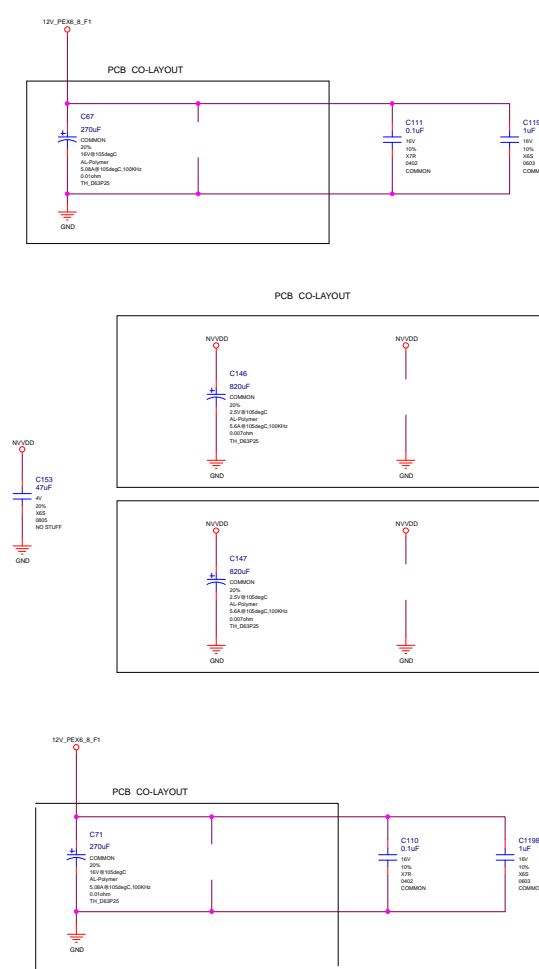


DROP OVR3i SOLUTION


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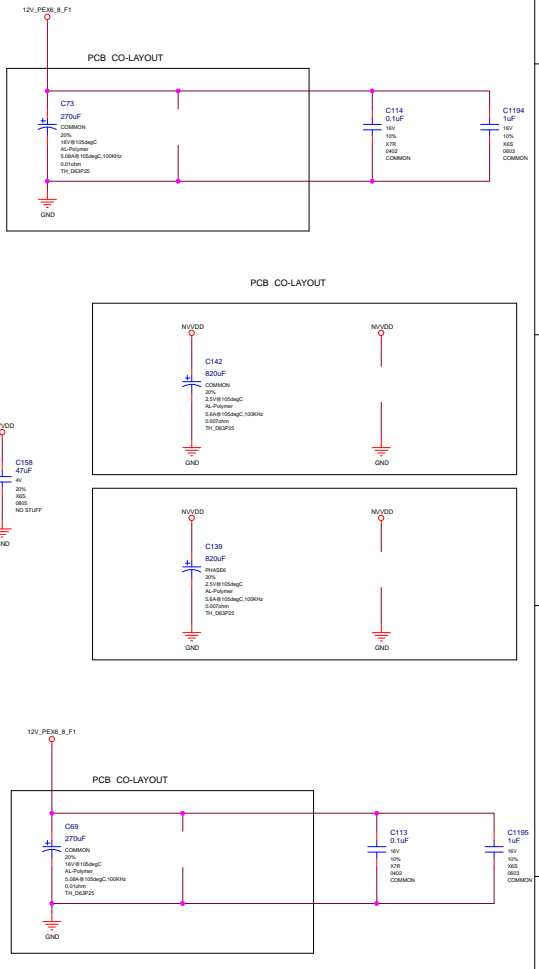
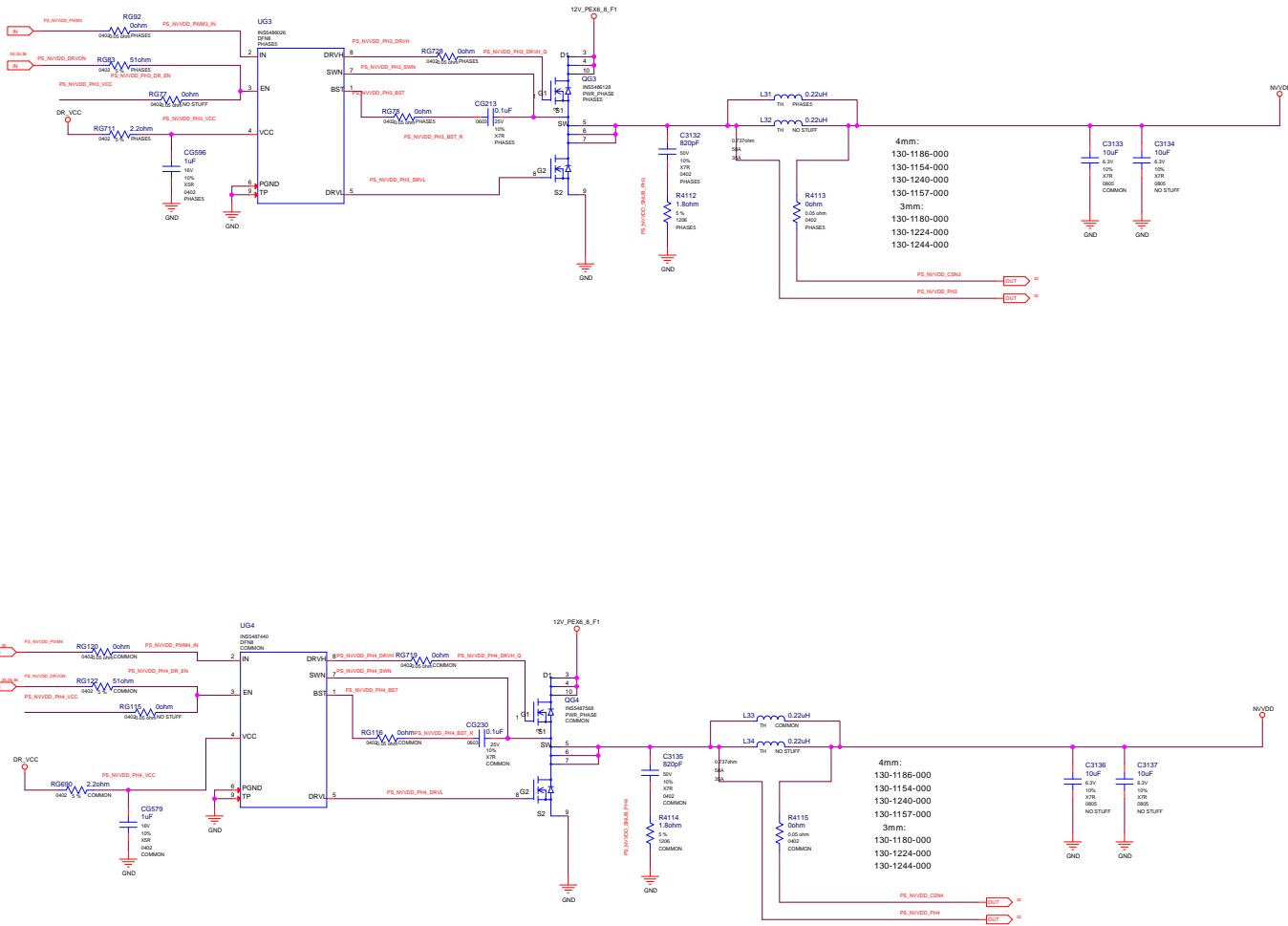
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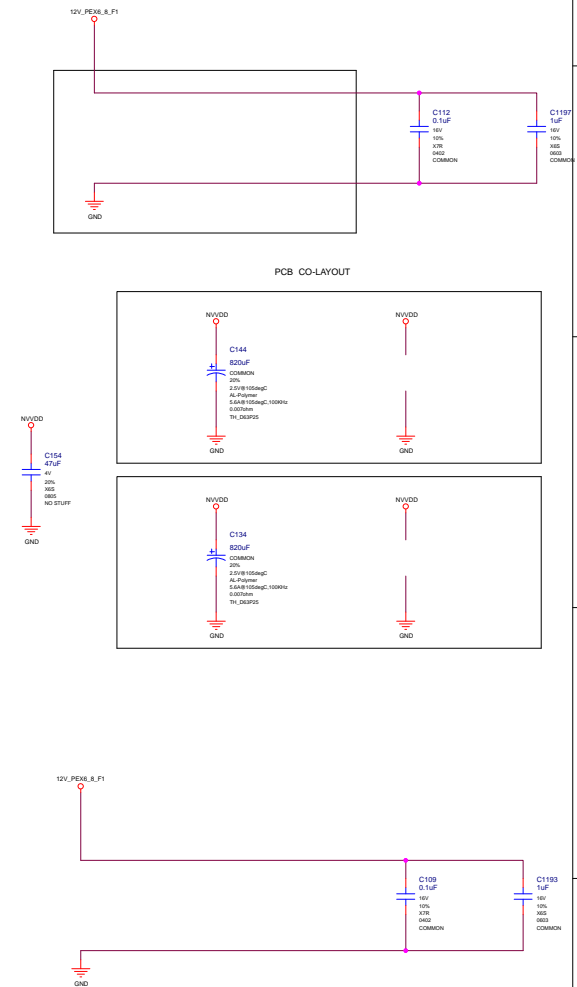


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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: NVDD Phase 2,1

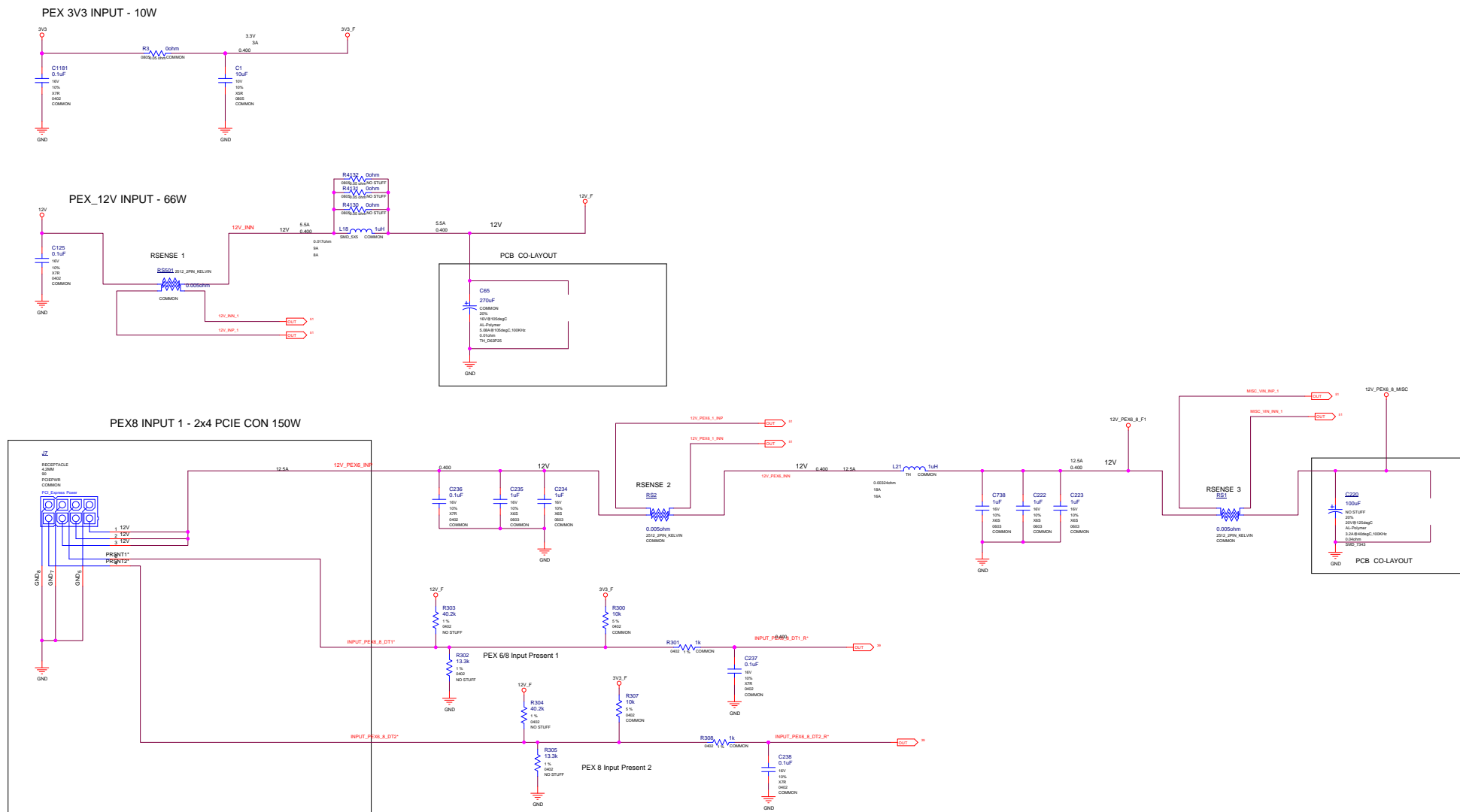
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DELETE RAIL BALANCE CIRCUIT

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ASSEMBLY	<ASSEMBLY DESCRIPTION>
PAGE DETAIL	PS: Input Power Balancing Switcher

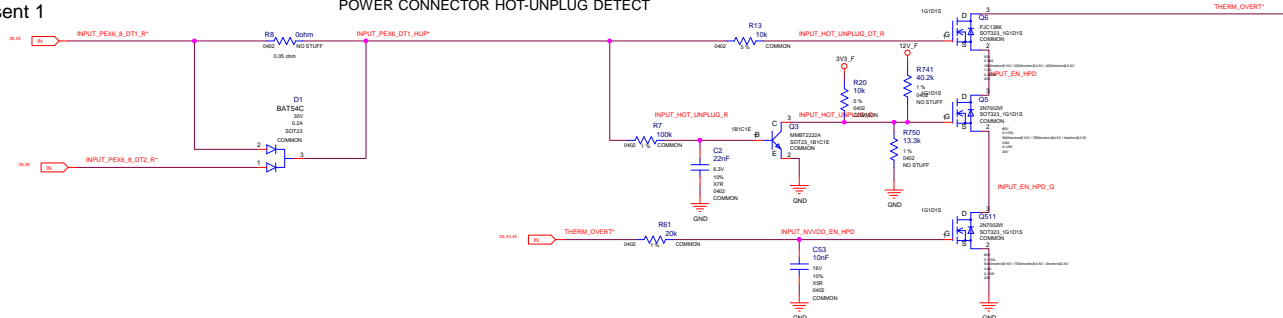
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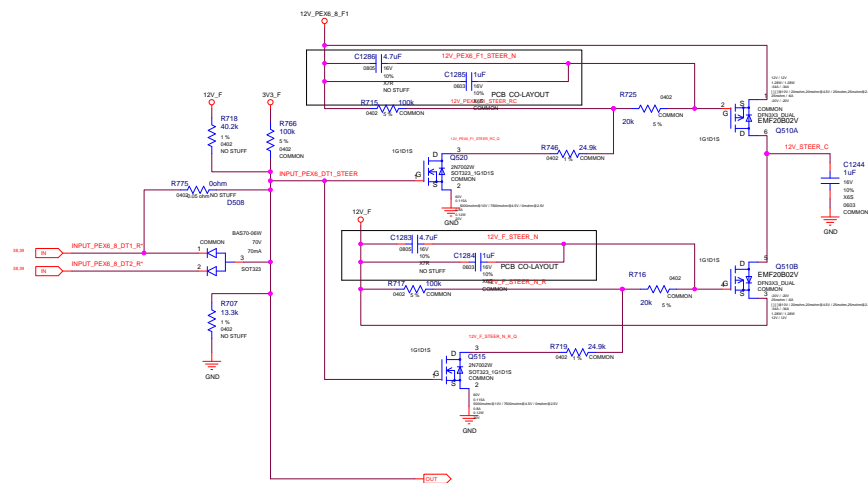
ASSEMBLY	«ASSEMBLY_DESCRIPTION»
PAGE DETAIL	PS: Input, Filtering, and Monitoring

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GUIDES CURRENT FROM PEX EDGE TO PEX 6 PIN INPUT AREA



10/10/15

3  
OUT  
6N139V  
10/10/15  
connection

1  
2  
GND

10/10/15

3  
OUT  
6N139V-1  
10/10/15  
connection

1  
2  
GND

1V8\_AON

R1  
2.2k  
1%  
0603  
COMMON

GPIO12\_LOW\_PERF\*

GPIO12\_LOW\_PERF\*

INPUT\_PEX8\_B\_DT1\_R

INPUT\_PEX8\_B\_DT2\_R

GPIO12_LOW_PERF*	GPU SPEED
0	Slow
1	Normal

ASSEMBLY	«ASSEMBLY_DESCRIPTION»
PAGE DETAIL	PS: STEERING, UPB & HOT-UNPLUG

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ASSEMBLY	<ASSEMBLY DESCRIPTION>
PAGE DETAIL	PS: Type-C BuckBoost

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PAGE DETAIL	PD PPC

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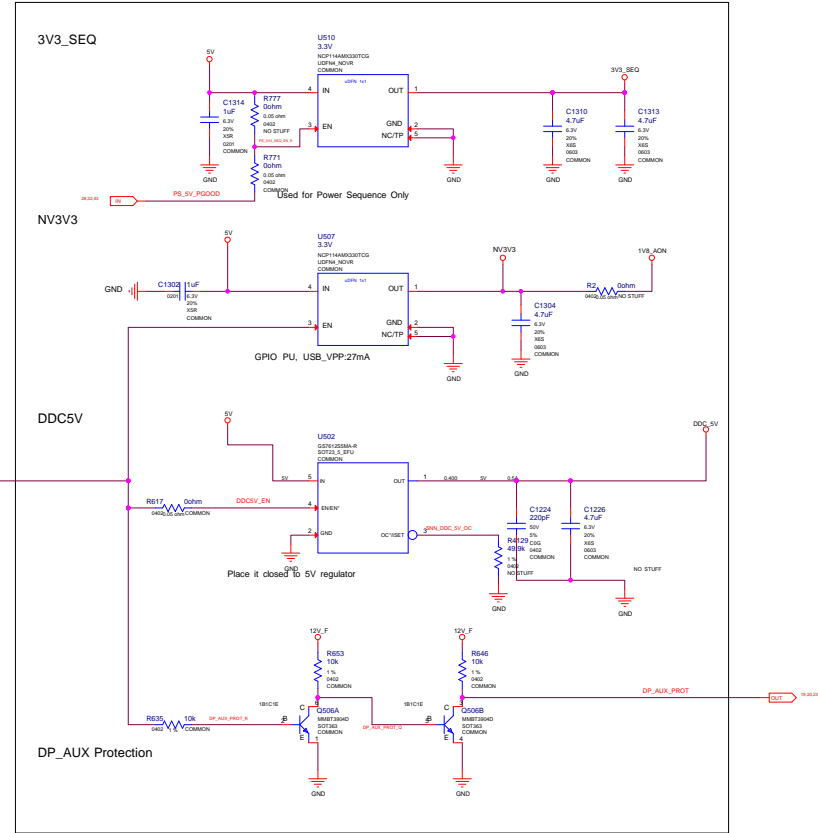
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ASSEMBLY	<ASSEMBLY DESCRIPTION>
PAGE DETAIL	PS: 12V & 3V3_A SWITCHER




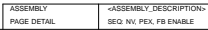
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ASSEMBLY	«ASSEMBLY_DESCRIPTION
PAGE DETAIL	SEQ: 5V, 1V8, NV3V3 ENABLE


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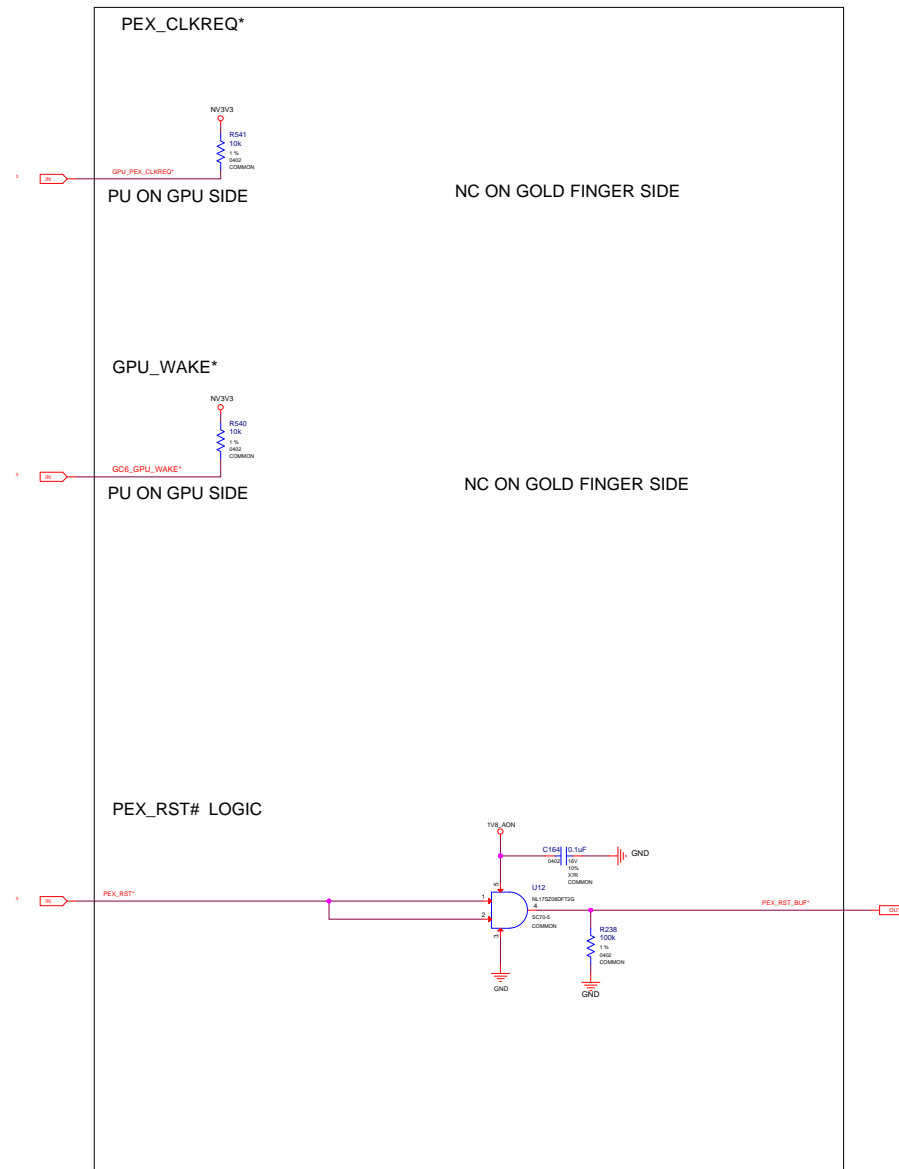
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ASSEMBLY PAGE DETAIL	<ASSEMBLY DESCRIPTION> SEQ. DISCHARGE
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ASSEMBLY	«ASSEMBLY_DESCRIPTION»
PAGE DETAIL	SEQ: MISC

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ASSEMBLY  
PAGE DETAIL

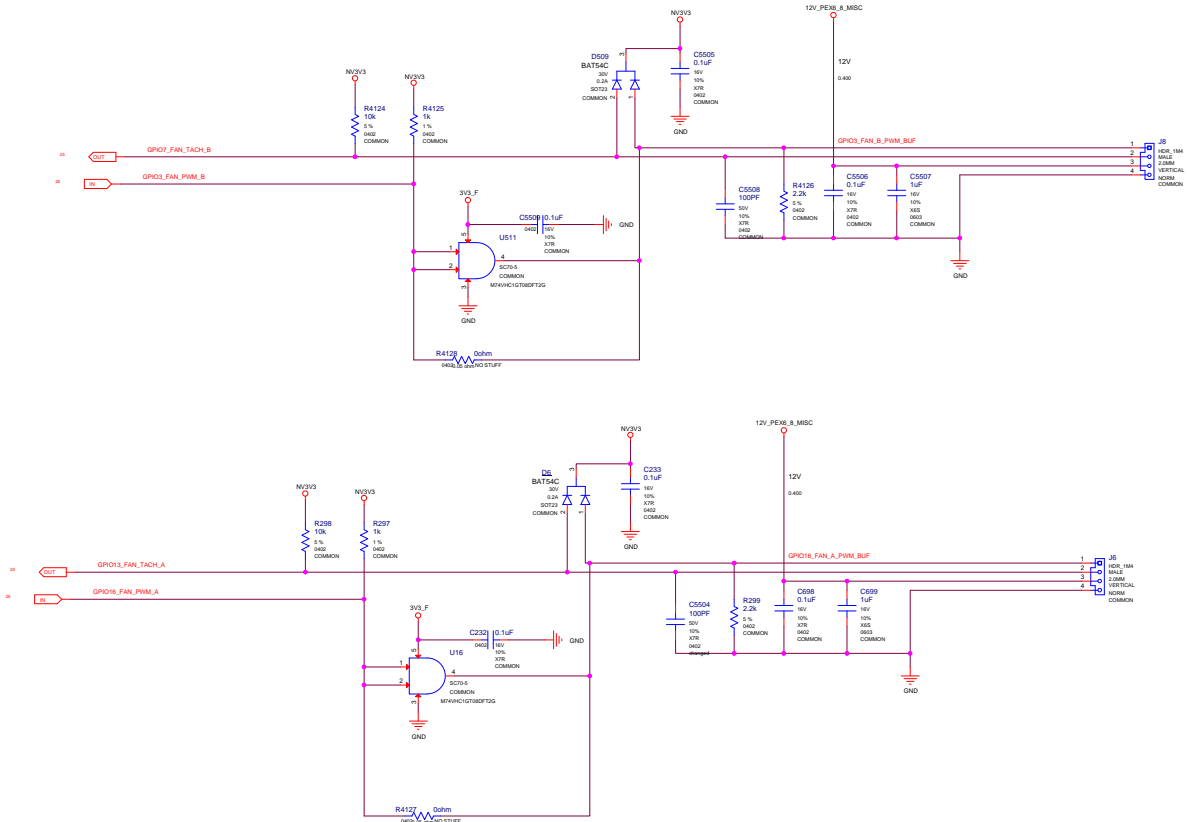
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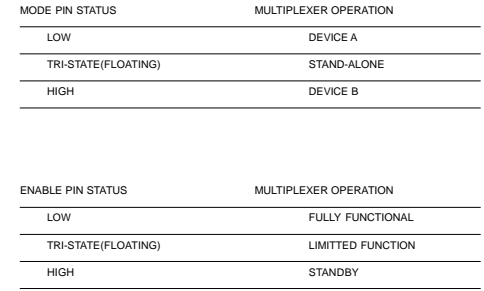
ASSEMBLY	<ASSEMBLY DESCRIPTION>
PAGE DETAIL	LED DRIVER & BOOST

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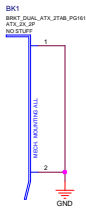
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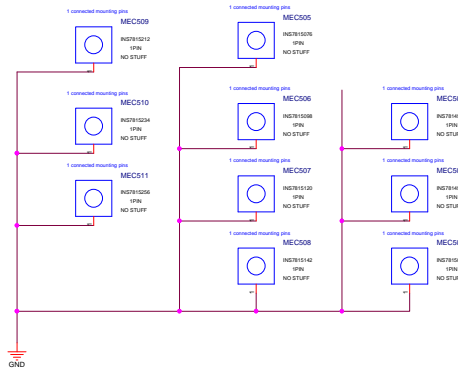
ENABLE PIN STATUS	MULTIPLEXER OPERATION
LOW	FULLY FUNCTIONAL
TRI-STATE(FLOATING)	LIMITED FUNCTION
HIGH	STANDBY

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Bracket:



MOUNTING HOLES FOR HS:




BACK STIFFENER:



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ASSEMBLY	ASSEMBLY DESCRIPTION
PG161 DETAIL	MECH

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ASSEMBLY	<ASSEMBLY DESCRIPTION>
PAGE DETAIL	PTC



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