NV11, 2MX32 SDR, RGB, INTERNAL DVI-I, AGP.

PCI DEVICE ID 0X0=0X110 FOR NV11.

STRAPS: AGP4X, SIDEBAND DIS, FAST WRITE ENA, ADP BIOS, NORMAL PCIAD, 14.318 MHZ STRAP_RAM_TYPE [5:2]=0000-2MX32 SDR SDRAM

HISTORY REV. COO:

- 1) C00: Implemented Separate DFP_VREF and DFP_RSET for each TMDS channel.
- 2) COO: Added RC networks for FBDQS and FBCLK.
- 3) C00: Fixed 64 bit memory interface.
- 4) COO: Added more bulk decoupling caps to 3.3V rail. 5) COO: Added alternative footprint for D800 and C808.
- 6) C00: Changed HSYNC/VSYNC caps from 220pf to 22pf.
- 7) C00: Recalculated DFP hi frequency caps to have RSF from $65\,\mathrm{mhz}$ to $108\,\mathrm{mhz}$.

602-P0039-0000-C00

NVIDIA Corporation 3535 Monroe St Santa Clara, CA 95051, USA NVIDIA Thursday, July 26, 2001



	5	4	3	2	1	
	BRACKET2 10K					
D						D
	SCREW <u>-2</u> 10K					
	SCREW <u>-1</u> 2 10K					
_						-
2						С
•						•
3						В
Ą						-
				Title <title></td><td></td><td></td></tr><tr><td></td><td></td><td></td><td></td><td>Size Document Number
A < Doc></td><td>Rev
<</td><td>v
<RevC</td></tr><tr><td></td><td>5</td><td>4</td><td>3</td><td>Date: Thursday, July 26, 2001</td><td>Sheet 1 of 1</td><td></td></tr></tbody></table></title>		





















