

P262: NV44/G3-64, TSOP
MEMORY x16

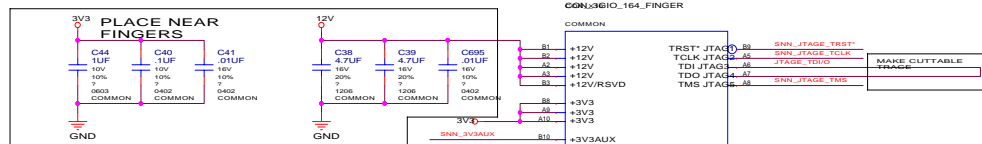
REV
HISTORY

A01

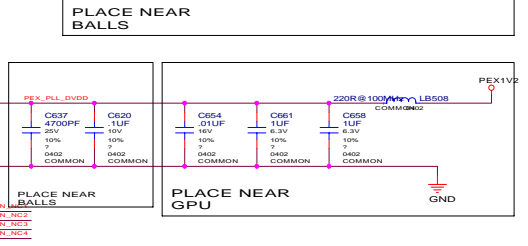
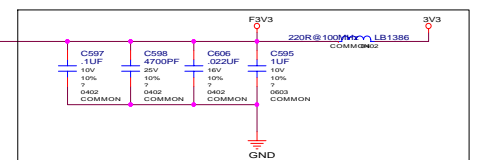
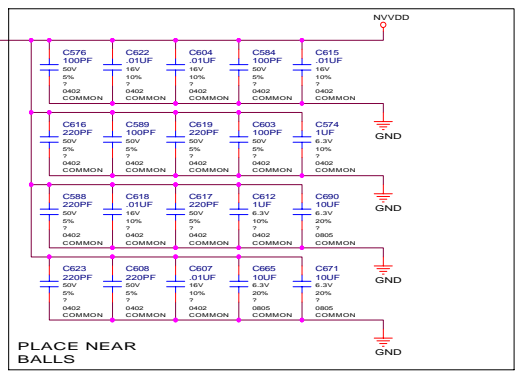
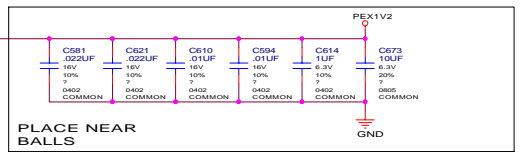
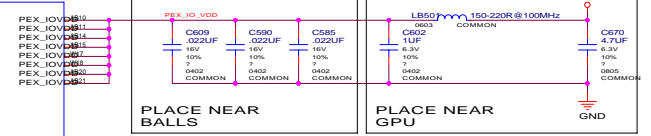
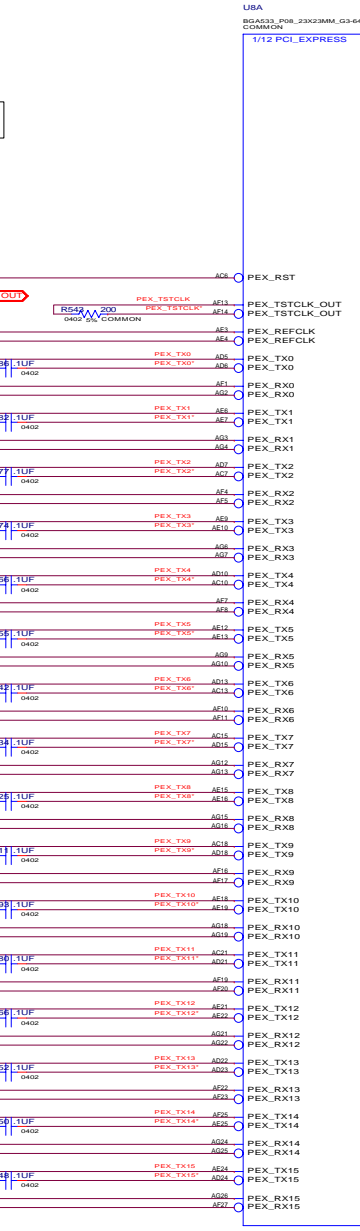
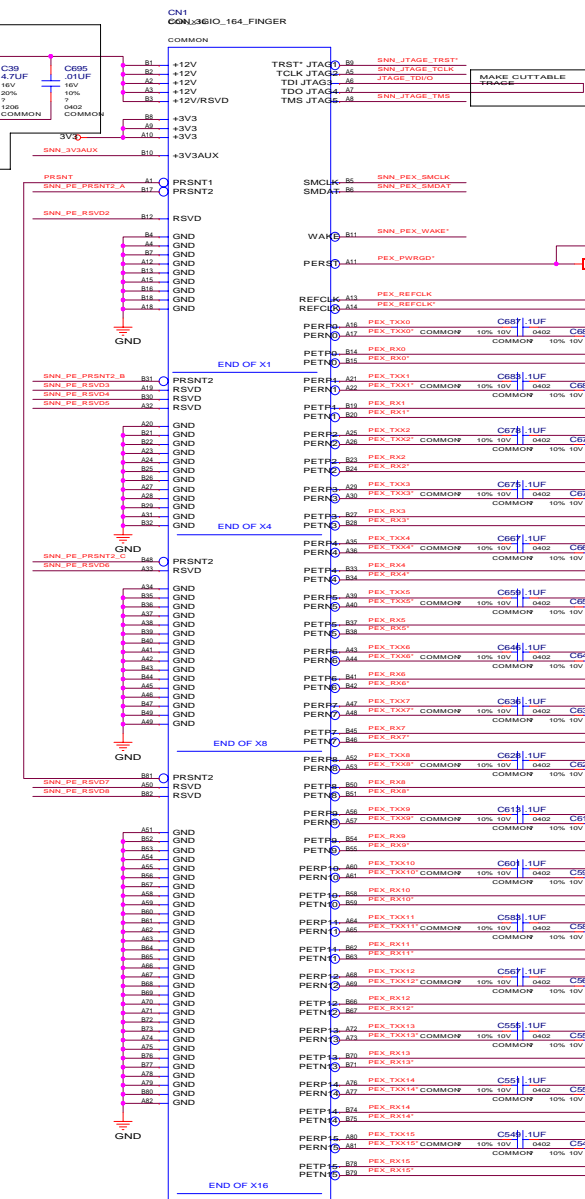
- 9/22/2004:
- Changed TMDS_PLLVDD to PLLVDD
 - Added bead option to PLLVDD rail of NV44
 - Changed RSET values for DACA and DACB
 - Changed DACA RSET FET to a dual package with GPIO11 control-Macrovision
 - Added F3V3 bypass to PLLVDD linear regulator
 - Changed location of R62

REV	VARIANT	NVPN	ASSEMBLY
B	BASE	600-10ppp-xxxx-ww	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES
1	0000	600-10262-0000-000	UNDEFINED FINAL
2	0001	600-10262-0001-000	<UNDEFINED>
3	0002	600-10262-0002-000	<UNDEFINED>
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PEX-Interface



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IN	PEX_T0TCLKN	PEX_T0T	25MM
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GPU: FB-Interface

UBB

8GA533_P0B_23X23MM_G3-64

COMMON

2712 FRAME_BUFFER

FB_VTT_E15

FB_VTT_E16

FB_VTT_E17

FB_VTT_E18

FB_VTT_E19

FB_VTT_E20

FB_VTT_E21

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FB_VTT_E157

FB_VTT_E158

FB_VTT_E159

FB_VTT_E160

FB_VTT_E161

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FB_VTT_E308

FB_VTT_E309

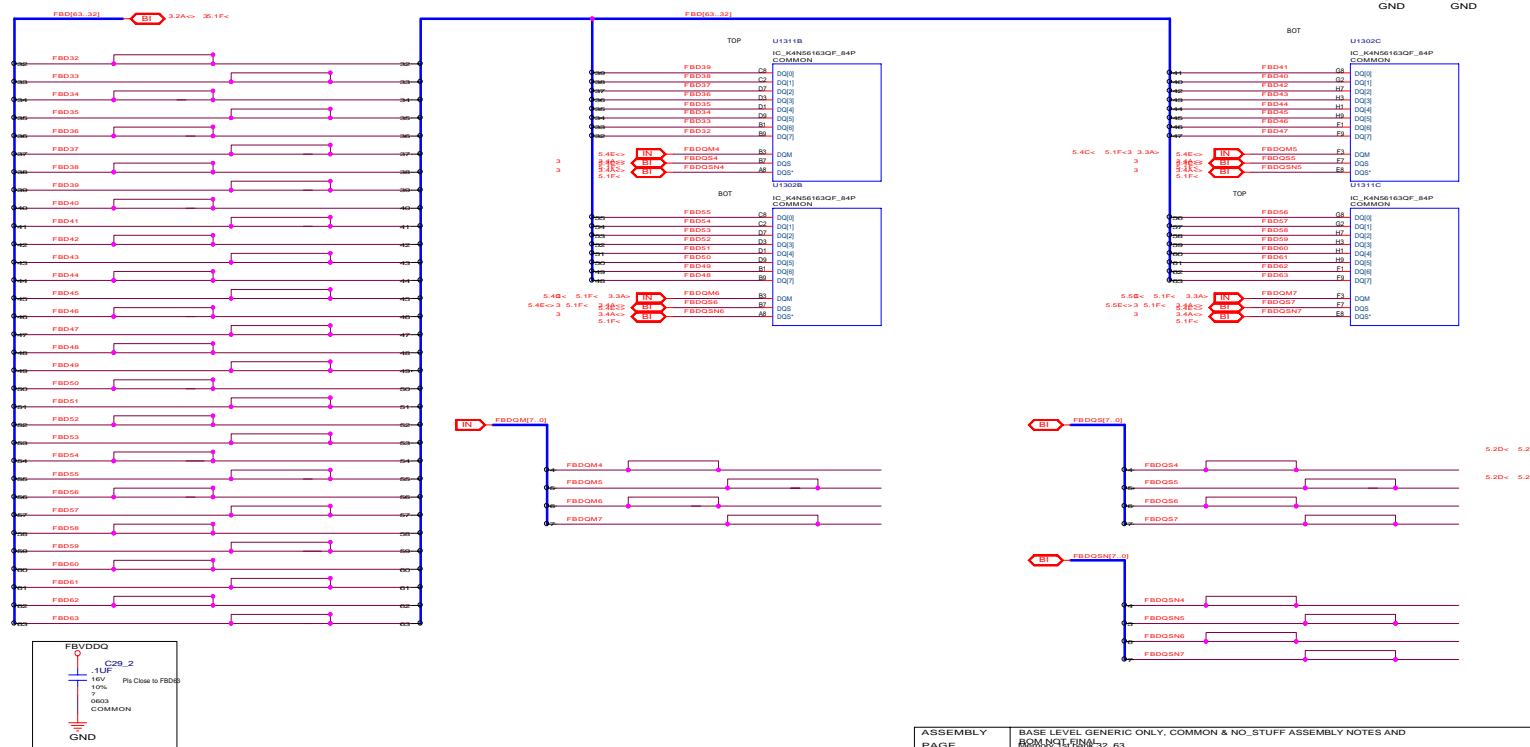
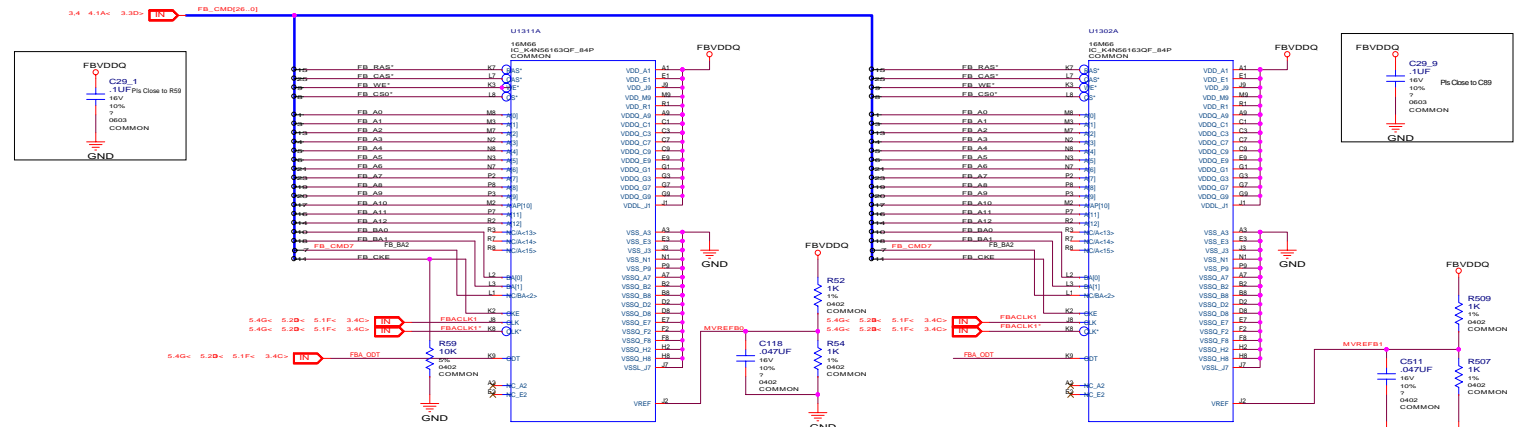
FB_VTT_E310

FB_VTT_E311

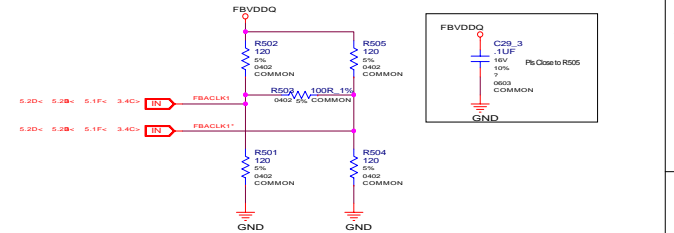
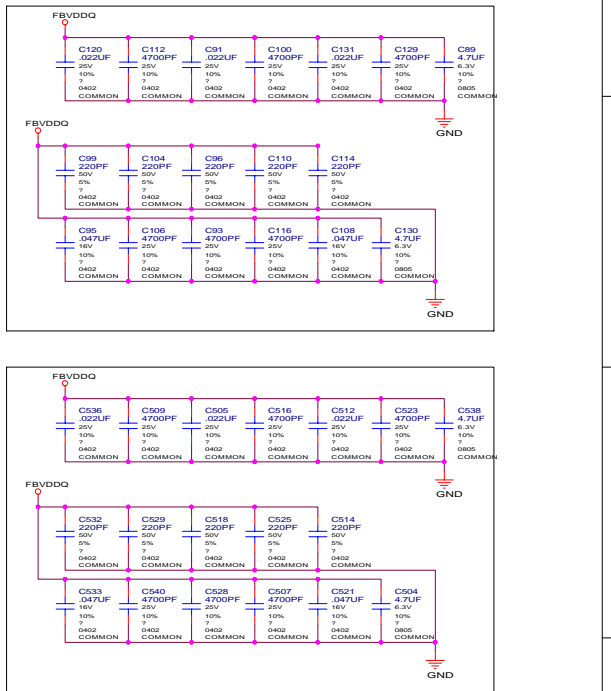


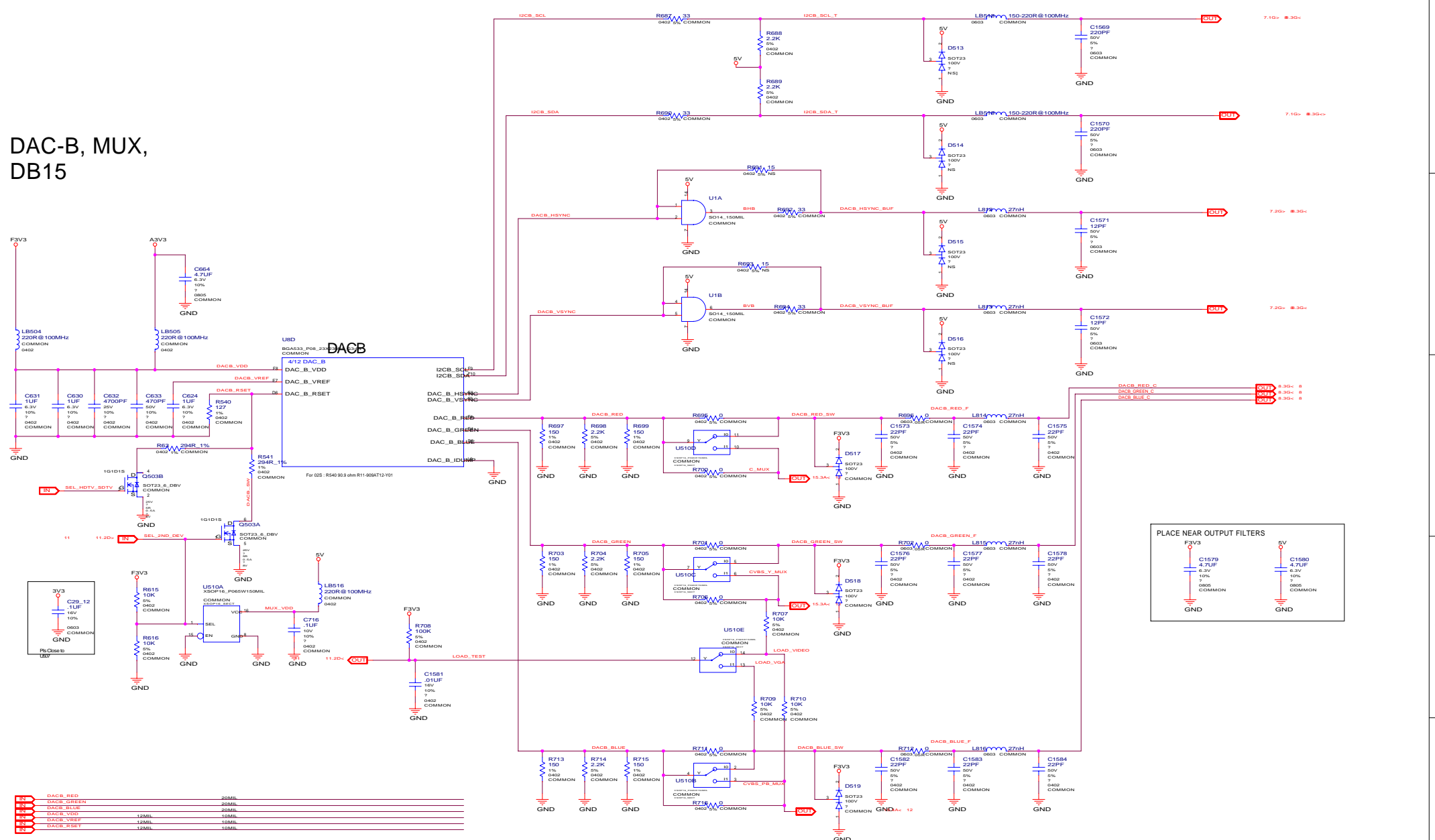
Figure 1: Schematic diagram of the FBVDDQ circuit. The diagram shows two main sections of the circuit, each connected to FBVDDQ and GND. The top section includes capacitors C537 through C539 and C517 through C531. The bottom section includes capacitors C535 through C537 and C519 through C513. A detailed view of the FBVDDQ pin connection is shown in a callout box, indicating the connection to GND and the placement of a 100pF capacitor.

Memory Bit 32..63



Net	Diffpair	NET_SPACING_RULE
IN	FBACLK1	25MMIL
IN	FBACLK1	25MMIL
IN	FBDOS[9..9]	10MMIL
IN	FBDOSM[9..9]	10MMIL
IN	FBDOSG[9..9]	10MMIL
IN	FBDOSM[7..9]	10MMIL



DAC-B, MUX,
DB15

PLACE NEAR OUTPUT FILTERS

The image shows two circuit diagrams illustrating the placement of output filters near power supply rails. Both diagrams show a capacitor connected between a power rail and ground (GND).

Left Diagram (3V3 Rail):

- Power Rail: 3V3
- Capacitor: C1579, 4.7uF, 10%, 0805, COMMON
- Ground: GND

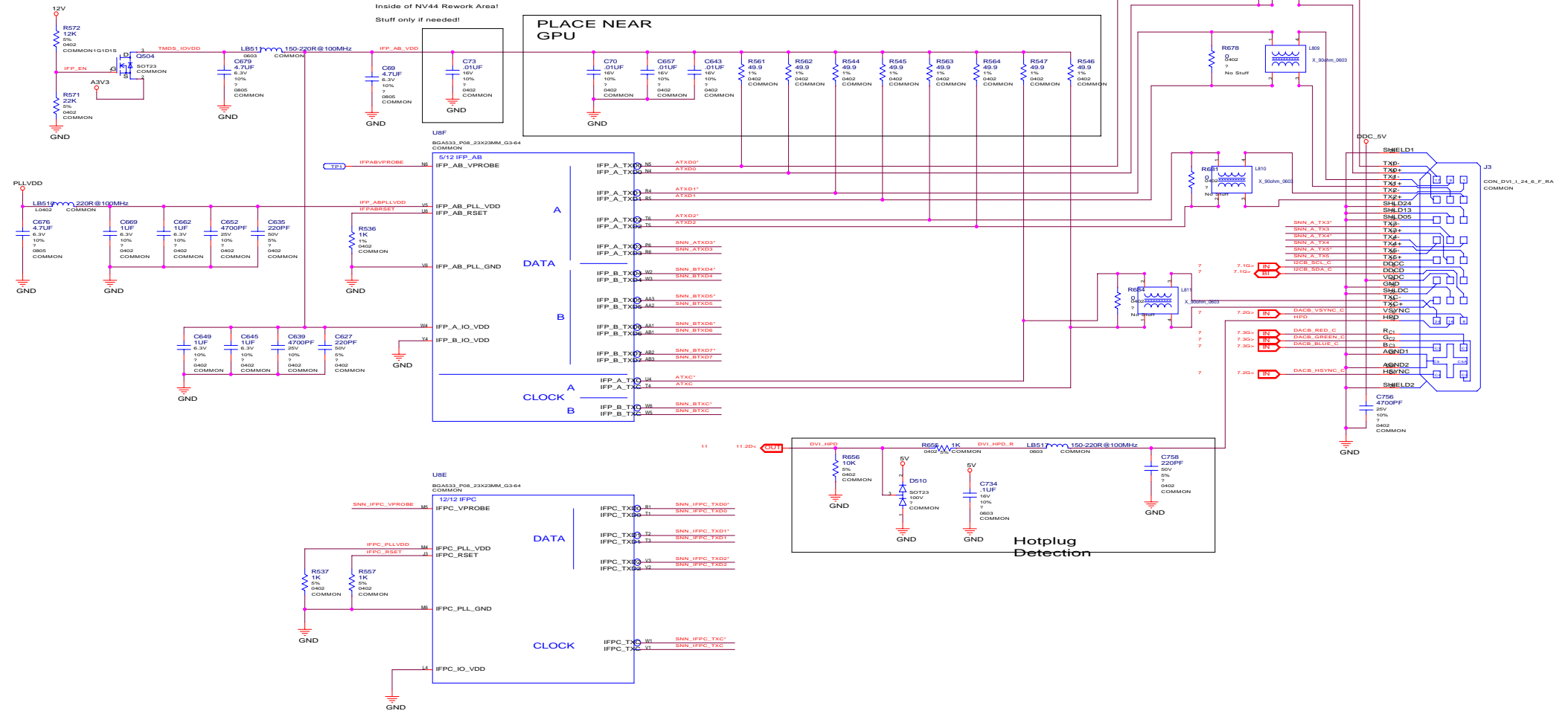
Right Diagram (5V Rail):

- Power Rail: 5V
- Capacitor: C1580, 4.7uF, 10%, 0805, COMMON
- Ground: GND

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Internal TMDS, DVI-Connector

Net Name	Diffpair	NET_SPACING_RULE	Voltage
RP_2A01_VDD			3.3V
RP_AB_VDD			3.3V
ATX01	ATX0	250M	
ATX02	ATX0	250M	
ATX03	ATX00	250M	
ATX04	ATX00	250M	
ATX05	ATX01	250M	
ATX06	ATX01	250M	
ATX07	ATX02	250M	
ATX08	ATX02	250M	

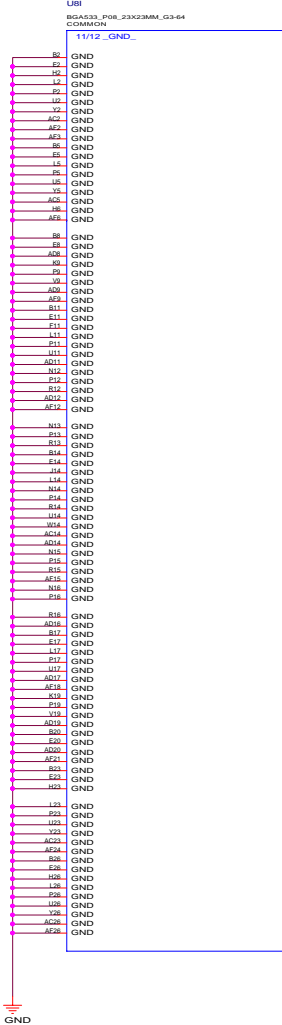
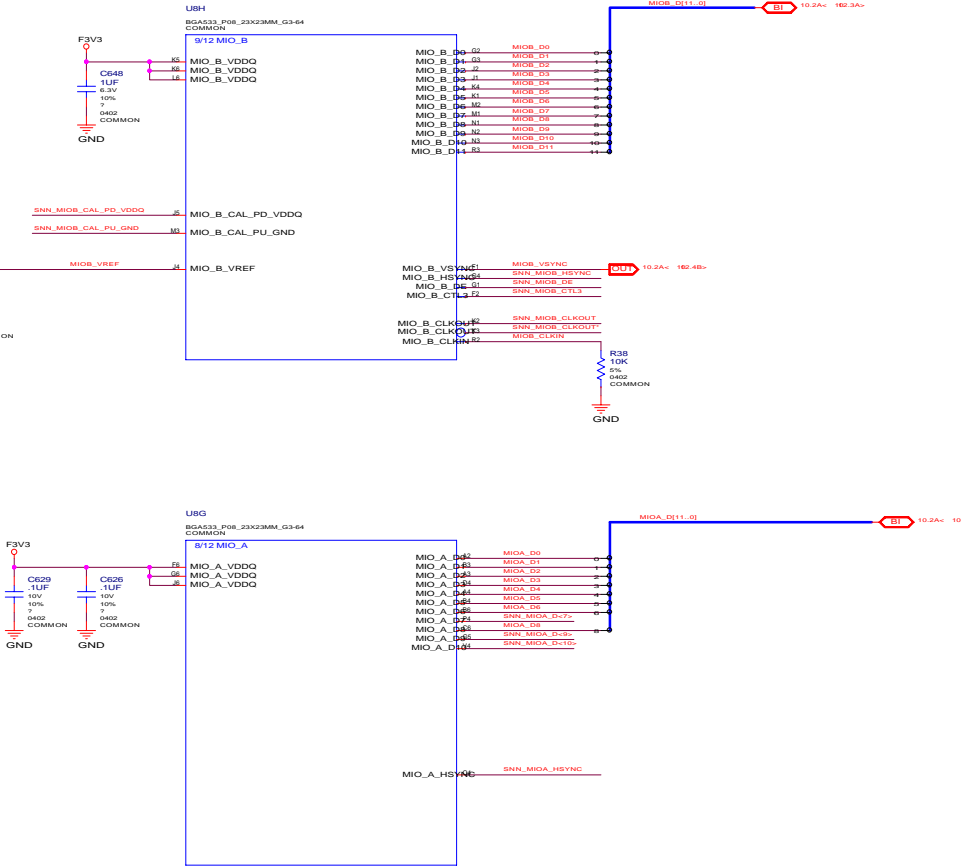


ASSEMBLY	BASE LEVEL GENERIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND
PAGE	BOM NOT FINAL
	TMDS

V034

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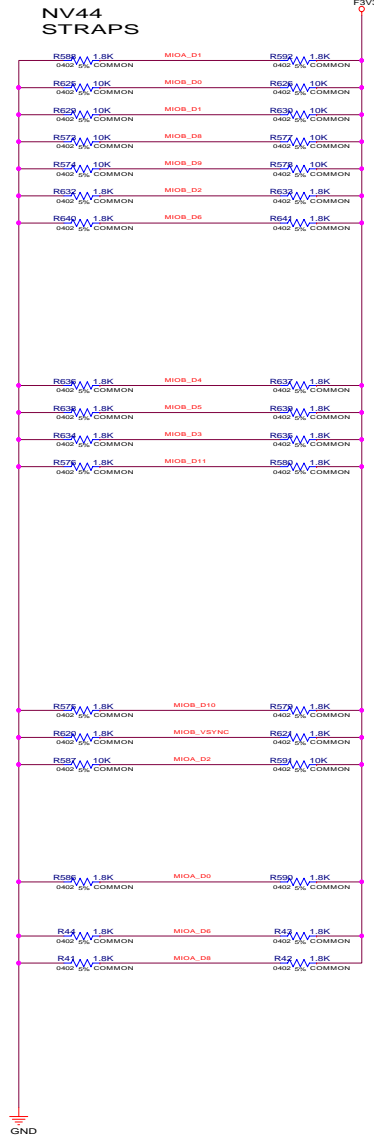
MIOA, MIOB
Interface,
LPC-ROM



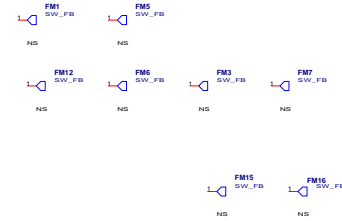
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ASSEMBLY	LPC_ROM
PAGE	MIOA, MIOB Interface.

STRAPS, Mechanical Parts



Bit	Signal	Values
00:	PCI_AD_SWAP	0 REVERSED 1 NORMAL
01:	SUB_VENDOR	0 VENDOR FROM 1 VENDOR FROM
02:	RAM_CFG_0	0000 VEH 0001 8Mx16DDR VEH 0010 VEH 0011 VEH 0100 VEH 0101 VEH 0110 VEH 0111 VEH 1000 VEH 1001 16Mx16DDR VEH 1010 VEH 1011 VEH 1100 RPU 1101 RPU 1110 RPU 1111 RPU
03:	RAM_CFG_1	
04:	RAM_CFG_2	
05:	RAM_CFG_3	
06:	CRYSTAL_0	00 13.500 MHz 01 14.31818 MHz 10 19.200 MHz 11 UNKNOWN
07:	TV_MODE_0	00 SFCAM 01 WFCAM 10 WFL CRT 11 CRT
08:	TV_MODE_1	
09:	AGP_30_8x	0 AGP8x 1 DISABLED
10:	AGP_SBA	0 SBA 1 DISABLED
11:	AGP_FASTWR	0 FW 1 DISABLED
12:	PCI_DEVID_0	1100 (default 0x00FC)
13:	PCI_DEVID_1	
20:	PCI_DEVID_2	
21:	PCI_DEVID_3	
14:	BUS_TYPE	0 PCI 1 AGP
15:	FP_IFACE	0 24BR 1 12BR (DEFAULT)
23:	FB_0	00 01 8M 1000 1280M (DEFAULT) 11M
24:	FB_1	
25:	BR	0 BRIDGE 1 DISABLED
26:	BR_128M	BR RATS IGNORED IF BRIDGE IS DISABLED
27:	BR_AGP	
28:	BR_IO	
29:	ROM_TYPE_0	00 PARALLEL 01 SERIAL_AT25F 10 SERIAL_SST45VF 11 LPC
30:	ROM_TYPE_1	
16:	USER_0	0000 (DEFAULT)
17:	USER_1	
18:	USER_2	
19:	USER_3	
PEX_PLL_EN_TERM100		
3GIO_PADCFG_LUT_ADDR0[]		
3GIO_PADCFG_LUT_ADDR1[]		



PCB
MEC_PH_4-40_SCREW
COMMON

MEC5
MEC_PH_4-40_SCREW
COMMON

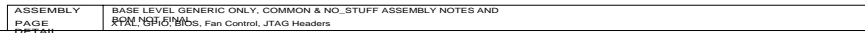
MEC3
MEC_HEX_JACK_SCREW
COMMON

MEC4
MEC_HEX_JACK_SCREW
COMMON

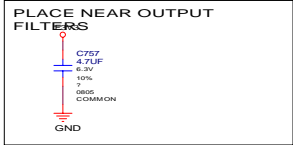
MEC1
MEC_HEX_JACK_SCREW
COMMON

MEC2
MEC_HEX_JACK_SCREW
COMMON

F	G	H												
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SMI_TRACE	20MIL													
SMI_TRACE	_____													
12MIL_TRACE	10MIL													
12MIL_TRACE	_____													
DISP_PLYVD	10MIL													
DISP_PLYVD	_____													



A	B	C	D	E	F	G	H
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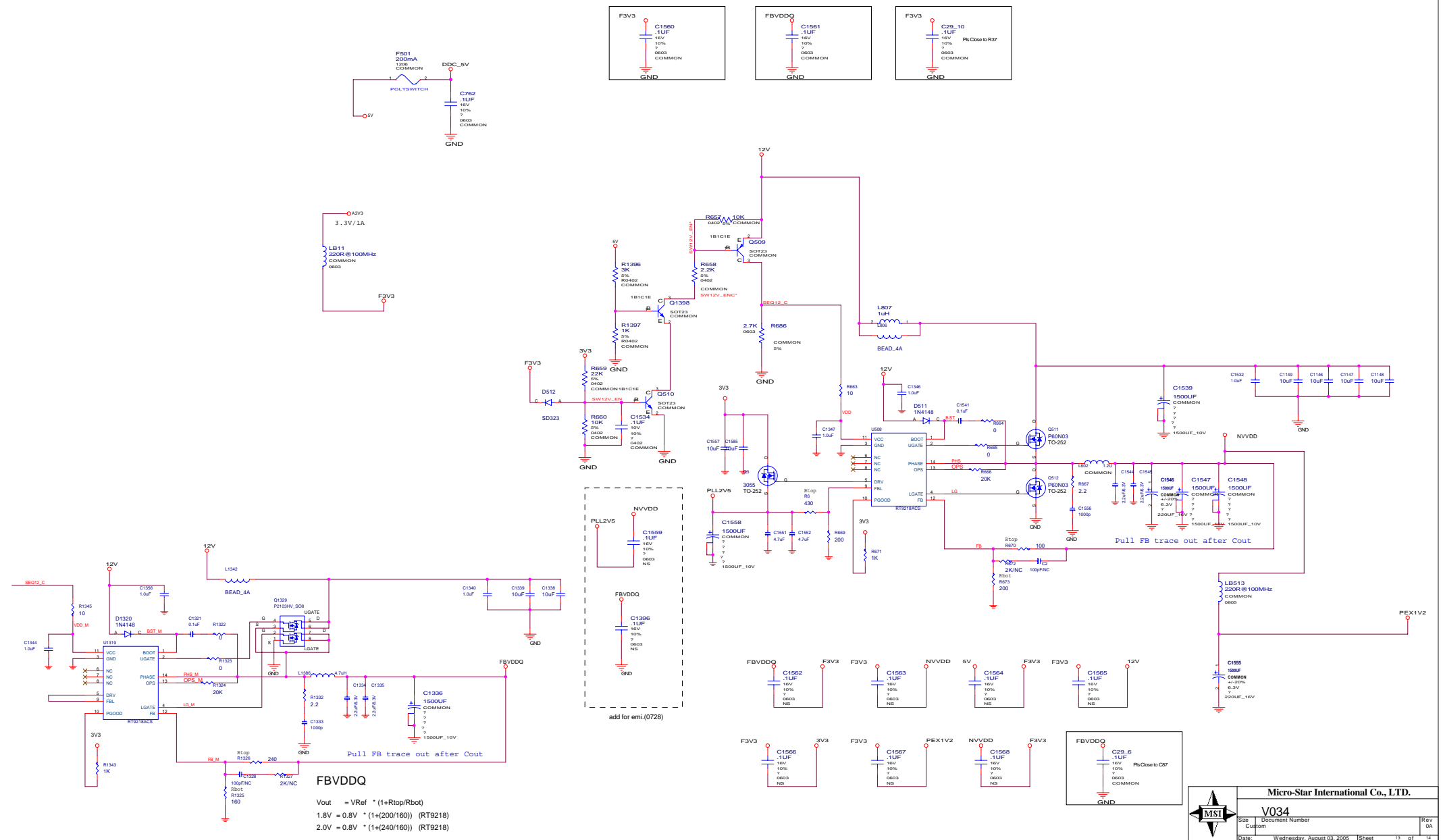


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DETAIL	PRO CONNECTORS: MiniDIN, 2x6
	HCR

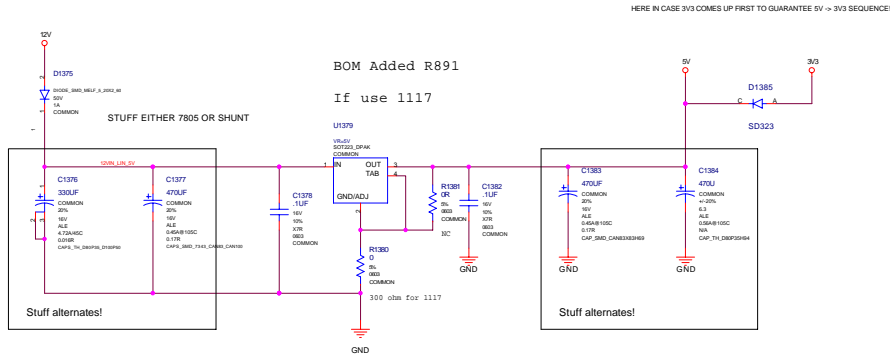
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A	B	C	D	E	F	G	H
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PowerSupplyII: 5V, DDC5V, A3V3, F3V3, TMD5_PLLVDD

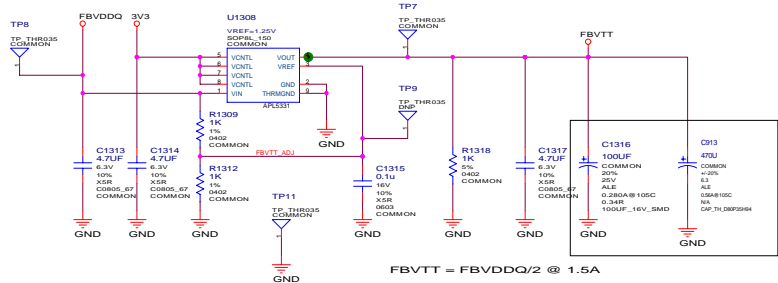


14 Others Power Supply (Linears)
5V,FBVDDQ,A3V3,3V3,TMDS_PLLVDD,TMDS_IOVDD,FBVTT

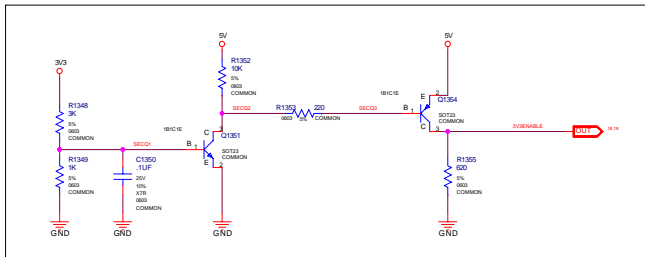


5V LOW COST REGULATOR

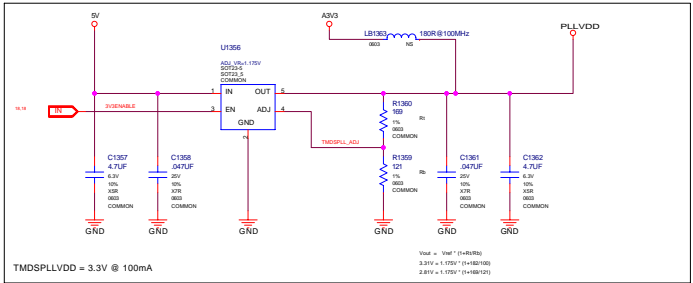
FBVTT TERMINATION



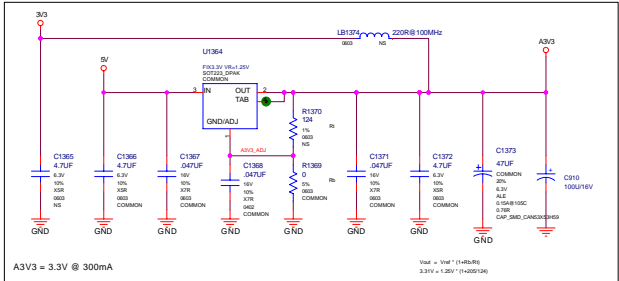
Power Sequencing



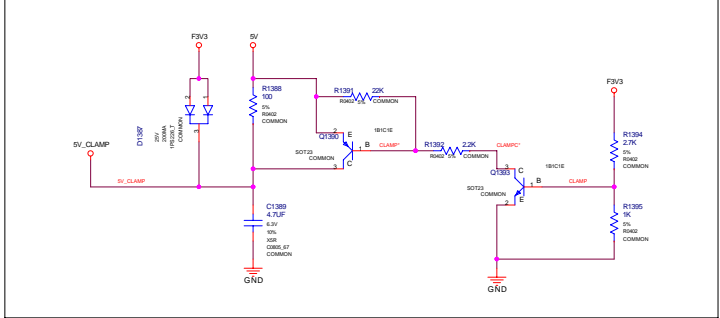
TMDS PLL Supply



A3V3 Power Supply



5V_CLAMP



Micro-Star International Co., L.T.D.

V034

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Date: Wednesday, August 03, 2005

Rev 0A

Sheet 14 of 14