
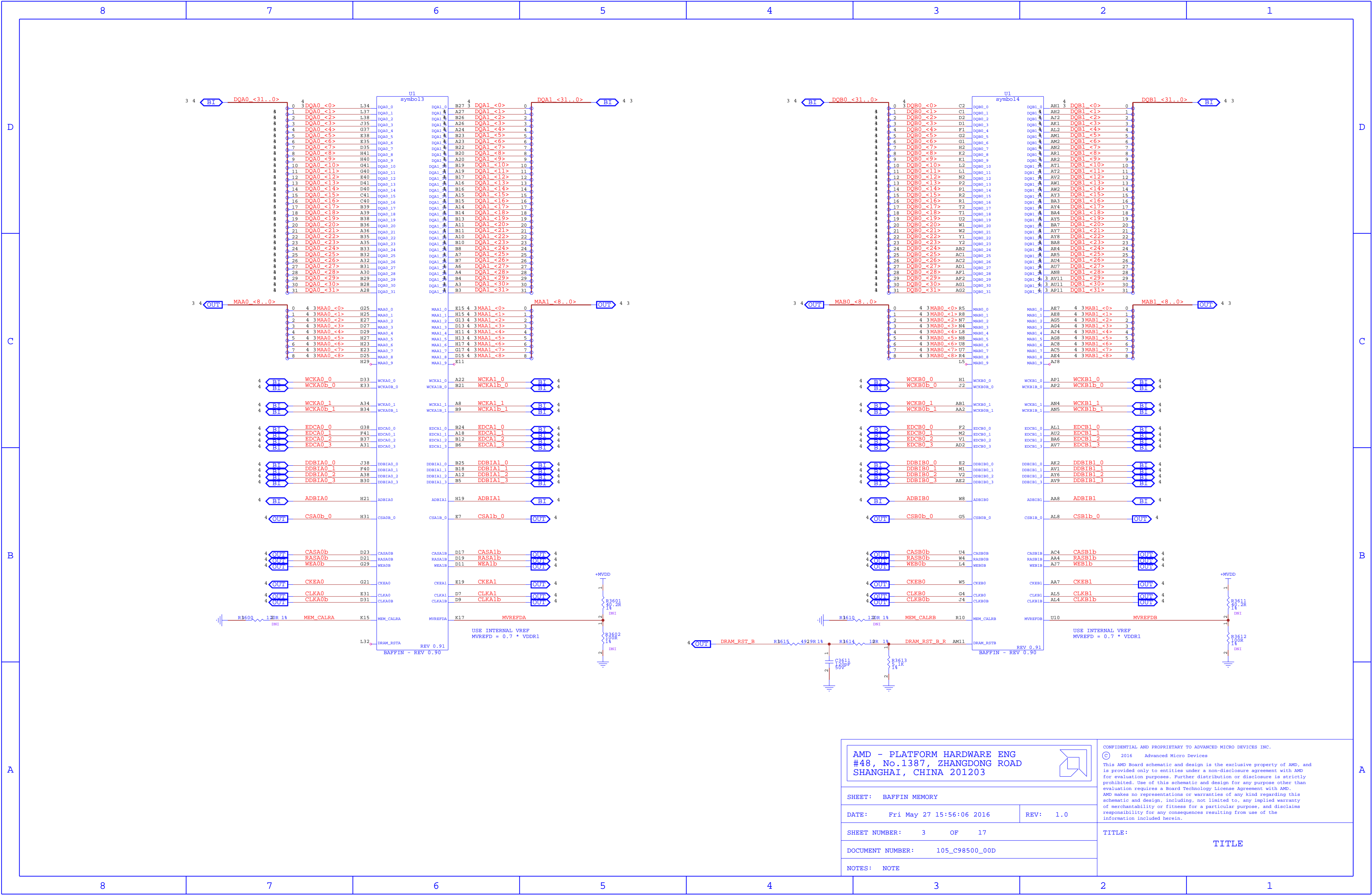


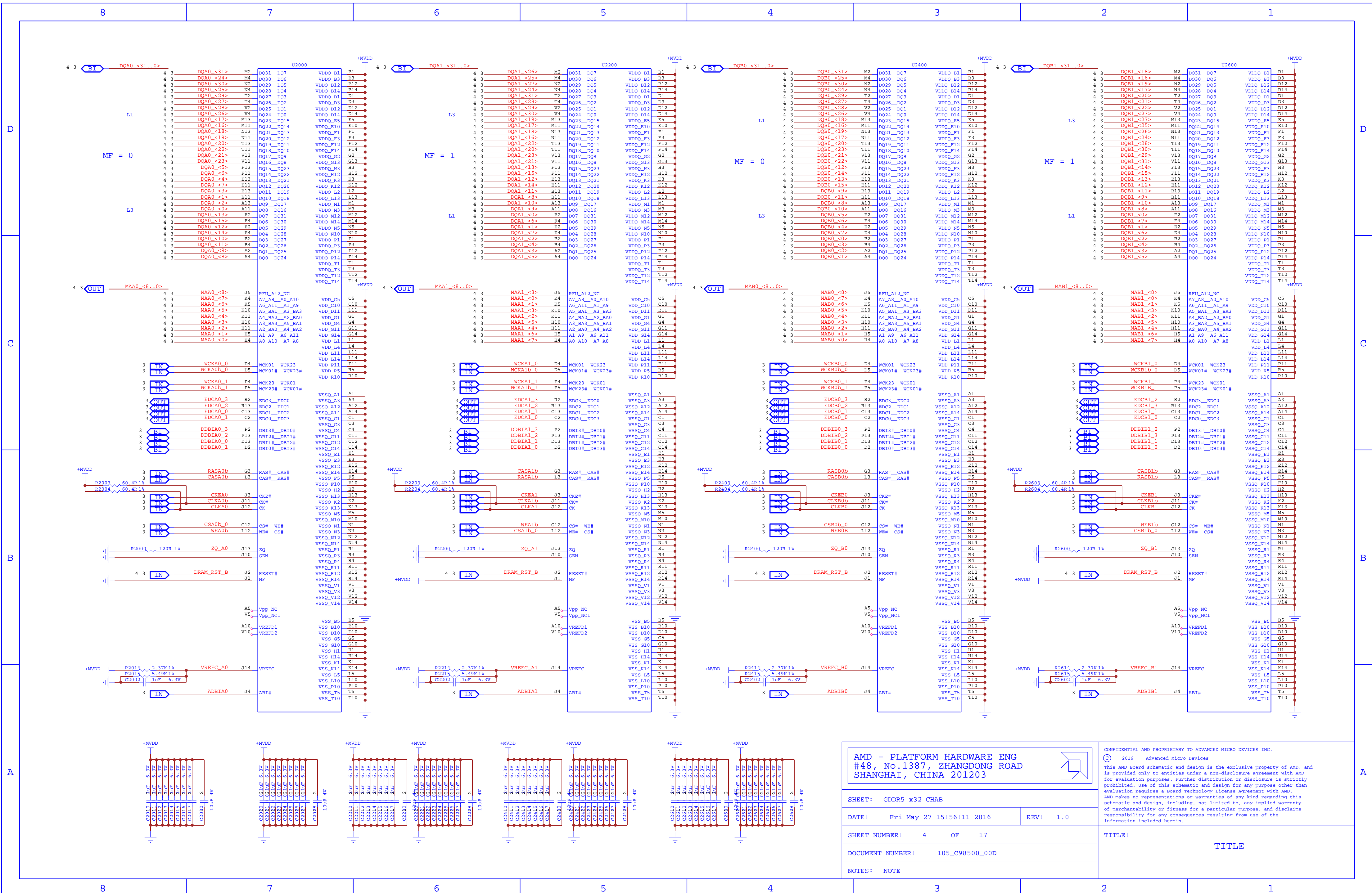
8	7	6	5	4	3	2	1
D							
C							
B							
A							
8	7	6	5	4	3	2	1

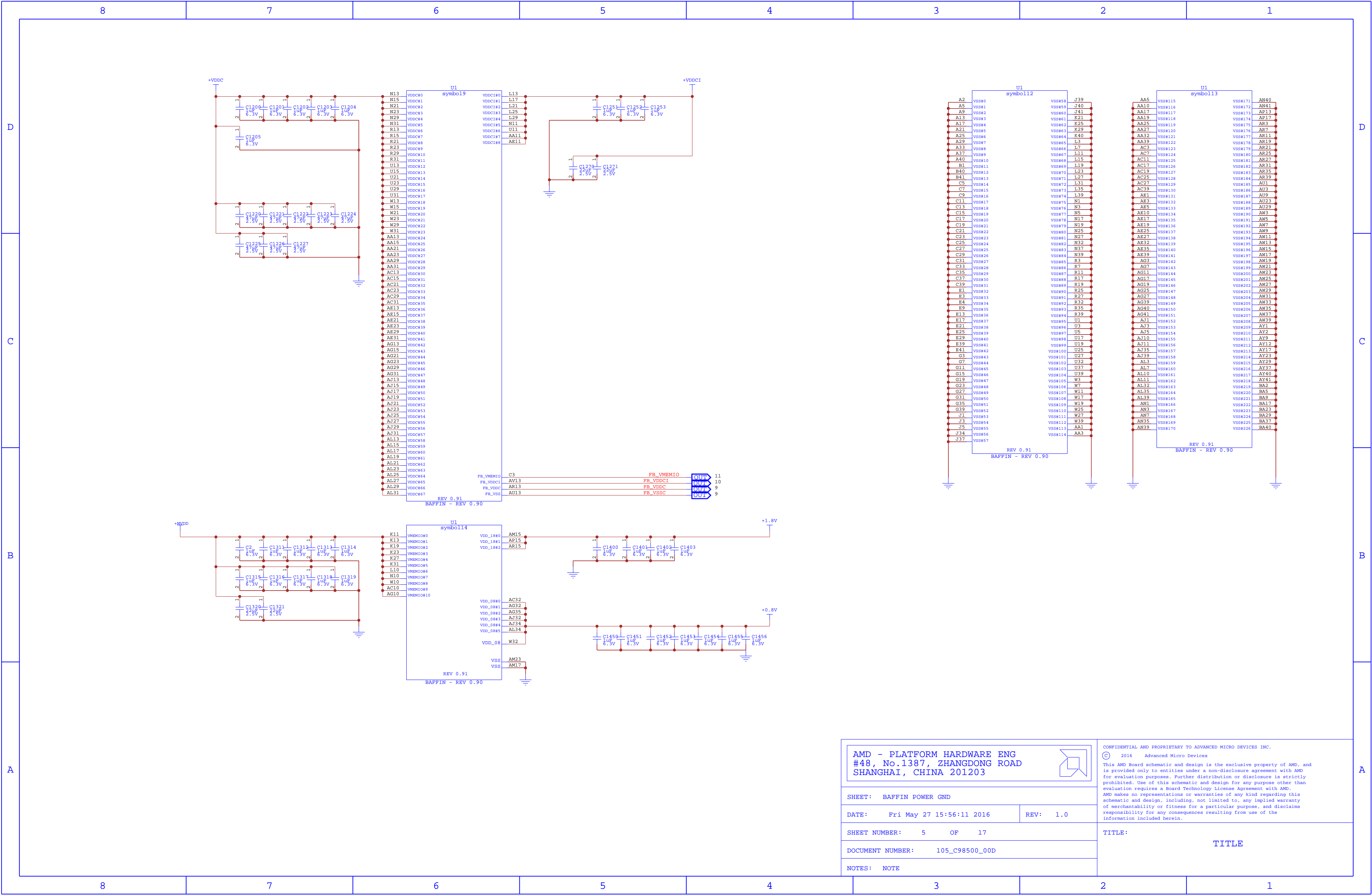
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SHEET: TOC			
DATE: Wed Sep 09 16:50:44 2015	REV: 1.0		
SHEET NUMBER: 1 OF 17		TITLE:	
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SHANGHAI, CHINA 201203

SHEET: BAFFIN POWER GND

DATE: Fri May 27 15:56:11 2016

SHEET NUMBER: 5 OF 17

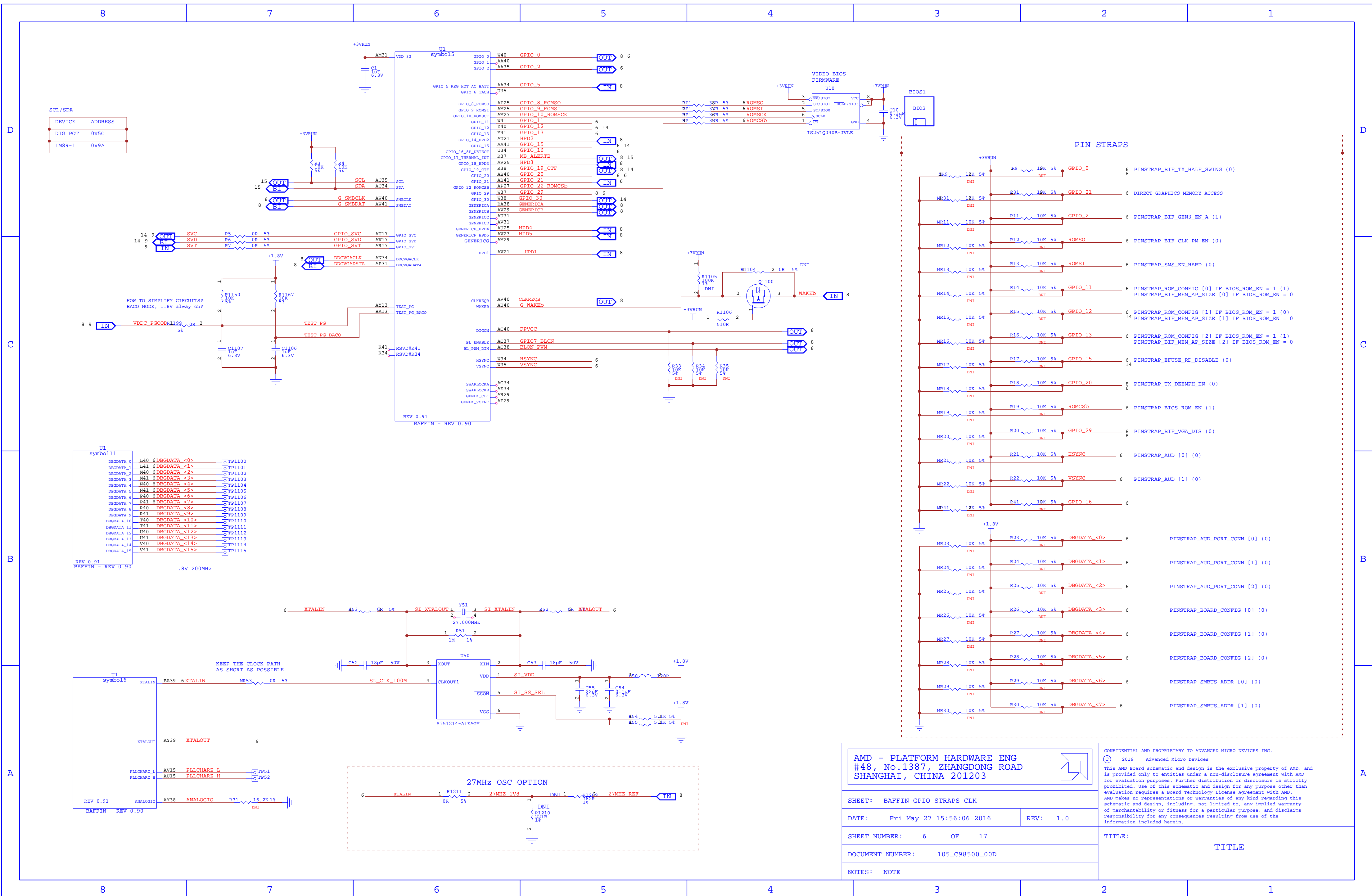
DOCUMENT NUMBER: 105_C98500_00D

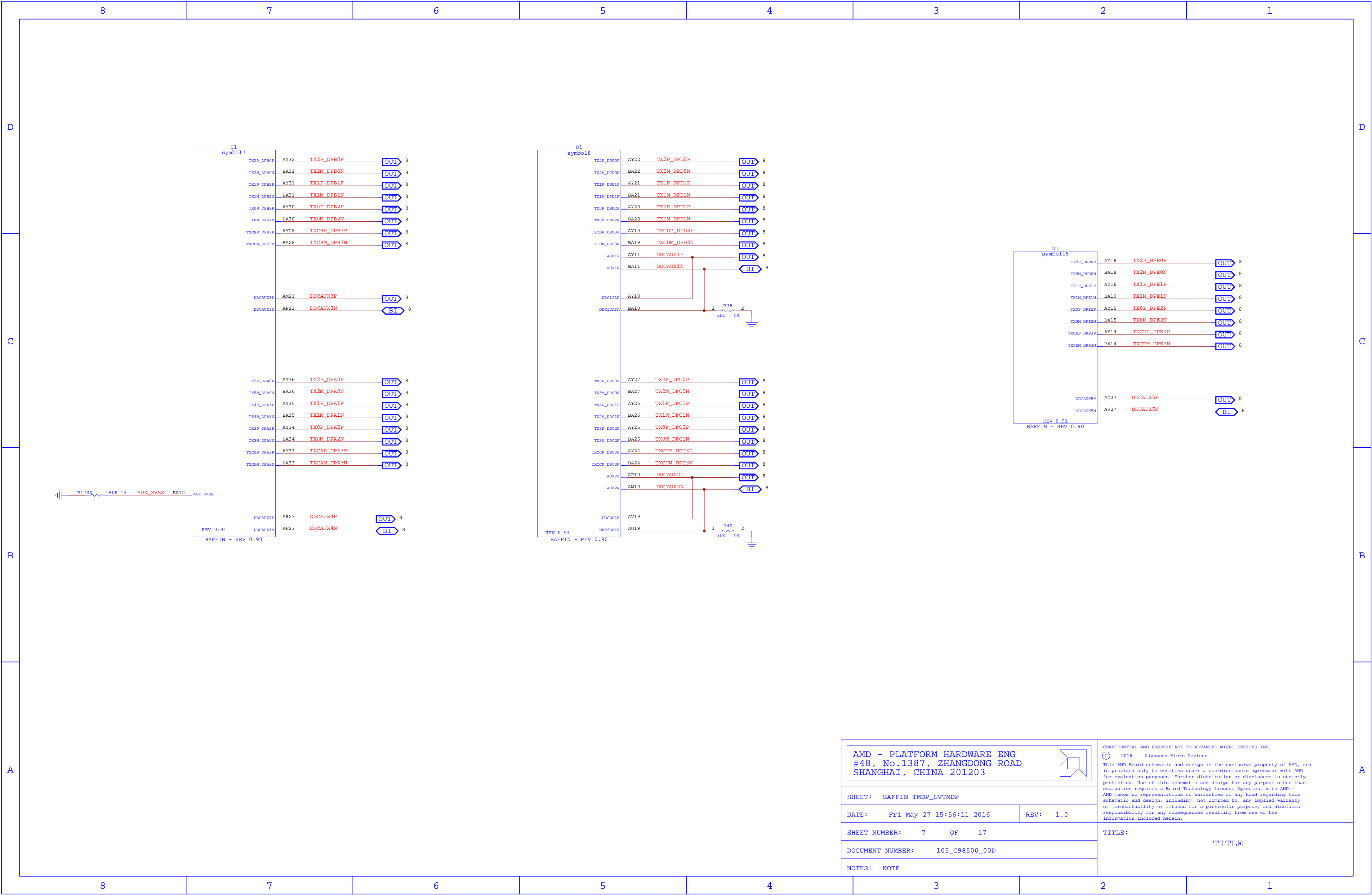
NOTES: NOTE


REV: 1.0

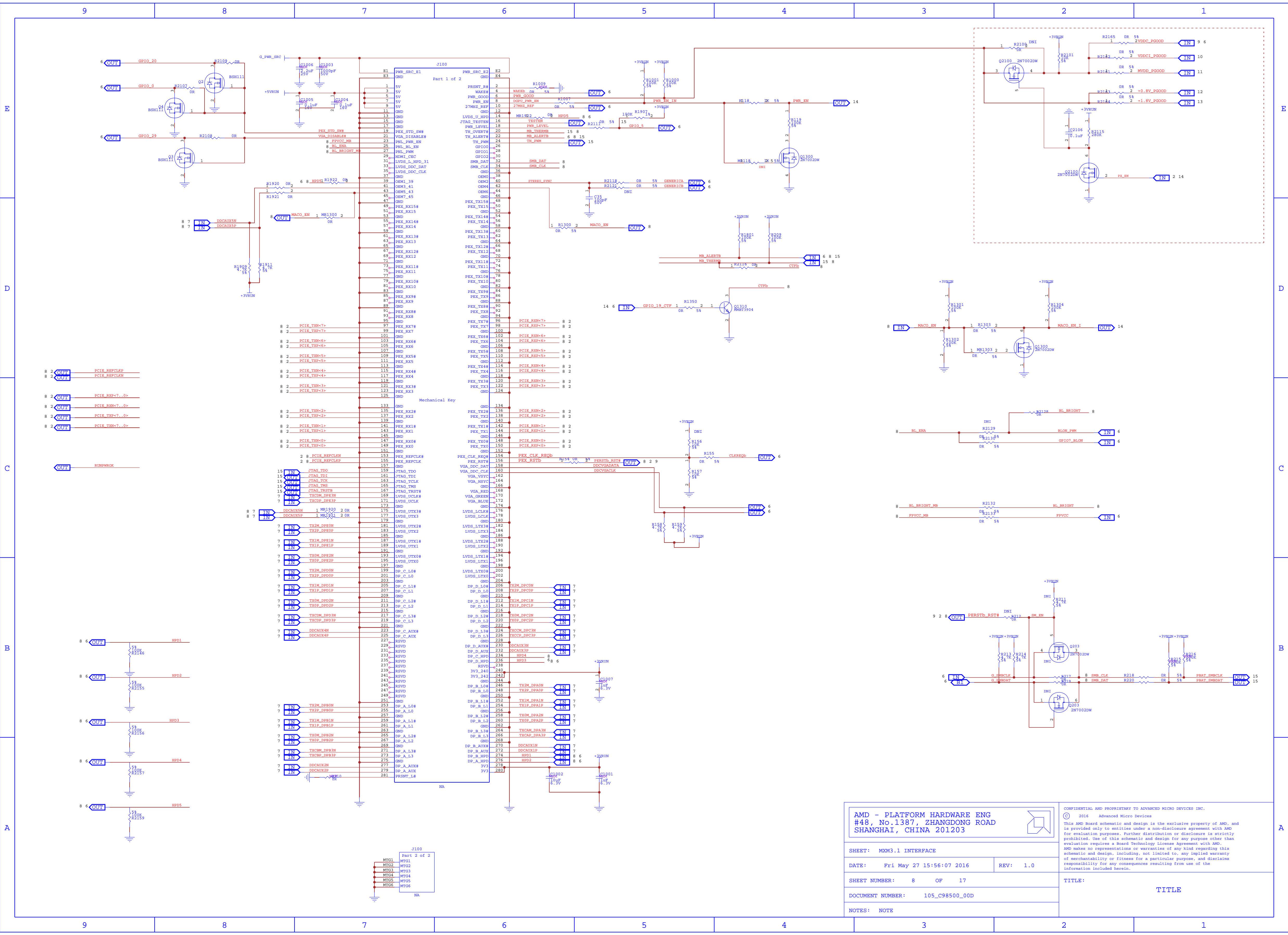
TITLE: TITLE

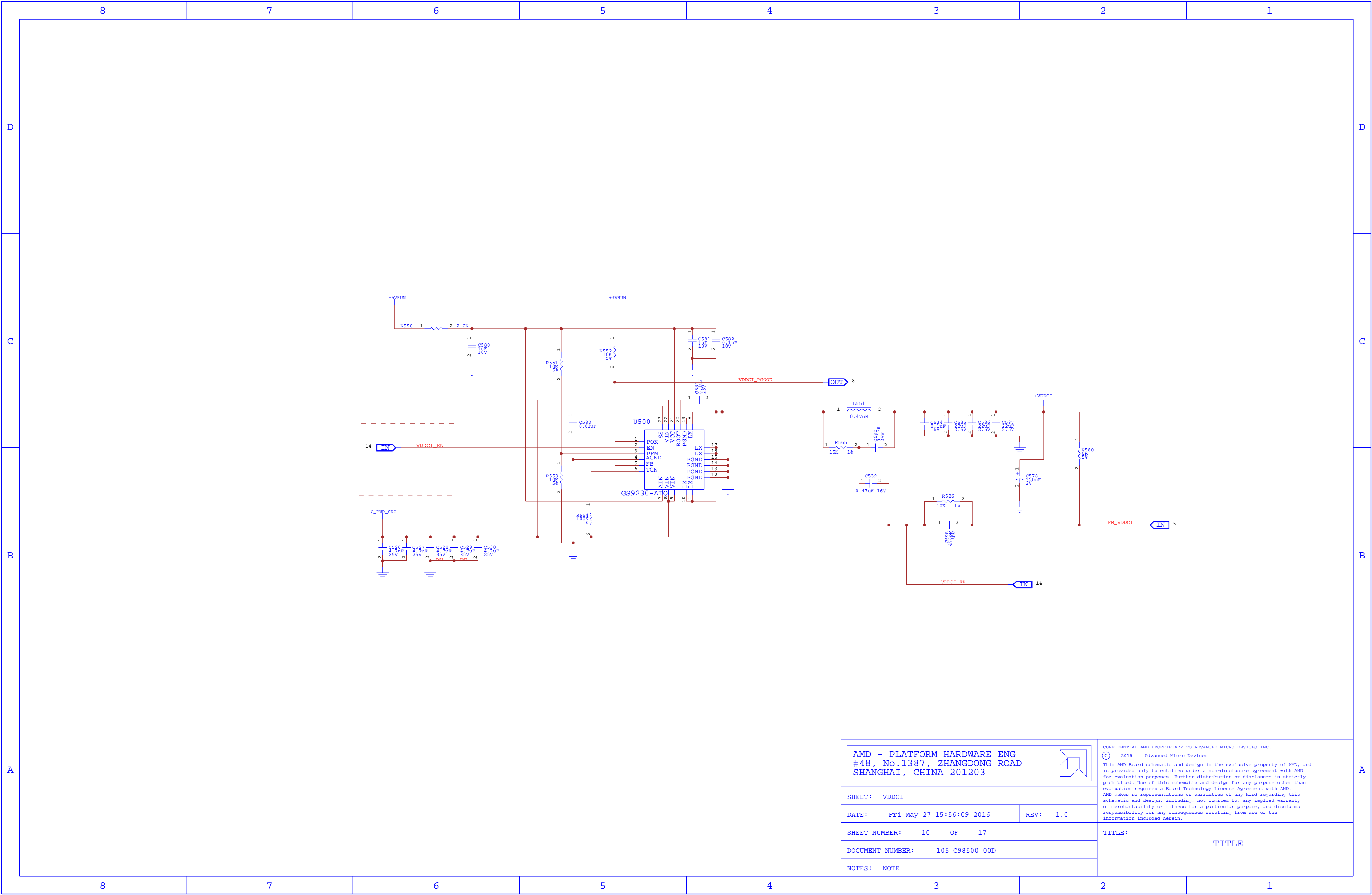
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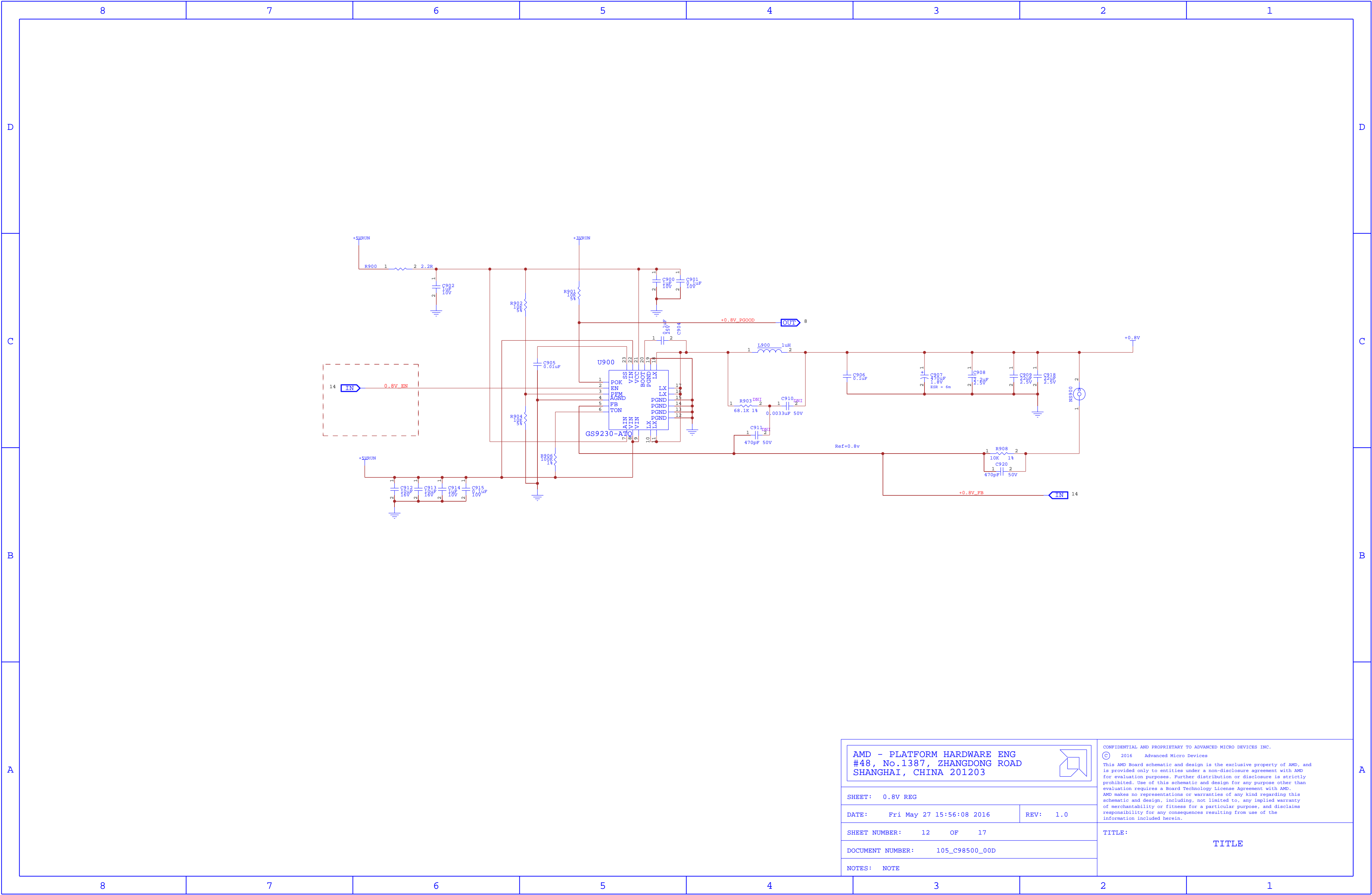


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SHEET: BAFFIN TMDP_LVTMDP			TITLE: TITLE
DATE: Fri May 27 15:56:11 2016	REV: 1.0		
SHEET NUMBER: 7 OF 17			
DOCUMENT NUMBER: 105_C98500_00D			
NOTES: NOTE			


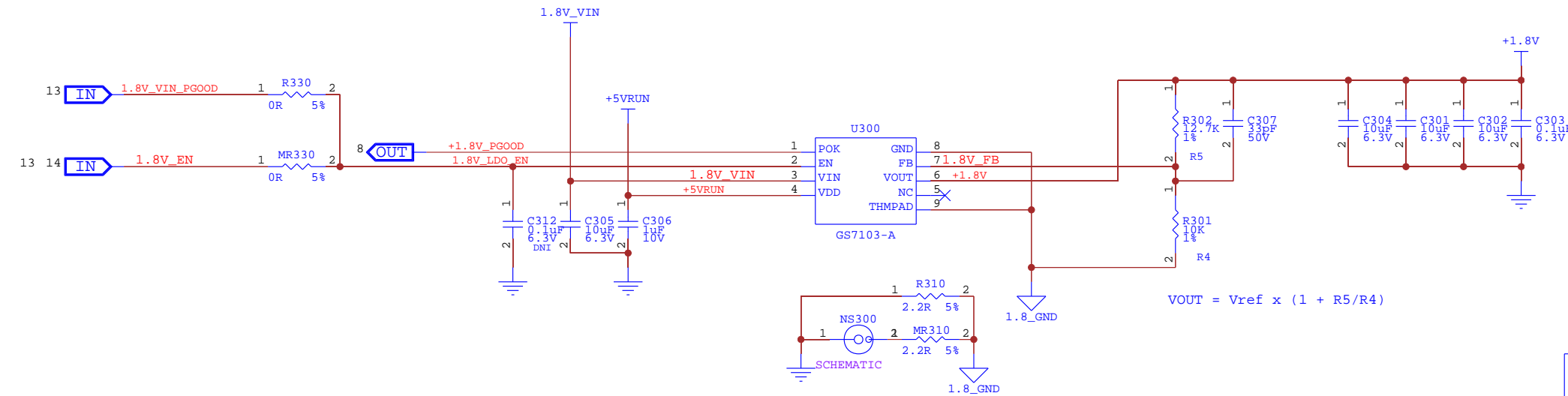




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SHEET: VDDCI		TITLE: TITLE	
DATE: Fri May 27 15:56:09 2016	REV: 1.0		
SHEET NUMBER: 10 OF 17			
DOCUMENT NUMBER: 105_C98500_00D			
NOTES: NOTE			



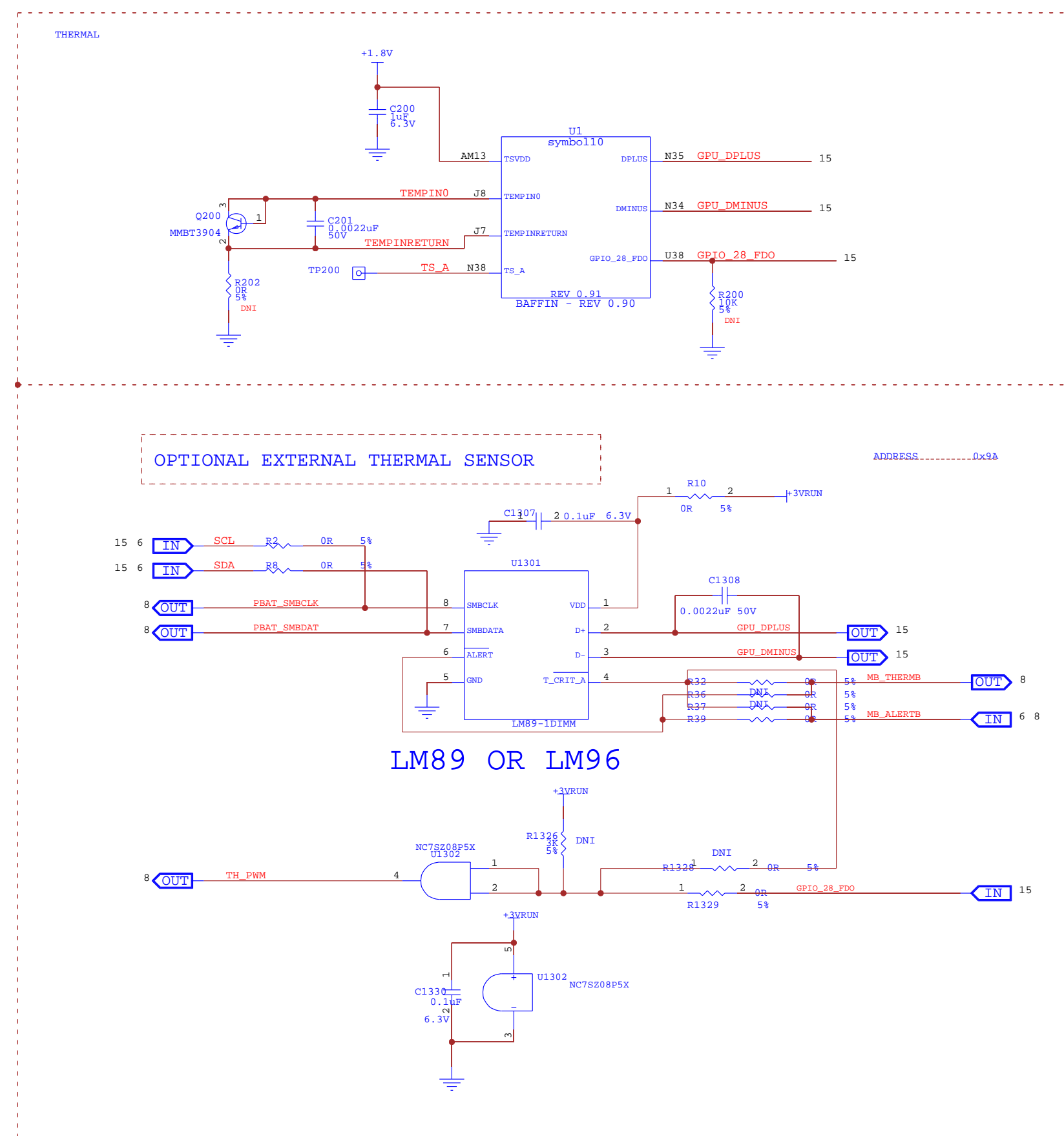
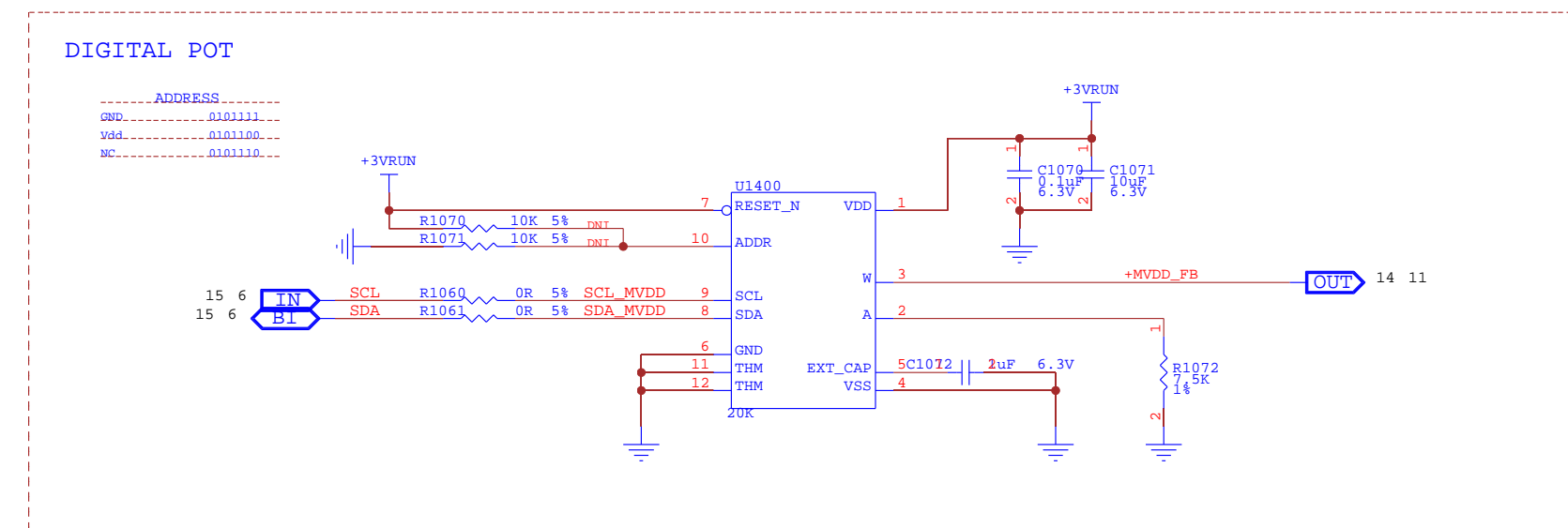
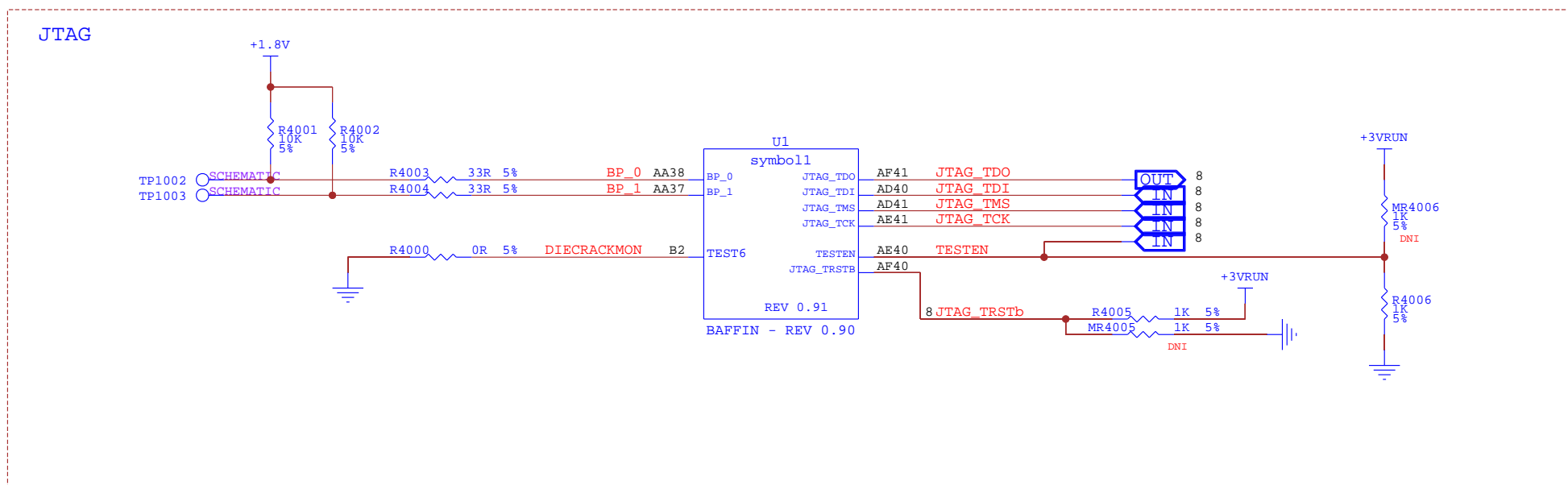
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SHEET: 0.8V REG		TITLE: TITLE		
DATE: Fri May 27 15:56:08 2016				REV: 1.0
SHEET NUMBER: 12 OF 17				
DOCUMENT NUMBER: 105_C98500_00D				
NOTES: NOTE				




TITLE:	
	TITLE

NOTES: NOTE

TITLE



<div style="display: flex; justify-content: space-between;"> <div> <p>AMD - PLATFORM HARDWARE ENG</p> <p>#48, No.1387, ZHANGDONG ROAD</p> <p>SHANGHAI, CHINA 201203</p> </div>  </div>		CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC. © 2016 Advanced Micro Devices This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representations or warranties of any kind regarding this schematic and design, including, not limited to, any implied warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information included herein.
SHEET:	DEBUG & THERMAL	
DATE:	Fri May 27 15:56:09 2016	REV: 1.0
SHEET NUMBER:	15 OF 17	TITLE:
DOCUMENT NUMBER:	105_C98500_00D	TITLE
NOTES:	NOTE	

8		7		6		5		4		3		2		1					
AMD 		TITLE:				DOCUMENT NUMBER: 105_C98500_00D				DATE: Fri Apr 29 15:13:15 2016				SHEET NUMBER: 17 OF 17				REV: 1.0	
REVISION HISTORY				ENGINEER: Daniel Mi		NOTES: NOTE				CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC. C • 2016 Advanced Micro Devices This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representations or warranties of any kind regarding this schematic and design, including, not limited to, any implied warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information included herein.				AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203					
SCH Rev		PCB Rev		Date		REVISION DESCRIPTON													
0		00A		08/23/2015		INITIAL DESIGN													
1		00B		11/25/2015		1.ADD DDCVGADATA/CLK 2.MVDD SWITCH GPIO CHANGED TO GPIO30 3.ADD GPIO21 PULL UP/DOWN 4.ADD 3.3V SWITCH 5.REMAPPING DPA, DPR AND DPC, DPD TO SUPPORT DL_DVI 6.UTAG FOLLOW MXM3.1 SPEC													
2		00C		04/25/2016		1. Add convertor for separate VDDCI design 2. Add convertor for separate VDDCI design 3. Add BACO/BOCO/BOMACO/BAMACO support circuit 4. Add LDO for 1.8V power rail, use convertor as input. 5. Add 1 CAP 0603 22uF for 1.8V power input before CLK generator													