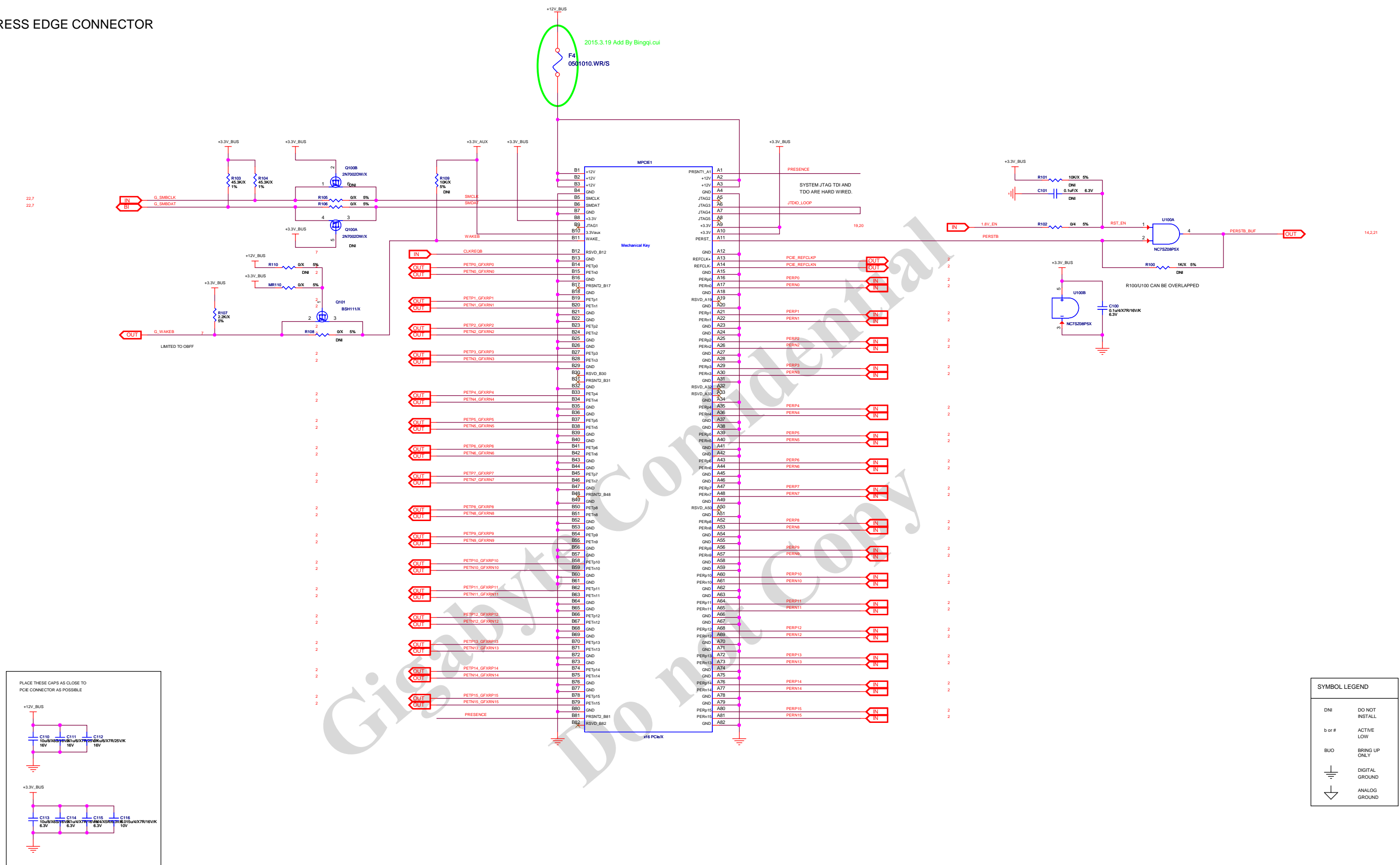
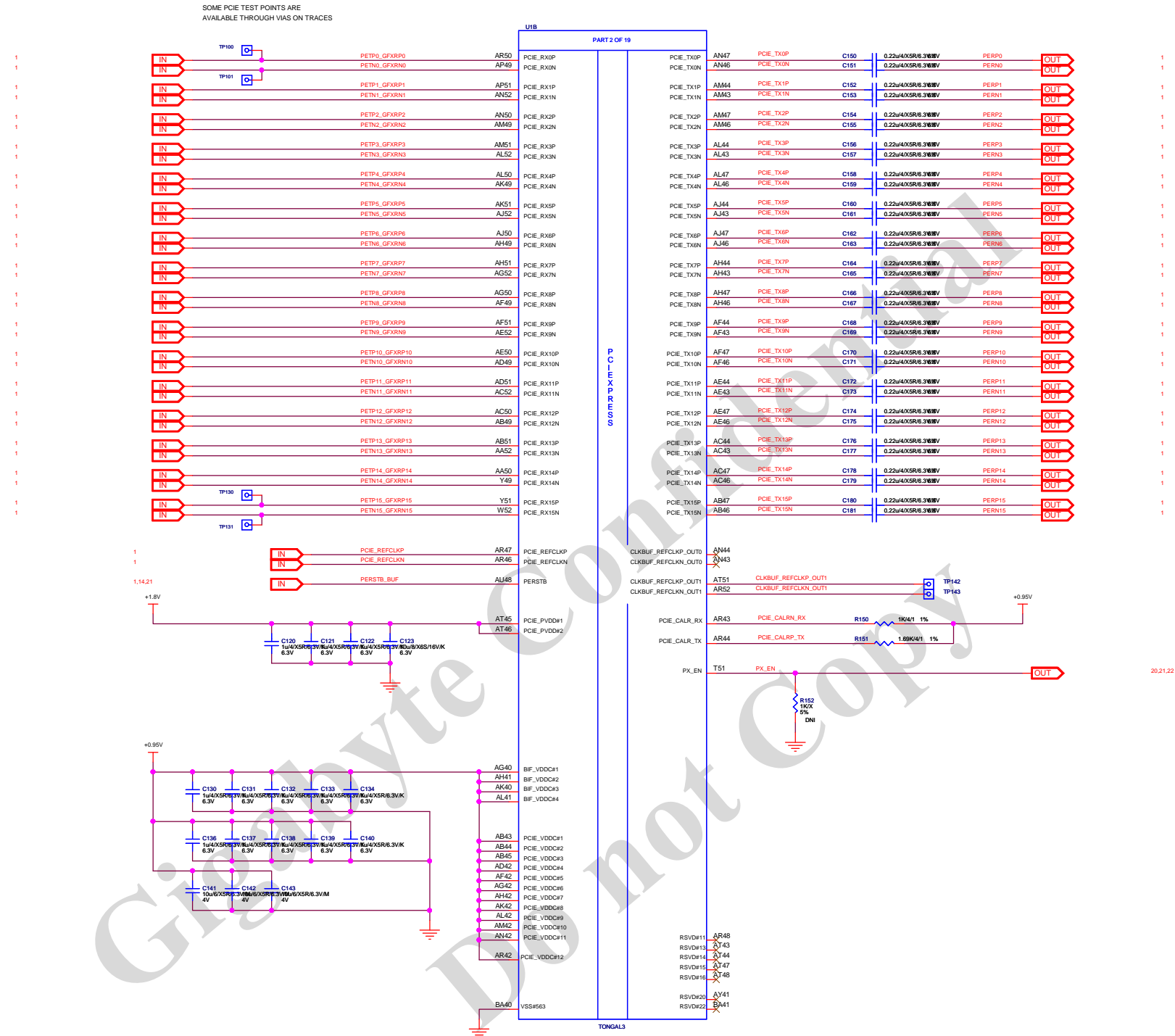


(1) PCI-EXPRESS EDGE CONNECTOR

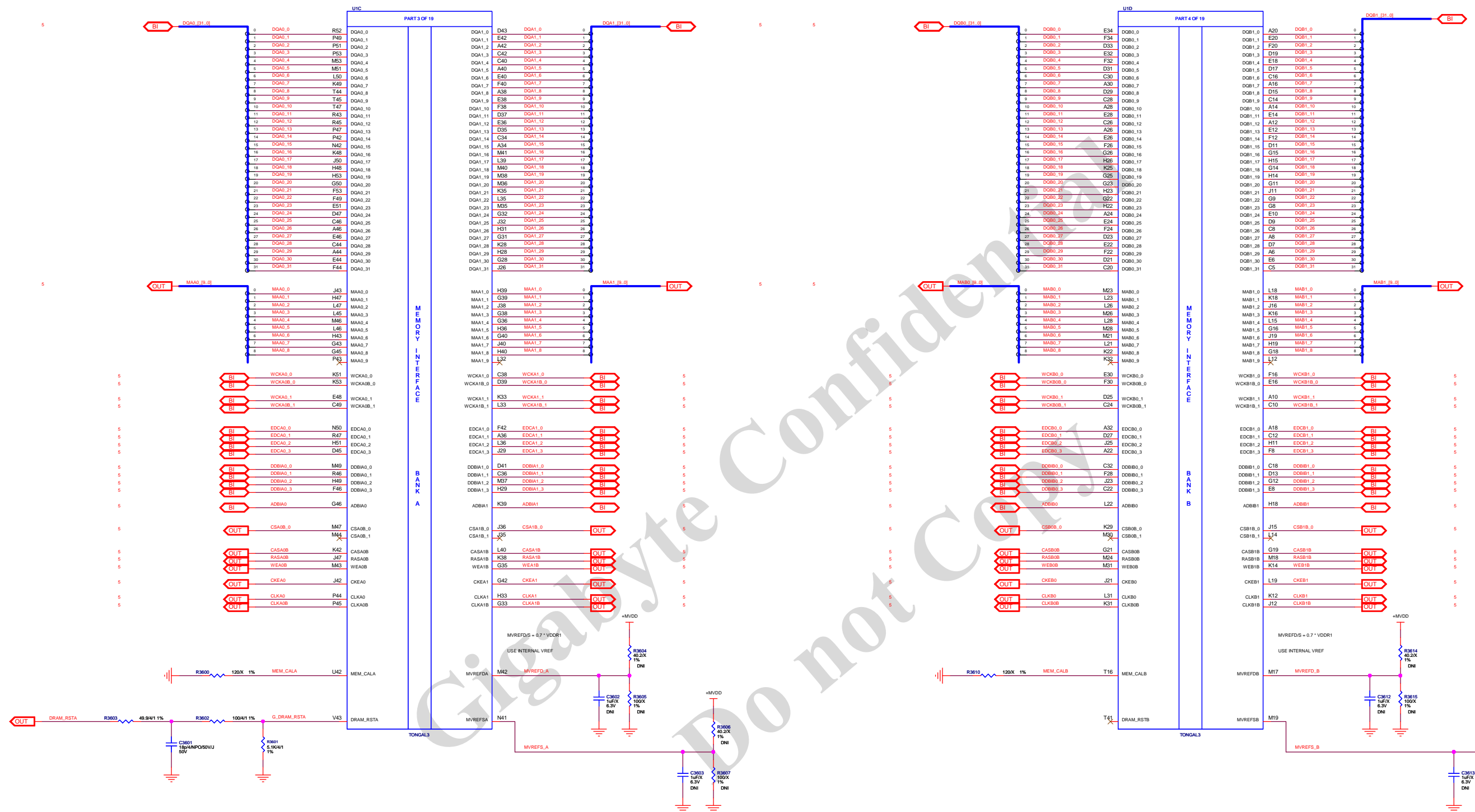


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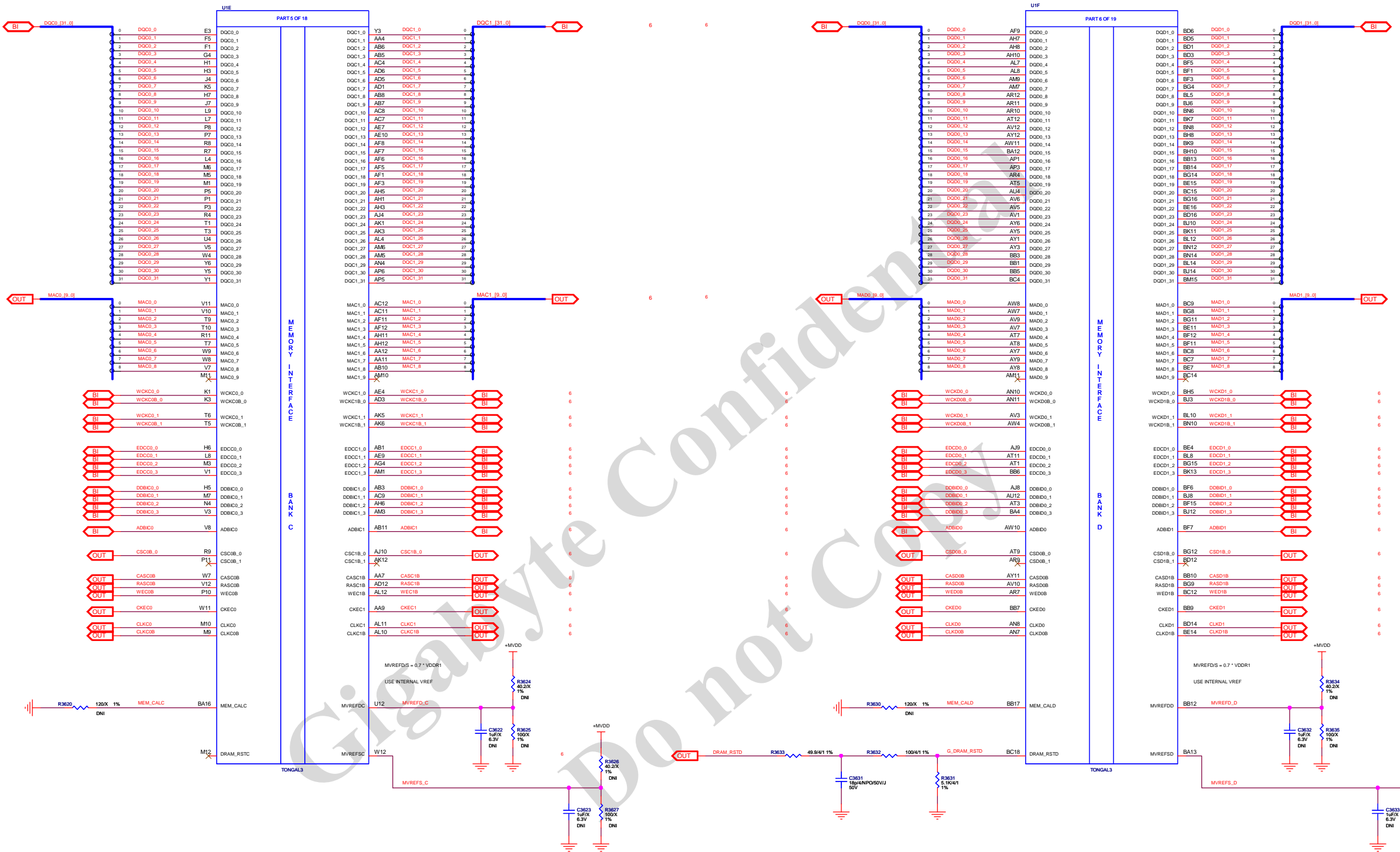
(2) PCIE INTERFACE



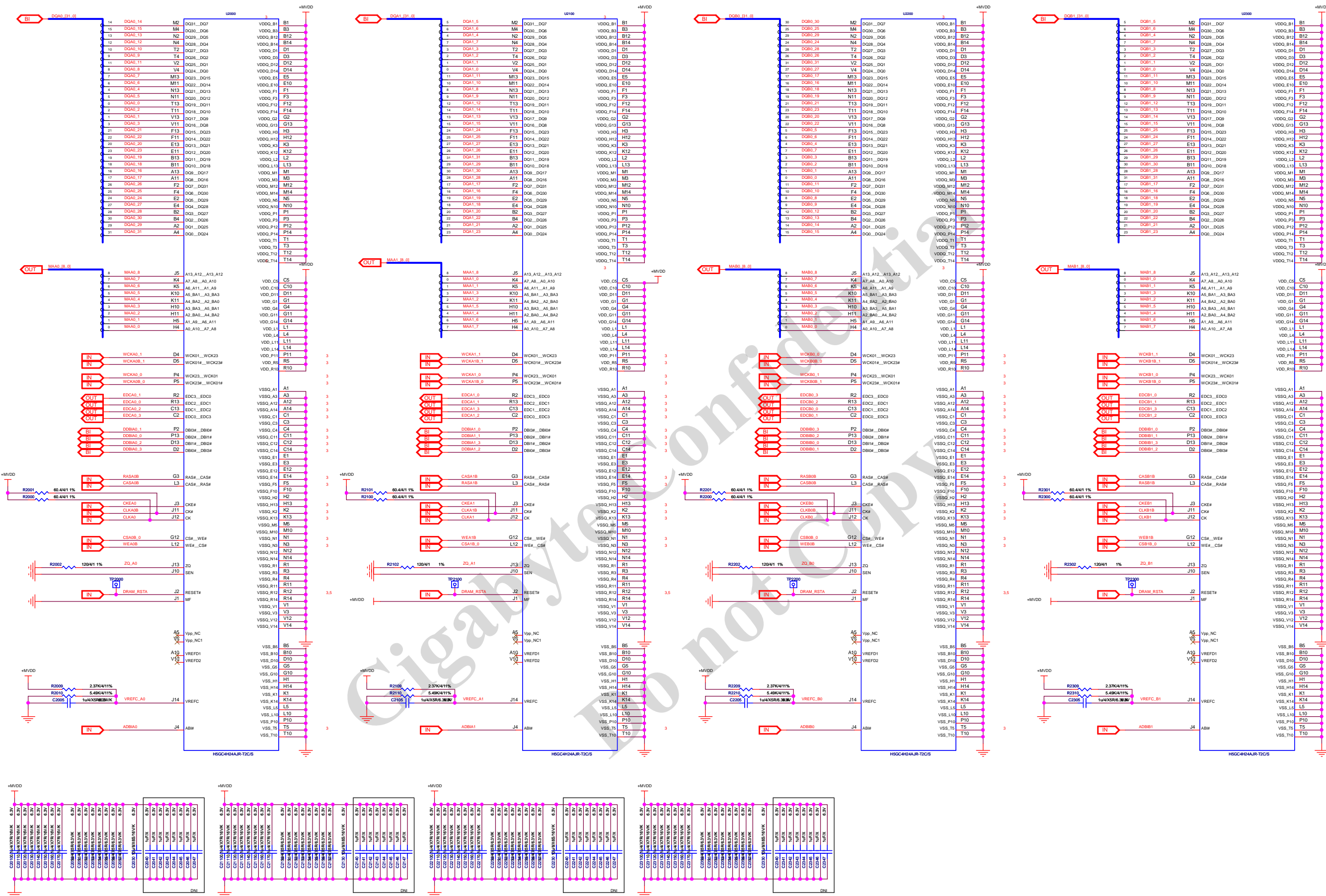
(3) MEM INTERFACE CH AB



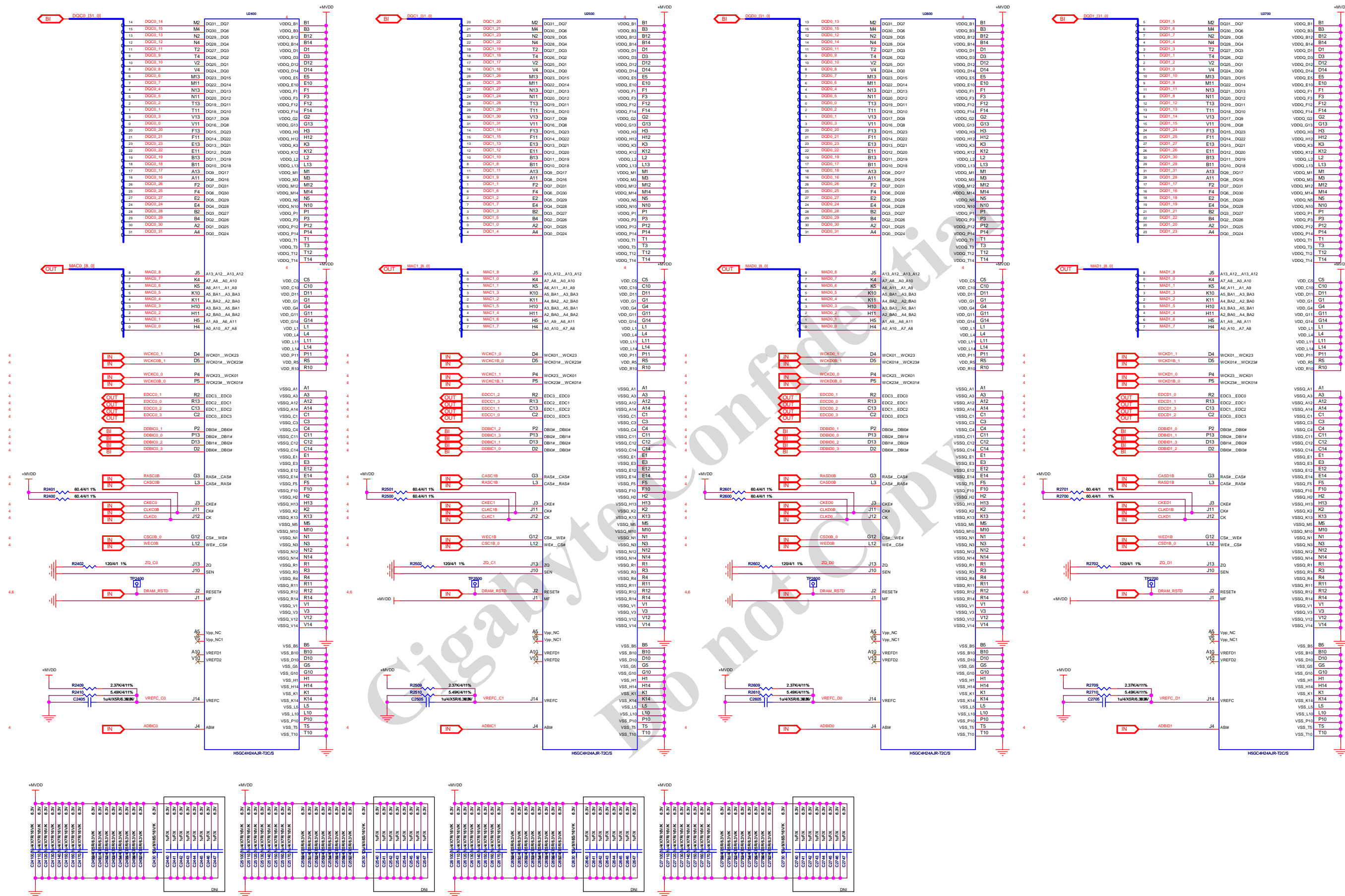
(4) MEM INTERFACE CH CD



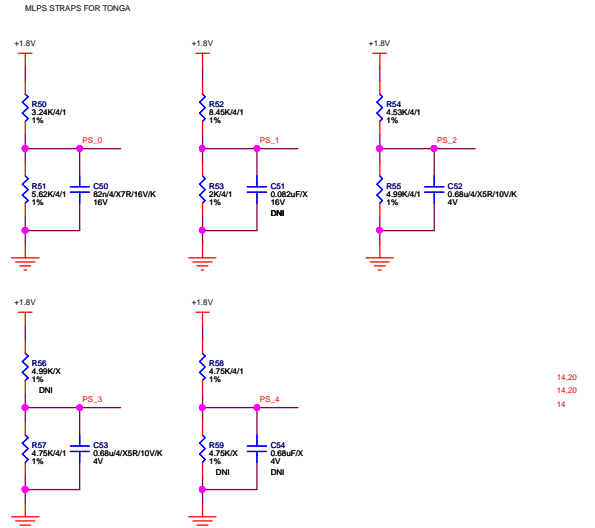
(5) GDDR5 MEM CH AB



(6) GDDR5 MEM CH CD

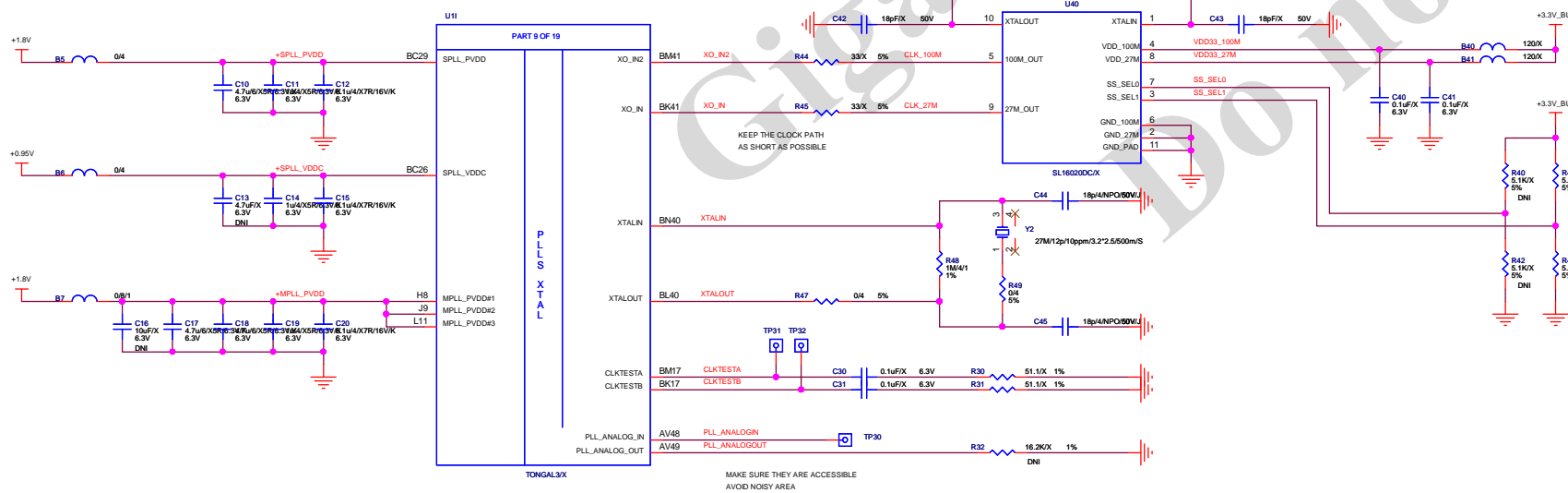
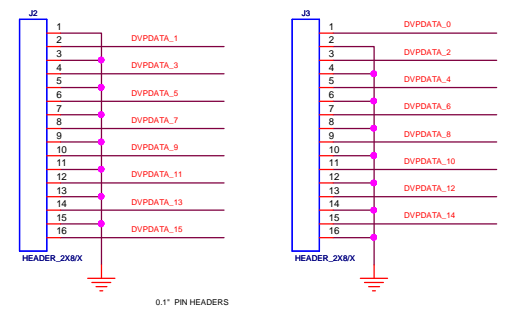
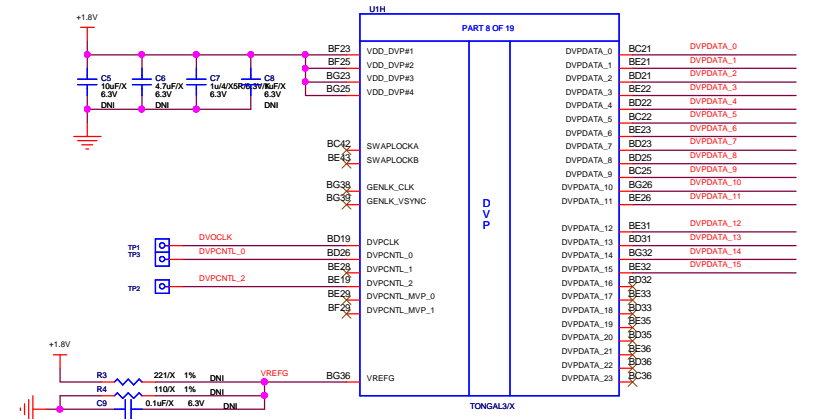
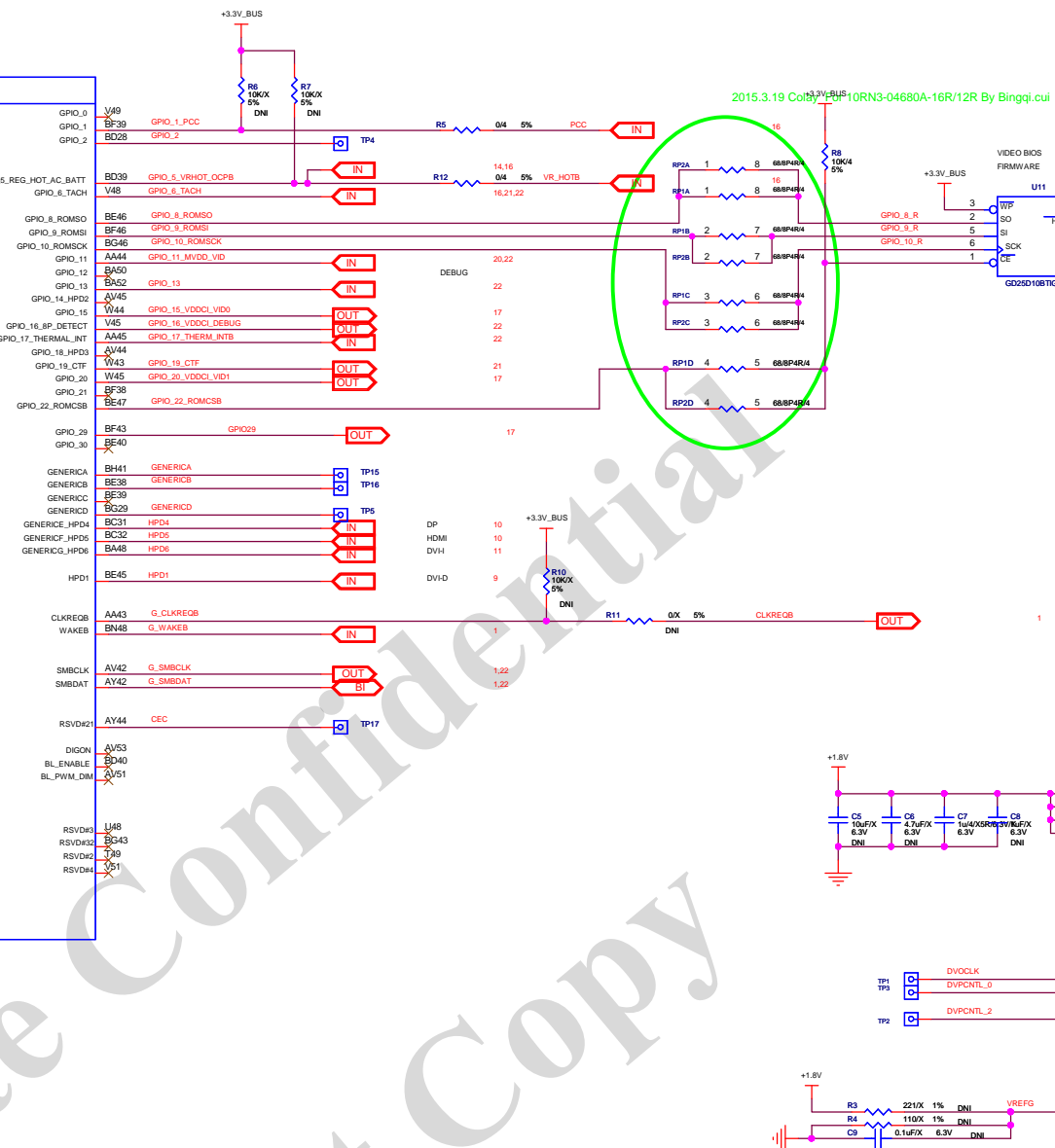
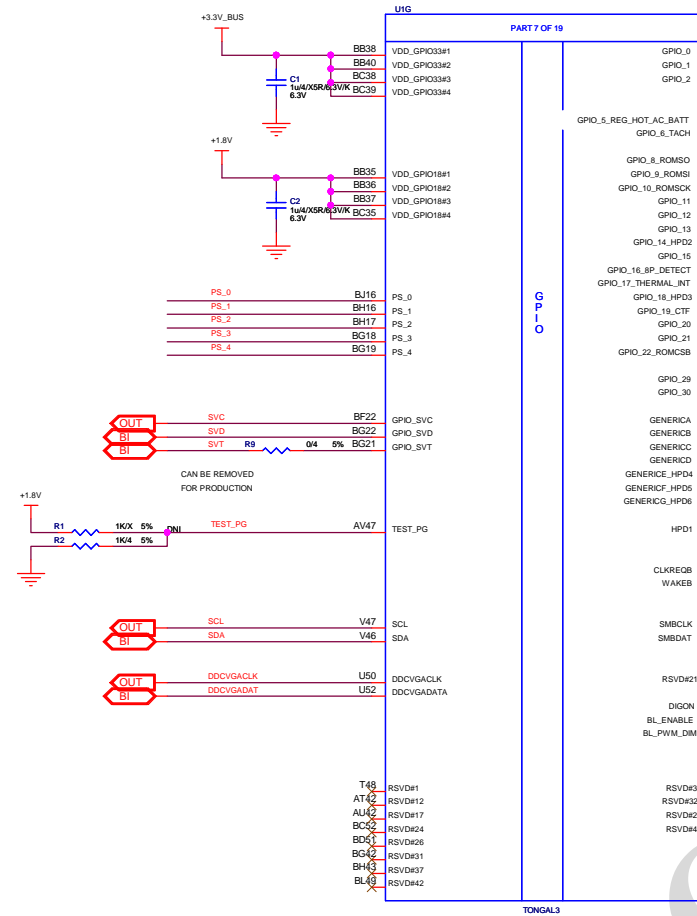


(7) GPIO STRAP CF PLL XTAL

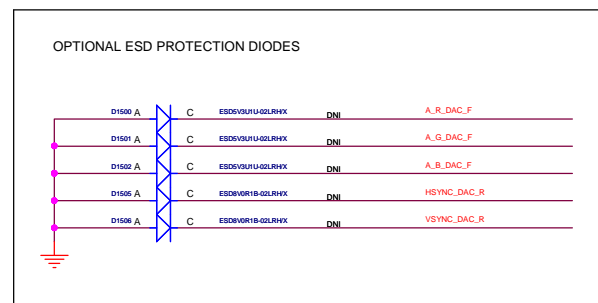
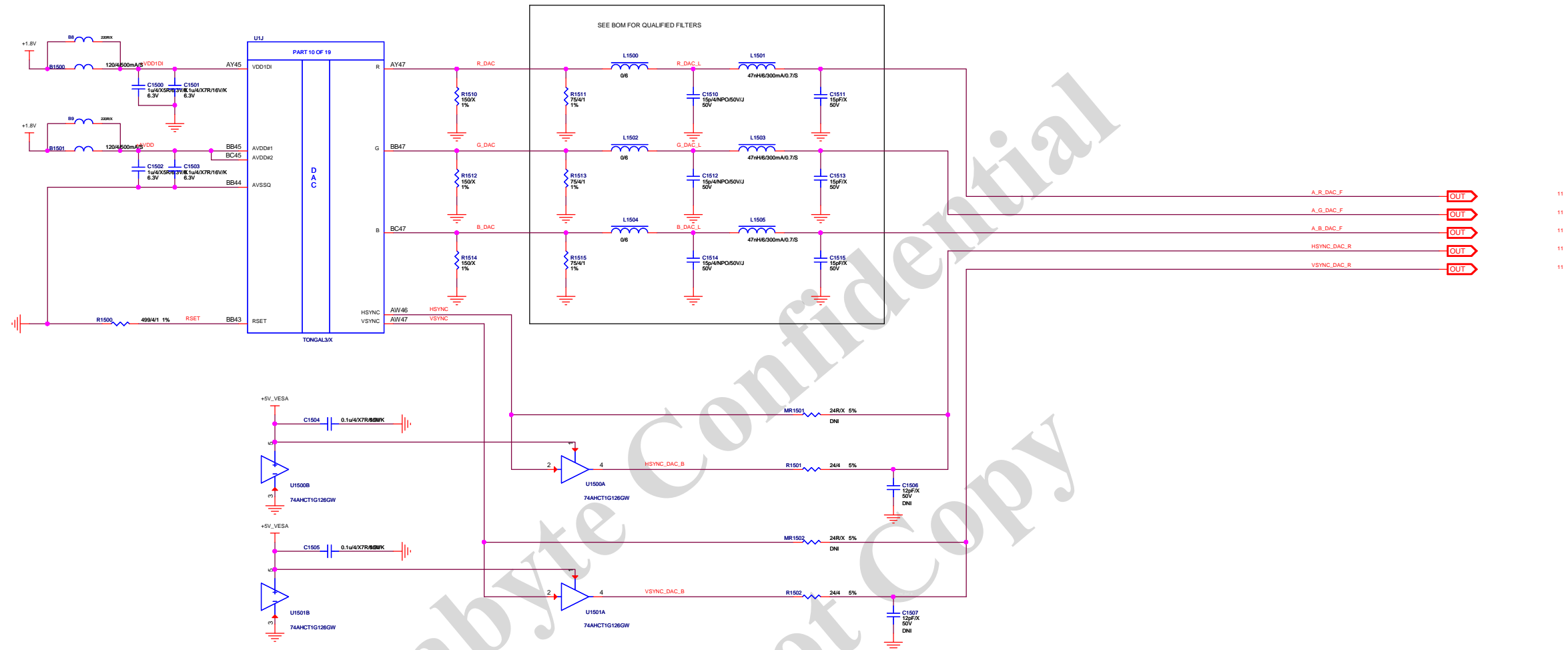


SCU/SDA BUS:			22
ICZ ADDRESS	FUNCTION	DEVICE	22
0x40	VDDGF/VDDC REGULATOR	NCP81002	
0xA6	1.8V REF DAC FOR PCC	UP1801	
0x58	DIGITAL POT FOR VDDCI	AD5272	
0x5C	DIGITAL POT FOR M/MD	AD5274	

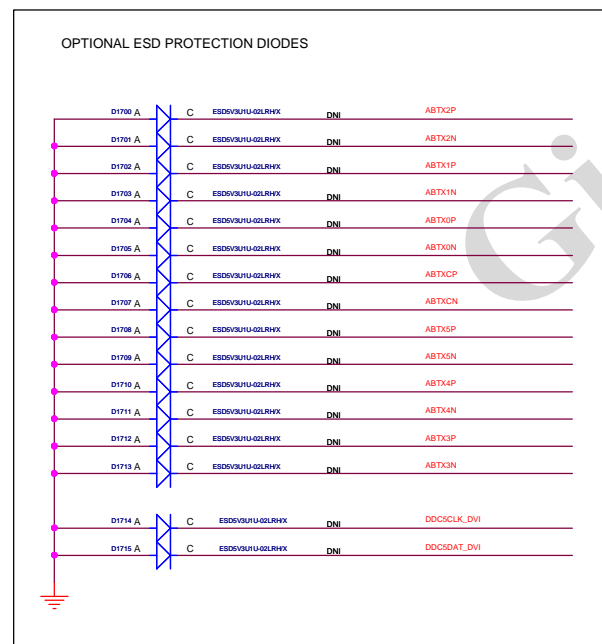
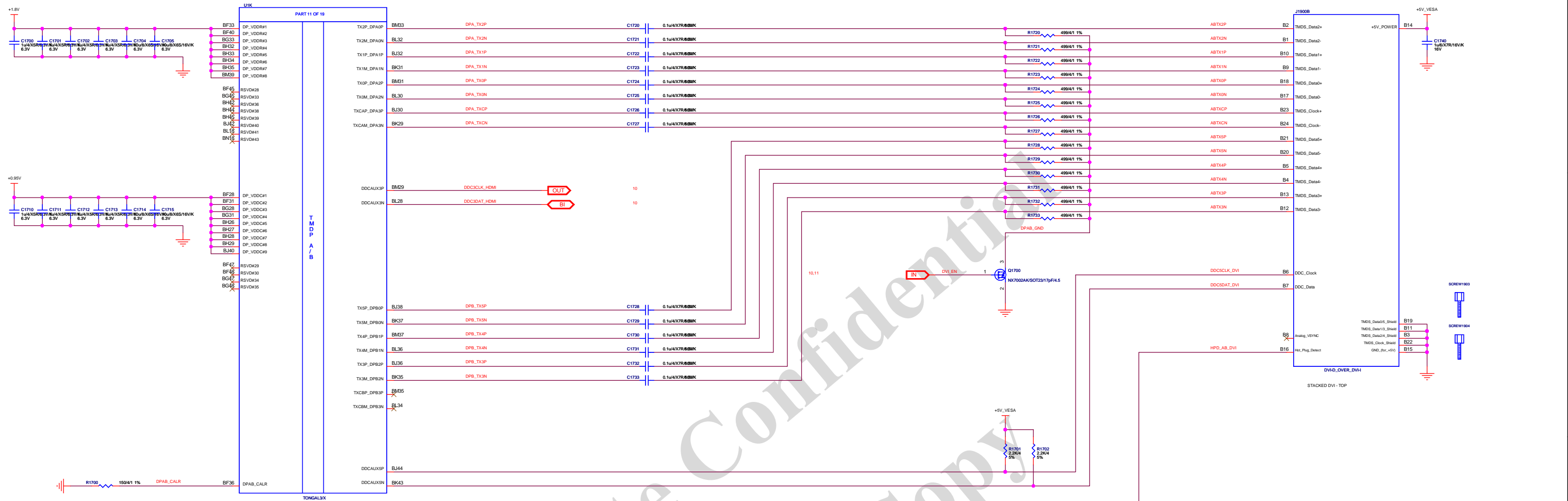
I2C ADDRESS	FUNCTION	DEVICE
0x98	EXT TEMP SENSOR	LM96163

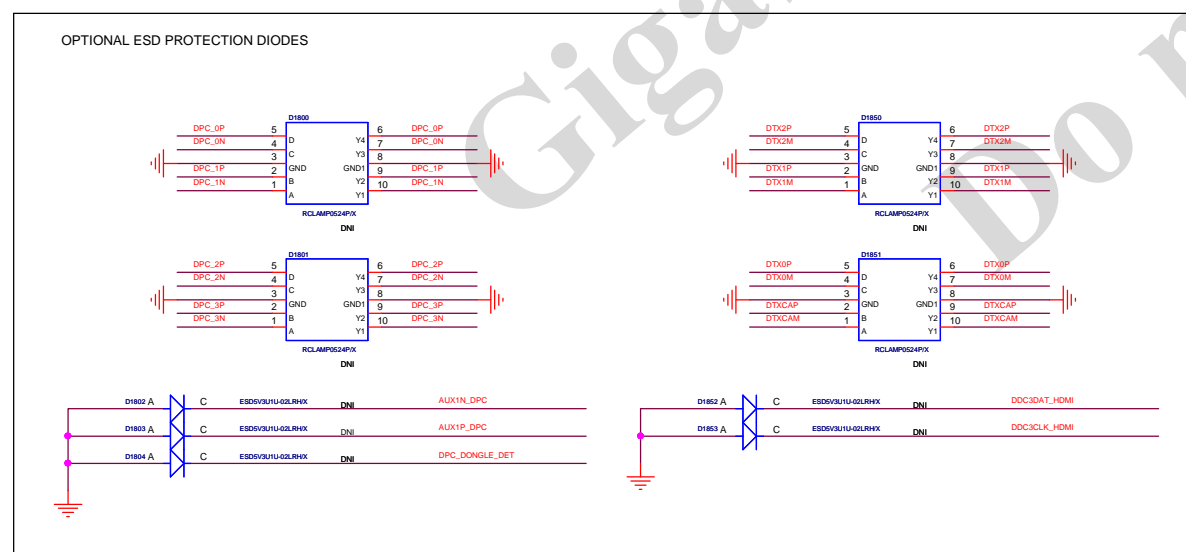
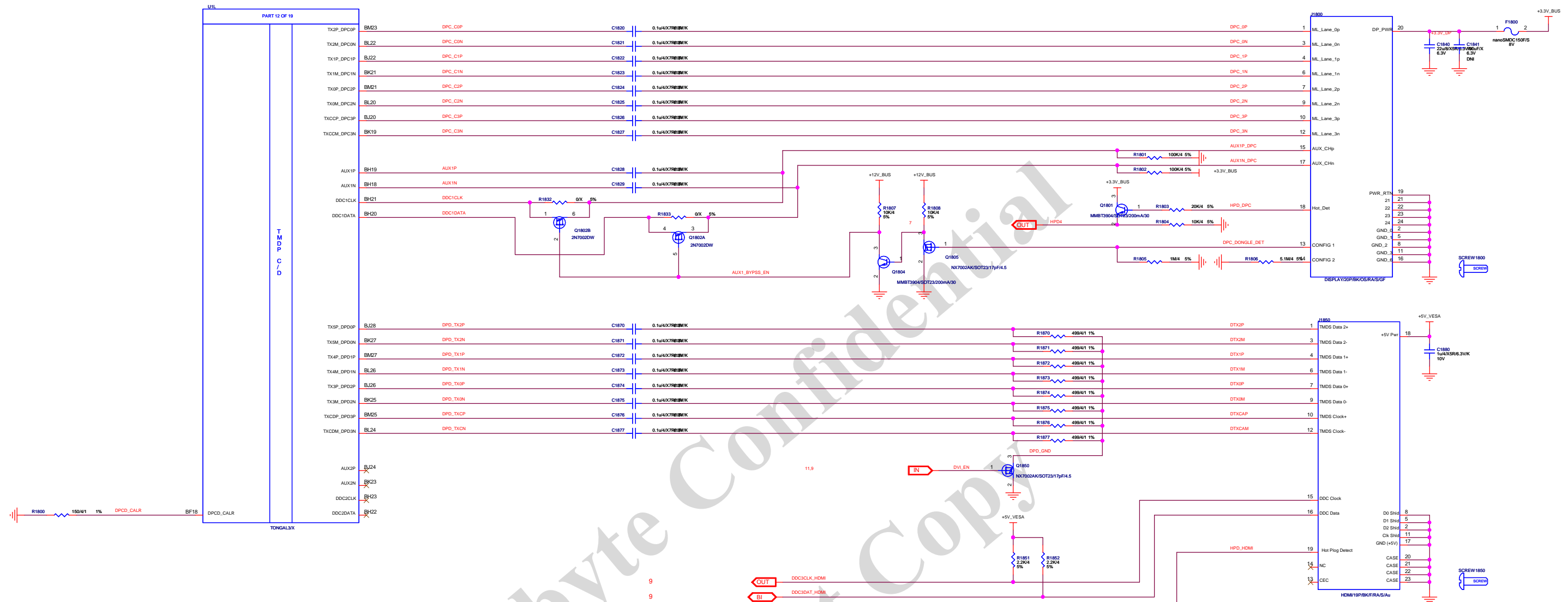


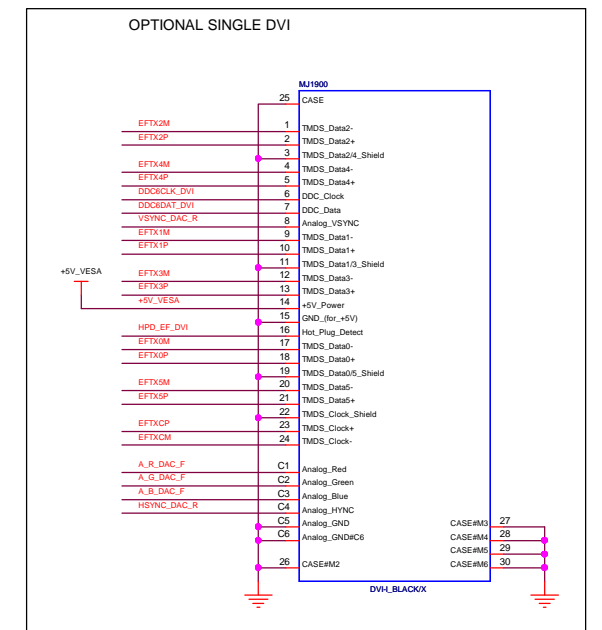
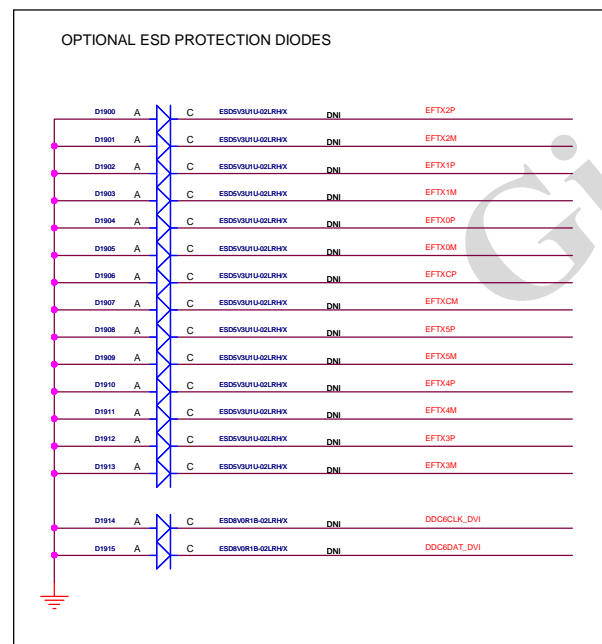
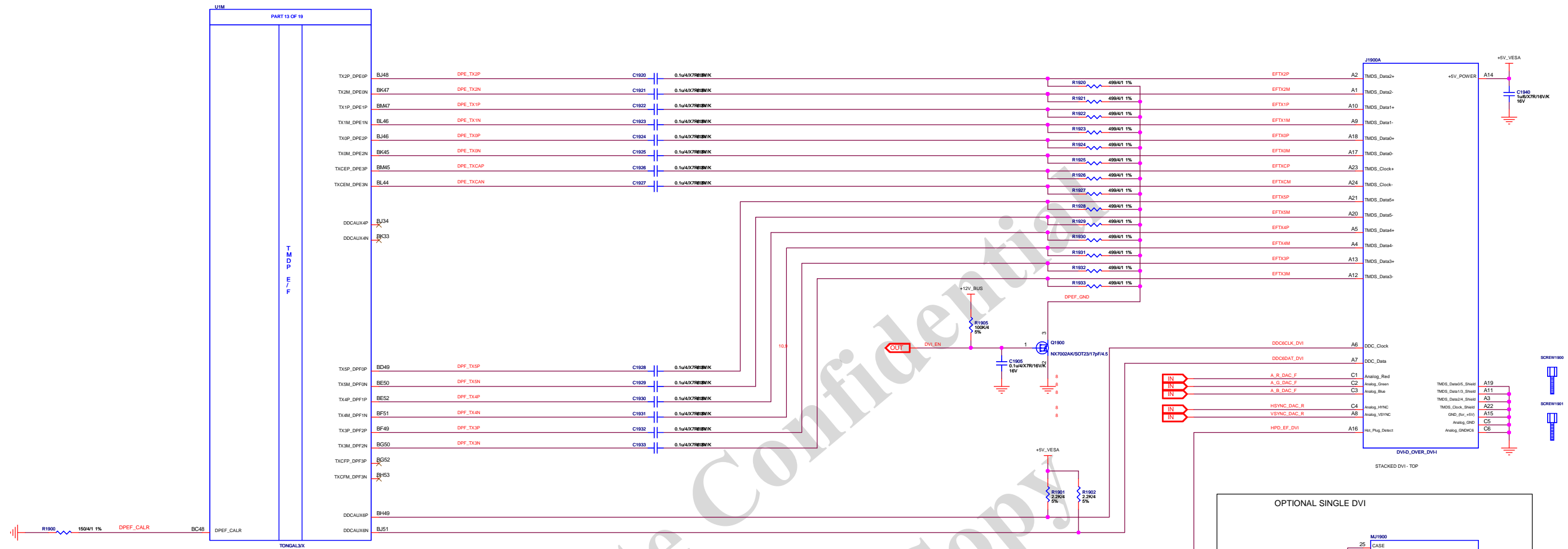
(8) DAC



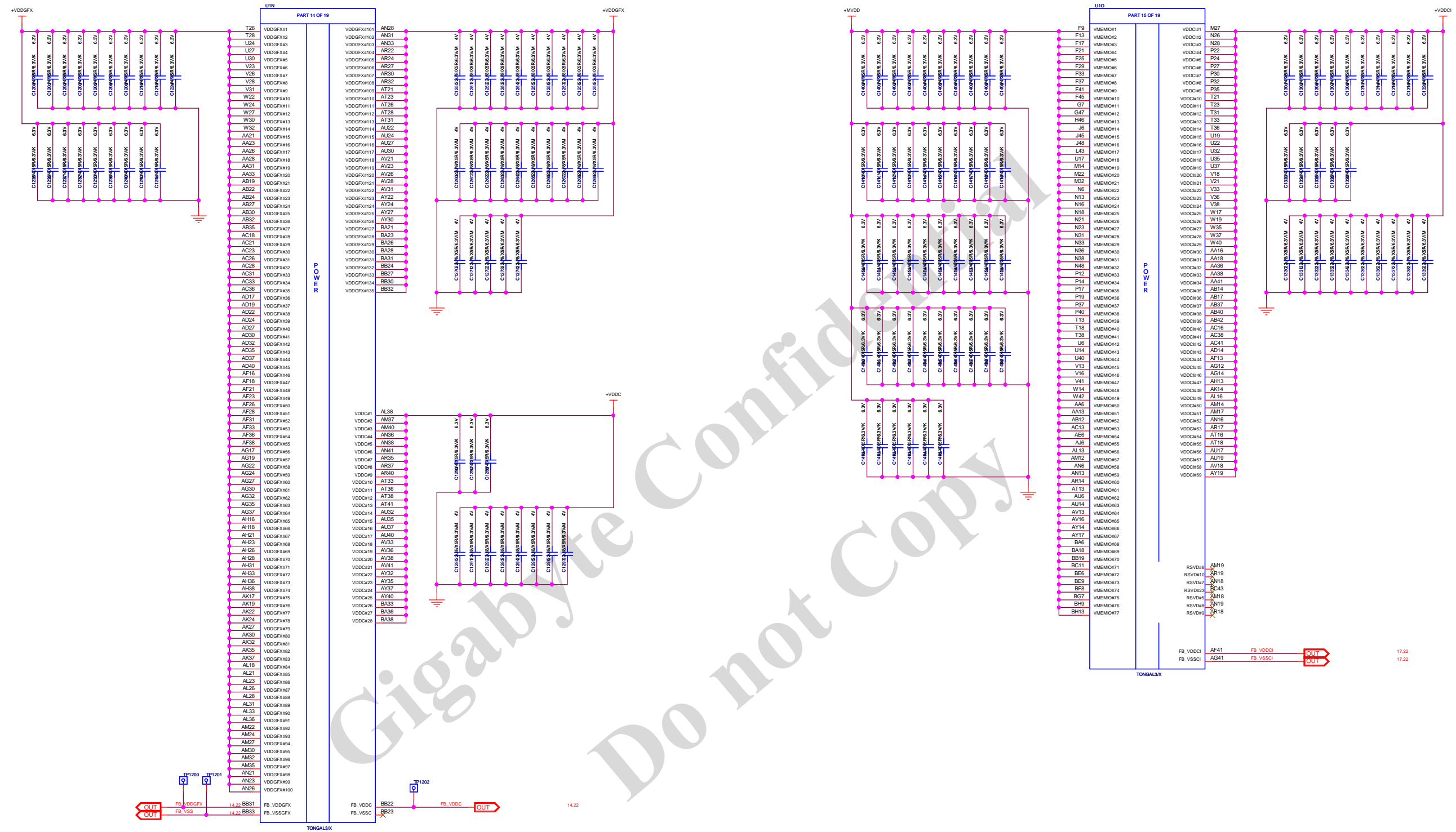
(9) TMDPAB sDVI



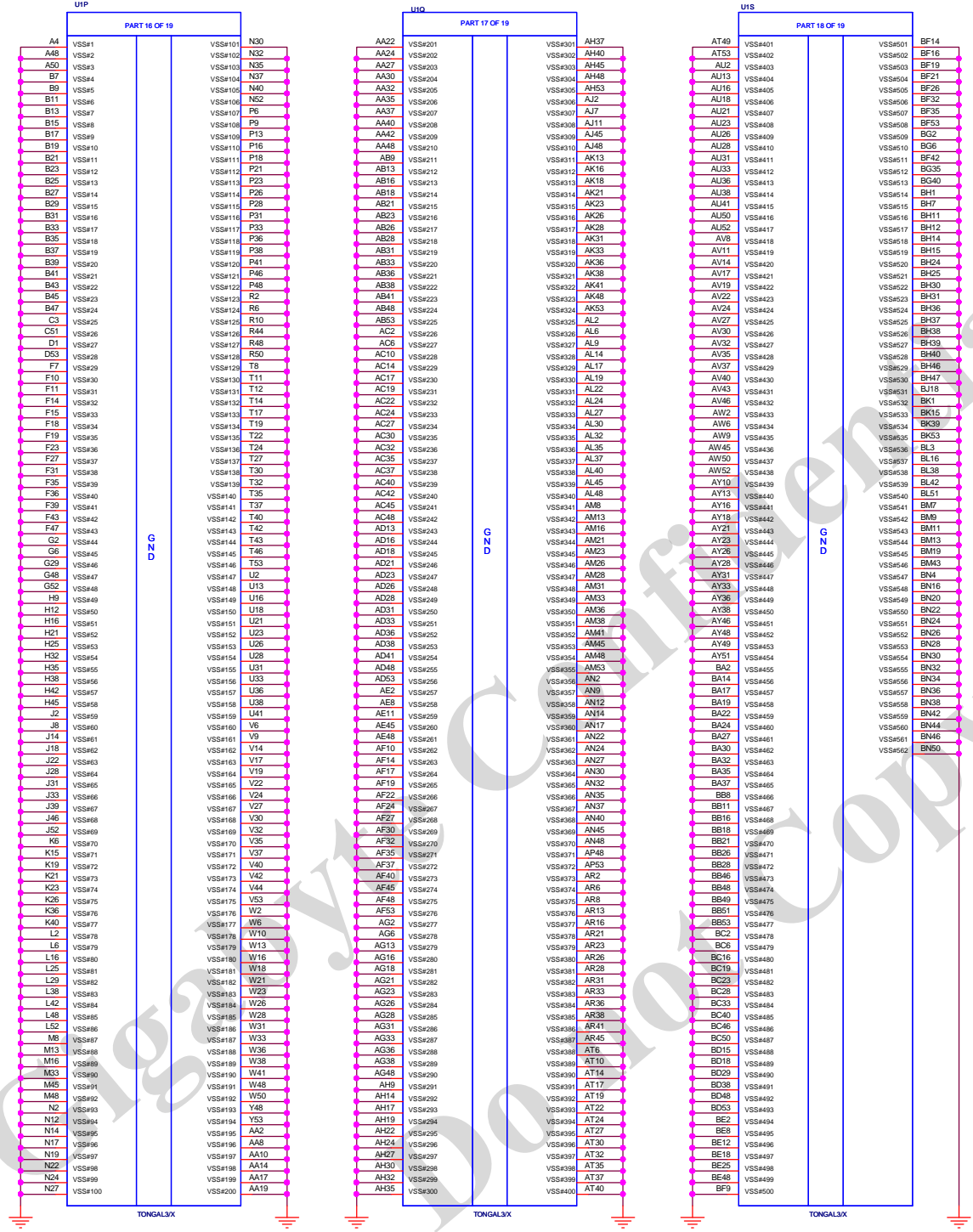




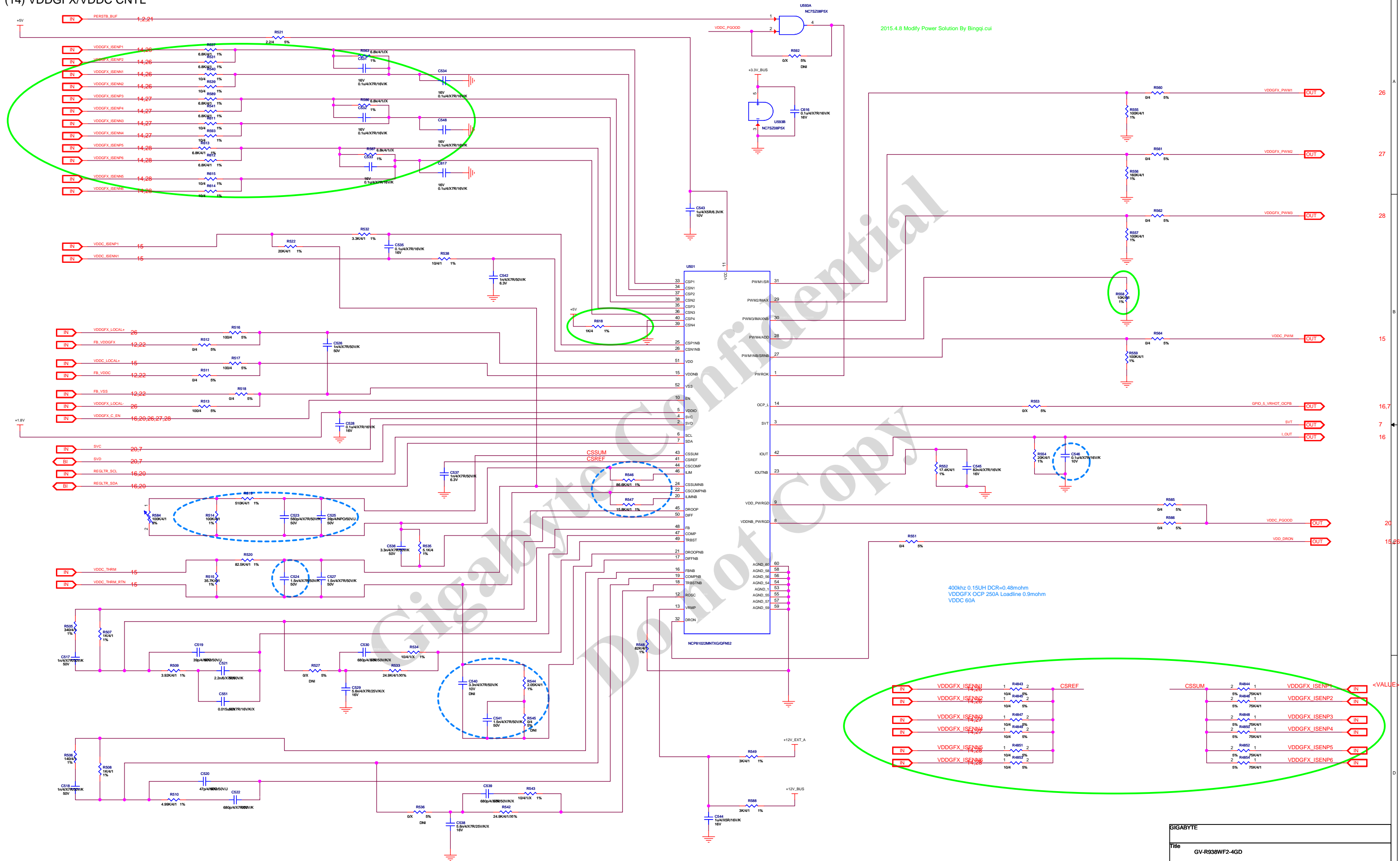
(12) POWER

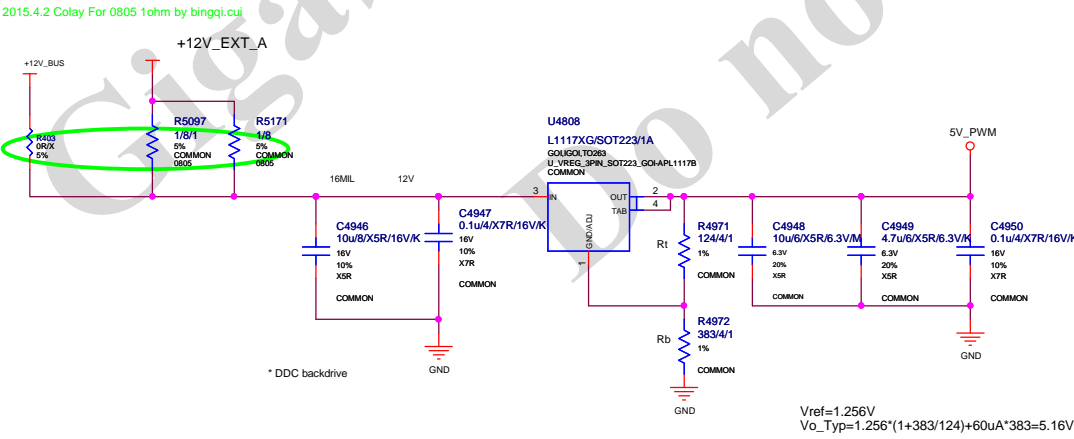
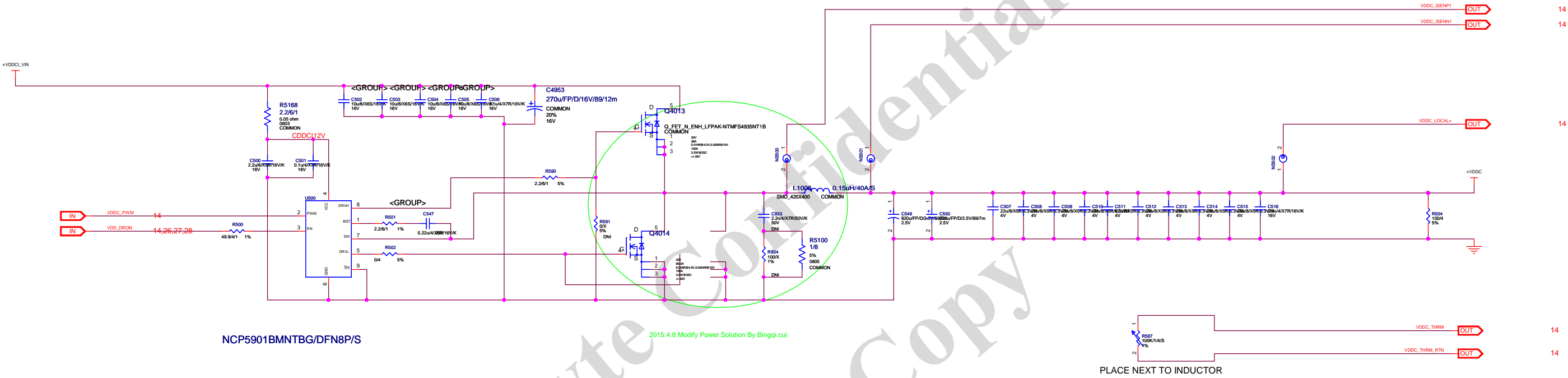


(13) GND

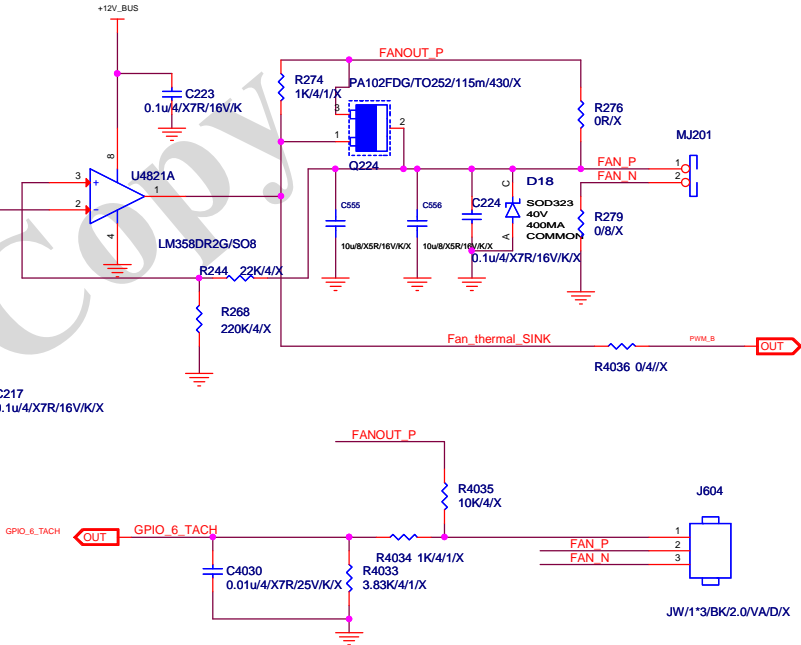
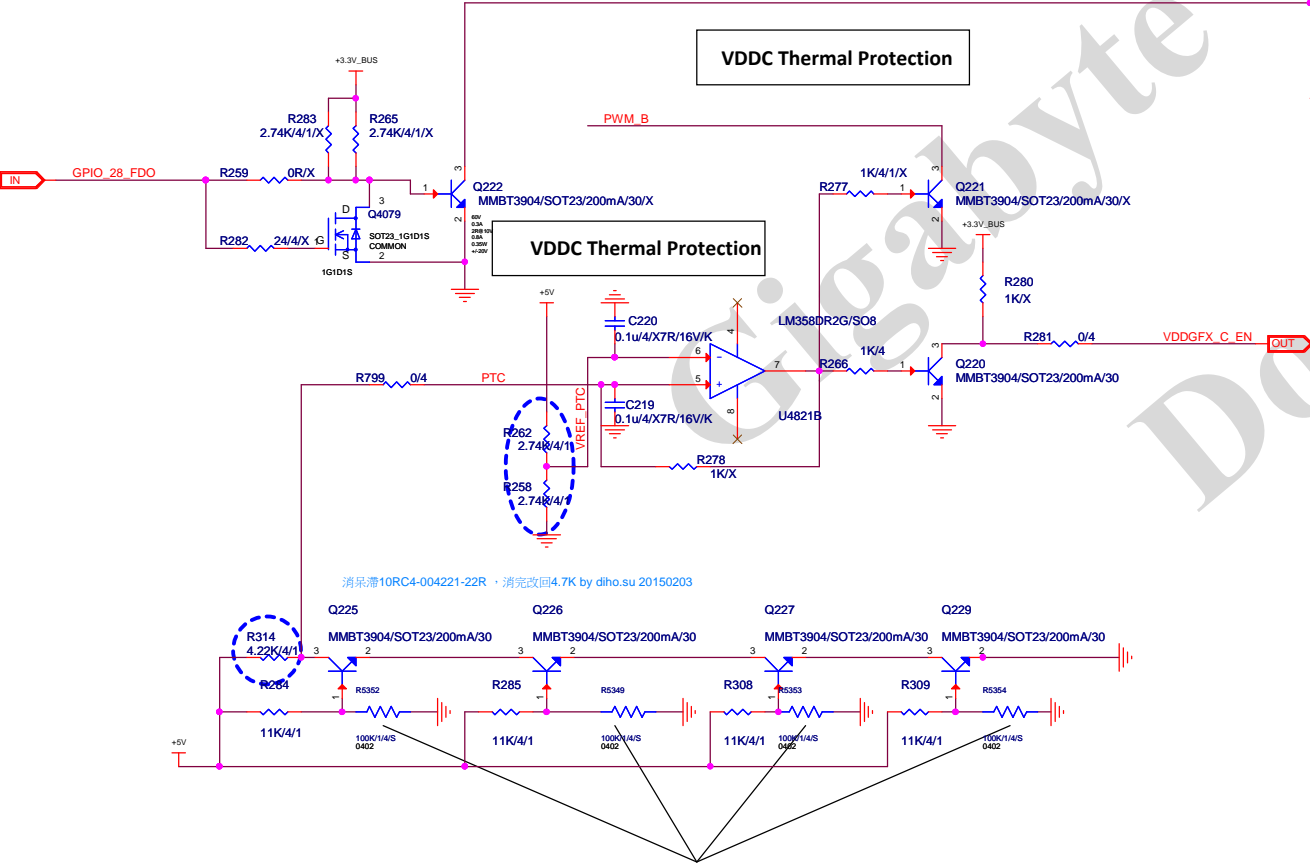
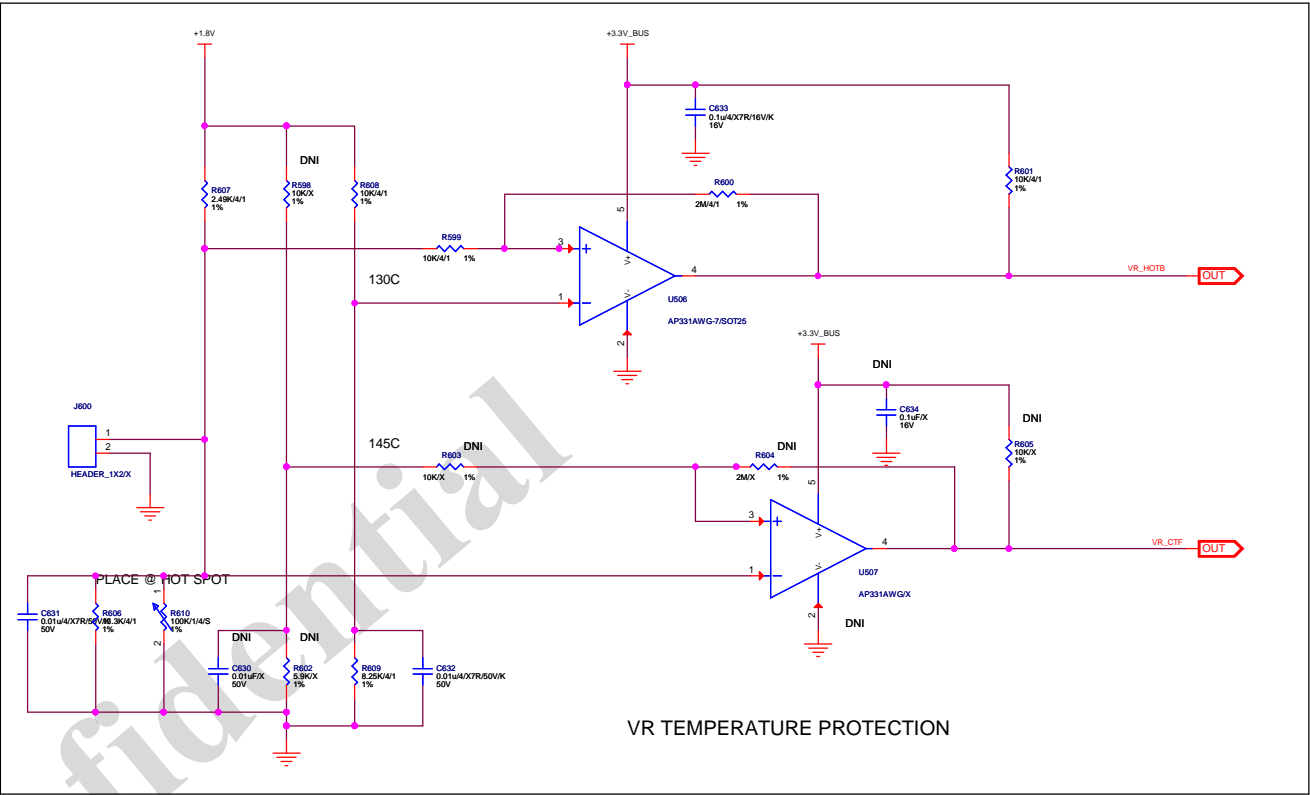
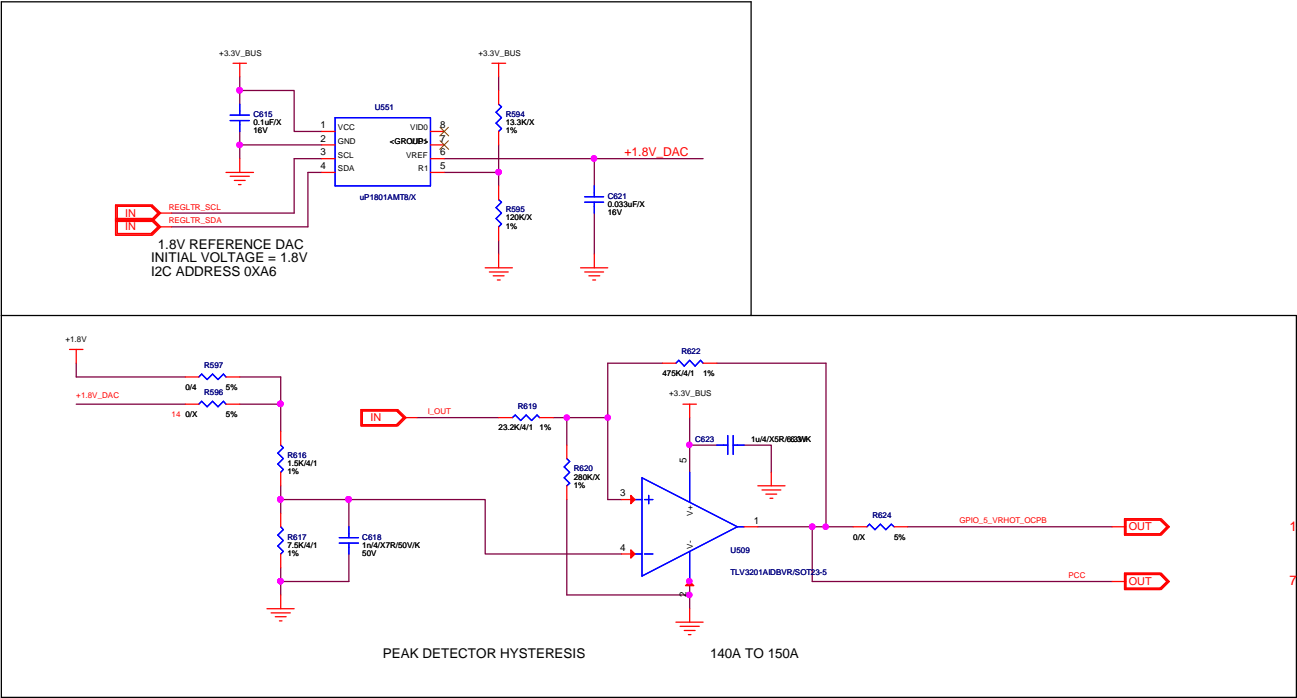


(14) VDDGFX/VDDC CNTL

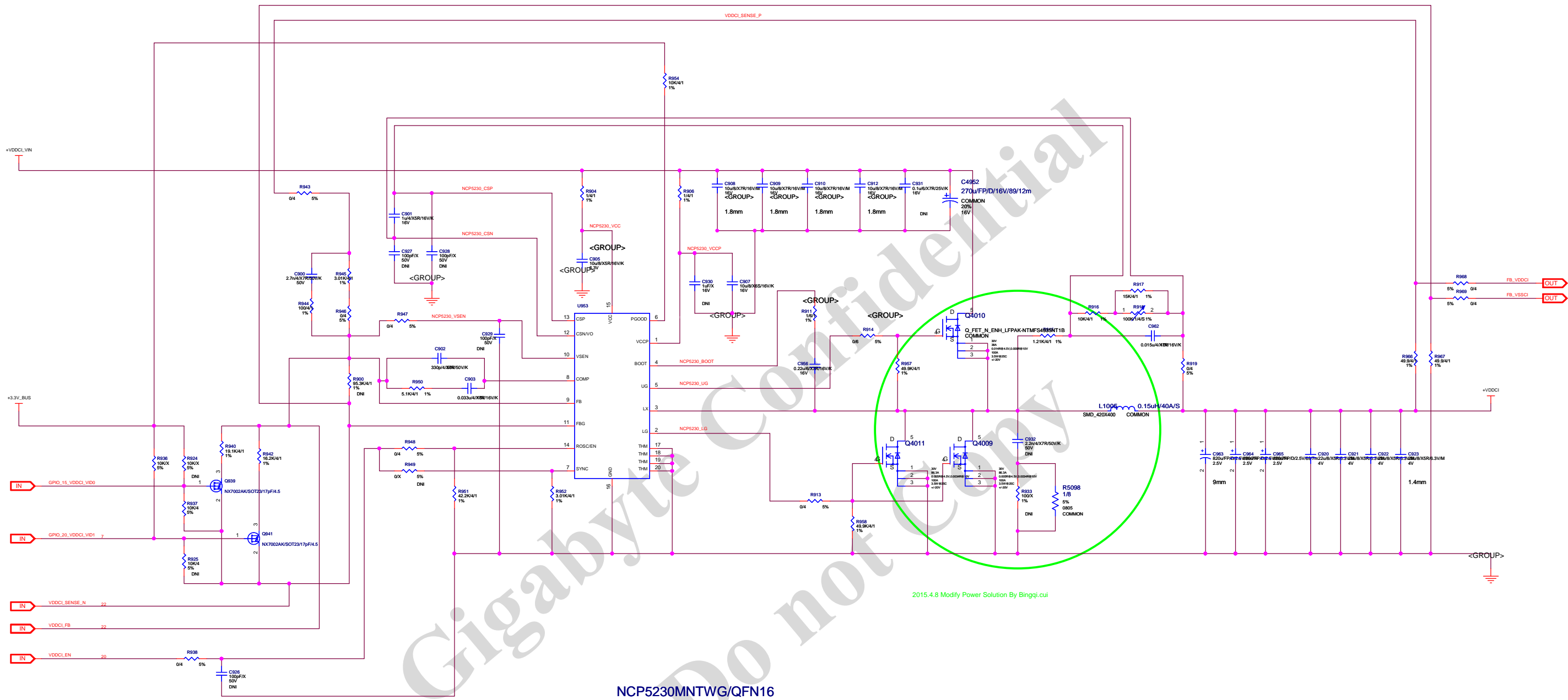




(17) PCC & VRHOT



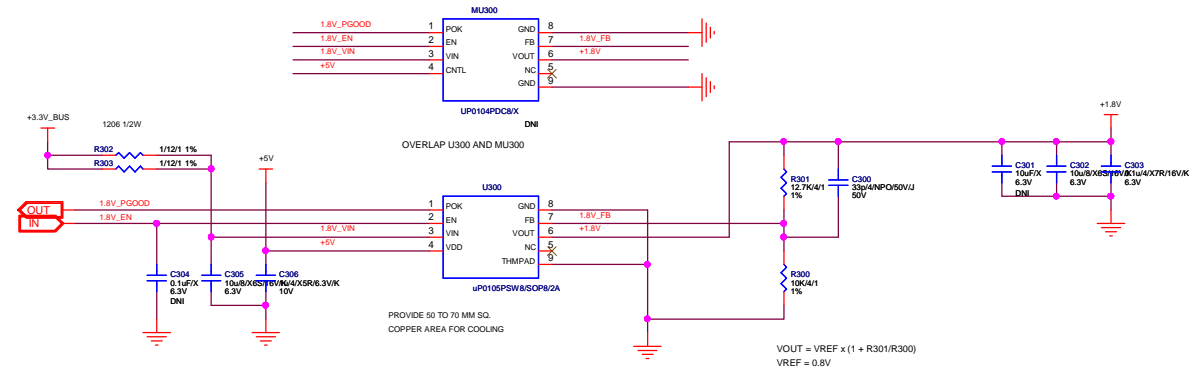
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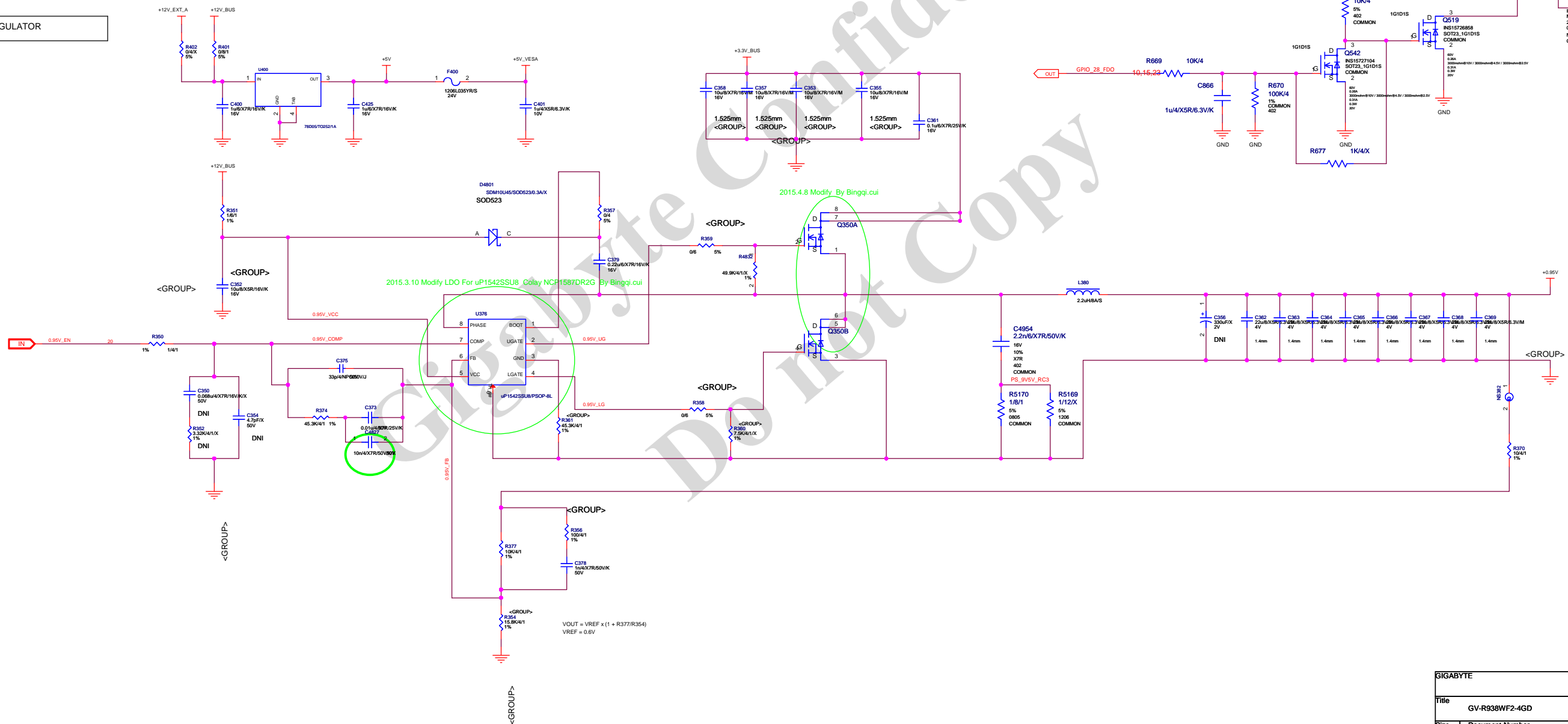
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(20) SMALL RAIL REGULATOR

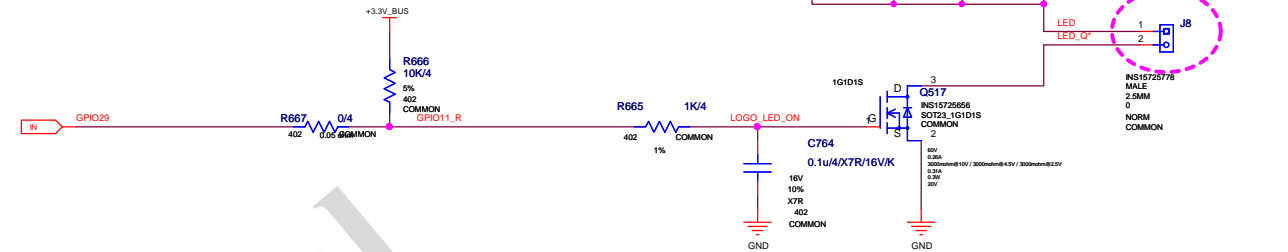
1.8V REGULATOR



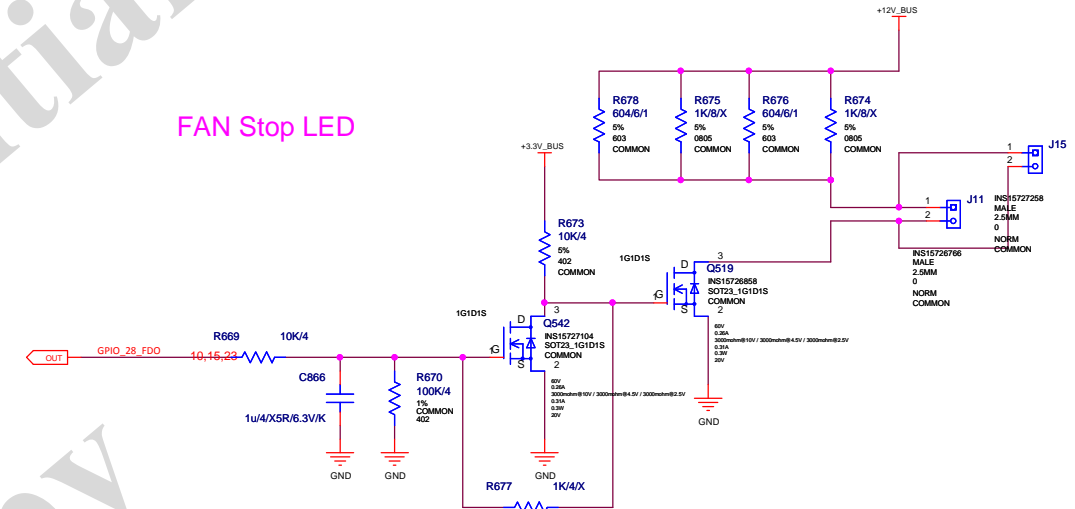
0.95V REGULATOR



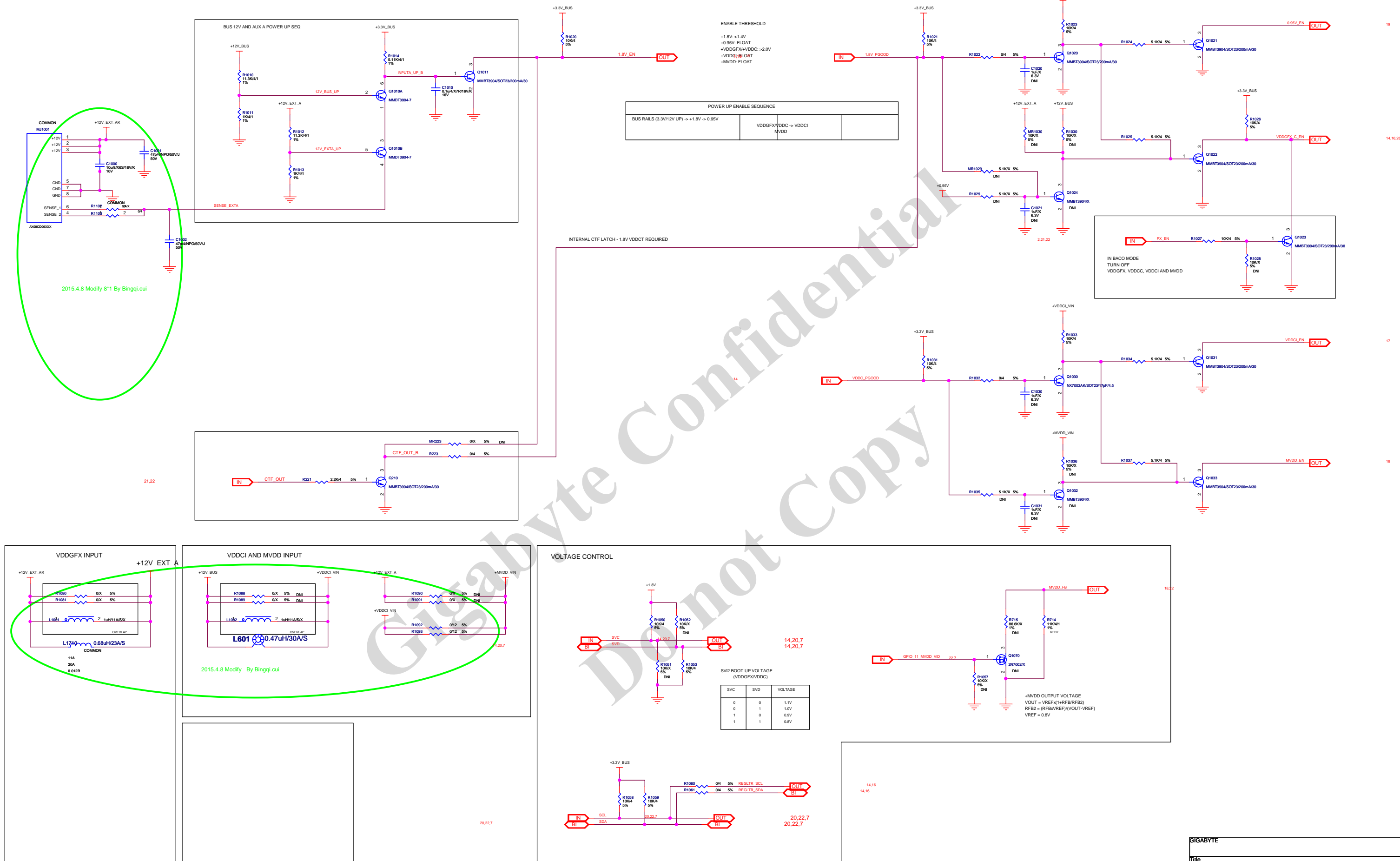
GeForce Logo LED



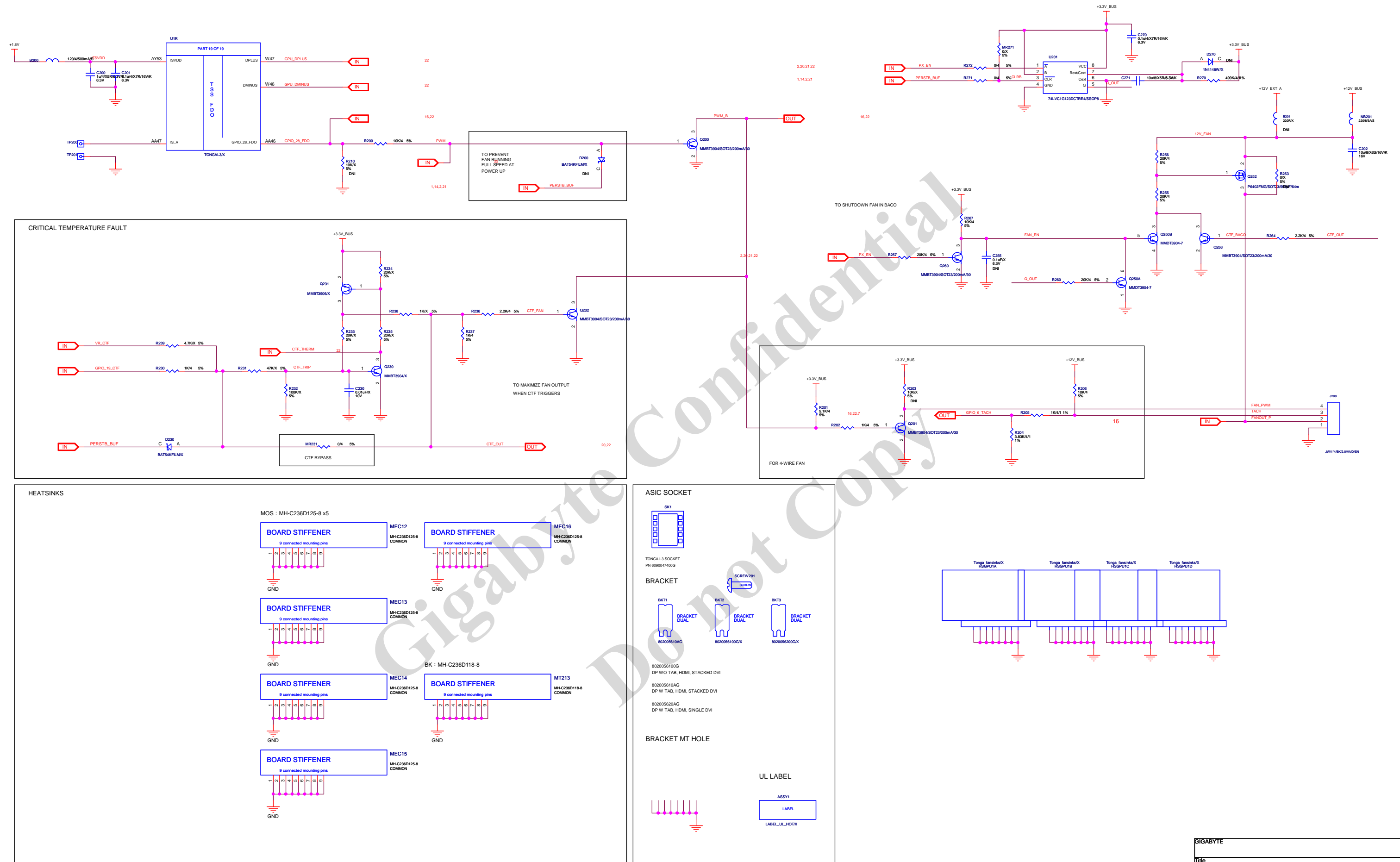
FAN Stop LED



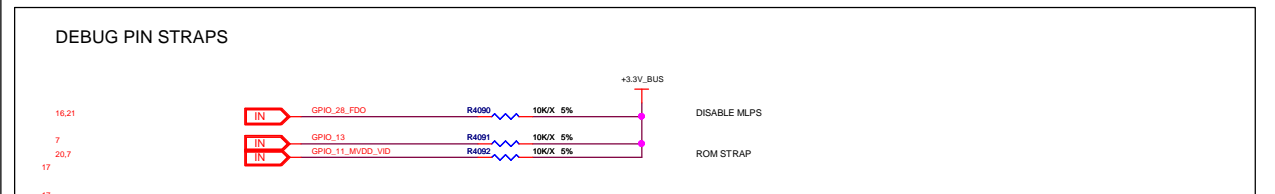
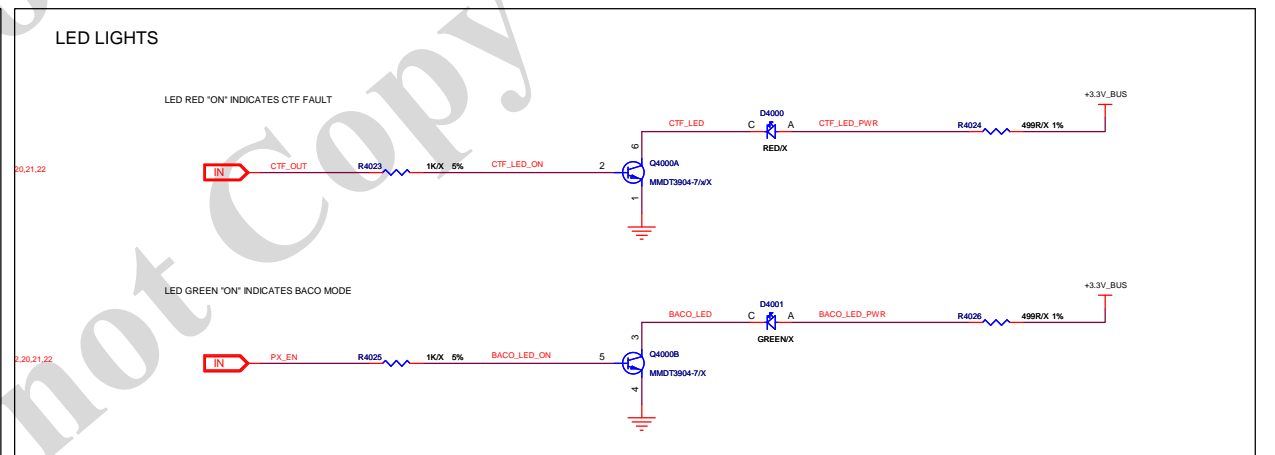
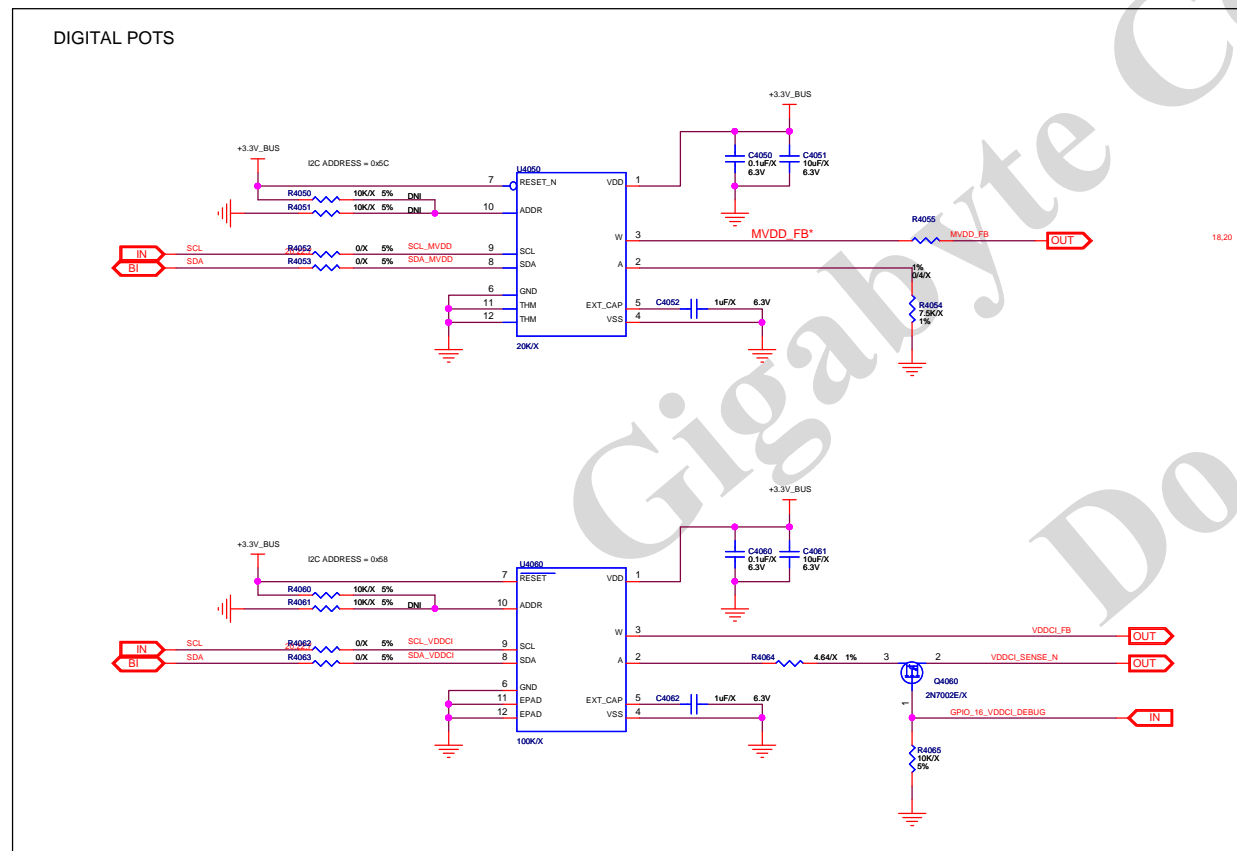
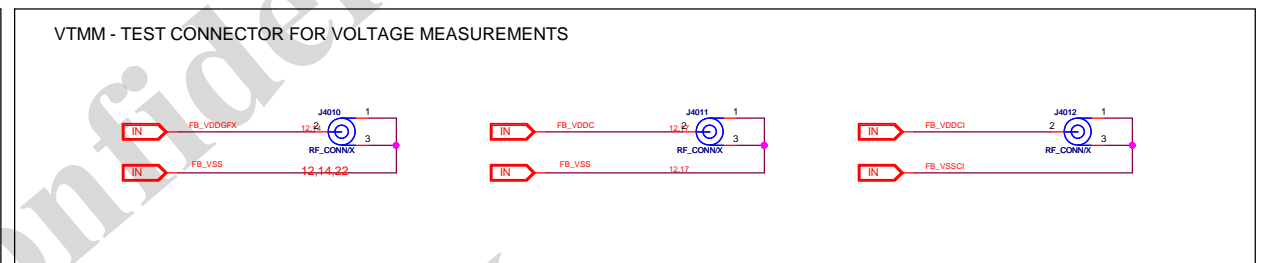
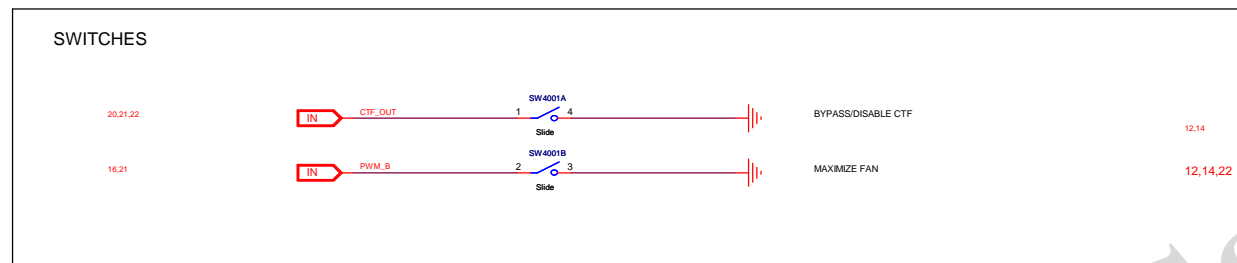
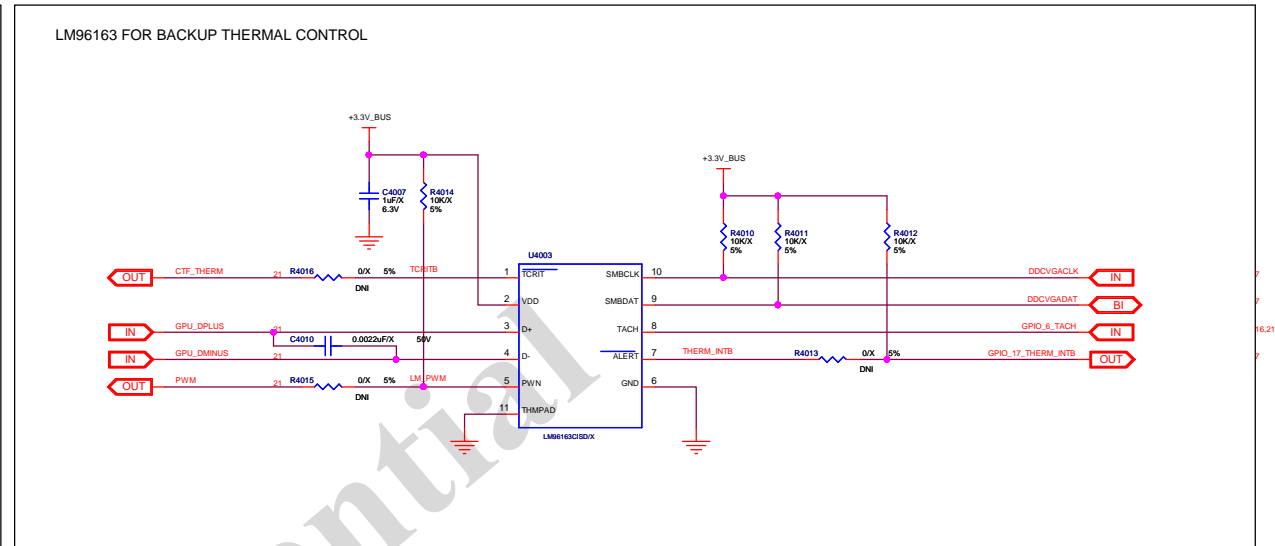
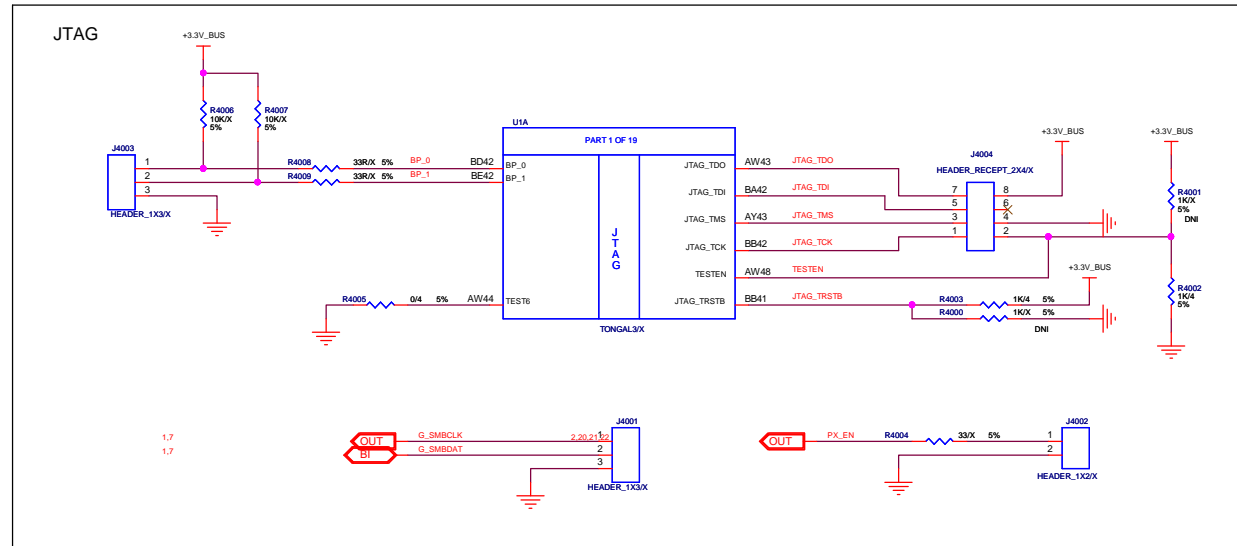
(21) POWER MANAGEMENT

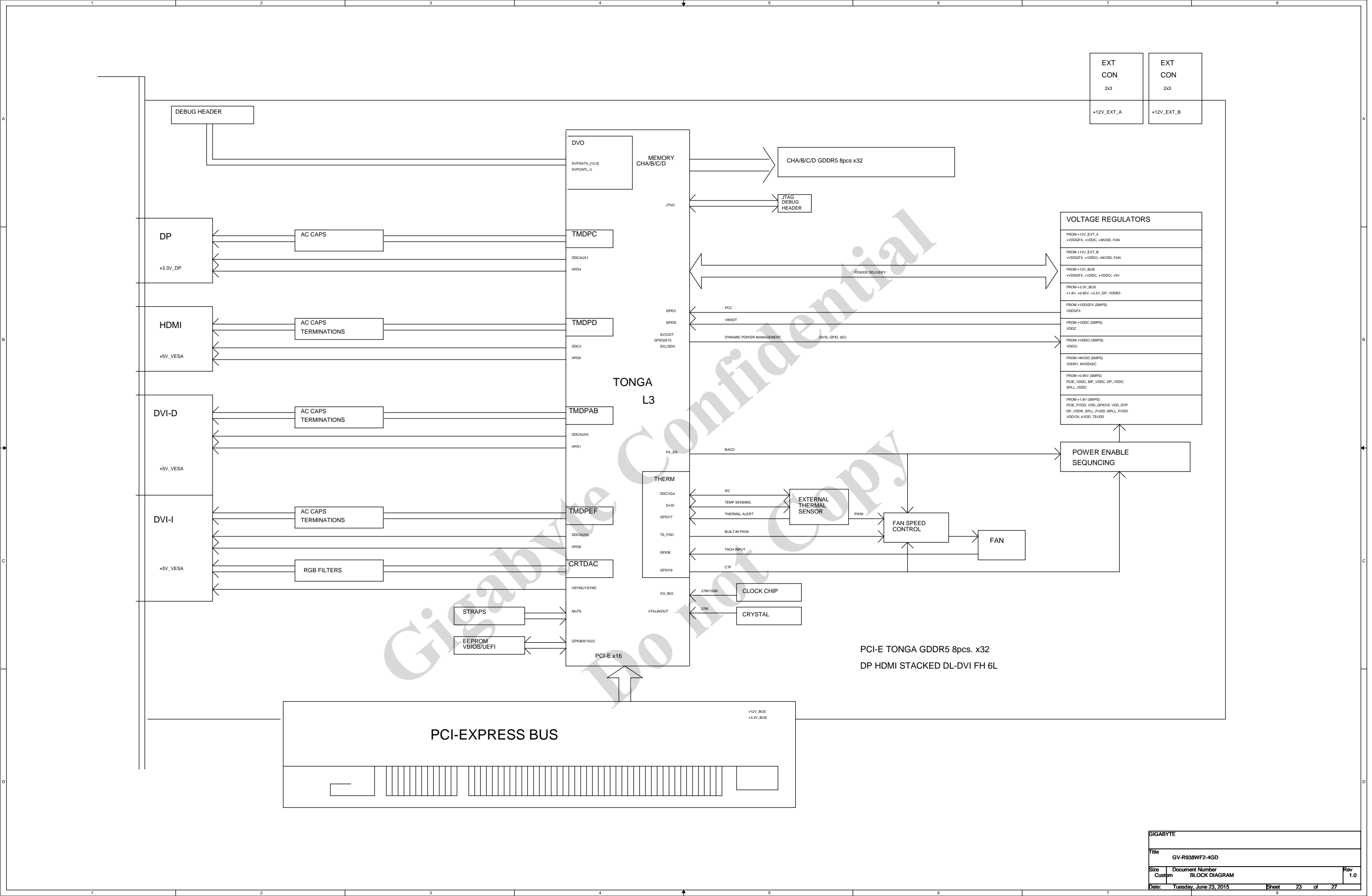


(22) MECH AND THERM MANAGEMENT



(23) DEBUG CIRCUIT





0	00A	04/26/2014	INITIAL SCHEMATIC
1	00B	05/26/2014	LAYOUT: FIX KELVIN CONNECTION FOR VDDCI RAIL ADD HEATSINK SYMBOL HS1

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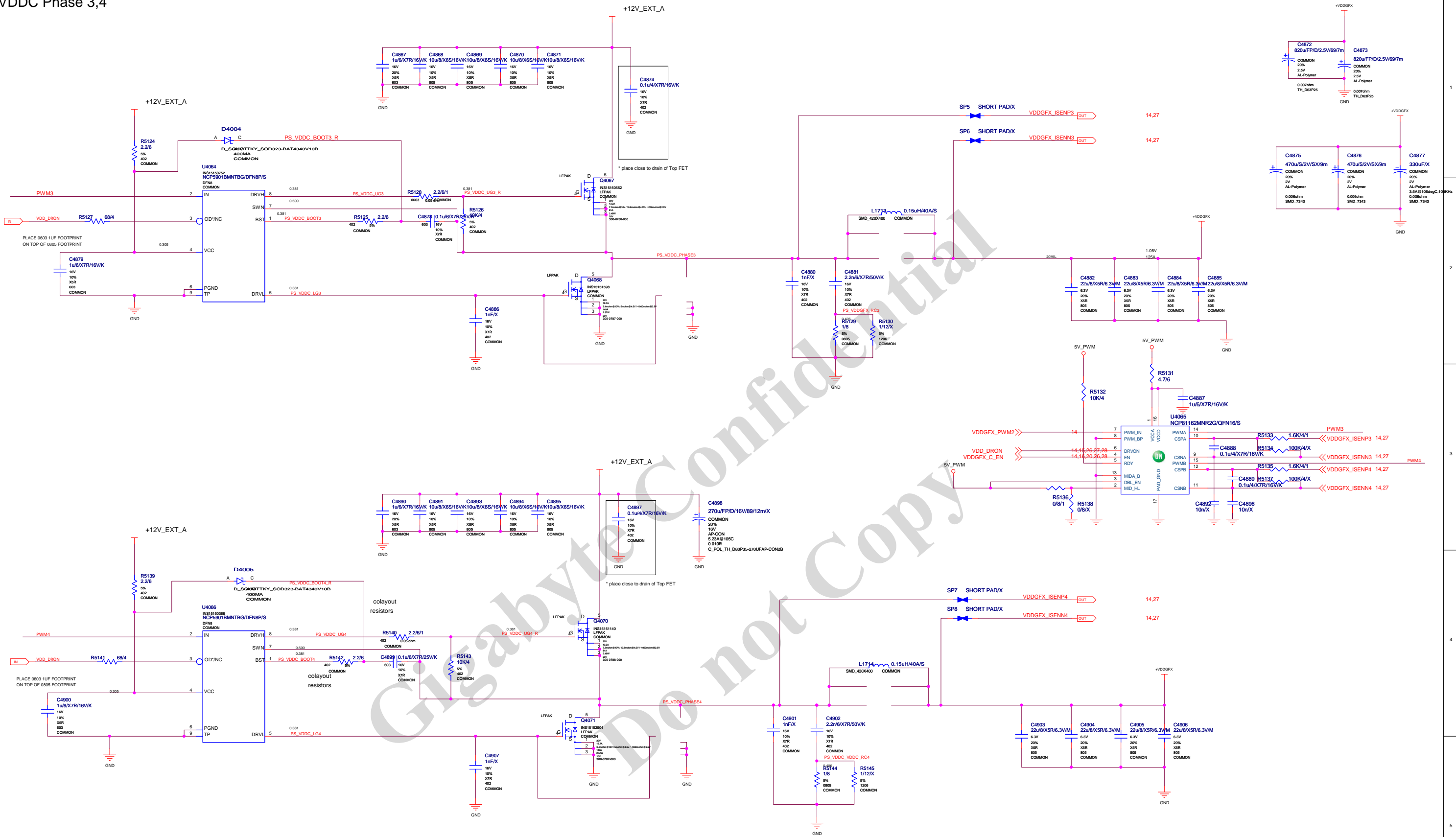
VDDC Phase 1,2



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PS_ VDDC Phase 3,4



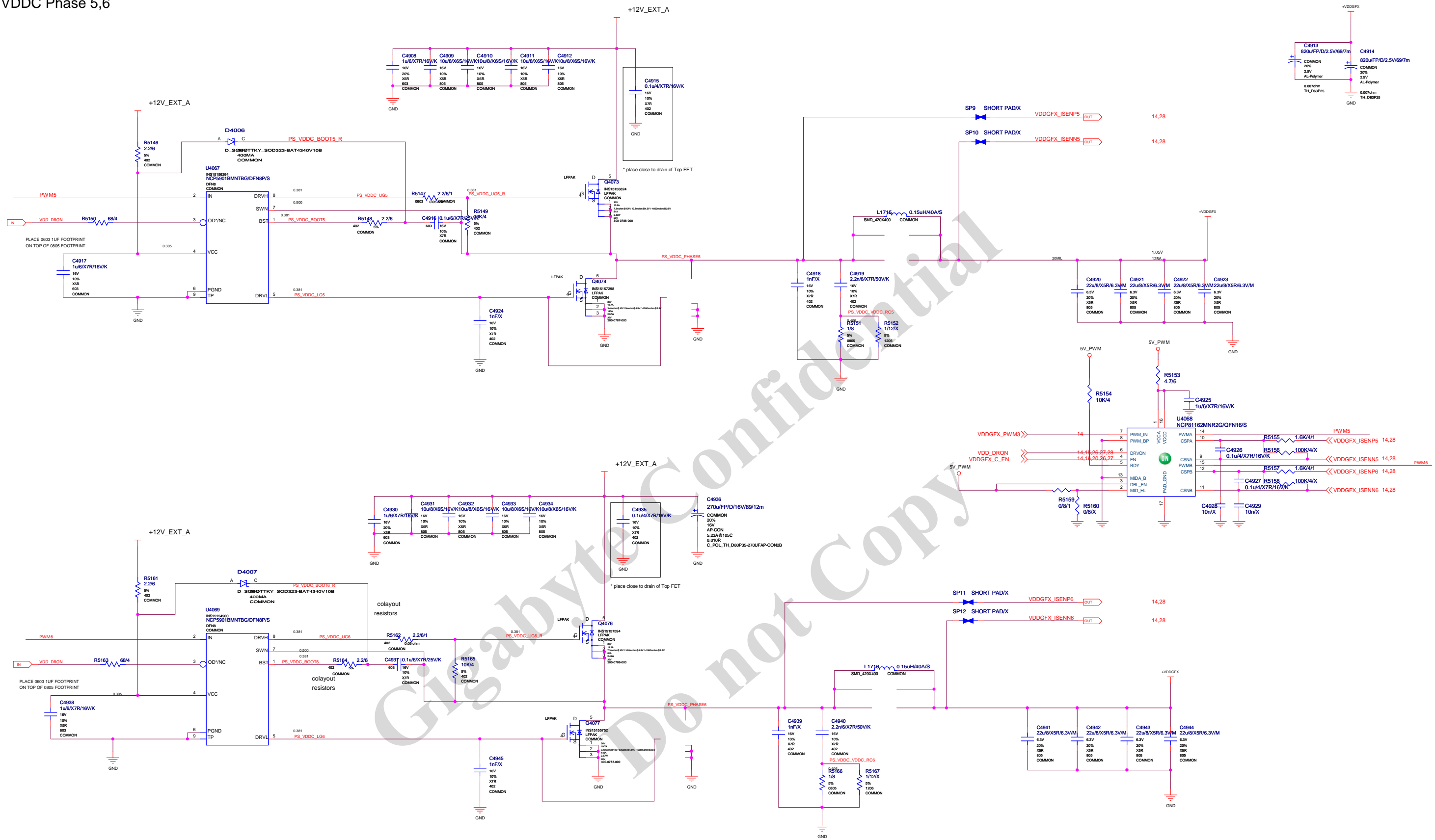
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PS: NVVDD Phase 1,2

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PS_ VDDC Phase 5,6



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PAGE DETAIL	PS: NVVDD Phase 1,2