

P872: GT218, DDR2 MEMORY 64MX16/128MX16

- Page 1: P872 Overview
- Page 2: PCI Express Interface
- Page 3: Frame Buffer Interface
- Page 4: DDR2 Memories Rank 0
- Page 5: DDR2 Memories Rank 1
- Page 6: DAC A VGA
- Page 7: DAC B VGA Header
- Page 8: IFPAB TMDS Interface
- Page 9: IFPC HDMI Connector
- Page 10: IFPD, IFPE Interface(Not used), Mechanical
- Page 11: XTAL, ROM, JTAG
- Page 12: Thermal Protection, Protected 3V3, Straps
- Page 13: GPU/Memory decaps on bottom (Test option)
- Page 14: Power Supply I: 5V,PEXVDD
- Page 15: Power Supply II: FBVDDQ
- Page 16: Power Supply II: NVVDD

REV	VARIANT	NVPN	ASSEMBLY
B	BASE	600-10872-BASE-100	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO. STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU0000	600-10872-0000-100	GT218-300-B, Dual-Rank, 589/1402/790, 1024MB/64b, 64Mx16 DDR3, DVI-DL+HDMI, DT
2	SKU0001	600-10872-0001-100	GT218-300-B, 589/1402/790, 512MB/64b, 64Mx16 DDR3, DVI-I+HDMI, DT
3	SKU0002	600-10872-0002-100	GT218-300-B, 589/1402/790, 1024MB/64b, 128MBx16 DDR3, DVI-I+HDMI, DT
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO. STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	P872 Overview

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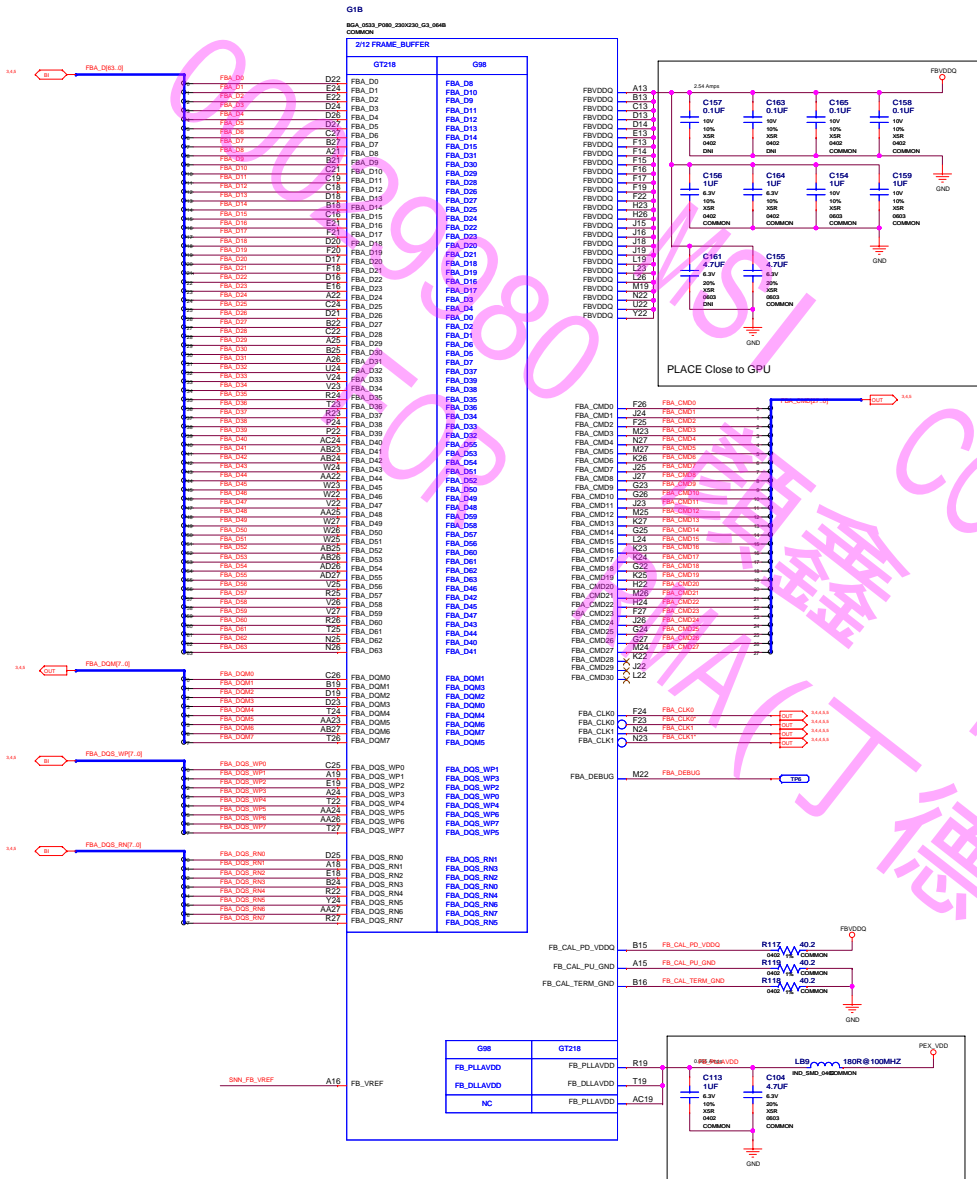
2701 SAN TOMAS EXPRESSWAY

SANTA CLARA, CA 95050, USA

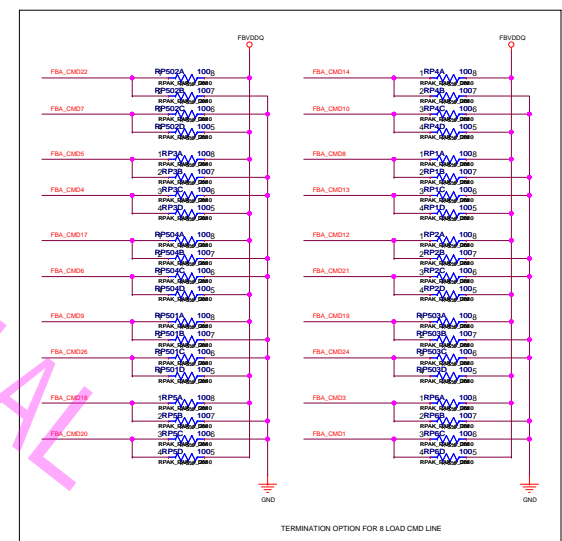
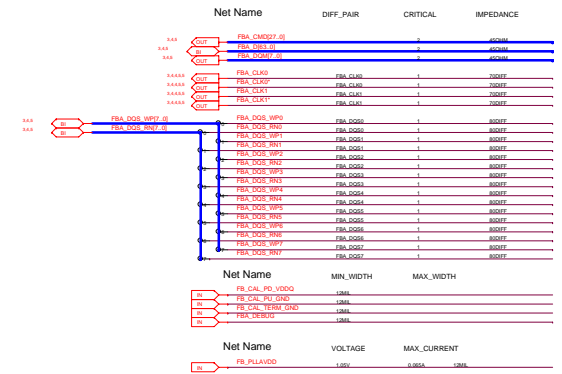
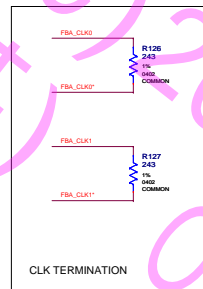
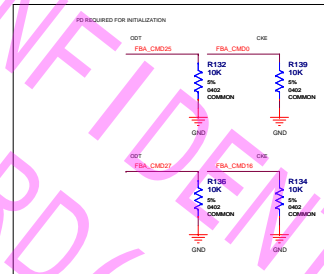


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PCB REV	P872-001	PAGE	
BOM REV	A	DATE	22-DEC-2009

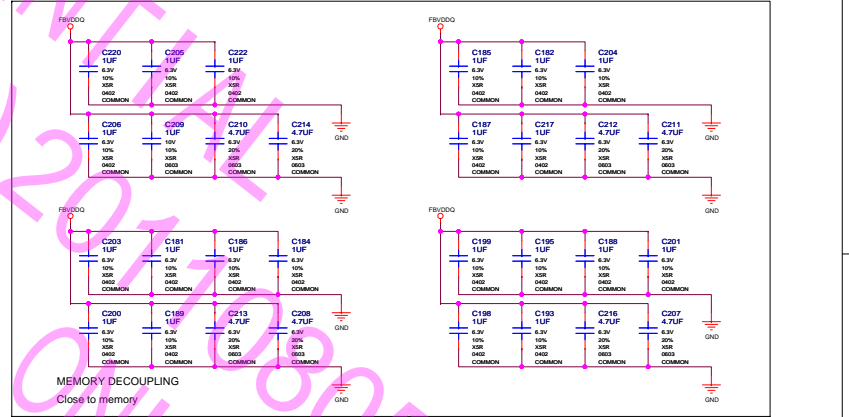
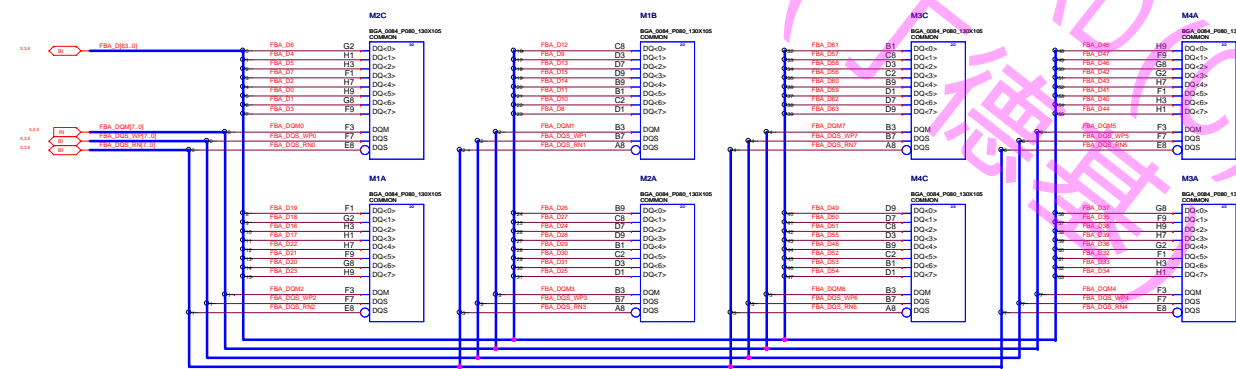
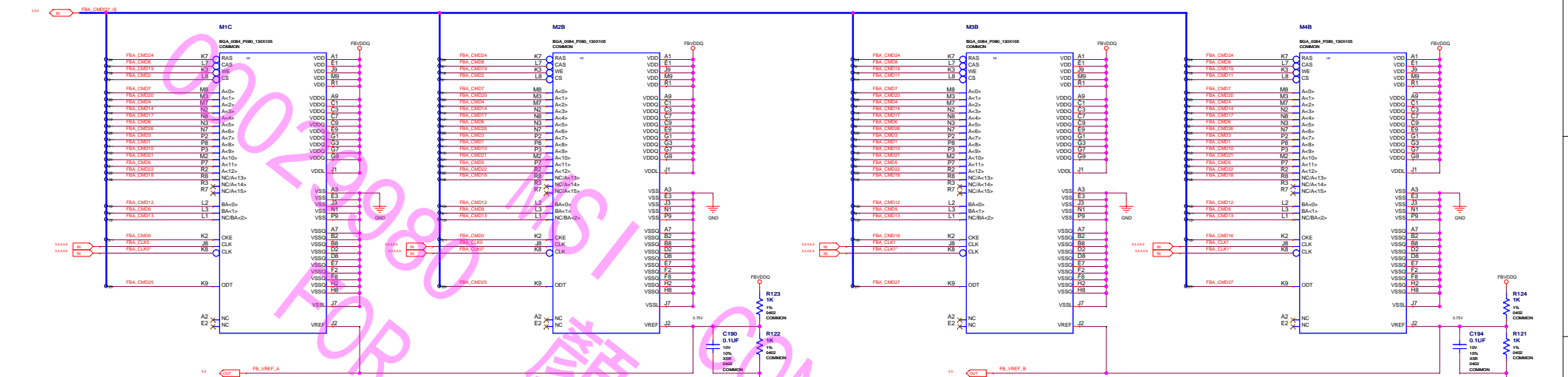
Frame Buffer Interface



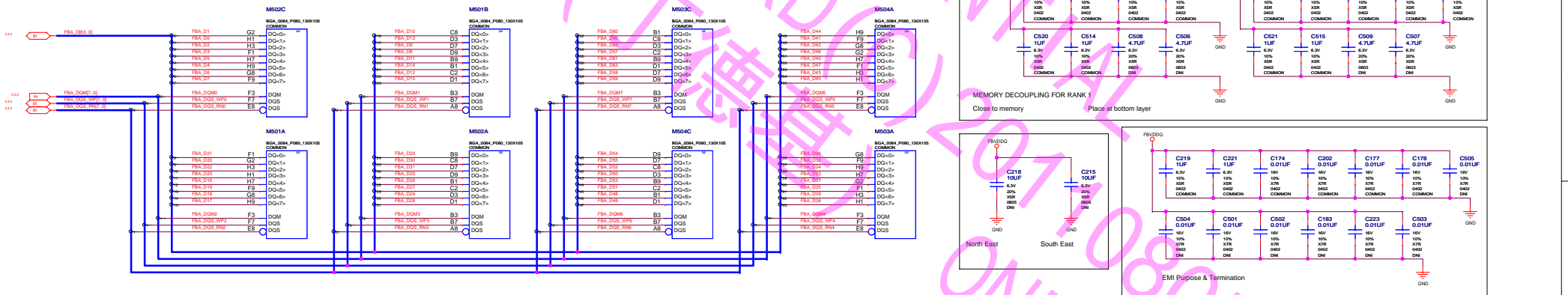
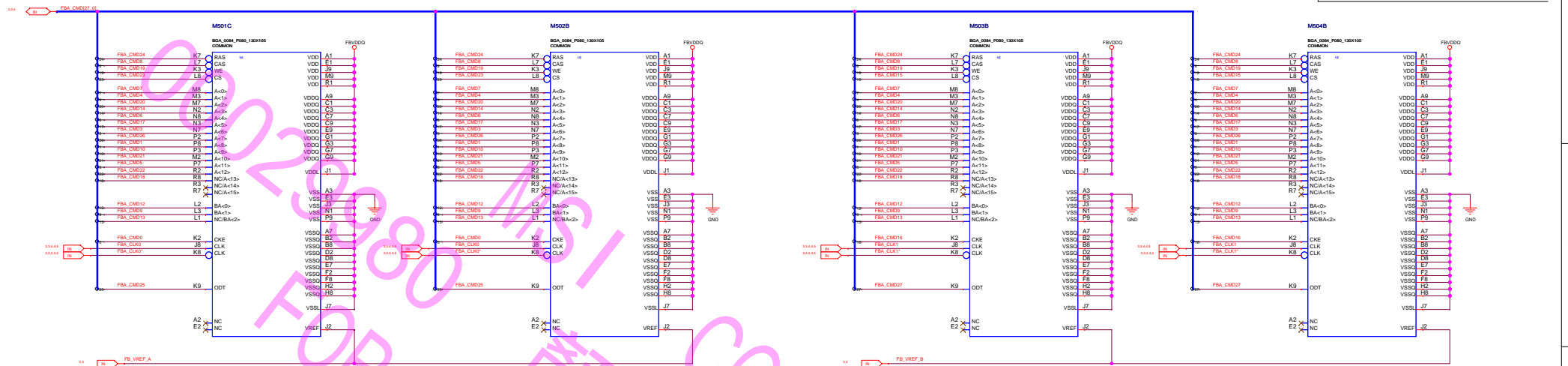
CMD-Add Map		RANK 0		RANK 1	
CMD	GT216 450-330	GT216 450-330	GT216 450-330	GT216 450-330	GT216 450-330
CAN00	CHE_1	NOT USED	CHE_1	NOT USED	NOT USED
CAN01	Auto	Auto	Auto	Auto	Auto
CAN02	CBS_1 ¹	NOT USED	CBS_1 ¹	NOT USED	NOT USED
CAN03	Auto	Auto	Auto	Auto	Auto
CAN04	Auto	Auto	Auto	Auto	Auto
CAN05	Auto	Auto	Auto	Auto	Auto
CAN06	Auto	Auto	Auto	Auto	Auto
CAN07	Auto	Auto	Auto	Auto	Auto
CAN08	CHE	CHE	CHE	CHE	CHE
CAN09	BA1	BA1	BA1	BA1	BA1
CAN10	Auto	Auto	Auto	Auto	Auto
CAN11	NOT USED	CBS_0 ¹	NOT USED	CBS_0 ¹	NOT USED
CAN12	BA2	BA2	BA2	BA2	BA2
CAN13	BA0	BA0	BA0	BA0	BA0
CAN14	BA0	BA0	BA0	BA0	BA0
CAN15	NOT USED	Auto	NOT USED	Auto	NOT USED
CAN16	NOT USED	CBS_1 ¹	NOT USED	CBS_1 ¹	NOT USED
CAN17	NOT USED	CHE	NOT USED	CHE	NOT USED
CAN18	Auto	Auto	Auto	Auto	Auto
CAN19	Auto	Auto	Auto	Auto	Auto
CAN20	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED
CAN21	Auto	Auto	Auto	Auto	Auto
CAN22	Auto	Auto	Auto	Auto	Auto
CAN23	Auto	Auto	Auto	Auto	Auto
CAN24	CBS_1 ¹	NOT USED	CBS_1 ¹	NOT USED	NOT USED
CAN25	BA0	BA0	BA0	BA0	BA0
CAN26	Auto	Auto	Auto	Auto	Auto
CAN27	NOT USED	CBS_1 ¹	NOT USED	CBS_1 ¹	NOT USED
CAN28	Auto	Auto	Auto	Auto	Auto
CAN29	REST	REST	REST	REST	REST
CAN30					
CAN31					



DDR2 Memories Rank 0




DDR2 Memories Rank 1

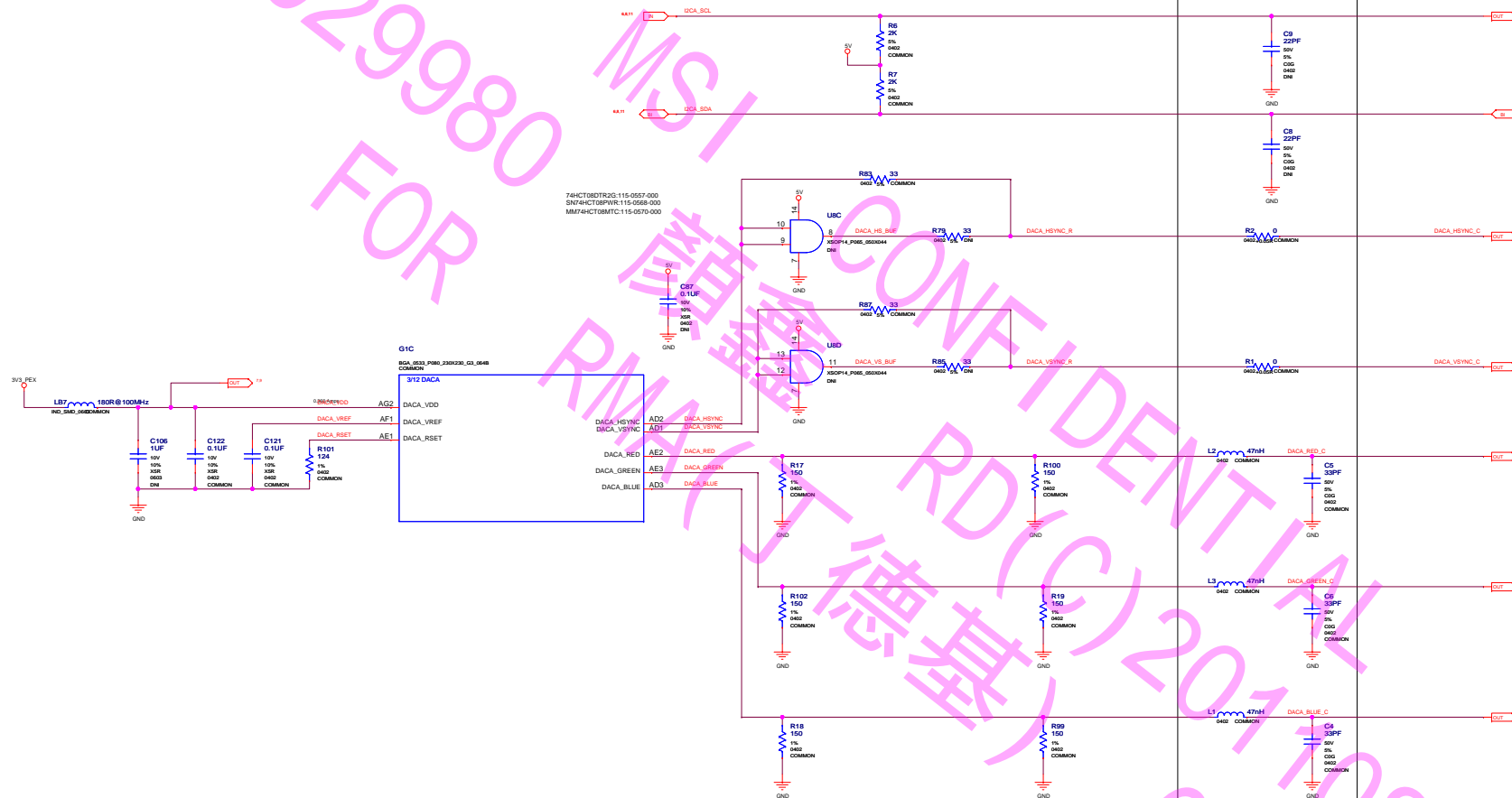


ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DDR3 Memories Rank 1

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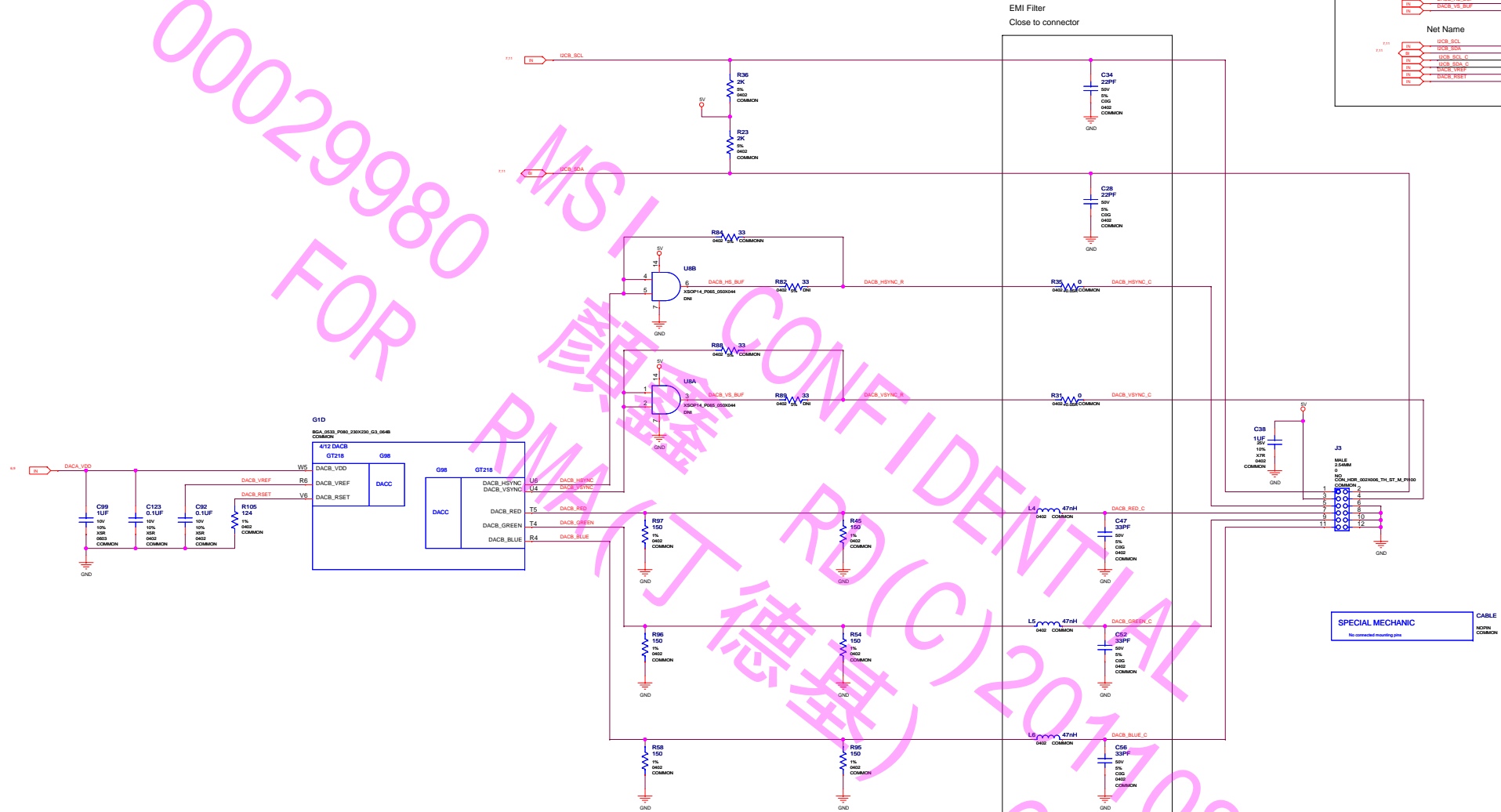
NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA			
NV_PN		600-10872-BASE-100	
PCB REV	PB72-A01	PAGE	
BOM REV	A	DATE	22-DEC-2009

DAC A VGA



	Net Name	CRITICAL	IMPEDANCE
	IN DADA_RIBD	1	50ohm
	OUT DADA_WRECK	1	50ohm
	IN DADA_BUBLE	1	50ohm
6.0	OUT DADA_RIBD_C	1	50ohm
	OUT DADA_WRECK_C	1	50ohm
	IN DADA_BUBLE_C	1	50ohm
	IN DADA_HYVING	2	50ohm
	OUT DADA_VR_BUP	2	50ohm
	IN DADA_VR_BUP	2	50ohm
	OUT DADA_HYVING_C	2	50ohm
6.0	OUT DADA_VTRNC_A	2	50ohm
	OUT DADA_VTRNC_C	2	50ohm
6.0	IN DADA_SCL	2	50ohm
6.0	IN DADA_SDA	2	50ohm
	IN DADA_RST_C	2	50ohm
	OUT DADA_RST_C	2	50ohm
	IN DADA_WREFP	2	50ohm
	IN DADA_RIBET	2	50ohm

DAC B VGA Header



Net Name		CRITICAL	IMPEDANCE
IN	DACB_RED	1	50ohm
IN	DACB_GREEN	1	50ohm
IN	DACB_BLUE	1	50ohm
IN	DACB_RED_C	1	50ohm
IN	DACB_GREEN_C	1	50ohm
IN	DACB_BLUE_C	1	50ohm
IN	DACB_HSYNC	2	50ohm
IN	DACB_VSYNC	2	50ohm
IN	DACB_HSYNC_C	2	50ohm
IN	DACB_VSYNC_C	2	50ohm
IN	DACB_PSI_BUF	2	50ohm
IN	DACB_VSI_BUF	2	50ohm

Net Name	MIN_WIDTH	MAX_WIDTH
IN	120B_SCL	
BI	120B_SDA	
	120B_SCL_C	
IN	120B_SDA_C	
IN	120B_VREF	120B
IN	120B_RESET	120B

SPECIAL MECHANIC
No connected mounting pins

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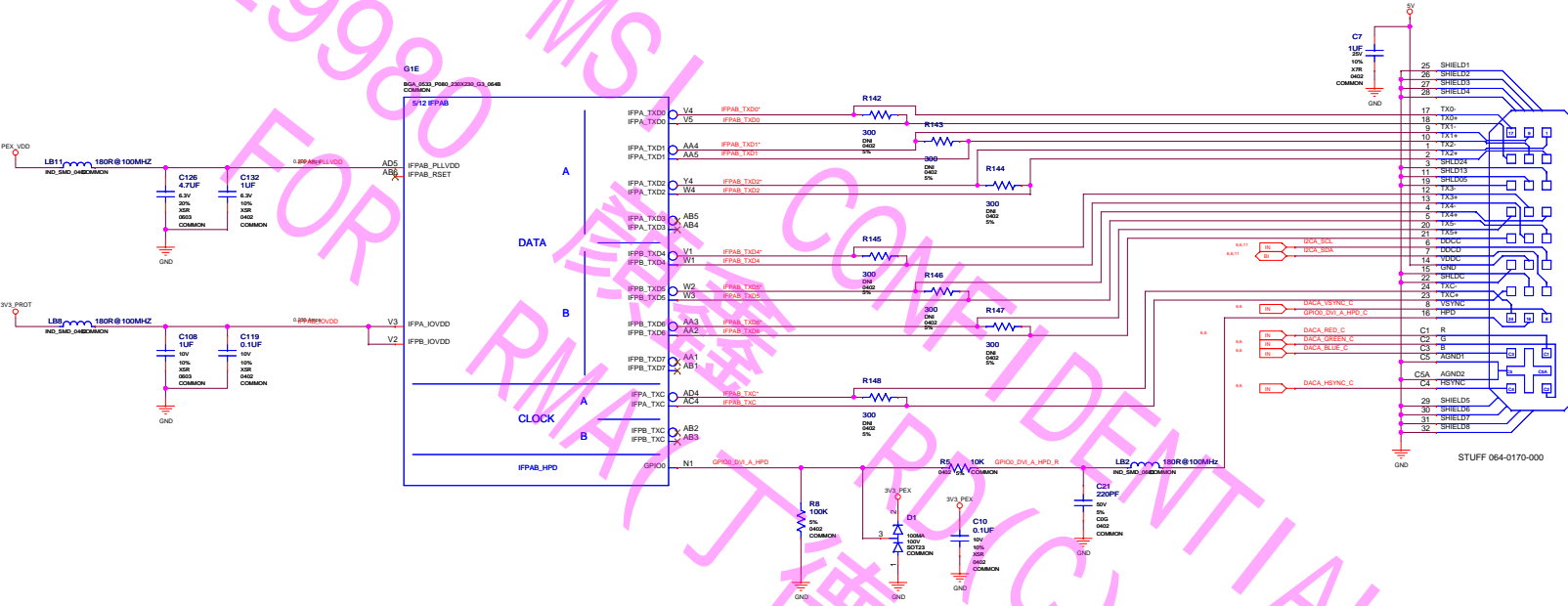
PCB REV	P872-A01	PAGE	
BOM REV	A	DATE	22-DEC-2009

A horizontal number line is shown, ranging from 0 to 100. The line is marked with vertical tick marks at intervals of 25 (0, 25, 50, 75, 100). The region between 0 and 25 is shaded in pink. The letter 'H' is placed below the number line at the 75 mark.



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IFPAB TMDs Interface



Net Name	DIFF_PAIR	CRITICAL	IMPEDANCE
IFPAB_TX00	IFPAB_TX00	I	5000T
IFPAB_TX01	IFPAB_TX01	I	5000T
IFPAB_TX02	IFPAB_TX02	I	5000T
IFPAB_TX03	IFPAB_TX03	I	5000T
IFPAB_TX04	IFPAB_TX04	I	5000T
IFPAB_TX05	IFPAB_TX05	I	5000T
IFPAB_TX06	IFPAB_TX06	I	5000T
IFPAB_TX07	IFPAB_TX07	I	5000T
IFPAB_TX08	IFPAB_TX08	I	5000T
IFPAB_TX09	IFPAB_TX09	I	5000T
IFPAB_TX10	IFPAB_TX10	I	5000T
IFPAB_TX11	IFPAB_TX11	I	5000T
IFPAB_TX12	IFPAB_TX12	I	5000T
IFPAB_TX13	IFPAB_TX13	I	5000T
IFPAB_TX14	IFPAB_TX14	I	5000T
IFPAB_TX15	IFPAB_TX15	I	5000T
IFPAB_TX16	IFPAB_TX16	I	5000T
IFPAB_TX17	IFPAB_TX17	I	5000T
IFPAB_TX18	IFPAB_TX18	I	5000T
IFPAB_TX19	IFPAB_TX19	I	5000T
IFPAB_TX20	IFPAB_TX20	I	5000T
IFPAB_TX21	IFPAB_TX21	I	5000T
IFPAB_TX22	IFPAB_TX22	I	5000T
IFPAB_TX23	IFPAB_TX23	I	5000T
IFPAB_TX24	IFPAB_TX24	I	5000T
IFPAB_TX25	IFPAB_TX25	I	5000T
IFPAB_TX26	IFPAB_TX26	I	5000T
IFPAB_TX27	IFPAB_TX27	I	5000T
IFPAB_TX28	IFPAB_TX28	I	5000T
IFPAB_TX29	IFPAB_TX29	I	5000T
IFPAB_TX30	IFPAB_TX30	I	5000T
IFPAB_TX31	IFPAB_TX31	I	5000T
IFPAB_TX32	IFPAB_TX32	I	5000T
IFPAB_TX33	IFPAB_TX33	I	5000T
IFPAB_TX34	IFPAB_TX34	I	5000T
IFPAB_TX35	IFPAB_TX35	I	5000T
IFPAB_TX36	IFPAB_TX36	I	5000T
IFPAB_TX37	IFPAB_TX37	I	5000T
IFPAB_TX38	IFPAB_TX38	I	5000T
IFPAB_TX39	IFPAB_TX39	I	5000T
IFPAB_TX40	IFPAB_TX40	I	5000T
IFPAB_TX41	IFPAB_TX41	I	5000T
IFPAB_TX42	IFPAB_TX42	I	5000T
IFPAB_TX43	IFPAB_TX43	I	5000T
IFPAB_TX44	IFPAB_TX44	I	5000T
IFPAB_TX45	IFPAB_TX45	I	5000T
IFPAB_TX46	IFPAB_TX46	I	5000T
IFPAB_TX47	IFPAB_TX47	I	5000T
IFPAB_TX48	IFPAB_TX48	I	5000T
IFPAB_TX49	IFPAB_TX49	I	5000T
IFPAB_TX50	IFPAB_TX50	I	5000T
IFPAB_TX51	IFPAB_TX51	I	5000T
IFPAB_TX52	IFPAB_TX52	I	5000T
IFPAB_TX53	IFPAB_TX53	I	5000T
IFPAB_TX54	IFPAB_TX54	I	5000T
IFPAB_TX55	IFPAB_TX55	I	5000T
IFPAB_TX56	IFPAB_TX56	I	5000T
IFPAB_TX57	IFPAB_TX57	I	5000T
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IFPAB_TX62	IFPAB_TX62	I	5000T
IFPAB_TX63	IFPAB_TX63	I	5000T
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IFPAB_TX66	IFPAB_TX66	I	5000T
IFPAB_TX67	IFPAB_TX67	I	5000T
IFPAB_TX68	IFPAB_TX68	I	5000T
IFPAB_TX69	IFPAB_TX69	I	5000T
IFPAB_TX70	IFPAB_TX70	I	5000T
IFPAB_TX71	IFPAB_TX71	I	5000T
IFPAB_TX72	IFPAB_TX72	I	5000T
IFPAB_TX73	IFPAB_TX73	I	5000T
IFPAB_TX74	IFPAB_TX74	I	5000T
IFPAB_TX75	IFPAB_TX75	I	5000T
IFPAB_TX76	IFPAB_TX76	I	5000T
IFPAB_TX77	IFPAB_TX77	I	5000T
IFPAB_TX78	IFPAB_TX78	I	5000T
IFPAB_TX79	IFPAB_TX79	I	5000T
IFPAB_TX80	IFPAB_TX80	I	5000T
IFPAB_TX81	IFPAB_TX81	I	5000T
IFPAB_TX82	IFPAB_TX82	I	5000T
IFPAB_TX83	IFPAB_TX83	I	5000T
IFPAB_TX84	IFPAB_TX84	I	5000T
IFPAB_TX85	IFPAB_TX85	I	5000T
IFPAB_TX86	IFPAB_TX86	I	5000T
IFPAB_TX87	IFPAB_TX87	I	5000T
IFPAB_TX88	IFPAB_TX88	I	5000T
IFPAB_TX89	IFPAB_TX89	I	5000T
IFPAB_TX90	IFPAB_TX90	I	5000T
IFPAB_TX91	IFPAB_TX91	I	5000T
IFPAB_TX92	IFPAB_TX92	I	5000T
IFPAB_TX93	IFPAB_TX93	I	5000T
IFPAB_TX94	IFPAB_TX94	I	5000T
IFPAB_TX95	IFPAB_TX95	I	5000T
IFPAB_TX96	IFPAB_TX96	I	5000T
IFPAB_TX97	IFPAB_TX97	I	5000T
IFPAB_TX98	IFPAB_TX98	I	5000T
IFPAB_TX99	IFPAB_TX99	I	5000T
IFPAB_TX100	IFPAB_TX100	I	5000T

Net Name	MIN_WIDTH	MAX_WIDTH
IFPAB_TX00	1000	1000
IFPAB_TX01	1000	1000
IFPAB_TX02	1000	1000
IFPAB_TX03	1000	1000
IFPAB_TX04	1000	1000
IFPAB_TX05	1000	1000
IFPAB_TX06	1000	1000
IFPAB_TX07	1000	1000
IFPAB_TX08	1000	1000
IFPAB_TX09	1000	1000
IFPAB_TX10	1000	1000
IFPAB_TX11	1000	1000
IFPAB_TX12	1000	1000
IFPAB_TX13	1000	1000
IFPAB_TX14	1000	1000
IFPAB_TX15	1000	1000
IFPAB_TX16	1000	1000
IFPAB_TX17	1000	1000
IFPAB_TX18	1000	1000
IFPAB_TX19	1000	1000
IFPAB_TX20	1000	1000
IFPAB_TX21	1000	1000
IFPAB_TX22	1000	1000
IFPAB_TX23	1000	1000
IFPAB_TX24	1000	1000
IFPAB_TX25	1000	1000
IFPAB_TX26	1000	1000
IFPAB_TX27	1000	1000
IFPAB_TX28	1000	1000
IFPAB_TX29	1000	1000
IFPAB_TX30	1000	1000
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IFPAB_TX32	1000	1000
IFPAB_TX33	1000	1000
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IFPAB_TX46	1000	1000
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IFPAB_TX48	1000	1000
IFPAB_TX49	1000	1000
IFPAB_TX50	1000	1000
IFPAB_TX51	1000	1000
IFPAB_TX52	1000	1000
IFPAB_TX53	1000	1000
IFPAB_TX54	1000	1000
IFPAB_TX55	1000	1000
IFPAB_TX56	1000	1000
IFPAB_TX57	1000	1000
IFPAB_TX58	1000	1000
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IFPAB_TX79	1000	1000
IFPAB_TX80	1000	1000
IFPAB_TX81	1000	1000
IFPAB_TX82	1000	1000
IFPAB_TX83	1000	1000
IFPAB_TX84	1000	1000
IFPAB_TX85	1000	1000
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IFPAB_TX88	1000	1000
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IFPAB_TX94	1000	1000
IFPAB_TX95	1000	1000
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IFPAB_TX97	1000	1000
IFPAB_TX98	1000	1000
IFPAB_TX99	1000	1000
IFPAB_TX100	1000	1000

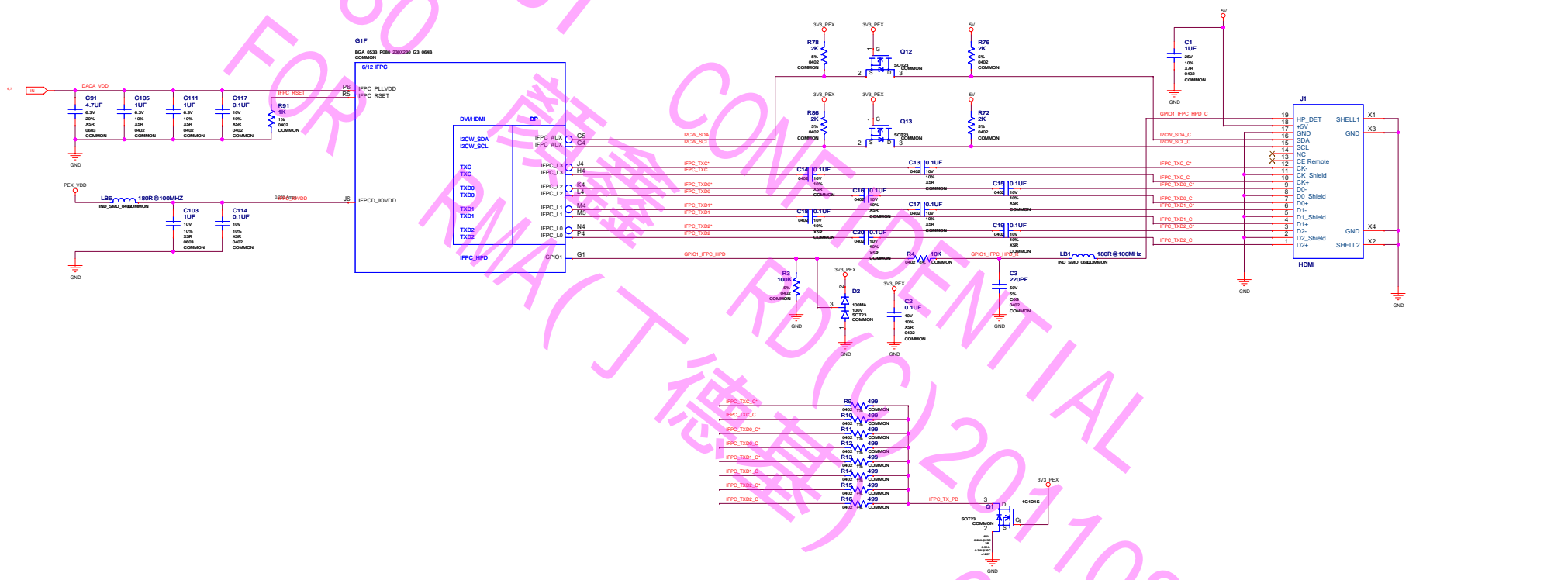
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IFPAB_TX01	1.80V	0.000A
IFPAB_TX02	1.80V	0.000A
IFPAB_TX03	1.80V	0.000A
IFPAB_TX04	1.80V	0.000A
IFPAB_TX05	1.80V	0.000A
IFPAB_TX06	1.80V	0.000A
IFPAB_TX07	1.80V	0.000A
IFPAB_TX08	1.80V	0.000A
IFPAB_TX09	1.80V	0.000A
IFPAB_TX10	1.80V	0.000A
IFPAB_TX11	1.80V	0.000A
IFPAB_TX12	1.80V	0.000A
IFPAB_TX13	1.80V	0.000A
IFPAB_TX14	1.80V	0.000A
IFPAB_TX15	1.80V	0.000A
IFPAB_TX16	1.80V	0.000A
IFPAB_TX17	1.80V	0.000A
IFPAB_TX18	1.80V	0.000A
IFPAB_TX19	1.80V	0.000A
IFPAB_TX20	1.80V	0.000A
IFPAB_TX21	1.80V	0.000A
IFPAB_TX22	1.80V	0.000A
IFPAB_TX23	1.80V	0.000A
IFPAB_TX24	1.80V	0.000A
IFPAB_TX25	1.80V	0.000A
IFPAB_TX26	1.80V	0.000A
IFPAB_TX27	1.80V	0.000A
IFPAB_TX28	1.80V	0.000A
IFPAB_TX29	1.80V	0.000A
IFPAB_TX30	1.80V	0.000A
IFPAB_TX31	1.80V	0.000A
IFPAB_TX32	1.80V	0.000A
IFPAB_TX33	1.80V	0.000A
IFPAB_TX34	1.80V	0.000A
IFPAB_TX35	1.80V	0.000A
IFPAB_TX36	1.80V	0.000A
IFPAB_TX37	1.80V	0.000A
IFPAB_TX38	1.80V	0.000A
IFPAB_TX39	1.80V	0.000A
IFPAB_TX40	1.80V	0.000A
IFPAB_TX41	1.80V	0.000A
IFPAB_TX42	1.80V	0.000A
IFPAB_TX43	1.80V	0.000A
IFPAB_TX44	1.80V	0.000A
IFPAB_TX45	1.80V	0.000A
IFPAB_TX46	1.80V	0.000A
IFPAB_TX47	1.80V	0.000A
IFPAB_TX48	1.80V	0.000A
IFPAB_TX49	1.80V	0.000A
IFPAB_TX50	1.80V	0.000A
IFPAB_TX51	1.80V	0.000A
IFPAB_TX52	1.80V	0.000A
IFPAB_TX53	1.80V	0.000A
IFPAB_TX54	1.80V	0.000A
IFPAB_TX55	1.80V	0.000A
IFPAB_TX56	1.80V	0.000A
IFPAB_TX57	1.80V	0.000A
IFPAB_TX58	1.80V	0.000A
IFPAB_TX59	1.80V	0.000A
IFPAB_TX60	1.80V	0.000A
IFPAB_TX61	1.80V	0.000A
IFPAB_TX62	1.80V	0.000A
IFPAB_TX63	1.80V	0.000A
IFPAB_TX64	1.80V	0.000A
IFPAB_TX65	1.80V	0.000A
IFPAB_TX66	1.80V	0.000A
IFPAB_TX67	1.80V	0.000A
IFPAB_TX68	1.80V	0.000A
IFPAB_TX69	1.80V	0.000A
IFPAB_TX70	1.80V	0.000A
IFPAB_TX71	1.80V	0.000A
IFPAB_TX72	1.80V	0.000A
IFPAB_TX73	1.80V	0.000A
IFPAB_TX74	1.80V	0.000A
IFPAB_TX75	1.80V	0.000A
IFPAB_TX76	1.80V	0.000A
IFPAB_TX77	1.80V	0.000A
IFPAB_TX78	1.80V	0.000A
IFPAB_TX79	1.80V	0.000A
IFPAB_TX80	1.80V	0.000A
IFPAB_TX81	1.80V	0.000A
IFPAB_TX82	1.80V	0.000A
IFPAB_TX83	1.80V	0.000A
IFPAB_TX84	1.80V	0.000A
IFPAB_TX85	1.80V	0.000A
IFPAB_TX86	1.80V	0.000A
IFPAB_TX87	1.80V	0.000A
IFPAB_TX88	1.80V	0.000A
IFPAB_TX89	1.80V	0.000A
IFPAB_TX90	1.80V	0.000A
IFPAB_TX91	1.80V	0.000A
IFPAB_TX92	1.80V	0.000A
IFPAB_TX93	1.80V	0.000A
IFPAB_TX94	1.80V	0.000A
IFPAB_TX95	1.80V	0.000A
IFPAB_TX96	1.80V	0.000A
IFPAB_TX97	1.80V	0.000A
IFPAB_TX98	1.80V	0.000A
IFPAB_TX99	1.80V	0.000A
IFPAB_TX100	1.80V	0.000A

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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	IFPAB TMDs Interface

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PCB REV	P072701	PAGE	
BOM REV	A	DATE	22-DEC-2009

IFPC HDMI Connector



Net Name	DIFF_PAIR	CRITICAL	IMPEDANCE
IFPC_TX00	IFPC_TX00	I	50000
IFPC_TX01	IFPC_TX01	I	50000
IFPC_TX02	IFPC_TX02	I	50000
IFPC_TX03	IFPC_TX03	I	50000
IFPC_TX04	IFPC_TX04	I	50000
IFPC_TX05	IFPC_TX05	I	50000
IFPC_TX06	IFPC_TX06	I	50000
IFPC_TX07	IFPC_TX07	I	50000
IFPC_TX08	IFPC_TX08	I	50000
IFPC_TX09	IFPC_TX09	I	50000
IFPC_TX10	IFPC_TX10	I	50000
IFPC_TX11	IFPC_TX11	I	50000
IFPC_TX12	IFPC_TX12	I	50000
IFPC_TX13	IFPC_TX13	I	50000
IFPC_TX14	IFPC_TX14	I	50000
IFPC_TX15	IFPC_TX15	I	50000
IFPC_TX16	IFPC_TX16	I	50000
IFPC_TX17	IFPC_TX17	I	50000
IFPC_TX18	IFPC_TX18	I	50000
IFPC_TX19	IFPC_TX19	I	50000
IFPC_TX20	IFPC_TX20	I	50000
IFPC_TX21	IFPC_TX21	I	50000
IFPC_TX22	IFPC_TX22	I	50000
IFPC_TX23	IFPC_TX23	I	50000
IFPC_TX24	IFPC_TX24	I	50000
IFPC_TX25	IFPC_TX25	I	50000
IFPC_TX26	IFPC_TX26	I	50000
IFPC_TX27	IFPC_TX27	I	50000
IFPC_TX28	IFPC_TX28	I	50000
IFPC_TX29	IFPC_TX29	I	50000
IFPC_TX30	IFPC_TX30	I	50000
IFPC_TX31	IFPC_TX31	I	50000
IFPC_TX32	IFPC_TX32	I	50000
IFPC_TX33	IFPC_TX33	I	50000
IFPC_TX34	IFPC_TX34	I	50000
IFPC_TX35	IFPC_TX35	I	50000
IFPC_TX36	IFPC_TX36	I	50000
IFPC_TX37	IFPC_TX37	I	50000
IFPC_TX38	IFPC_TX38	I	50000
IFPC_TX39	IFPC_TX39	I	50000
IFPC_TX40	IFPC_TX40	I	50000
IFPC_TX41	IFPC_TX41	I	50000
IFPC_TX42	IFPC_TX42	I	50000
IFPC_TX43	IFPC_TX43	I	50000
IFPC_TX44	IFPC_TX44	I	50000
IFPC_TX45	IFPC_TX45	I	50000
IFPC_TX46	IFPC_TX46	I	50000
IFPC_TX47	IFPC_TX47	I	50000
IFPC_TX48	IFPC_TX48	I	50000
IFPC_TX49	IFPC_TX49	I	50000
IFPC_TX50	IFPC_TX50	I	50000
IFPC_TX51	IFPC_TX51	I	50000
IFPC_TX52	IFPC_TX52	I	50000
IFPC_TX53	IFPC_TX53	I	50000
IFPC_TX54	IFPC_TX54	I	50000
IFPC_TX55	IFPC_TX55	I	50000
IFPC_TX56	IFPC_TX56	I	50000
IFPC_TX57	IFPC_TX57	I	50000
IFPC_TX58	IFPC_TX58	I	50000
IFPC_TX59	IFPC_TX59	I	50000
IFPC_TX60	IFPC_TX60	I	50000
IFPC_TX61	IFPC_TX61	I	50000
IFPC_TX62	IFPC_TX62	I	50000
IFPC_TX63	IFPC_TX63	I	50000
IFPC_TX64	IFPC_TX64	I	50000
IFPC_TX65	IFPC_TX65	I	50000
IFPC_TX66	IFPC_TX66	I	50000
IFPC_TX67	IFPC_TX67	I	50000
IFPC_TX68	IFPC_TX68	I	50000
IFPC_TX69	IFPC_TX69	I	50000
IFPC_TX70	IFPC_TX70	I	50000
IFPC_TX71	IFPC_TX71	I	50000
IFPC_TX72	IFPC_TX72	I	50000
IFPC_TX73	IFPC_TX73	I	50000
IFPC_TX74	IFPC_TX74	I	50000
IFPC_TX75	IFPC_TX75	I	50000
IFPC_TX76	IFPC_TX76	I	50000
IFPC_TX77	IFPC_TX77	I	50000
IFPC_TX78	IFPC_TX78	I	50000
IFPC_TX79	IFPC_TX79	I	50000
IFPC_TX80	IFPC_TX80	I	50000
IFPC_TX81	IFPC_TX81	I	50000
IFPC_TX82	IFPC_TX82	I	50000
IFPC_TX83	IFPC_TX83	I	50000
IFPC_TX84	IFPC_TX84	I	50000
IFPC_TX85	IFPC_TX85	I	50000
IFPC_TX86	IFPC_TX86	I	50000
IFPC_TX87	IFPC_TX87	I	50000
IFPC_TX88	IFPC_TX88	I	50000
IFPC_TX89	IFPC_TX89	I	50000
IFPC_TX90	IFPC_TX90	I	50000
IFPC_TX91	IFPC_TX91	I	50000
IFPC_TX92	IFPC_TX92	I	50000
IFPC_TX93	IFPC_TX93	I	50000
IFPC_TX94	IFPC_TX94	I	50000
IFPC_TX95	IFPC_TX95	I	50000
IFPC_TX96	IFPC_TX96	I	50000
IFPC_TX97	IFPC_TX97	I	50000
IFPC_TX98	IFPC_TX98	I	50000
IFPC_TX99	IFPC_TX99	I	50000

Net Name	MIN_WIDTH	MAX_WIDTH
IFPC_TX00	1.000	1.000
IFPC_TX01	1.000	1.000
IFPC_TX02	1.000	1.000
IFPC_TX03	1.000	1.000
IFPC_TX04	1.000	1.000
IFPC_TX05	1.000	1.000
IFPC_TX06	1.000	1.000
IFPC_TX07	1.000	1.000
IFPC_TX08	1.000	1.000
IFPC_TX09	1.000	1.000
IFPC_TX10	1.000	1.000
IFPC_TX11	1.000	1.000
IFPC_TX12	1.000	1.000
IFPC_TX13	1.000	1.000
IFPC_TX14	1.000	1.000
IFPC_TX15	1.000	1.000
IFPC_TX16	1.000	1.000
IFPC_TX17	1.000	1.000
IFPC_TX18	1.000	1.000
IFPC_TX19	1.000	1.000
IFPC_TX20	1.000	1.000
IFPC_TX21	1.000	1.000
IFPC_TX22	1.000	1.000
IFPC_TX23	1.000	1.000
IFPC_TX24	1.000	1.000
IFPC_TX25	1.000	1.000
IFPC_TX26	1.000	1.000
IFPC_TX27	1.000	1.000
IFPC_TX28	1.000	1.000
IFPC_TX29	1.000	1.000
IFPC_TX30	1.000	1.000
IFPC_TX31	1.000	1.000
IFPC_TX32	1.000	1.000
IFPC_TX33	1.000	1.000
IFPC_TX34	1.000	1.000
IFPC_TX35	1.000	1.000
IFPC_TX36	1.000	1.000
IFPC_TX37	1.000	1.000
IFPC_TX38	1.000	1.000
IFPC_TX39	1.000	1.000
IFPC_TX40	1.000	1.000
IFPC_TX41	1.000	1.000
IFPC_TX42	1.000	1.000
IFPC_TX43	1.000	1.000
IFPC_TX44	1.000	1.000
IFPC_TX45	1.000	1.000
IFPC_TX46	1.000	1.000
IFPC_TX47	1.000	1.000
IFPC_TX48	1.000	1.000
IFPC_TX49	1.000	1.000
IFPC_TX50	1.000	1.000
IFPC_TX51	1.000	1.000
IFPC_TX52	1.000	1.000
IFPC_TX53	1.000	1.000
IFPC_TX54	1.000	1.000
IFPC_TX55	1.000	1.000
IFPC_TX56	1.000	1.000
IFPC_TX57	1.000	1.000
IFPC_TX58	1.000	1.000
IFPC_TX59	1.000	1.000
IFPC_TX60	1.000	1.000
IFPC_TX61	1.000	1.000
IFPC_TX62	1.000	1.000
IFPC_TX63	1.000	1.000
IFPC_TX64	1.000	1.000
IFPC_TX65	1.000	1.000
IFPC_TX66	1.000	1.000
IFPC_TX67	1.000	1.000
IFPC_TX68	1.000	1.000
IFPC_TX69	1.000	1.000
IFPC_TX70	1.000	1.000
IFPC_TX71	1.000	1.000
IFPC_TX72	1.000	1.000
IFPC_TX73	1.000	1.000
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IFPC_TX92	1.000	1.000
IFPC_TX93	1.000	1.000
IFPC_TX94	1.000	1.000
IFPC_TX95	1.000	1.000
IFPC_TX96	1.000	1.000
IFPC_TX97	1.000	1.000
IFPC_TX98	1.000	1.000
IFPC_TX99	1.000	1.000

Net Name	VOLTAGE	MAX_CURRENT
IFPC_TX00	1.000	1.000
IFPC_TX01	1.000	1.000
IFPC_TX02	1.000	1.000
IFPC_TX03	1.000	1.000
IFPC_TX04	1.000	1.000
IFPC_TX05	1.000	1.000
IFPC_TX06	1.000	1.000
IFPC_TX07	1.000	1.000
IFPC_TX08	1.000	1.000
IFPC_TX09	1.000	1.000
IFPC_TX10	1.000	1.000
IFPC_TX11	1.000	1.000
IFPC_TX12	1.000	1.000
IFPC_TX13	1.000	1.000
IFPC_TX14	1.000	1.000
IFPC_TX15	1.000	1.000
IFPC_TX16	1.000	1.000
IFPC_TX17	1.000	1.000
IFPC_TX18	1.000	1.000
IFPC_TX19	1.000	1.000
IFPC_TX20	1.000	1.000
IFPC_TX21	1.000	1.000
IFPC_TX22	1.000	1.000
IFPC_TX23	1.000	1.000
IFPC_TX24	1.000	1.000
IFPC_TX25	1.000	1.000
IFPC_TX26	1.000	1.000
IFPC_TX27	1.000	1.000
IFPC_TX28	1.000	1.000
IFPC_TX29	1.000	1.000
IFPC_TX30	1.000	1.000
IFPC_TX31	1.000	1.000
IFPC_TX32	1.000	1.000
IFPC_TX33	1.000	1.000
IFPC_TX34	1.000	1.000
IFPC_TX35	1.000	1.000
IFPC_TX36	1.000	1.000
IFPC_TX37	1.000	1.000
IFPC_TX38	1.000	1.000
IFPC_TX39	1.000	1.000
IFPC_TX40	1.000	1.000
IFPC_TX41	1.000	1.000
IFPC_TX42	1.000	1.000
IFPC_TX43	1.000	1.000
IFPC_TX44	1.000	1.000
IFPC_TX45	1.000	1.000
IFPC_TX46	1.000	1.000
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IFPC_TX48	1.000	1.000
IFPC_TX49	1.000	1.000
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IFPC_TX54	1.000	1.000
IFPC_TX55	1.000	1.000
IFPC_TX56	1.000	1.000
IFPC_TX57	1.000	1.000
IFPC_TX58	1.000	1.000
IFPC_TX59	1.000	1.000
IFPC_TX60	1.000	1.000
IFPC_TX61	1.000	1.000
IFPC_TX62	1.000	1.000
IFPC_TX63	1.000	1.000
IFPC_TX64	1.000	1.000
IFPC_TX65	1.000	1.000
IFPC_TX66	1.000	1.000
IFPC_TX67	1.000	1.000
IFPC_TX68	1.000	1.000
IFPC_TX69	1.000	1.000
IFPC_TX70	1.000	1.000
IFPC_TX71	1.000	1.000
IFPC_TX72	1.000	1.000
IFPC_TX73	1.000	1.000
IFPC_TX74	1.000	1.000
IFPC_TX75	1.000	1.000
IFPC_TX76	1.000	1.000
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IFPC_TX78	1.000	1.000
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IFPC_TX80	1.000	1.000
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IFPC_TX84	1.000	1.000
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IFPC_TX88	1.000	1.000
IFPC_TX89	1.000	1.000
IFPC_TX90	1.000	1.000
IFPC_TX91	1.000	1.000
IFPC_TX92	1.000	1.000
IFPC_TX93	1.000	1.000
IFPC_TX94	1.000	1.000
IFPC_TX95	1.000	1.000
IFPC_TX96	1.000	1.000
IFPC_TX97	1.000	1.000
IFPC_TX98	1.000	1.000
IFPC_TX99	1.000	1.000

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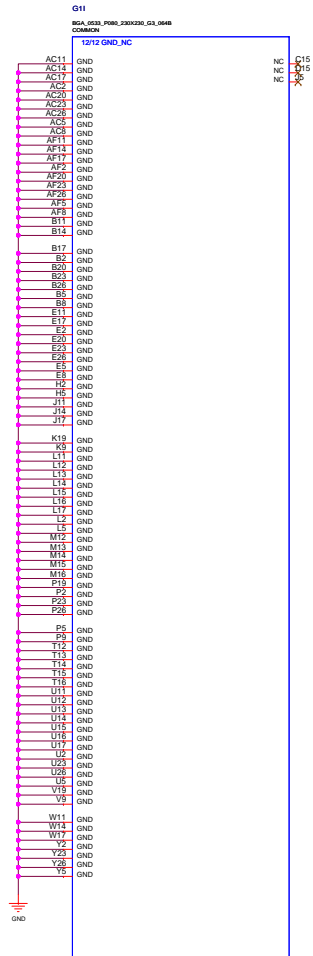
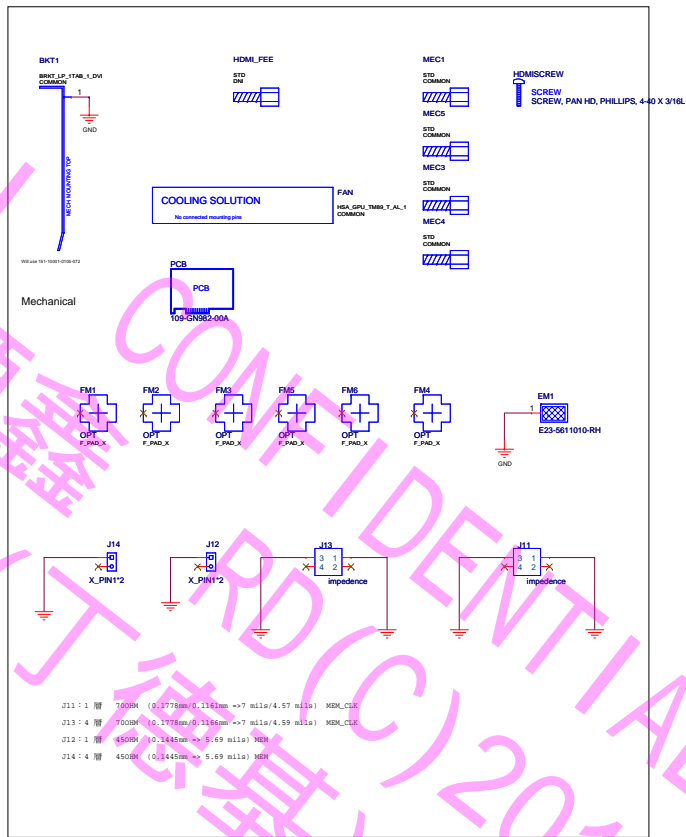
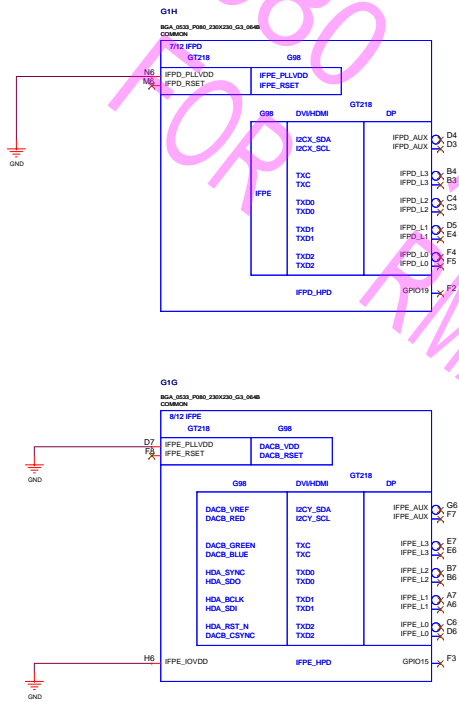
BOM REV

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PAGE

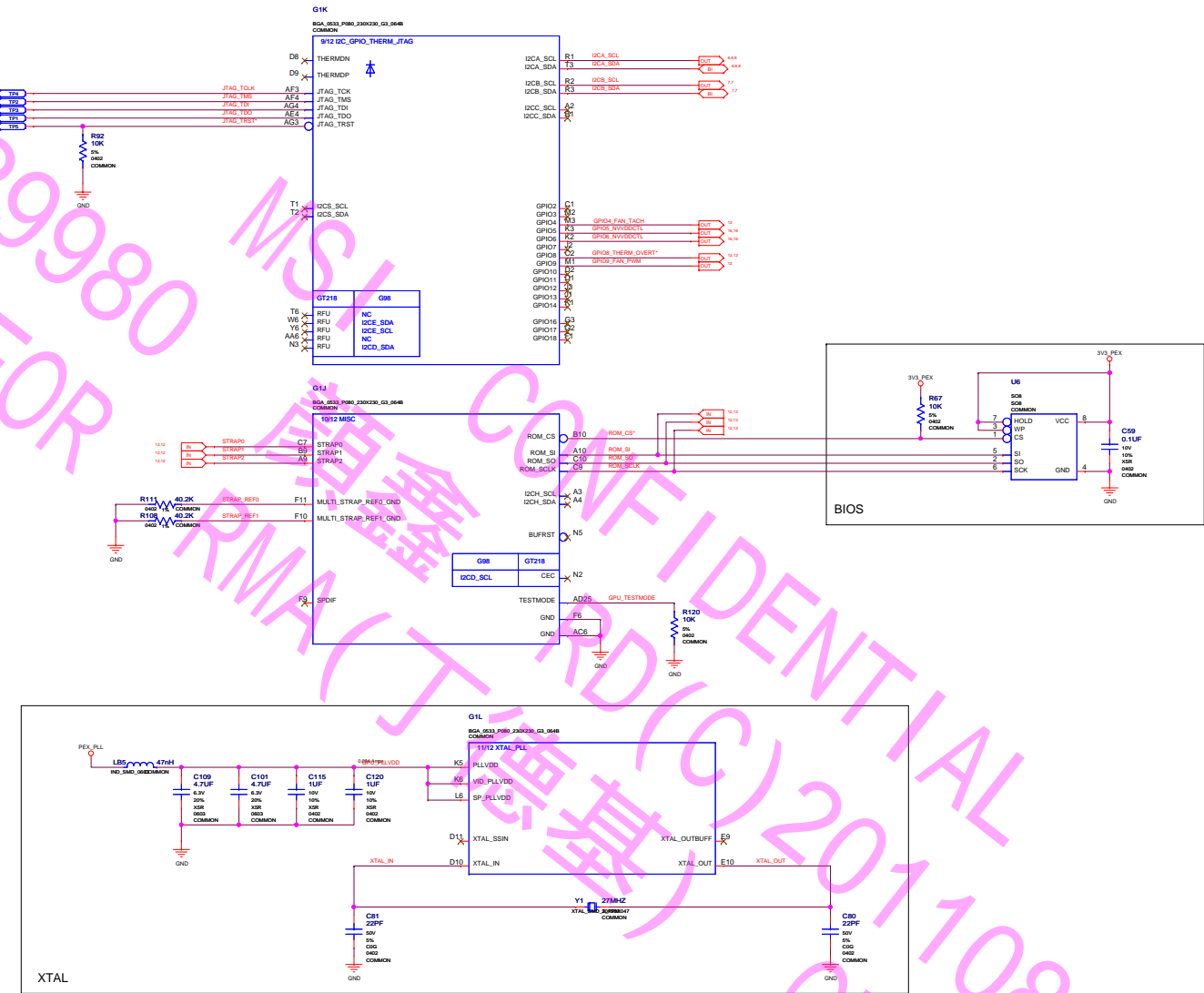
1

IFPD, IFPE Interface(Not used), Mechanical



XTAL, ROM, JTAG

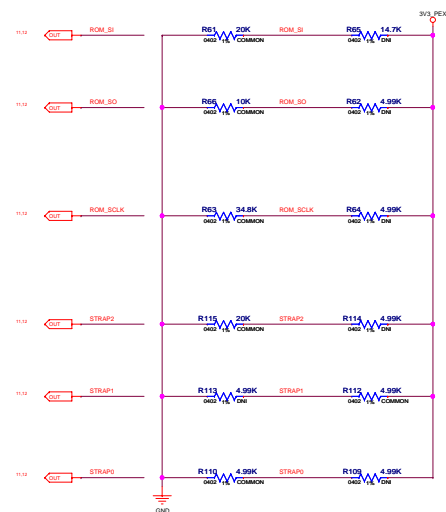
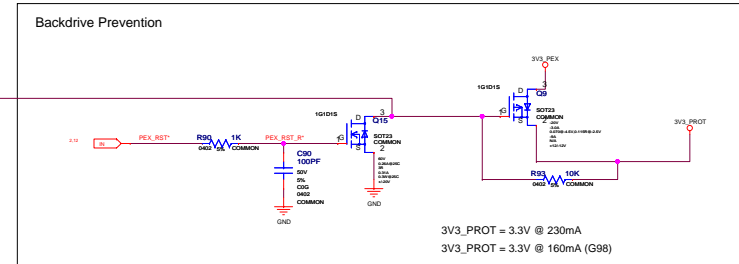
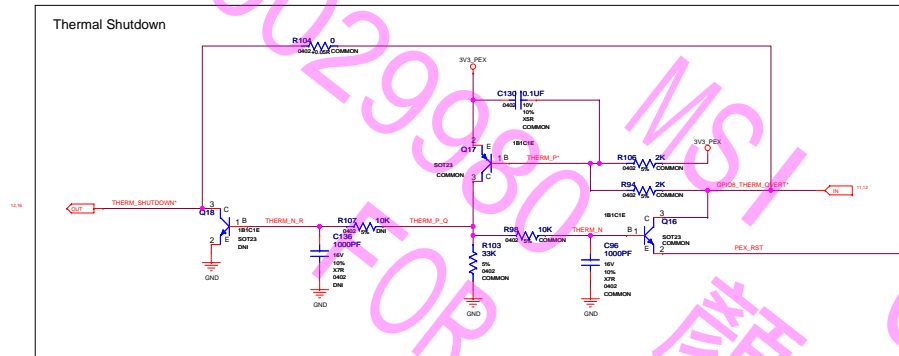
Net Name		CRITICAL	IMPEDANCE
IN	XTAL_OUT	1	
	XTAL_IN	1	
Net Name		VOLTAGE	MAX_CURRENT
IN	GPU_PL1VDD	1.05V	0.000A 10000



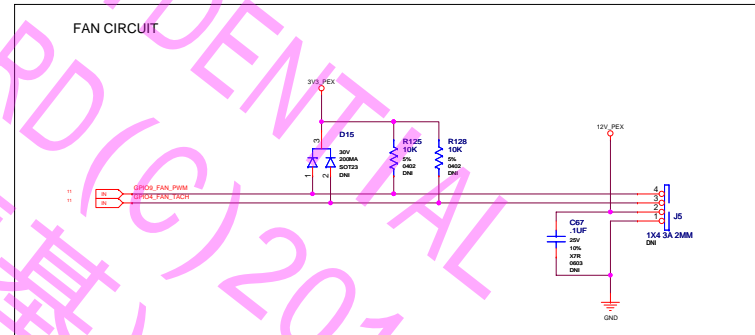
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SANTA CLARA, CA 95050, USA			
NV_PN	600-10872-BASE-100		
PCB REV	P072701	PAGE	
BOM REV	A	DATE	22-DEC-2009

Thermal Protection, Protected 3V3, Straps



GT218 Straps			MLS Mode		Mode		REPORT
Bit Signal			Values		ML	Def.	Def.
					Multilevel Straps		
03	RAMPQ[0]	0001	Hymn (0000-10)		5K to GND	0000	
03	RAMPQ[0]	0011	Sawtooth (0000-10)		10K to GND	0001	
03	RAMPQ[0]	1001	Hymn (0000-10, Double-Pulse)		15K to GND	0010	
03	RAMPQ[0]	1011	Sawtooth (0000-10, Double-Pulse)		20K to GND	0011	
03	RAMPQ[0]	0111	Sawtooth (0000-10)		30K to GND	0100	
03	VCIL_417	0	27T (Default)		40K to GND	0101	
03	FR01	0	288M (Default)		5K to VCC	0110	
01	DS05_ALT_ADDR0	0	0.0V		10K to VCC	1001	
01	DS05_ALT_ADDR0	1	0.0V		20K to VCC	1010	
00	VGA_VIDE	0	Clock mode 300		30K to VCC	1011	
00	VGA_VIDE	1	Clock mode 300		40K to VCC	1100	
03	PCOL_DEV[05], EXT	0	GT218-300-A1		20K to VCC	1101	
03	PCOL_DEV[05], EXT	1	0.0V		40K to VCC	1110	
02	SUB_VENDOR	0	No BIOS				
02	SUB_VENDOR	1	BIOS				
01	SLOT_0A_CFG	0	Disable				
01	SLOT_0A_CFG	1	Enable				
00	PEX_PXL_EN_TERR100	0	Disable				
00	PEX_PXL_EN_TERR100	1	Enable				
03	PCOL_DEV[05]	0101	GT218-300-A1				
01	PCOL_DEV[05]	0	Default				
01	PCOL_DEV[05]	1	Default				
01	PCOL_DEV[05]	0	Default				
03	XS0_PADCFG_LUT_ADDR[0]	0000	GSDTOR_DEFAULT		1000	GSDTOR_DEFAULT	
03	XS0_PADCFG_LUT_ADDR[0]	0001	MOBILE_DEFAULT		1001	MOBILE_XTRMS_NAMP	
03	XS0_PADCFG_LUT_ADDR[0]	0010	MOBILE_XTRMS_LAMP		1010	MOBILE_XTRMS_LAMP	
03	XS0_PADCFG_LUT_ADDR[0]	0011	MOBILE_XTRMS_LAMP		1011	MOBILE_XTRMS_LAMP	
03	XS0_PADCFG_LUT_ADDR[0]	0100	MOBILE_XTRMS_LAMP		1100	MOBILE_XTRMS_LAMP	
03	XS0_PADCFG_LUT_ADDR[0]	0101	MOBILE_XTRMS_J99NAMP		1101	MOBILE_XTRMS_J99NAMP	
03	XS0_PADCFG_LUT_ADDR[0]	0110	MOBILE_XTRMS_J99NAMP		1110	MOBILE_XTRMS_J99NAMP	
03	XS0_PADCFG_LUT_ADDR[0]	0111	MOBILE_XTRMS_J99NAMP		1111	MOBILE_XTRMS_J99NAMP	
02	USER[0]	0000	Default				
02	USER[0]	0001	Default				
02	USER[0]	0010	Default				
02	USER[0]	0011	Default				
02	USER[0]	0100	Default				
02	USER[0]	0101	Default				
02	USER[0]	0110	Default				
02	USER[0]	0111	Default				

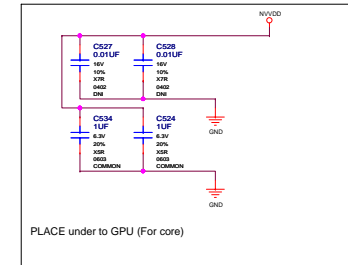
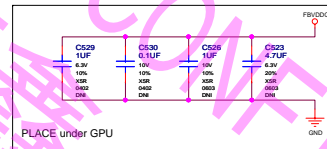
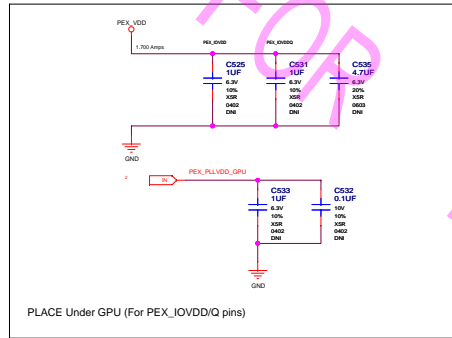


The diagram shows the timing of several signals relative to a common clock or reference. The signals are:

- PEEX_RST***: Active-low reset signal, transitions from high to low at approximately 2.10 ns.
- GPIOP_THERM_OVERT***: Thermal over-temperature interrupt, transitions from high to low at approximately 11.10 ns.
- THERM_SHUTDOWN***: Thermal shutdown signal, transitions from high to low at approximately 12.10 ns.
- ROM_SI**: ROM Strobe Input, transitions from low to high at approximately 13.10 ns.
- ROM_SO**: ROM Strobe Output, transitions from low to high at approximately 13.10 ns.
- ROM_SCLK**: ROM Serial Clock, transitions from low to high at approximately 13.10 ns.
- THERM1**: Thermal sensor 1 output, transitions from low to high at approximately 13.10 ns.
- THERM2**: Thermal sensor 2 output, transitions from low to high at approximately 13.10 ns.
- THERM3**: Thermal sensor 3 output, transitions from low to high at approximately 13.10 ns.

The voltage levels are indicated by horizontal lines: 3.3V (top), 2.25V (middle), and 1.8V (bottom). The time markers are 2.10, 11.10, 12.10, and 13.10 ns.

GPU/Memory decaps on Bottom (Test Option)




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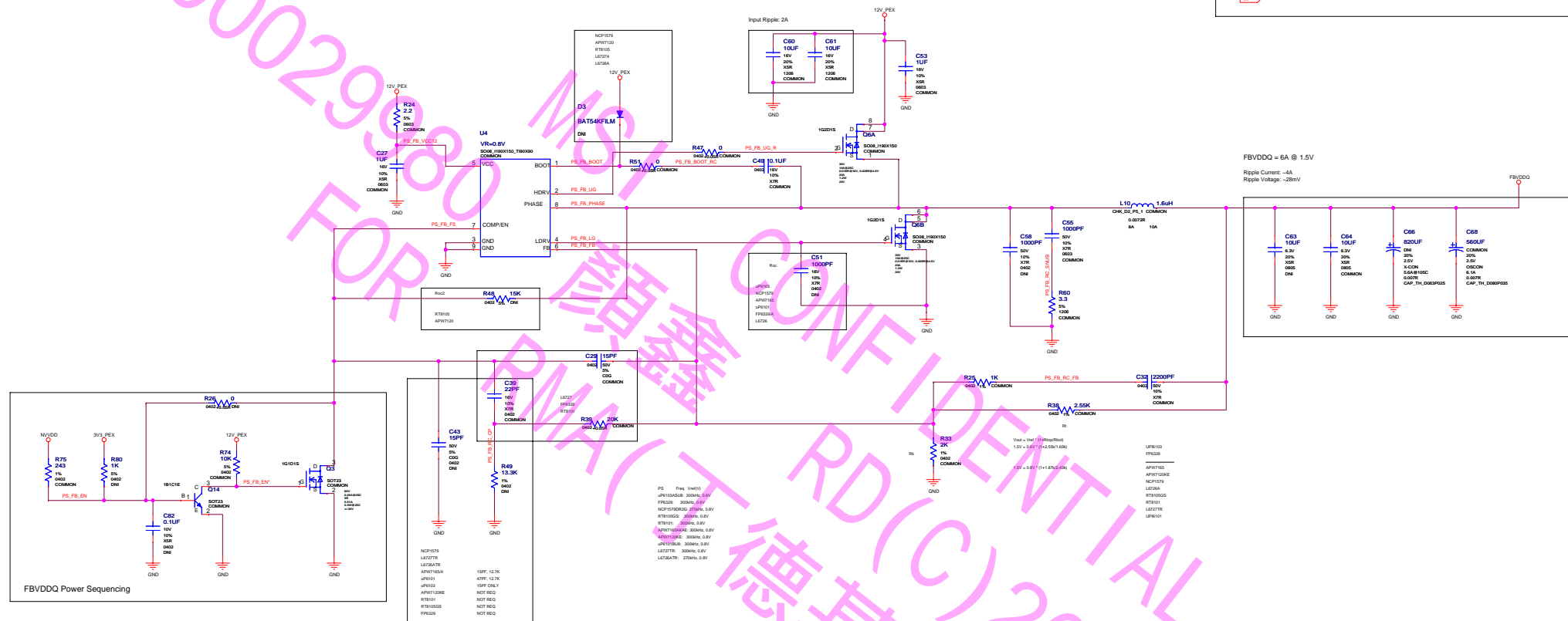
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Power Supply II: FBVDDQ

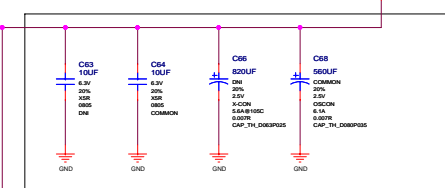


Net Name	MIN_LINE_WIDTH	VOLTAGE	NV_NET_MAX_CURRENT
FB/VDDO 	FB/VDDO	1.8V	8A
PS_FB_FS	10MIL		
PS_FB_VDD12	10MIL		
PS_FB_ROOT	10MIL		
PS_FB_US0	10MIL		
PS_FB_US1	10MIL		
PS_FB_US2	10MIL		
PS_FB_US3	10MIL		
PS_FB_US4	10MIL		
PS_FB_US5	10MIL		
PS_FB_US6	10MIL		
PS_FB_US7	10MIL		
PS_FB_US8	10MIL		
PS_FB_US9	10MIL		
PS_FB_US_CP	10MIL		
PS_FB_US_R	10MIL		
PS_FB_RC_CP	10MIL		
PS_FB_RC_FB	10MIL		
PS_FB_RC_SRA3B	10MIL		

FBVDDQ = 6A @ 1.5V

Ripple Current: ~4A

Ripple Voltage: ~28mV



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Power Supply II: FBVDDQ

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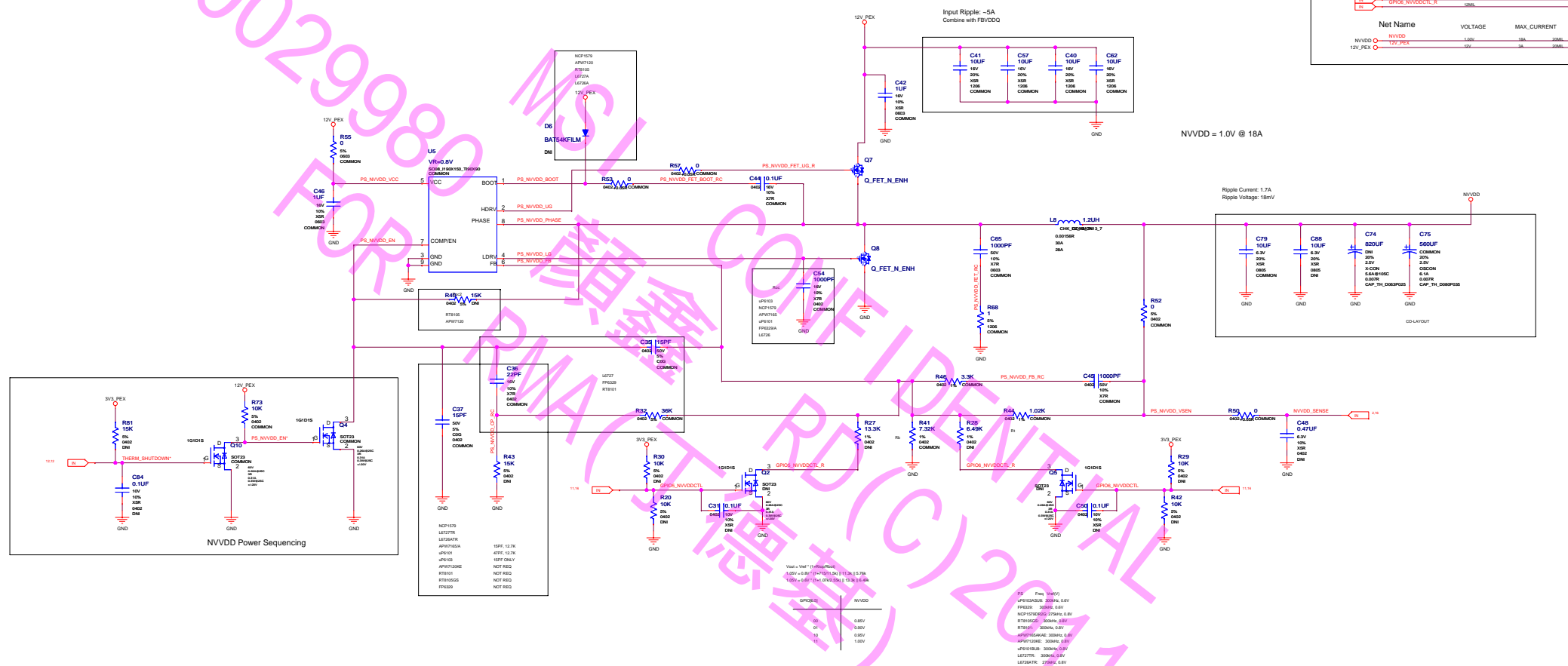
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	RAC
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PAG
DAT

	DATA
3	

Power Supply III: NVVDD



Net Name		MIN_WIDTH	MAX_WIDTH
24	PS_NNVIDD_BN	128	256
24	PE_NNVIDD_BN	128	256
24	PE_NNVIDD_VGG	128	256
24	PE_NNVIDD_POOL	128	256
24	PE_NNVIDD_POOL_F0F	128	256
24	PE_NNVIDD_POOL_F0F2	128	256
24	PS_NNVIDD_L0	128	256
24	PE_NNVIDD_L0_R	128	256
24	PE_NNVIDD_L0	128	256
24	PE_NNVIDD_POOL2D_R	128	256
24	PS_NNVIDD_RC	128	256
24	PE_NNVIDD_PB	128	256
24	PE_NNVIDD_PB_R	128	256
24	PE_NNVIDD_PB_R0	128	256
24	PS_NNVIDD_VGGN	128	256
4/16	24	NNVIDD_DEPTH	128
4/16	24	GP0PS_NNVIDDCL_R	128
4/16	24	GP0PS_NNVIDDCL_R	128
11/16	24	GP0PS_NNVIDDCL_L	128
24	24	GP0PS_NNVIDDCL_R	128

Net Name		VOLTAGE	MAX_CURRENT
NNVIDD	12V_PEX	1.05V	24A
12V_PEX	12V_PEX	1.0V	24A

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
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