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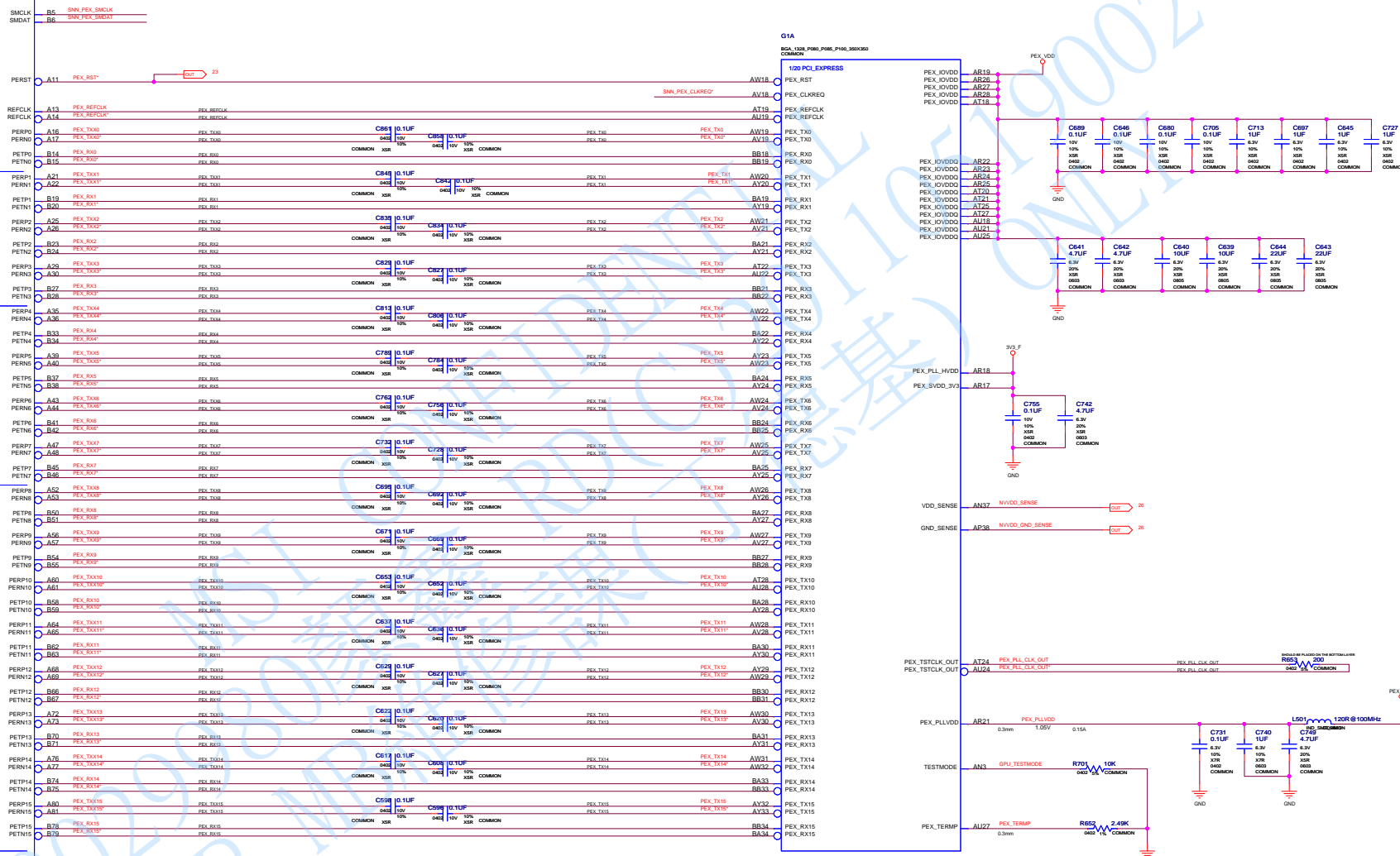
REV	VARIANT	NVPIN	ASSEMBLY
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1	SKU2001	600-11060-0001-200	GF100-271 42 1600MHz/2000MHz 1024MB (fpccs)64Mx16 BGA170 GDDR5 (non grd), DV1+DV1min+HDM, 140W
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5	SKU2005	600-11060-0005-200	GF100-271 42 1600MHz/2000MHz 1024MB (fpccs)64Mx16 BGA170 GDDR5 on FBB/SPIC, DV1+DV1min+HDM, 140W
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11	SKU2011	600-11060-0011-200	GF100-262 42 1400/2000MHz 1024MB (fpccs) 64Mx16 BGA170 GDDR5 (non grd), DV1+DV1min+HDM, 95W
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15	SKU2021	600-11060-0021-200	GF100-TBD 3/3 1400/2000MHz 1536MB (12pcps) 64Mx16 BGA170 GDDR5 (non grd), DV1+DV1min+HDM, 125W

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The schematic diagram illustrates the internal circuitry of the PEX_FINGER_PEX_124 connector. It features a 3V0 F supply at the top left, connected to a network of capacitors (C32, C34, C36, C862, C86, C114, C873) and resistors (R27, R28, R29, R30, R31, R32, R33, R34, R35, R36, R37, R38, R39, R40, R41, R42, R43, R44, R45, R46, R47, R48, R49, R50, R51, R52, R53, R54, R55, R56, R57, R58, R59, R60, R61, R62, R63, R64, R65, R66, R67, R68, R69, R70, R71, R72, R73, R74, R75, R76, R77, R78, R79, R80, R81, R82, R83, R84, R85, R86, R87, R88, R89, R90, R91, R92, R93, R94, R95, R96, R97, R98, R99, R100, R101, R102, R103, R104, R105, R106, R107, R108, R109, R110, R111, R112, R113, R114, R115, R116, R117, R118, R119, R120, R121, R122, R123, R124, R125, R126, R127, R128, R129, R130, R131, R132, R133, R134, R135, R136, R137, R138, R139, R140, R141, R142, R143, R144, R145, R146, R147, R148, R149, R150, R151, R152, R153, R154, R155, R156, R157, R158, R159, R160, R161, R162, R163, R164, R165, R166, R167, R168, R169, R170, R171, R172, R173, R174, R175, R176, R177, R178, R179, R180, R181, R182, R183, R184, R185, R186, R187, R188, R189, R190, R191, R192, R193, R194, R195, R196, R197, R198, R199, R200, R201, R202, R203, R204, R205, R206, R207, R208, R209, R210, R211, R212, R213, R214, R215, R216, R217, R218, R219, R220, R221, R222, R223, R224, R225, R226, R227, R228, R229, R230, R231, R232, R233, R234, R235, R236, R237, R238, R239, R240, R241, R242, R243, R244, R245, R246, R247, R248, R249, R250, R251, R252, R253, R254, R255, R256, R257, R258, R259, R260, R261, R262, R263, R264, R265, R266, R267, R268, R269, R270, R271, R272, R273, R274, R275, R276, R277, R278, R279, R280, R281, R282, R283, R284, R285, R286, R287, R288, R289, R290, R291, R292, R293, R294, R295, R296, R297, R298, R299, R300, R301, R302, R303, R304, R305, R306, R307, R308, R309, R310, R311, R312, R313, R314, R315, R316, R317, R318, R319, R320, R321, R322, R323, R324, R325, R326, R327, R328, R329, R330, R331, R332, R333, R334, R335, R336, R337, R338, R339, R340, R341, R342, R343, R344, R345, R346, R347, R348, R349, R350, R351, R352, R353, R354, R355, R356, R357, R358, R359, R360, R361, R362, R363, R364, R365, R366, R367, R368, R369, R370, R371, R372, R373, R374, R375, R376, R377, R378, R379, R380, R381, R382, R383, R384, R385, R386, R387, R388, R389, R390, R391, R392, R393, R394, R395, R396, R397, R398, R399, R400, R401, R402, R403, R404, R405, R406, R407, R408, R409, R410, R411, R412, R413, R414, R415, R416, R417, R418, R419, R420, R421, R422, R423, R424, R425, R426, R427, R428, R429, R430, R431, R432, R433, R434, R435, R436, R437, R438, R439, R440, R441, R442, R443, R444, R445, R446, R447, R448, R449, R450, R451, R452, R453, R454, R455, R456, R457, R458, R459, R460, R461, R462, R463, R464, R465, R466, R467, R468, R469, R470, R471, R472, R473, R474, R475, R476, R477, R478, R479, R480, R481, R482, R483, R484, R485, R486, R487, R488, R489, R490, R491, R492, R493, R494, R495, R496, R497, R498, R499, R500, R501, R502, R503, R504, R505, R506, R507, R508, R509, R510, R511, R512, R513, R514, R515, R516, R517, R518, R519, R520, R521, R522, R523, R524, R525, R526, R527, R528, R529, R530, R531, R532, R533, R534, R535, R536, R537, R538, R539, R540, R541, R542, R543, R544, R545, R546, R547, R548, R549, R550, R551, R552, R553, R554, R555, R556, R557, R558, R559, R560, R561, R562, R563, R564, R565, R566, R567, R568, R569, R570, R571, R572, R573, R574, R575, R576, R577, R578, R579, R580, R581, R582, R583, R584, R585, R586, R587, R588, R589, R590, R591, R592, R593, R594, R595, R596, R597, R598, R599, R600, R601, R602, R603, R604, R605, R606, R607, R608, R609, R610, R611, R612, R613, R614, R615, R616, R617, R618, R619, R620, R621, R622, R623, R624, R625, R626, R627, R628, R629, R630, R631, R632, R633, R634, R635, R636, R637, R638, R639, R640, R641, R642, R643, R644, R645, R646, R647, R648, R649, R650, R651, R652, R653, R654, R655, R656, R657, R658, R659, R660, R661, R662, R663, R664, R665, R666, R667, R668, R669, R670, R671, R672, R673, R674, R675, R676, R677, R678, R679, R680, R681, R682, R683, R684, R685, R686, R687, R688, R689, R690, R691, R692, R693, R694, R695, R696, R697, R698, R699, R700, R701, R702, R703, R704, R705, R706, R707, R708, R709, R710, R711, R712, R713, R714, R715, R716, R717, R718, R719, R720, R721, R722, R723, R724, R725, R726, R727, R728, R729, R730, R731, R732, R733, R734, R735, R736, R737, R738, R739, R740, R741, R742, R743, R744, R745, R746, R747, R748, R749, R750, R751, R752, R753, R754, R755, R756, R757, R758, R759, R760, R761, R762, R763, R764, R765, R766, R767, R768, R769, R770, R771, R772, R773, R774, R775, R776, R777, R778, R779, R780, R781, R782, R783, R784, R785, R786, R787, R788, R789, R790, R791, R792, R793, R794, R795, R796, R797, R798, R799, R800, R801, R802, R803, R804, R805, R806, R807, R808, R809, R810, R811, R812, R813, R814, R815, R816, R817, R818, R819, R820, R821, R822, R823, R824, R825, R826, R827, R828, R829, R830, R831, R832, R833, R834, R835, R836, R8



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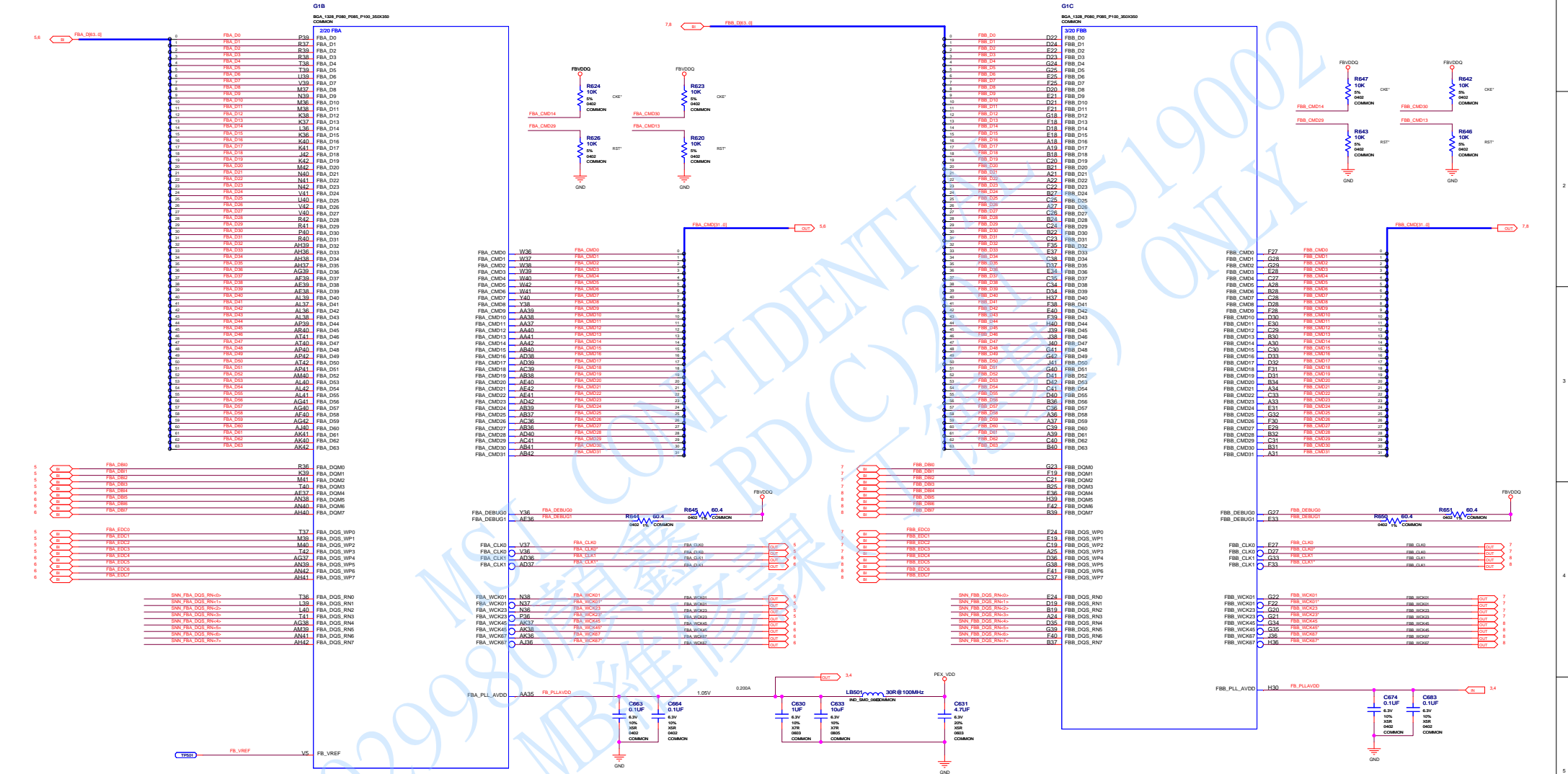


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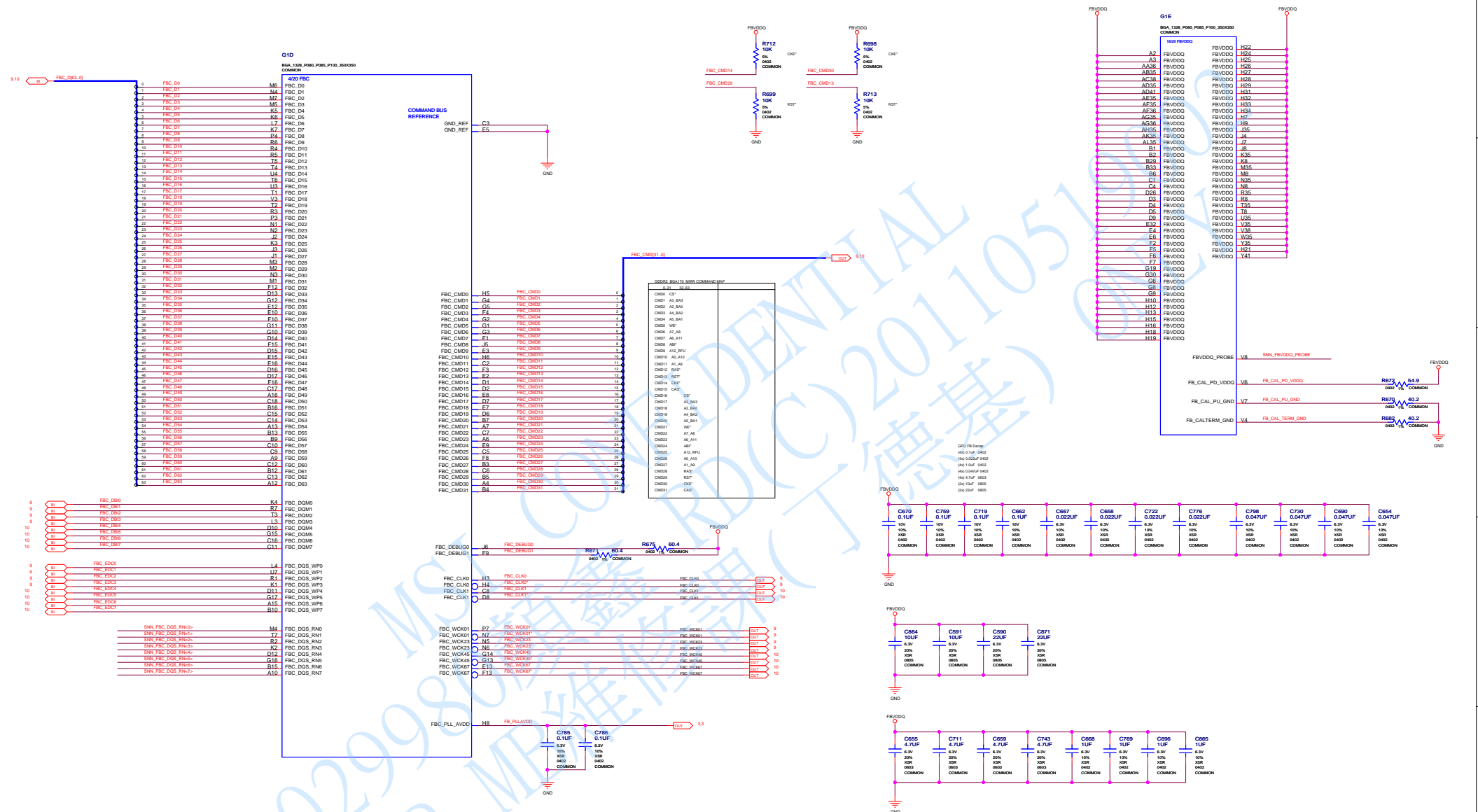
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MEMORY: GPU FB Partition A and B



MEMORY: GPU FB Partition C AND GPU FB Decap



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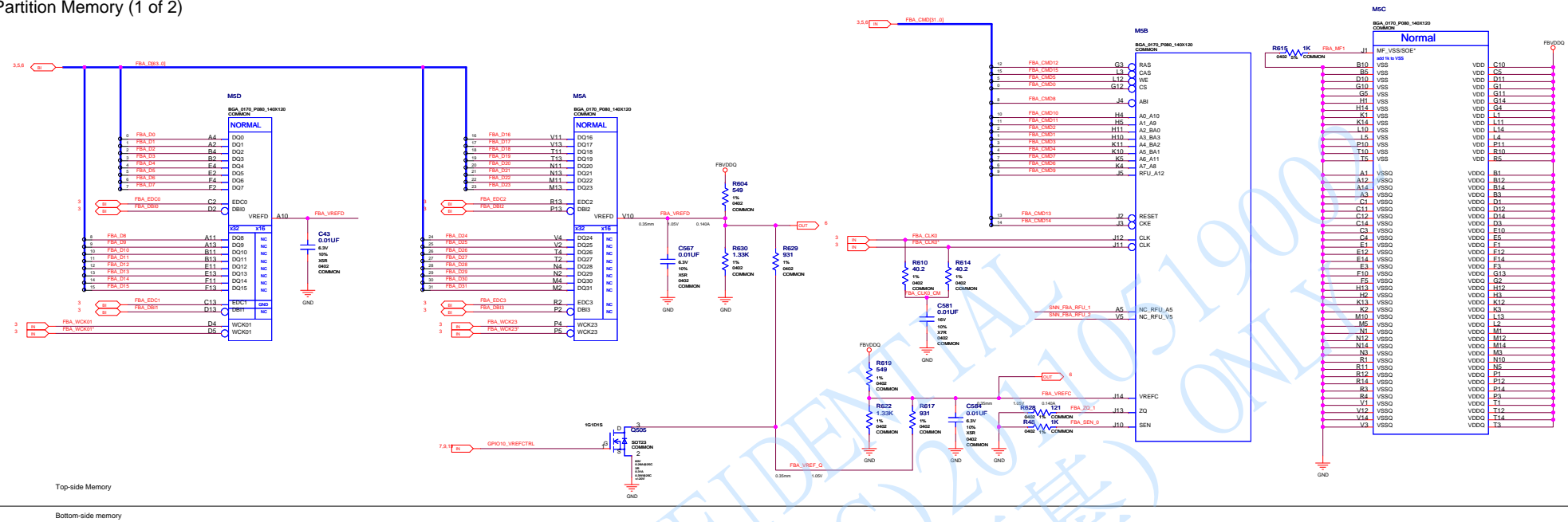
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PAGE DETAIL	GPU FB Partition C and GPU FB Decap


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FBA Partition Memory (1 of 2)



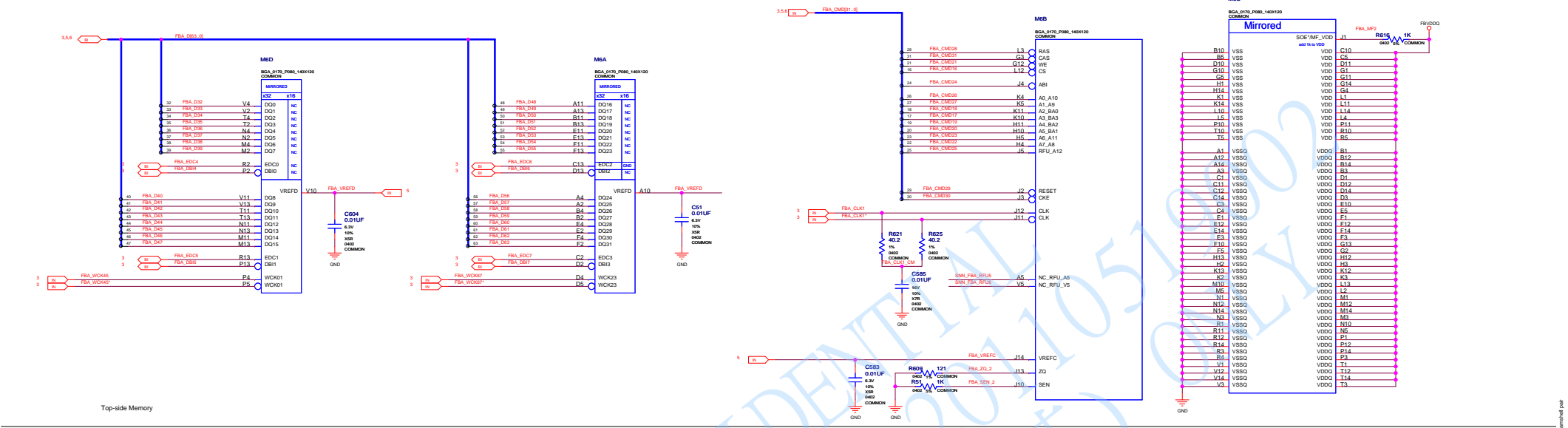
Clams in pair

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FBA Partition Memory (2 Of 2)



Top-side Memory

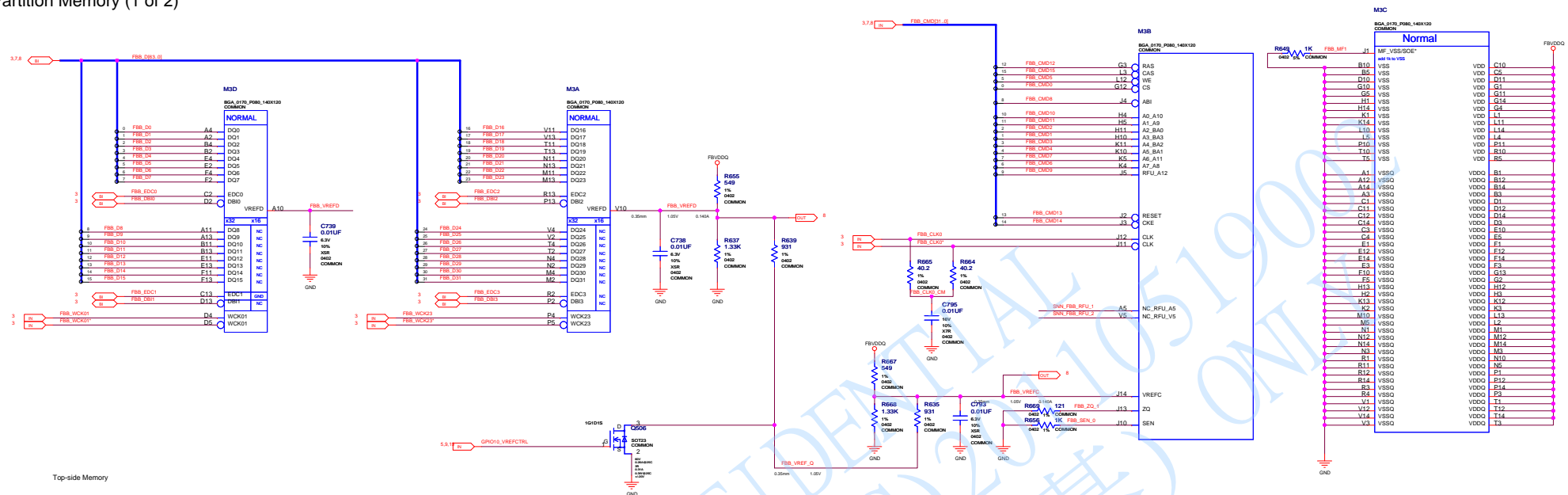
Bottom-side memory

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Clamshell pair

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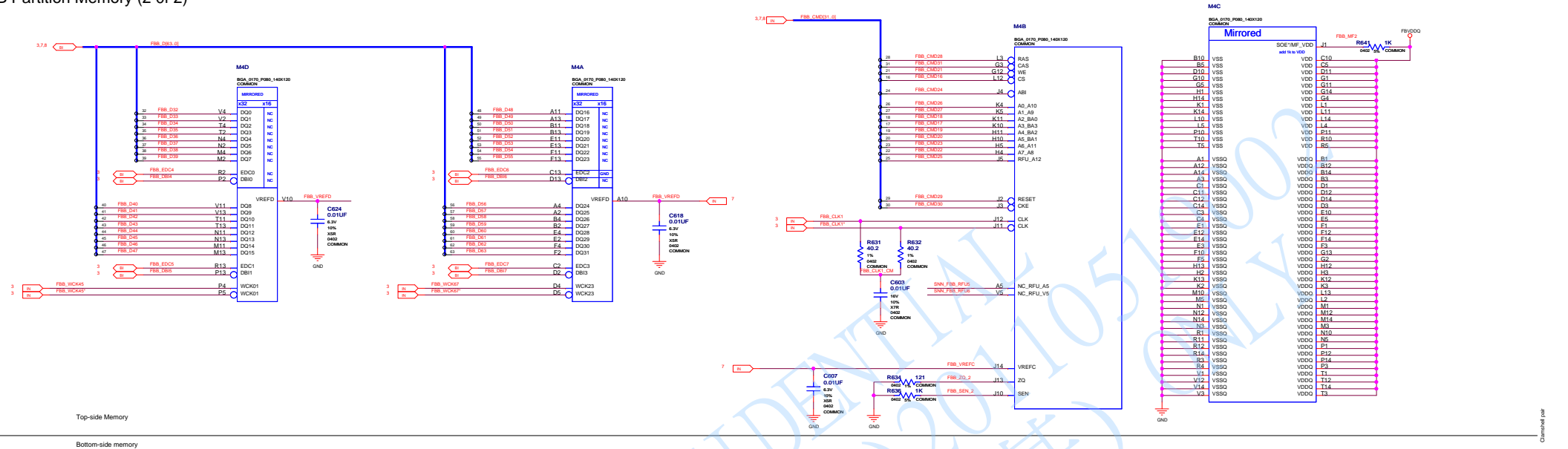
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Figure 1 shows a horizontal beam of length L . The beam is supported by a pin support at the left end ($x=0$) and a roller support at the right end ($x=L$). A uniformly distributed load of intensity q is applied downwards along the entire length of the beam. The coordinate x is measured from the left end. The beam is divided into two segments by a vertical line at $x=L/2$. The left segment is labeled 'H' and the right segment is labeled 'H'.

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FBB Partition Memory (2 Of 2)



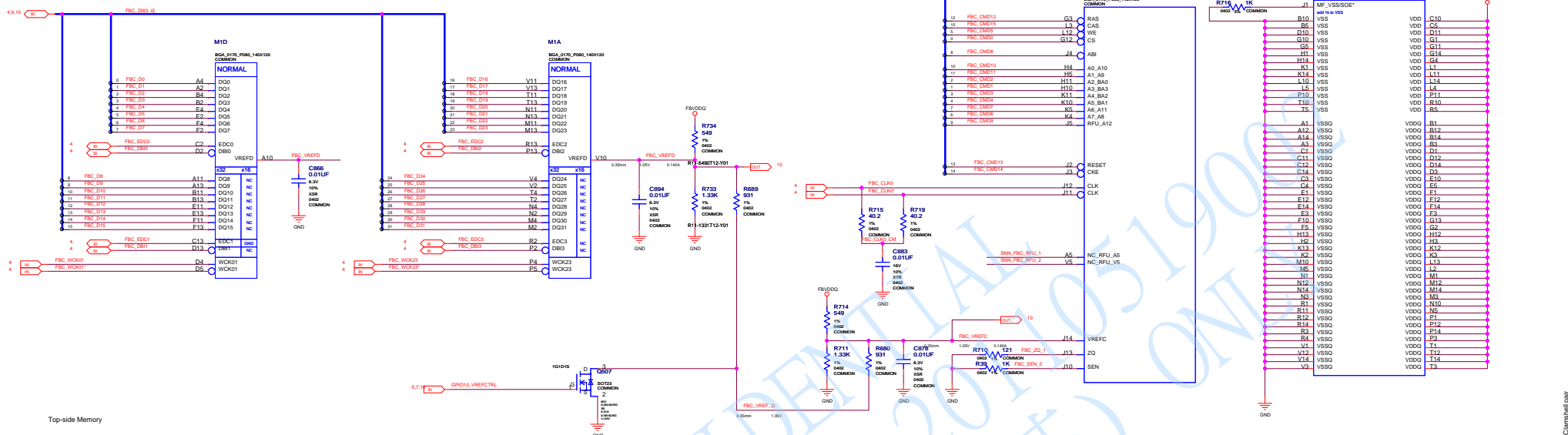
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FBC Partition Memory (1 of 2)



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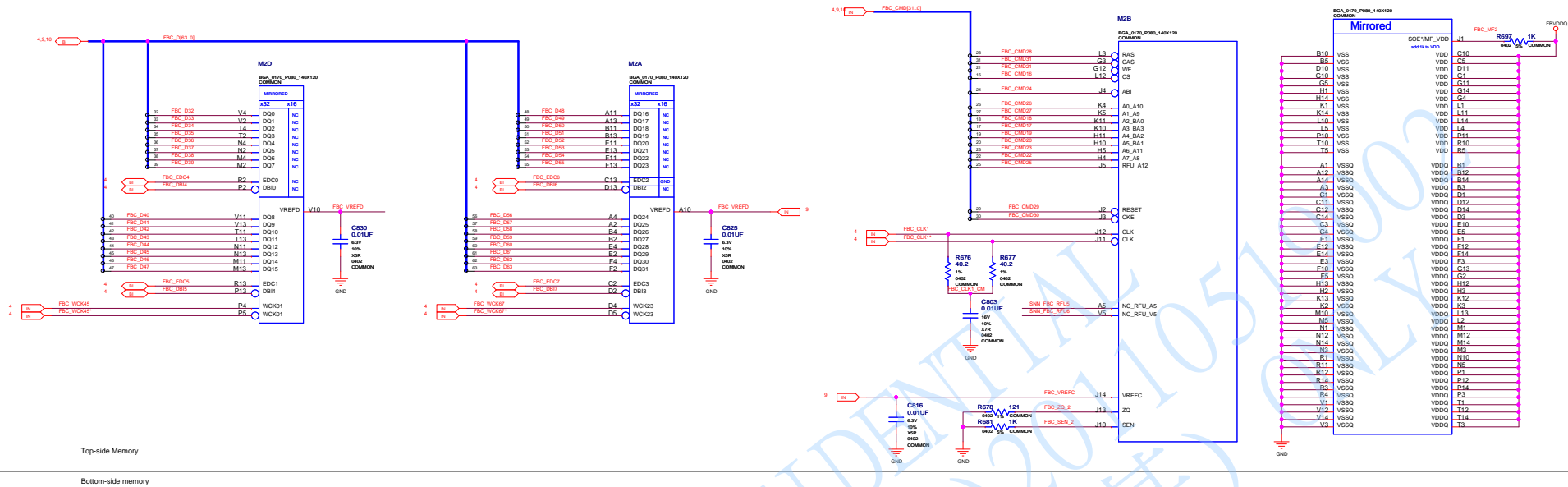
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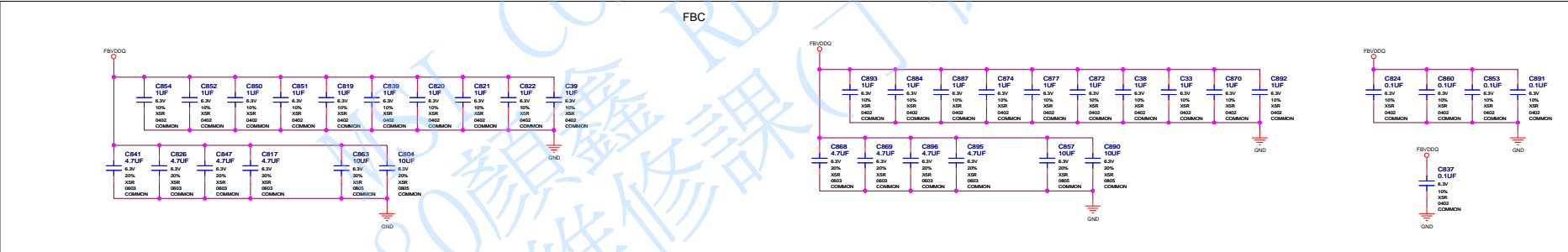
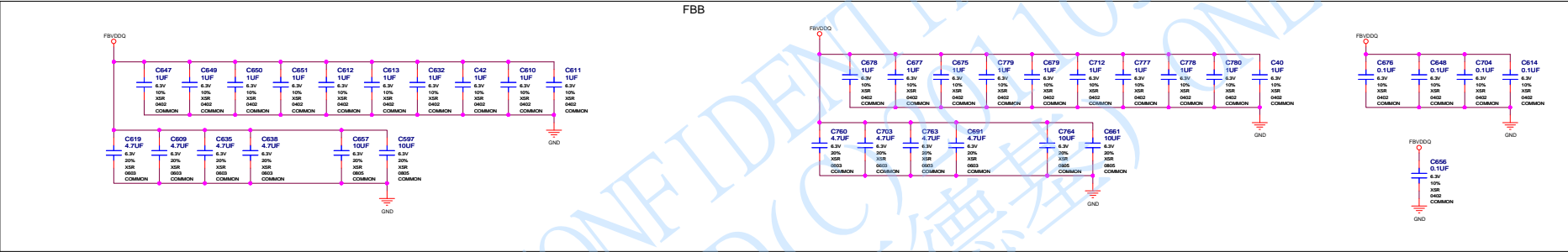
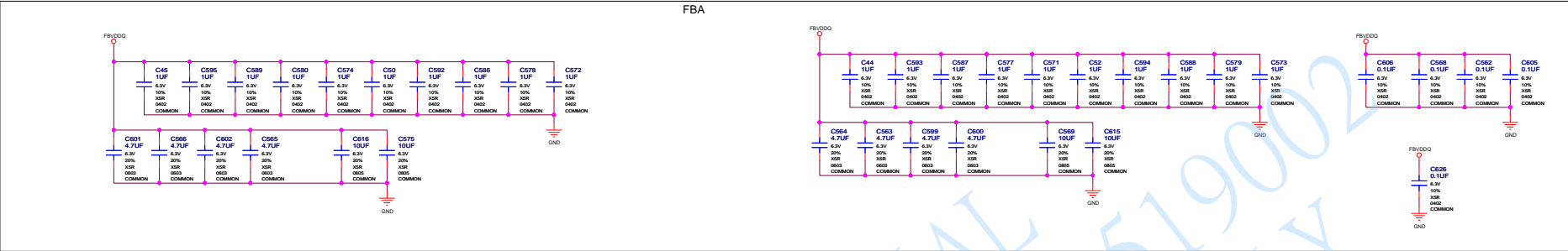


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FB MEMORY DECAP



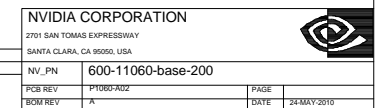
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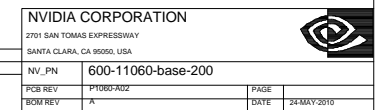
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DACA RGB-FILTER



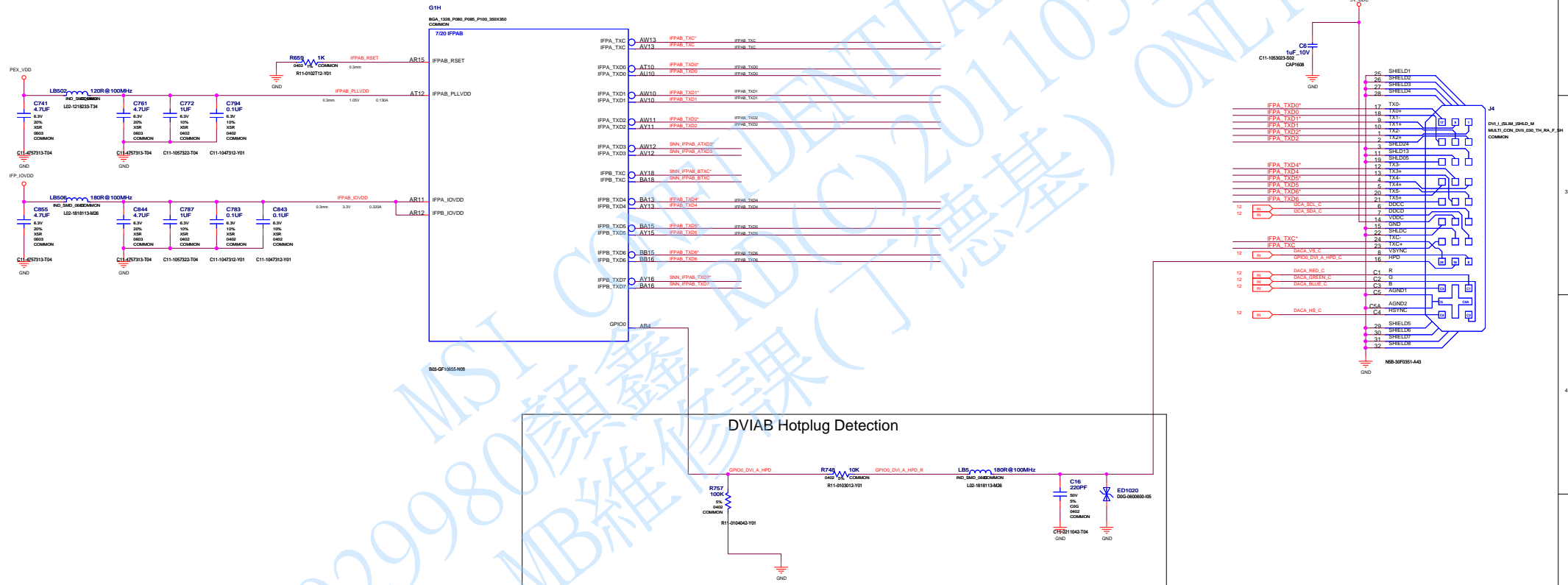
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DACB RGB-FILTER



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IFP A/B Interface -- DVI Connector, South



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PAGE DETAIL	IFP A/B Interface -- DVI Connector, South

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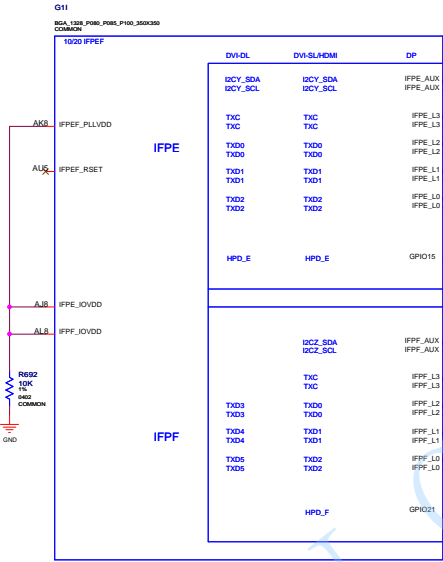


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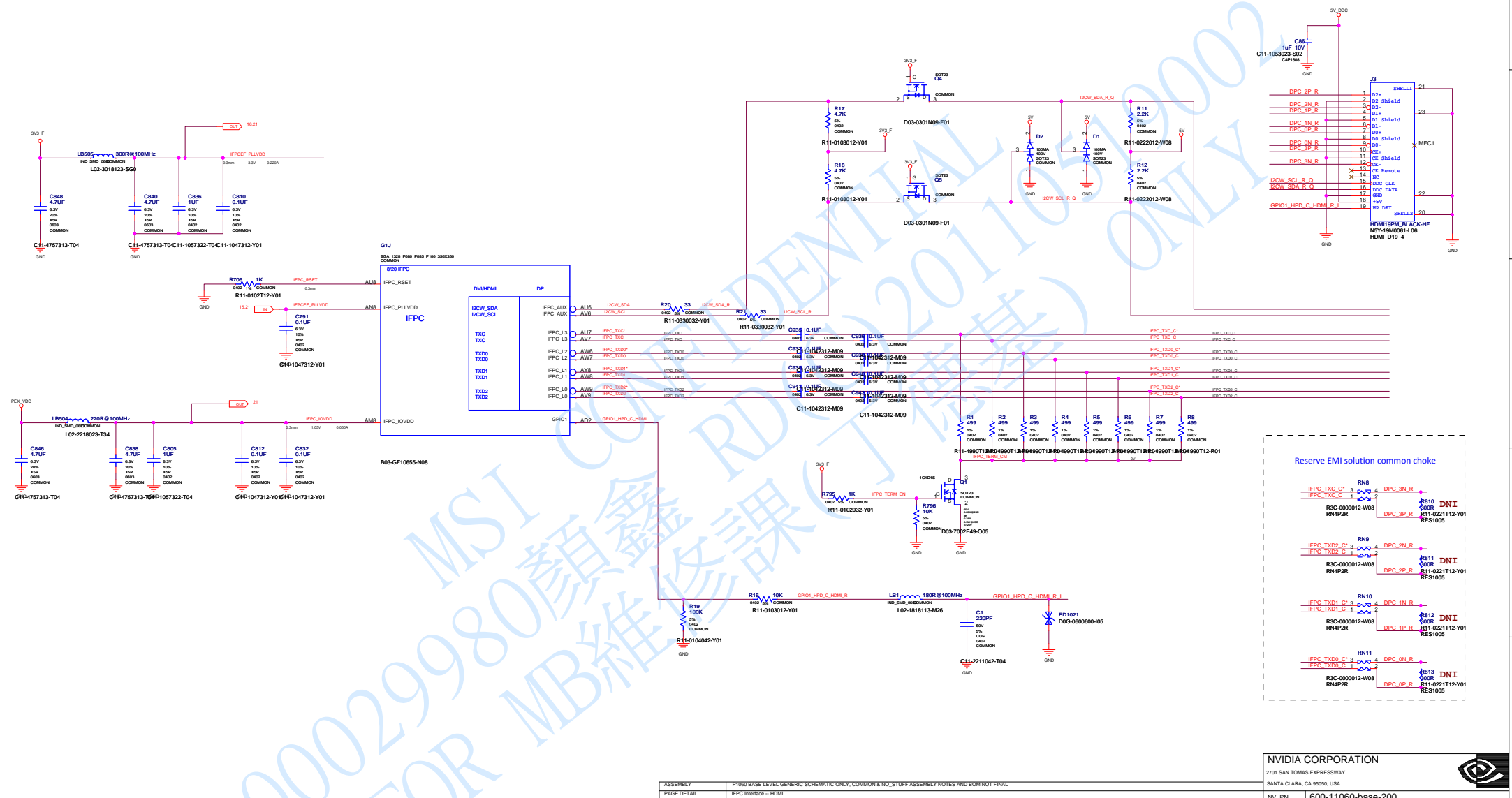
IFP E/F Interface -- DVI Connector, Mid



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IFP C miniHDMI, North



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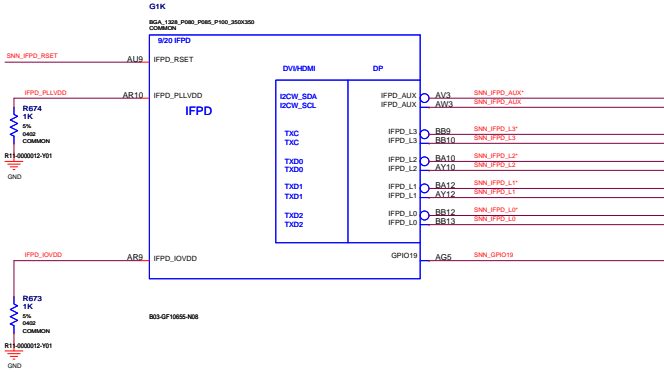


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IFP D INTERFACE (UNUSED), DP from IFPE, Mid

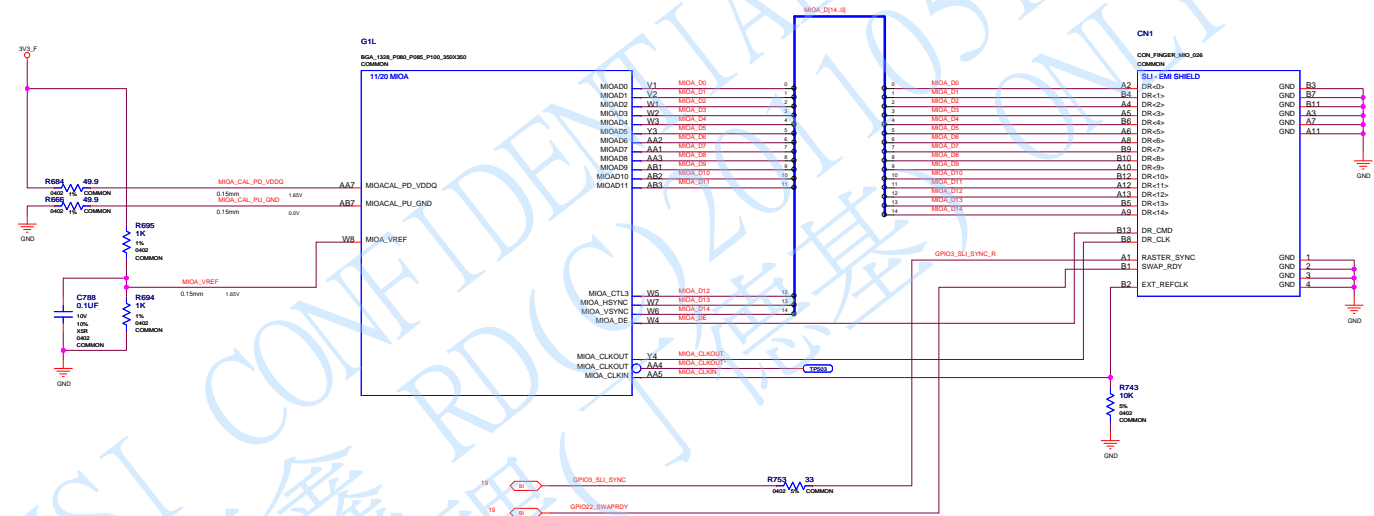
- Three cases to be considered:
1. DP AUX to DP connector: AUX AC coupled
 2. DP AUX to DP-DVI dongle: AUX pass through
 3. DP AUX to DVI connector: AUX level shifted to DDC



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MIO A Interface, SLI



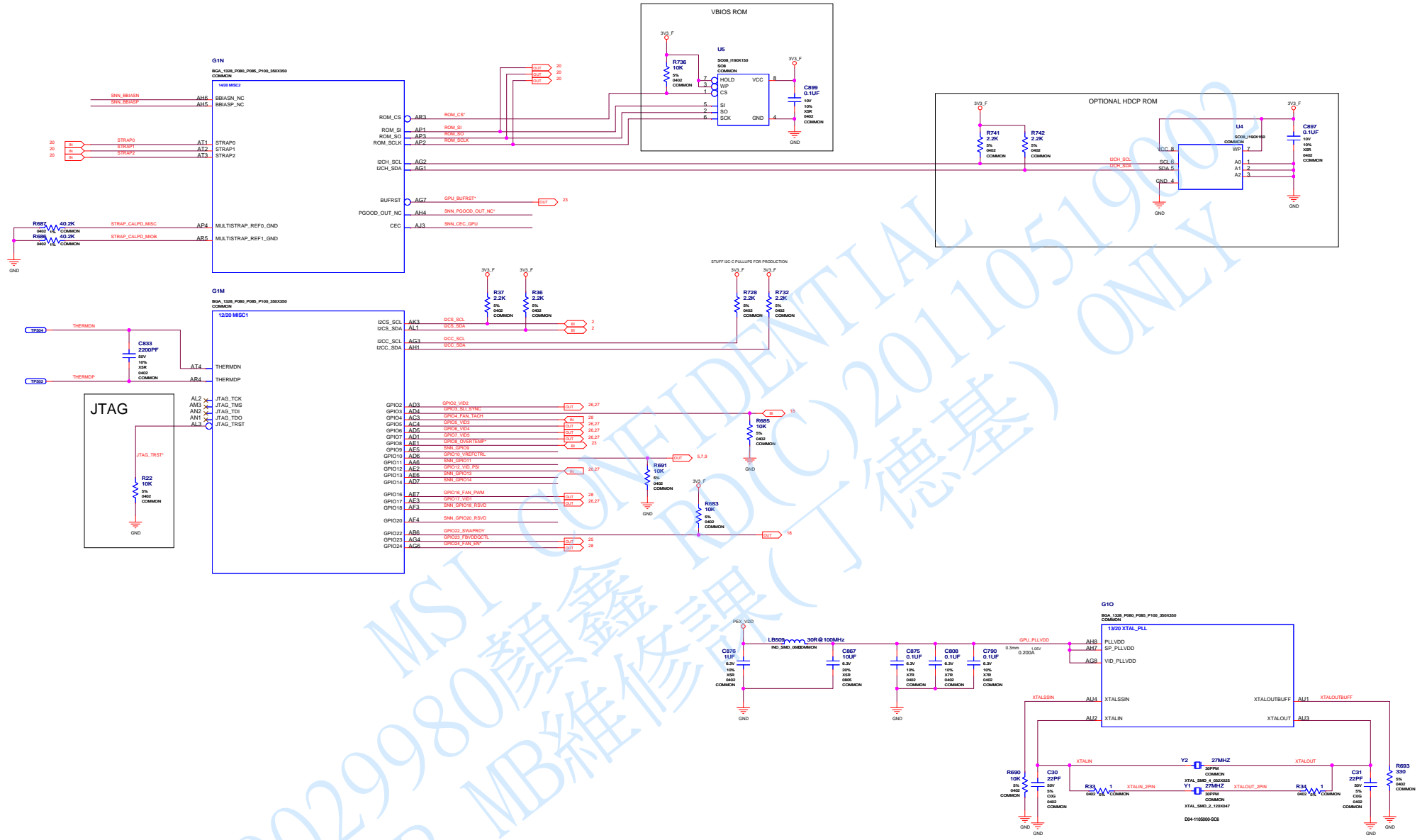
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PAGE DETAIL	MIO A Interface, SLI

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MISC: GPIO, I2C, ROM, HDCP, and XTAL

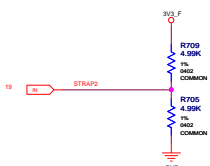
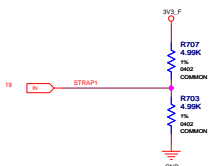
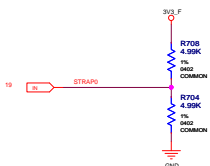
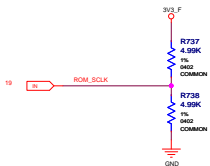
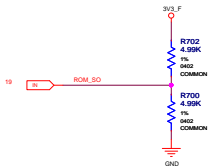
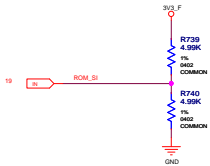


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ASSEMBLY	P1060 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	MISC: GPIO, I2C, ROM, HDCP, and XTAL

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Strap Configuration



ROM_SO	VGA_DEVICE DMS_A17_A00M PL_3_BAH_SIZE XCLK_A17	LSB MSB	1: VGA_DEVICE 0: Disabled 0: 255MB/PCIE 0: 277MHz	PU 10K	PU 10K
ROM_SI	RAM_CFG2[0]		Out: 64MB/16 128MB Samsung Out: 64MB/16 128MB Hynix Out: 64MB/16 128MB Samsung	45K 35K 35K	45K 45K 35K
ROM_SCLK	PEX_PLL_EN_TERM[0] SLUT_CLK_CONFIG SCLK_VENOR PCI_DEV0_DEV7	LSB MSB	0: DISABLED (PCIE) 1: GFI and HCH COMMON RESET CLK 1: VBUS/ROM IS PRESENT 0: PCIE1/PCIE0		35K
STRAP0	USER_BRT[0]		Out: Default		5K
STRAP1	SGD_PADCFG_LUT_A0M[0]		Out: Default (High Imp)		5K
STRAP2	PCI_DEV02[0]		Out: PCI DEV02 (A0000000)		10K

Multi-level Strap Resistor Values	
Strap PU/PD Res (Ohm)	
0	PD 15
1	PD 10
2	PD 15
3	PD 20
4	PD 25
5	PD 30
6	PD 35
7	PD 40
8	PU 5
9	PU 10
10	PU 15
11	PU 20
12	PU 25
13	PU 30
14	PU 35
15	PU 40

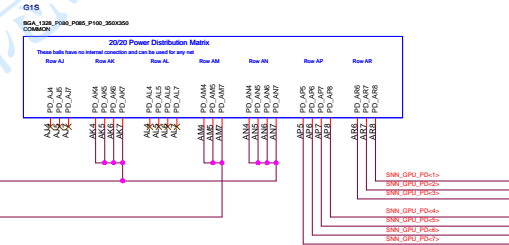
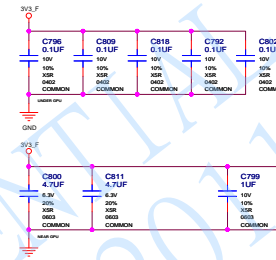
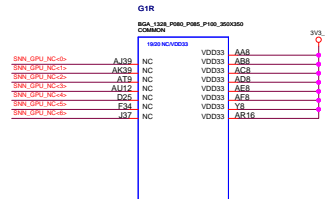
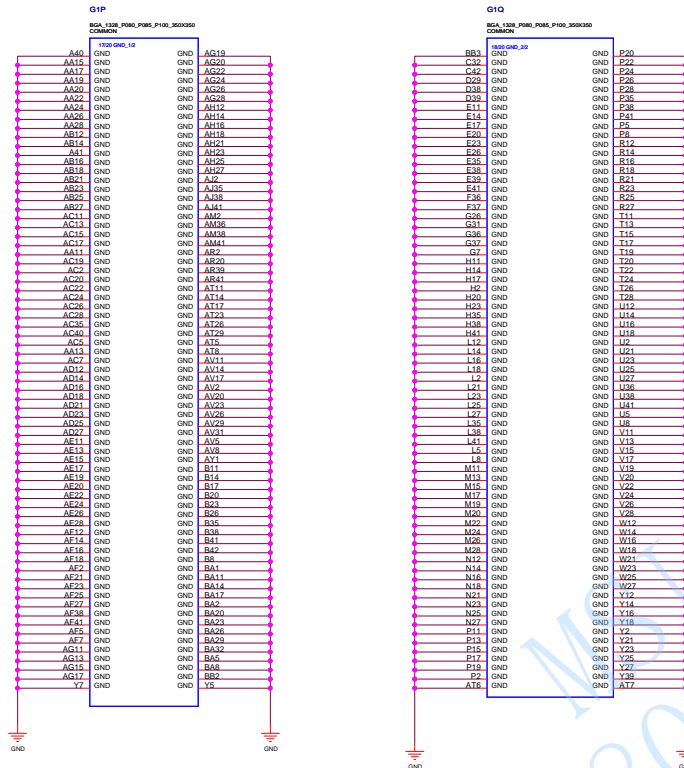
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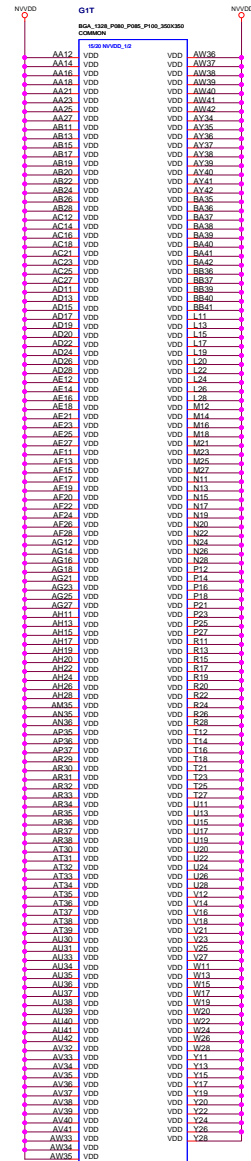


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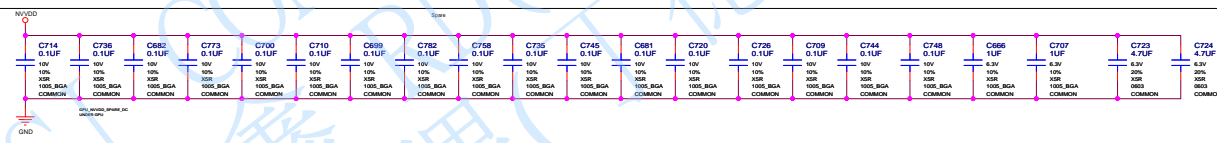
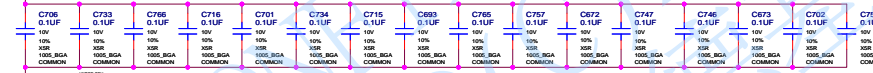
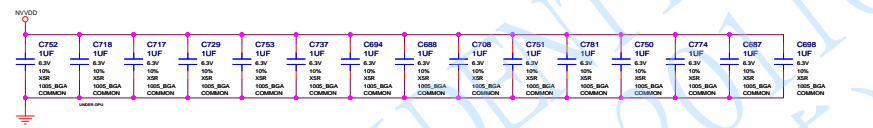
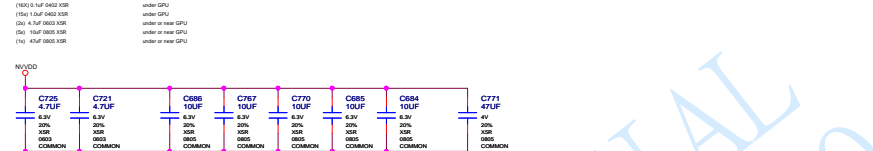
PWR and GND Signals



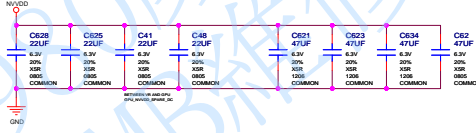
NVVDD Decoupling



GF106 NVVDD Decap Guidelines



SPARE LOW-FREQUENCY CAPS



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PAGE DETAIL	NVVDD Decoupling

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PS I: 3V3, 12V, and 12V_EXT, 5V,

3V3 Power Supply Filter

3V3_F = 3.3V @ 2.4A

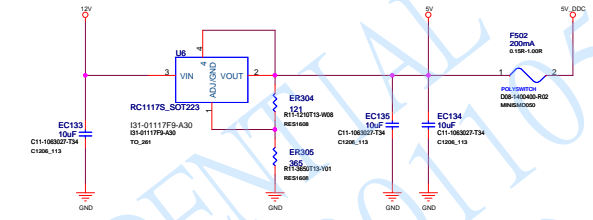


12V Power Supply Filter

12V_F = 12V @ 5.5A



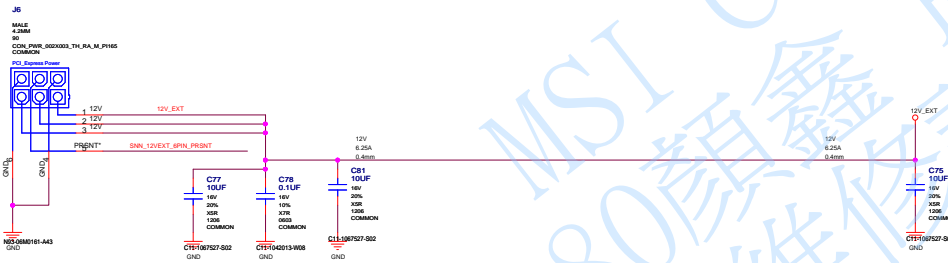
5V POWER SUPPLY, DDC POWER



12V_EXT Power Connector

12V_EXT Power Supply Filter

12V_EXT_F = 12V @ 6A



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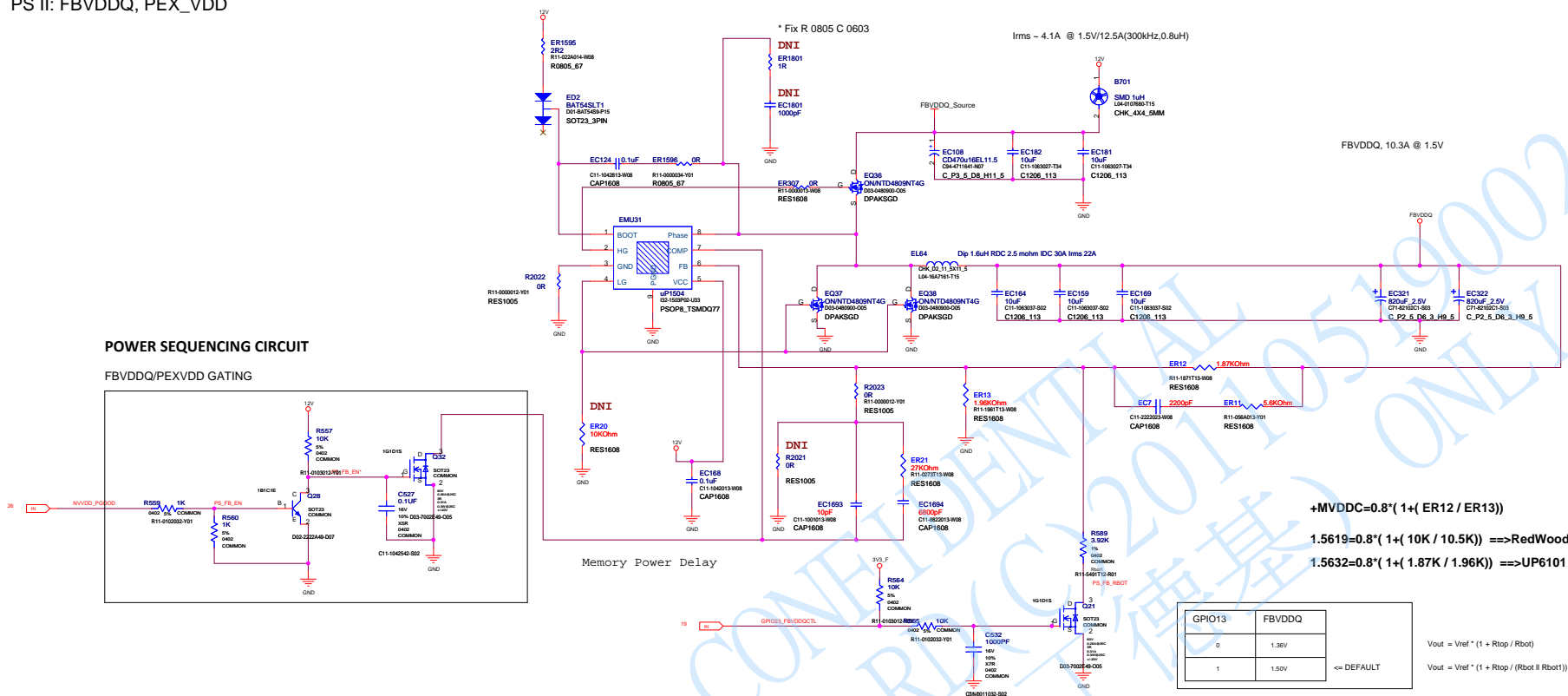
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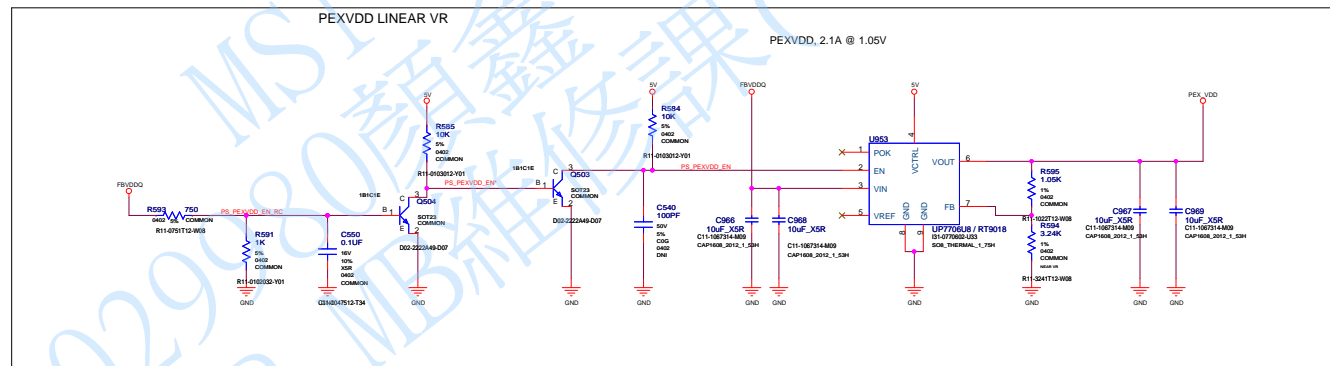
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PS II: FBVDDQ, PEX_VDD



CONTROLLER	Fosc	VREF
UP103A	300KHz	0.8V
APW1120	300KHz	0.8V
APW1165	300KHz	0.8V
RT8165	300KHz	0.8V
NCP1679	275KHz	0.8V
RT8101	300KHz	0.8V
UP1010B	300KHz	0.8V
L6783A	300KHz	0.8V
L6726A	275KHz	0.8V
FP6329	300KHz	0.6V
L6727	300KHz	0.8V

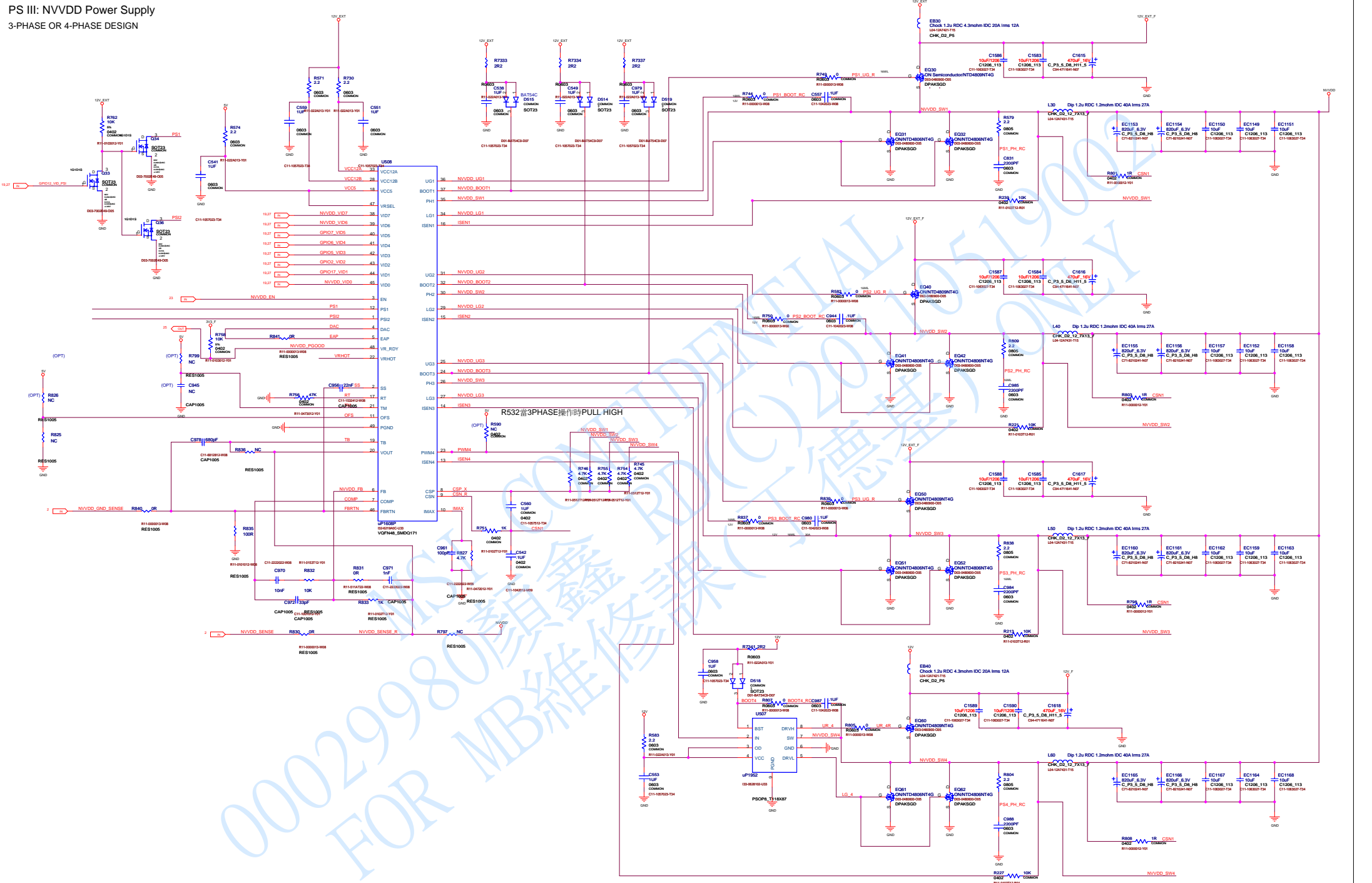


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PS III: NVVDD Power Supply

3-PHASE OR 4-PHASE DESIGN

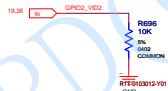
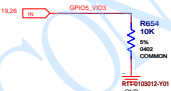
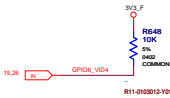
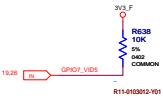


PS IV: NVAPI VID

NVAPI VID									
GPIO7 GPIO8 GPIO9 GPIO10 GPIO11 GPIO12									
GPIO7	GPIO8	GPIO9	GPIO10	GPIO11	GPIO12	GPIO13	GPIO14	GPIO15	GPIO16
0	0	0	0	0	0	1.2500			
0	0	0	0	1	1	1.2500			
0	0	0	1	0	1	1.2500			
0	0	0	1	1	1	1.2500			
0	0	1	0	0	1	1.2500			
0	0	1	0	1	1	1.2500			
0	0	1	1	0	0	1.2500			
0	0	1	1	1	1	1.2500			
0	1	0	0	0	1	1.2500			
0	1	0	0	1	1	1.2500			
0	1	0	1	0	0	1.2500			
0	1	0	1	1	1	1.2500			
0	1	1	0	0	0	1.2500			
0	1	1	0	1	1	1.2500			
0	1	1	1	0	0	1.2500			
0	1	1	1	1	1	1.2500			
1	0	0	0	0	1	1.2500			
1	0	0	0	1	1	1.2500			
1	0	0	1	0	0	1.2500			
1	0	0	1	1	1	1.2500			
1	0	1	0	0	0	1.2500			
1	0	1	0	1	1	1.2500			
1	0	1	1	0	0	1.2500			
1	0	1	1	1	1	1.2500			
1	1	0	0	0	0	1.2500			
1	1	0	0	1	1	1.2500			
1	1	0	1	0	0	1.2500			
1	1	0	1	1	1	1.2500			
1	1	1	0	0	0	1.2500			
1	1	1	0	1	1	1.2500			
1	1	1	1	0	0	1.2500			
1	1	1	1	1	1	1.2500			



VI is single-phase clock oscillation mode
when PSI is asserted low




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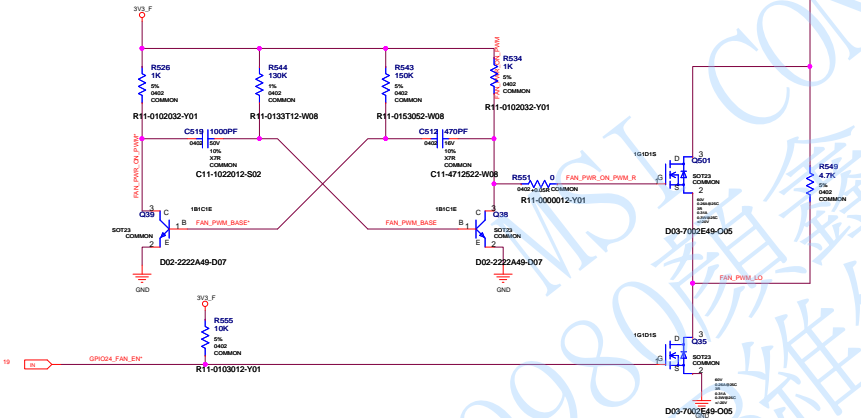
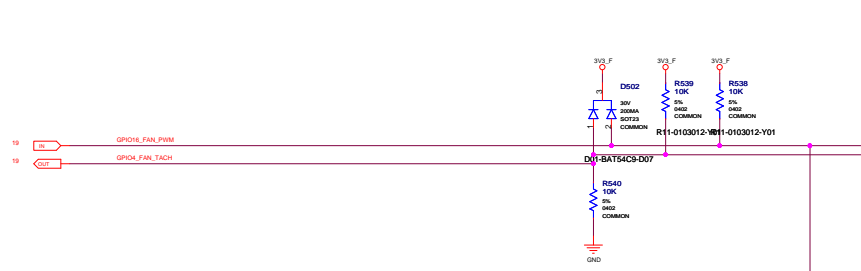
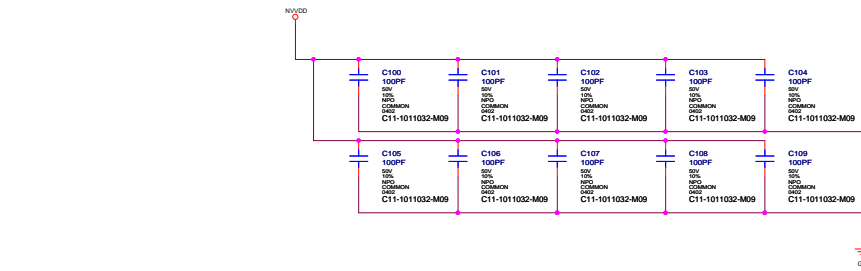
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Thermal, Mechanical, and Bracket



MEC4
MEC SCREW_HEX JACK
COMMON
E42-5047007-H75

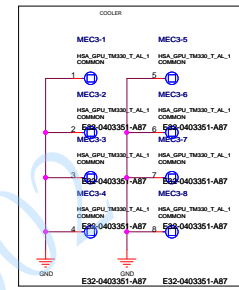
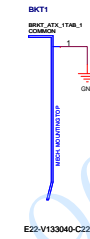
MC1
JPCB Posttype
E43-1203004-P65

MEC5
MEC SCREW_HEX JACK
COMMON
E42-5047007-H75

MEC6
MEC SCREW_HEX JACK
COMMON
E42-5047007-H75

MEC7
MEC SCREW_HEX JACK
COMMON
E42-5047007-H75

Mechanical

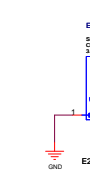
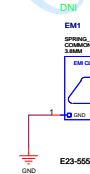


SPECIAL MECHANIC

BOARD STIFFENER

PCB1
PCB
E43-11000-1000-A02
P80-0V23640-V29

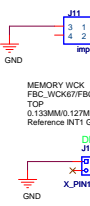
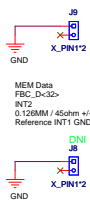
Reserve EMI solution EMI Spring



Reserve FM1-FM6 for PCB ME



Reserve J8 - J13 for Impedance check



NVIDIA CORPORATION

2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA



NV_PN 600-11060-base-200

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BOM REV A

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PAGE DETAIL Thermal and Mechanical