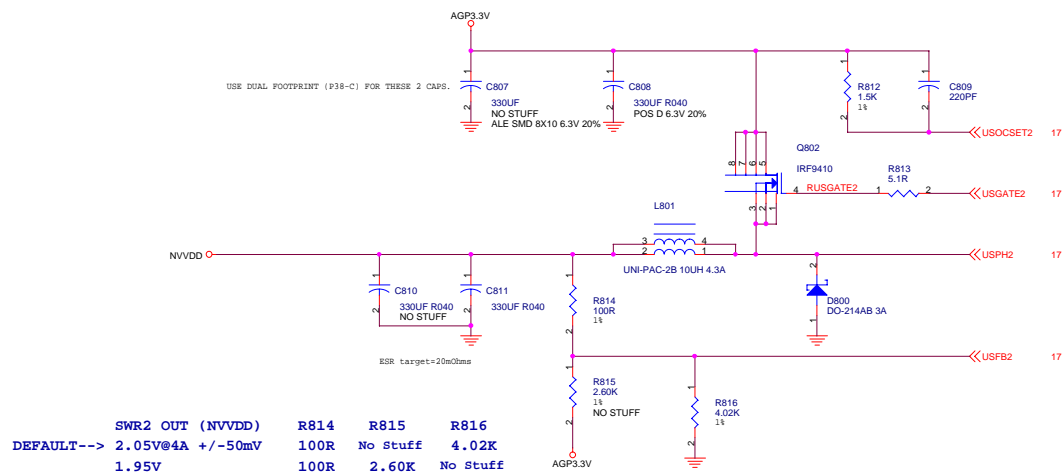


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	FLASH ROM	
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SWR2 OUT (NVVDD)

DEFAULT--> 2.05V@4A +/-50mV

1.95V

R814 100R

R815 No Stuff

R816 4.02K

100R 2.60K No Stuff

$$R816=100/((2.05V/2)-1)$$

$$R815=130/(2-1.95v)$$



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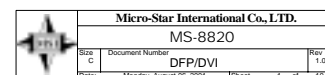
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Size	Document Number	Rev
B	NV15 DDR POWER ADDER	1.0
Date:	Monday, August 06, 2001	Sheet 1 of 18

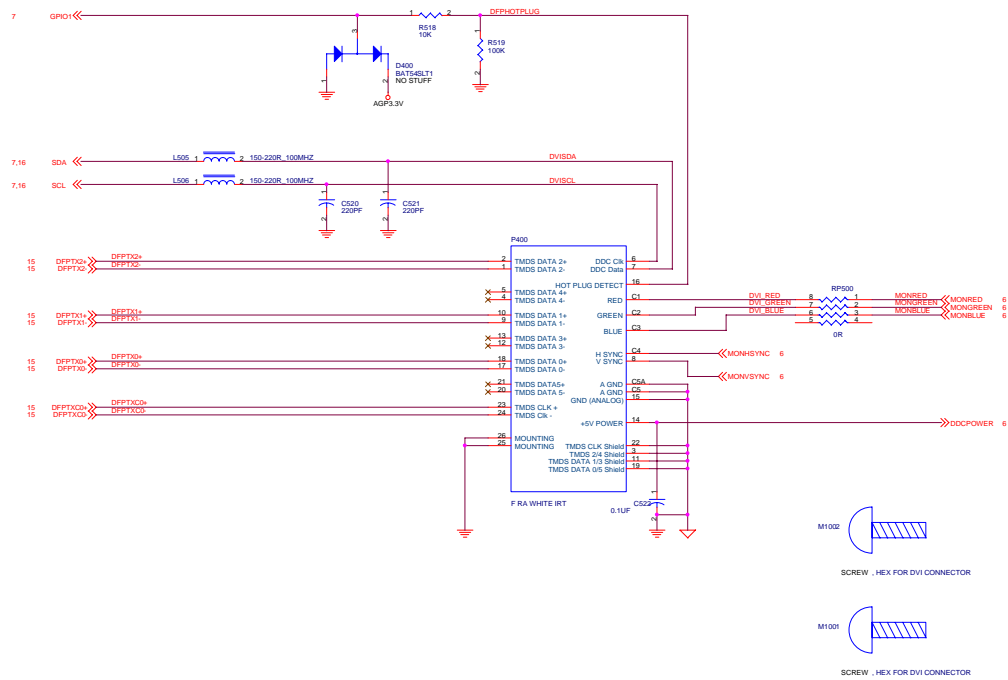








<<TVED1118 5,7,15,16,19  
 <<R0CMCL 7,19  
 <<DPCLK 7,15  
 <<DPD1113 7,15  
 <<DPVSYN 7,15



MFB067	22R	RP325	5	FBD067
MFB068			3	FBD068
MFB065			2	FBD065
MFB064			1	FBD064
MFB075	22R	RP327	4	FBD075
MFB072			6	FBD072
MFB073			7	FBD073
MFB074			8	FBD074
MFB066	22R	RP329	8	FBD066
MFB070			2	FBD070
MFB071			3	FBD071
MFB069			4	FBD069
MFB077	22R	RP331	1	FBD077
MFB078			6	FBD078
MFB079			7	FBD079
MFB076			5	FBD076

MFB095	22R	4	RP334	5	FBD095
MFB091		3		6	FBD091
MFB092		2		7	FBD092
MFB094		1		8	FBD094
MFB084	22R	5	RP335	4	FBD084
MFB083		6		3	FBD083
MFB085		7		2	FBD085
MFB086		8		1	FBD086
MFB081	22R	8	RP338	1	FBD081
MFB082		7		2	FBD082
MFB080		6		3	FBD080
MFB087		5		4	FBD087
MFB088	22R	1	RP330	8	FBD088
MFB090		2		7	FBD090
MFB093		4		5	FBD093

<< MFB0(127-0) 10,11,12  
 << MFB0M(15-8) 12  
 << FBD(127-0) 8,11  
 << FBD0M(15-8) 8

MFB097	22R	5	RP332	4	FBD097
MFB096		6	3		FBD096
MFB098		7	2		FBD098
MFB100		8	1		FBD100
MFB108	22R	5	RP334	4	FBD108
MFB110		6	3		FBD110
MFB109		7	2		FBD109
MFB111		8	1		FBD111
MFB103	22R	8	RP336	1	FBD103
MFB101		7	2		FBD101
MFB102		6	3		FBD102
MFB099		5	4		FBD099
MFB104	22R	1	RP338	8	FBD104
MFB105		2	7		FBD105
MFB107		3	6		FBD107
MFB106		4	5		FBD106

MFB0115	22R	4	RP333	5	FBD0115
MFB0114		3		6	FBD0114
MFB0113		2		7	FBD0113
MFB0112		1		8	FBD0112
MFB0119	22R	4	RP335	5	FBD0119
MFB0118		3		6	FBD0118
MFB0117		2		7	FBD0117
MFB0116		1		8	FBD0116
MFB0123	22R	8	RP337	1	FBD0123
MFB0122		7		2	FBD0122
MFB0121		6		3	FBD0121
MFB0120		5		4	FBD0120
MFB0126	22R	8	RP339	1	FBD0126
MFB0125		7		2	FBD0125
MFB0127		6		3	FBD0127
MFB0124		5		4	FBD0124

MFB0M11	R384	1	2 22R	FBD0M11
MFB0M10	R385	1	2 22R	FBD0M10
MFB0M9	R386	1	2 22R	FBD0M9
MFB0M8	R387	1	2 22R	FBD0M8
MFB0M14	R388	1	2 22R	FBD0M14
MFB0M15	R389	1	2 22R	FBD0M15
MFB0M12	R390	1	2 22R	FBD0M12
MFB0M13	R391	1	2 22R	FBD0M13

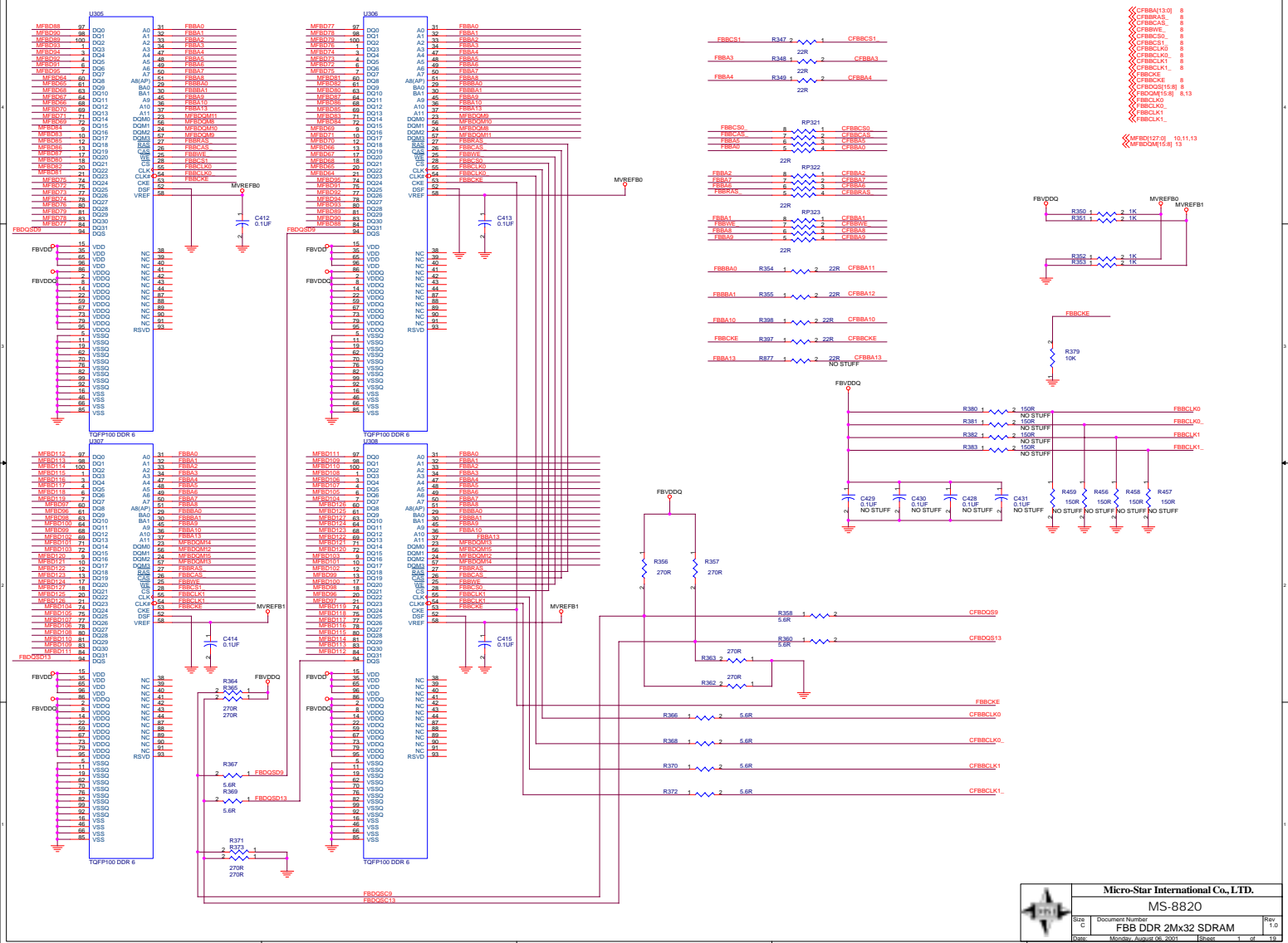


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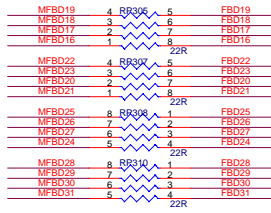
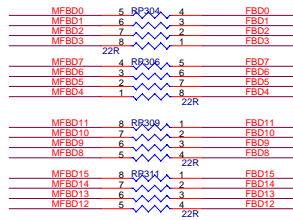
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Size B	Document Number	Rev 2.0
	FBB DDR 2Mx32 SDRAM TERM	
Date:	Monday, August 06, 2001	Sheet 1 of 1

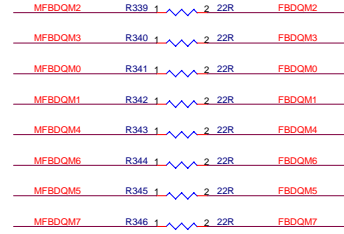
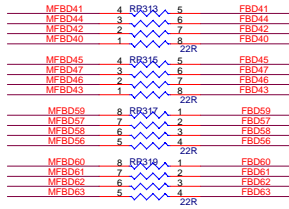
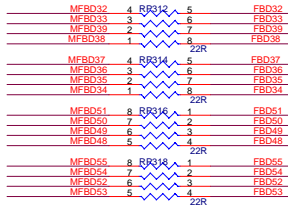








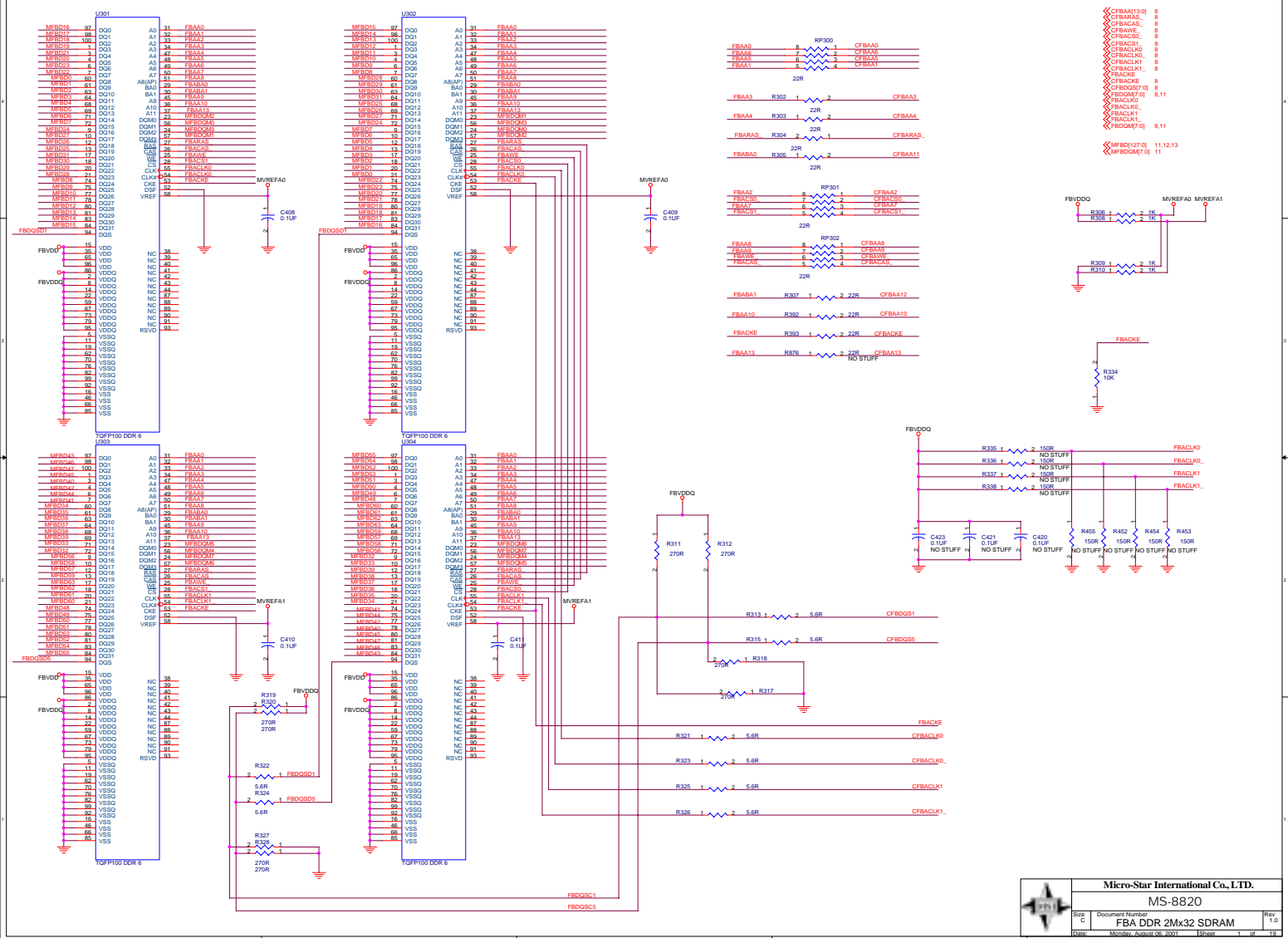
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<<MFB0QM[7:0] 10  
<<FBD[127:0] 8,13  
<<FBDQM[7:0] 8

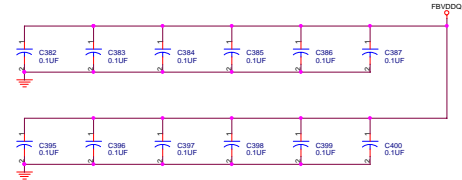
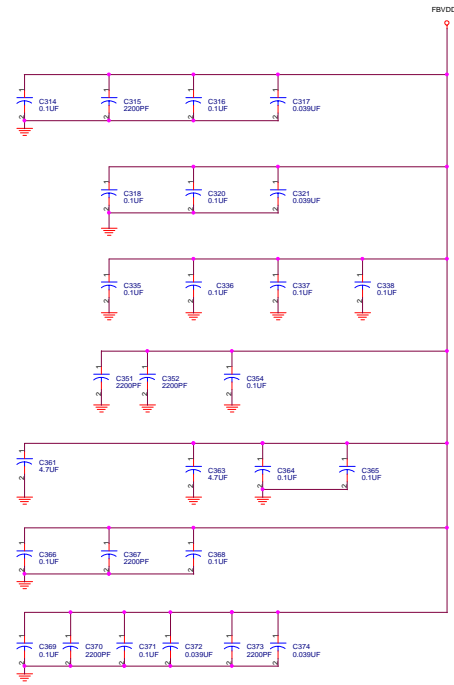
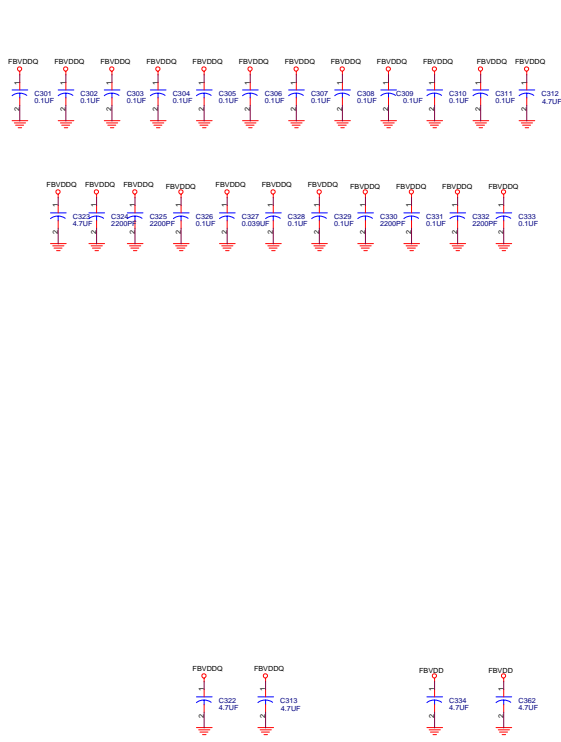


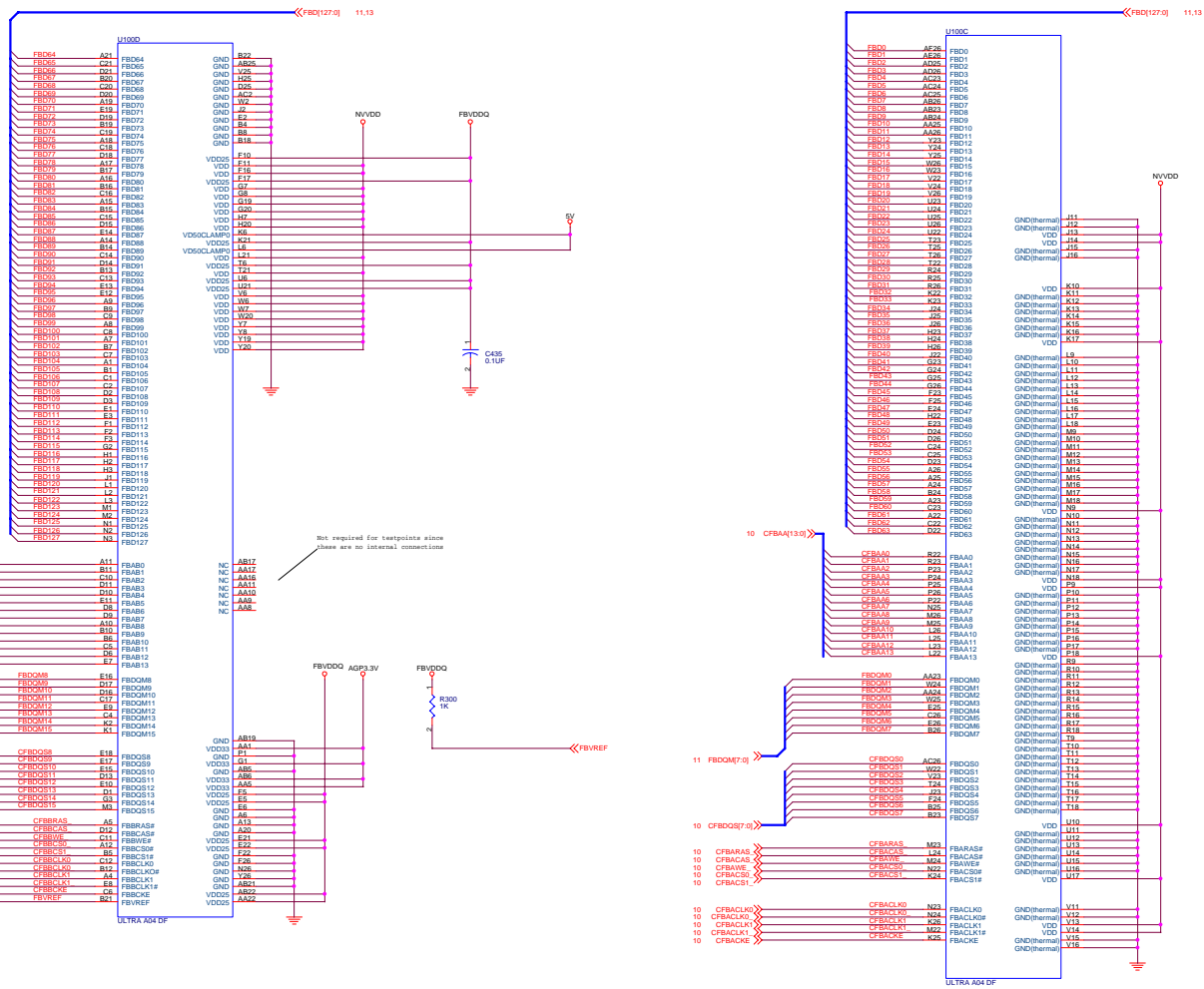
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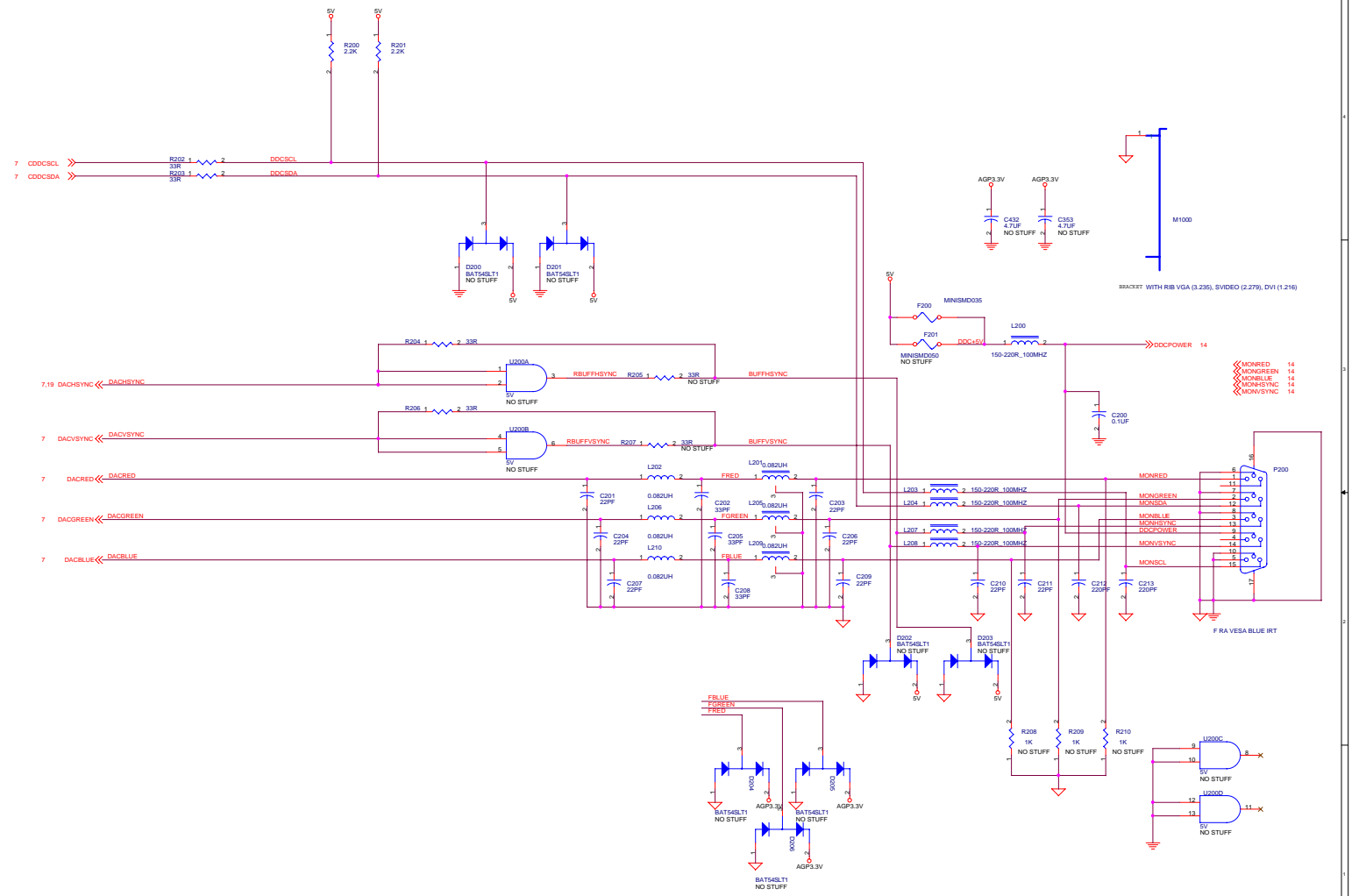
Size B	Document Number	Rev 1.0
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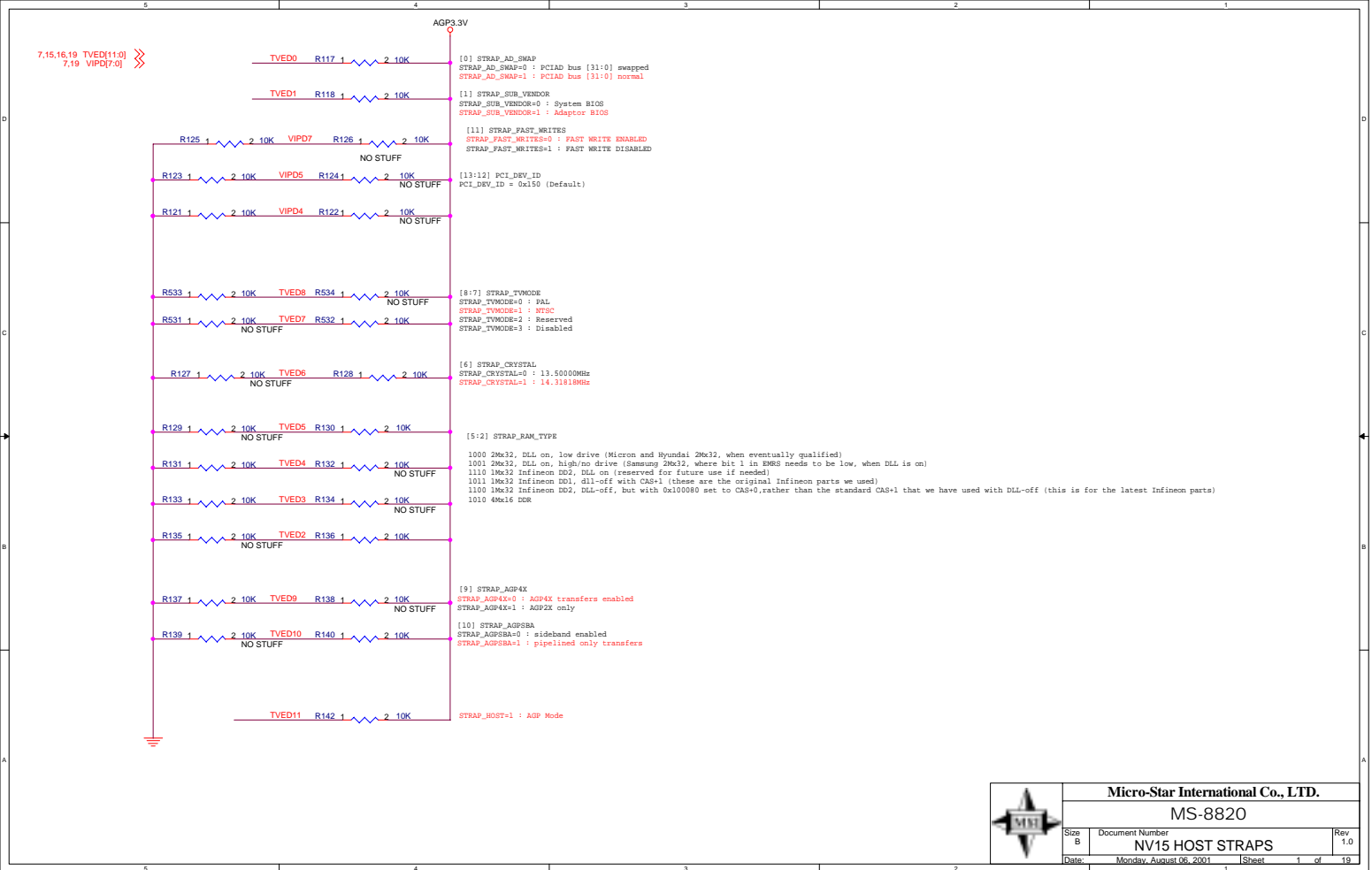


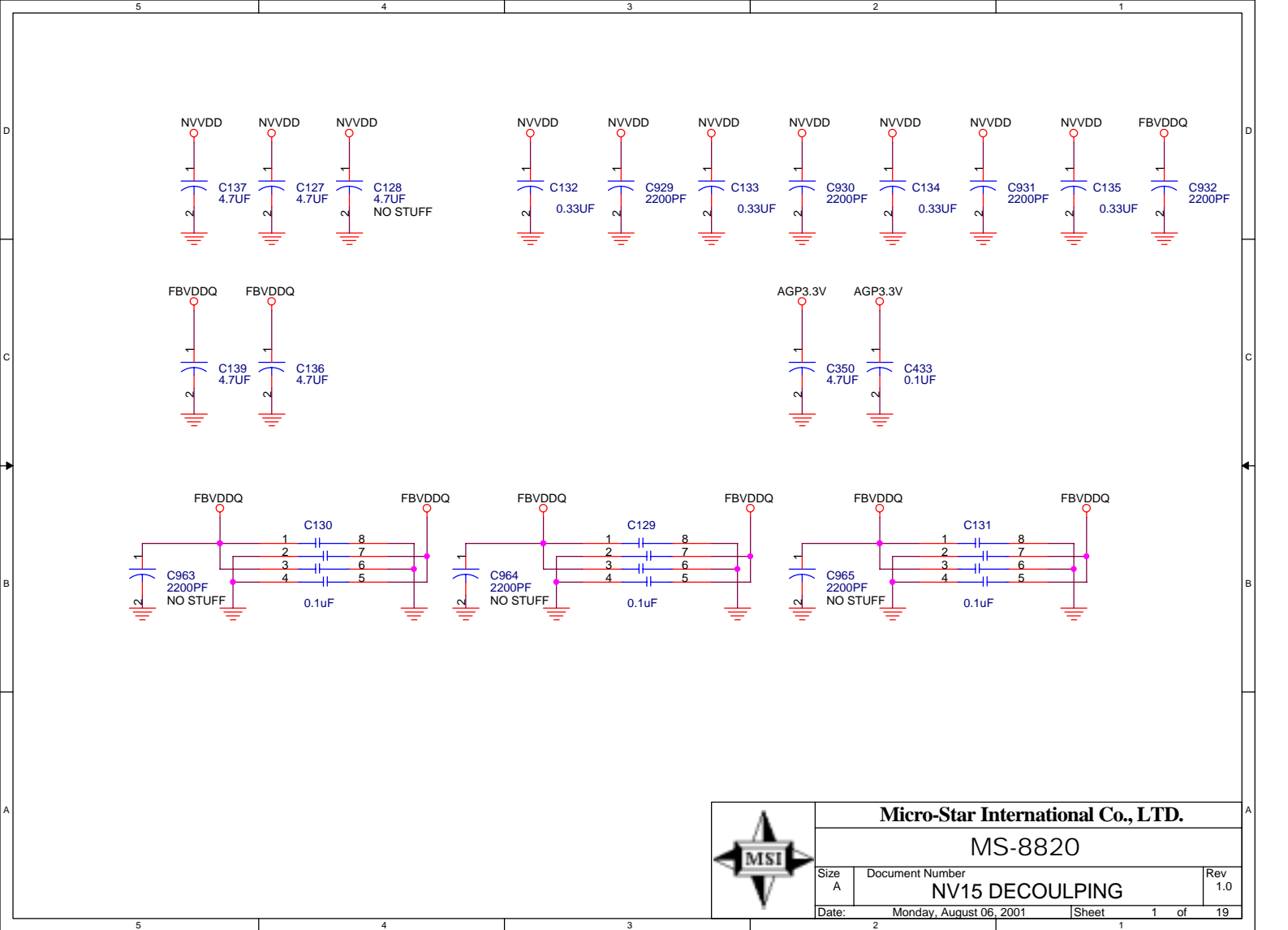






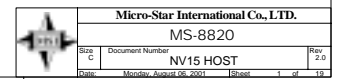













NV15, 1/2MX32 DDR, RGB, TV\_OUT, VIDEO\_IN, AGP4X.

PCI DEVICE ID 0X0=0X150.

Design Notes:

B00:FIXED TMDS CLKs CONNECTIONS.  
B00:ADDED 4MX32 SUPPORT.  
B00:Corrected memory configuration resistors.  
B00:Changed C809 to 4.7UF.  
B00:Changed R811 from 100R to 105R  
B00:C128 changed to no stuff  
B00:Removed 4mx32 function (R876, R877= NO STUFF)

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