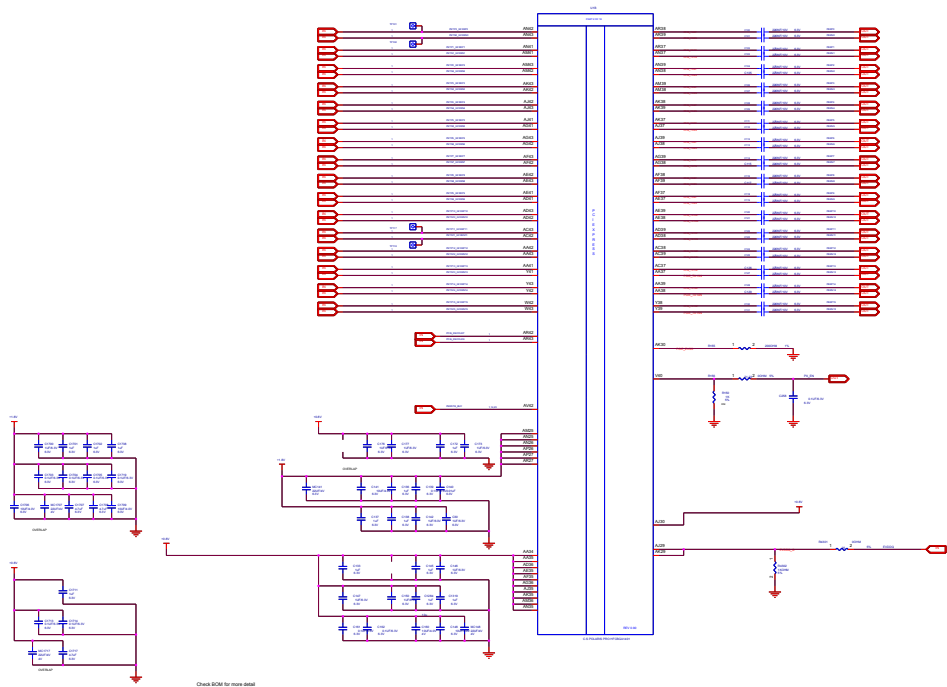
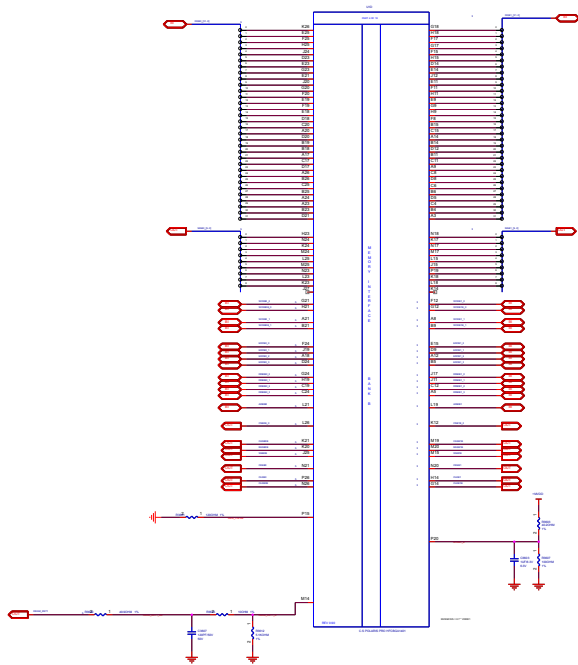
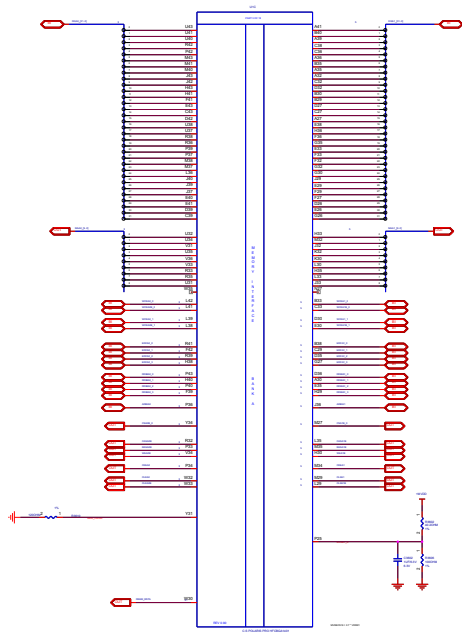
[illegible]

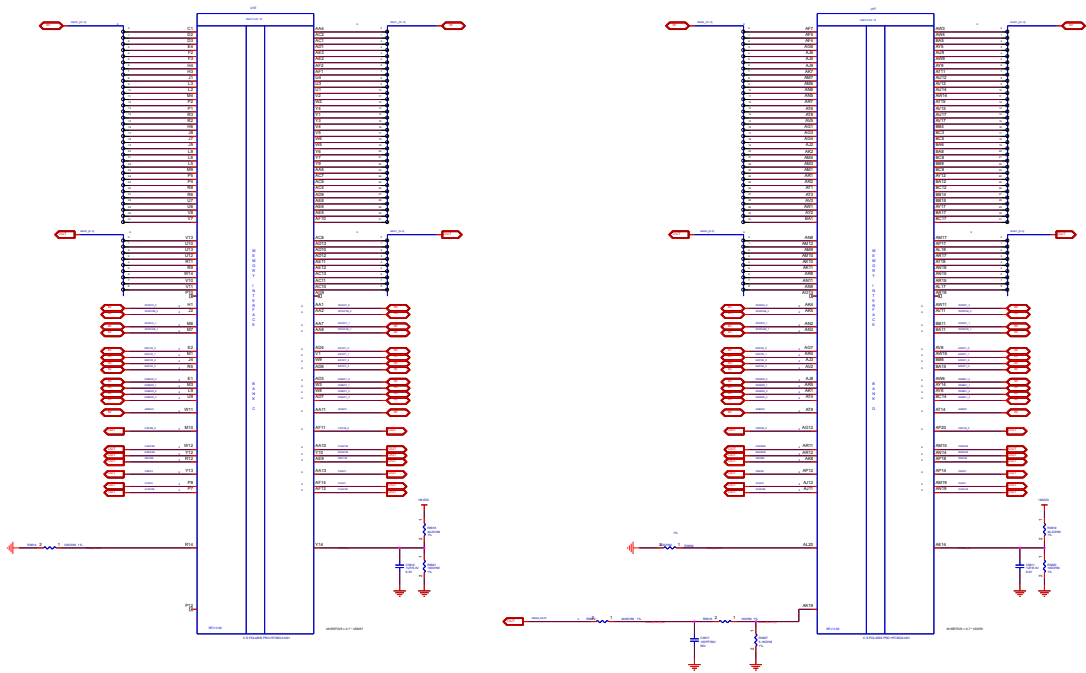
(2) ELLESMERE PCIE INTERFACE



(3) ELLESMERE MEM INTERFACE CH A/B

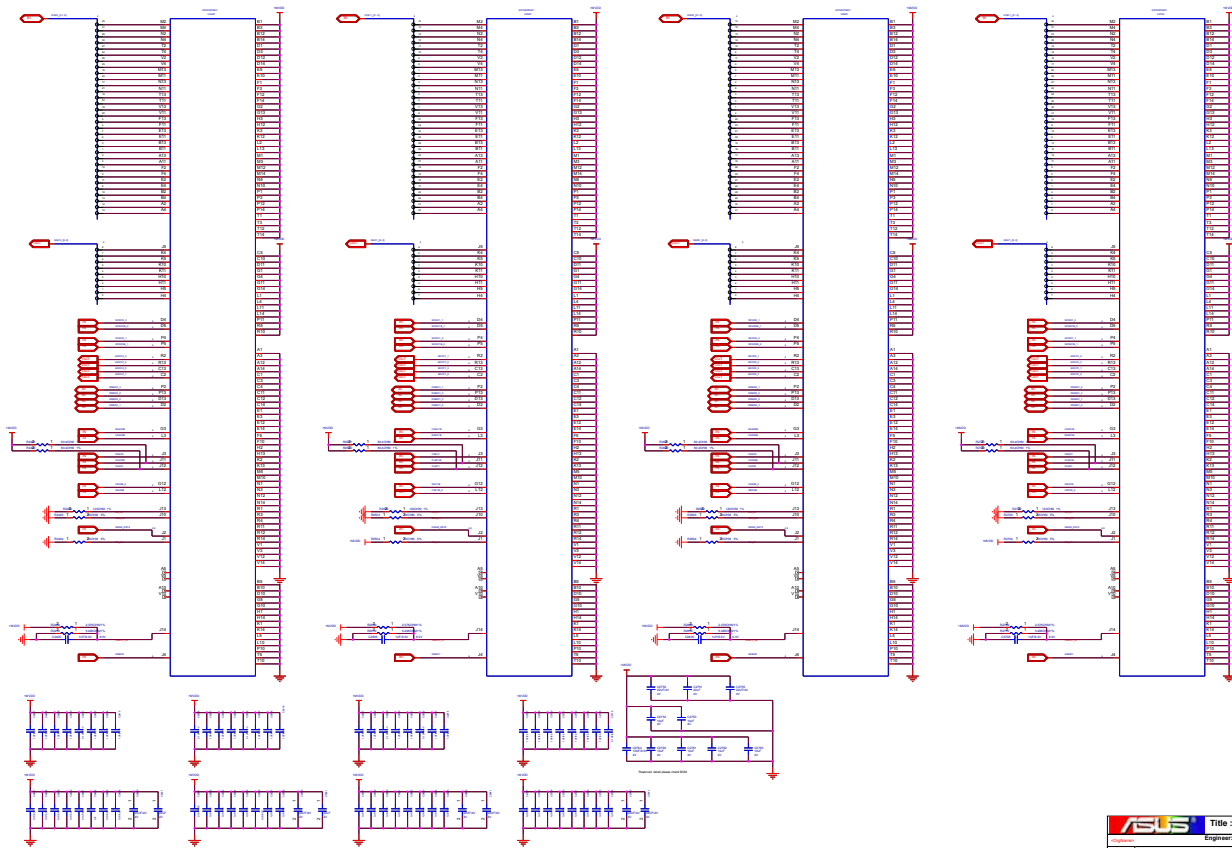


(4) ELLESMERE MEM INTERFACE CH C/D

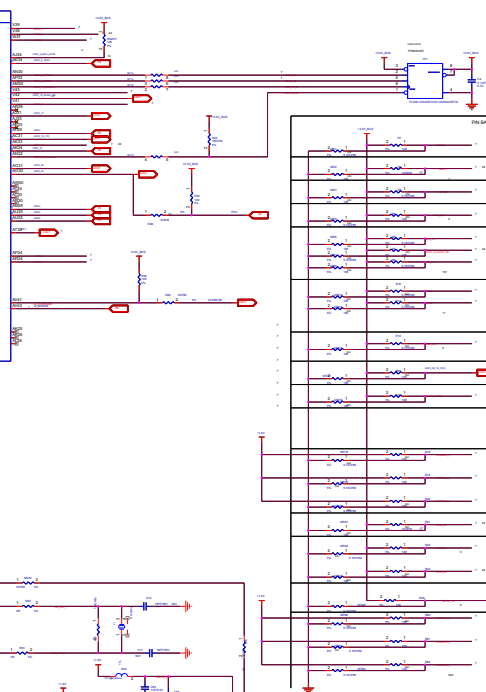
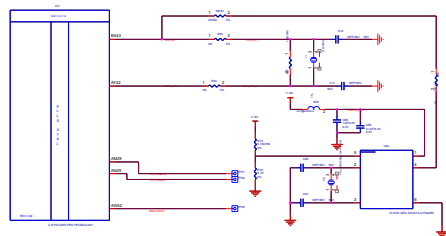
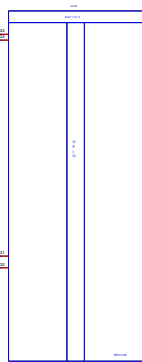


(5) GDDR5 MEMORY CH A/B

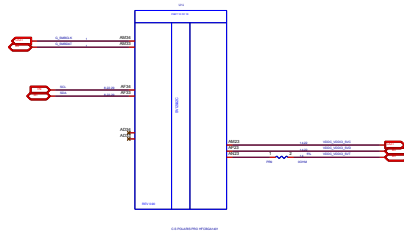


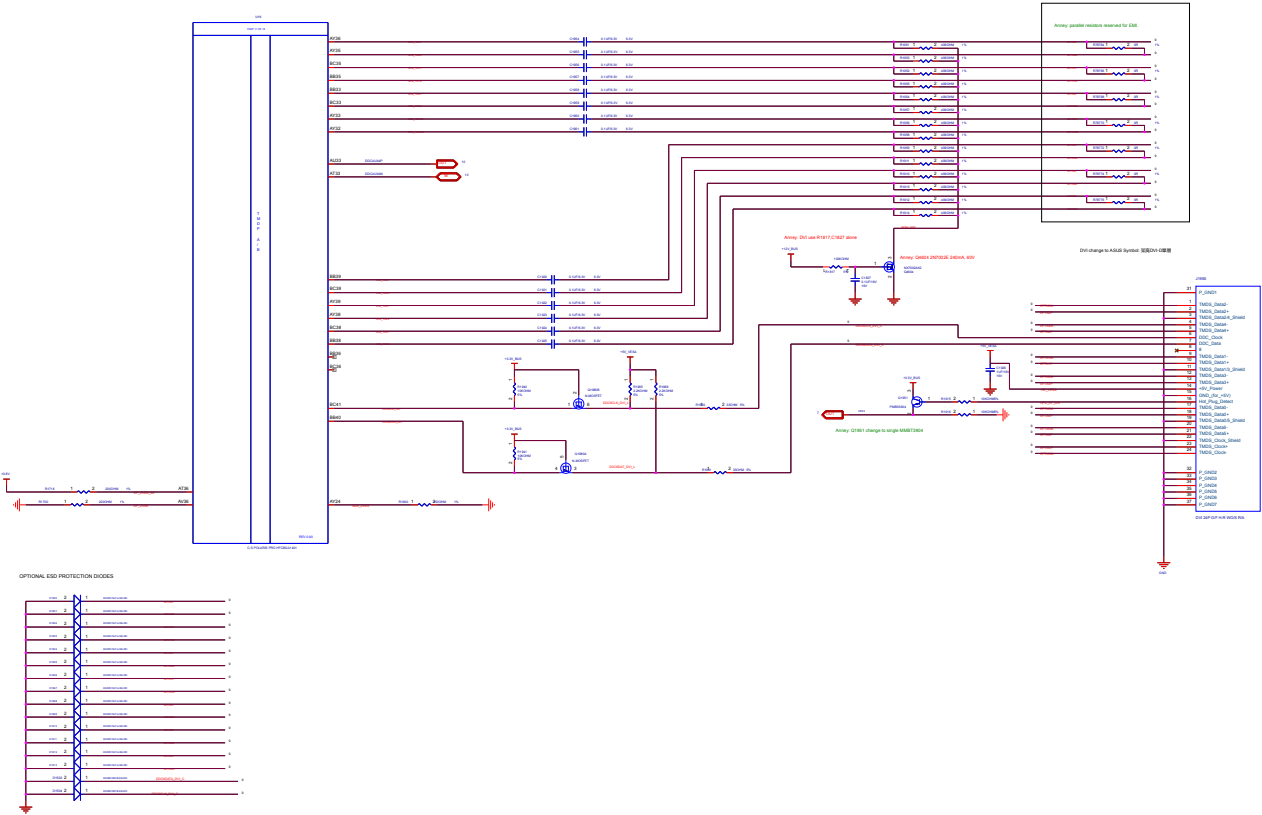


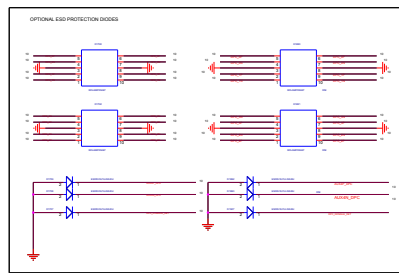
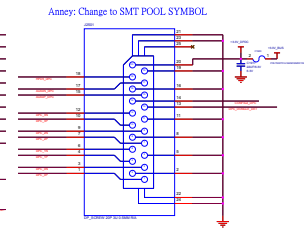
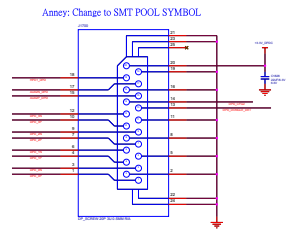
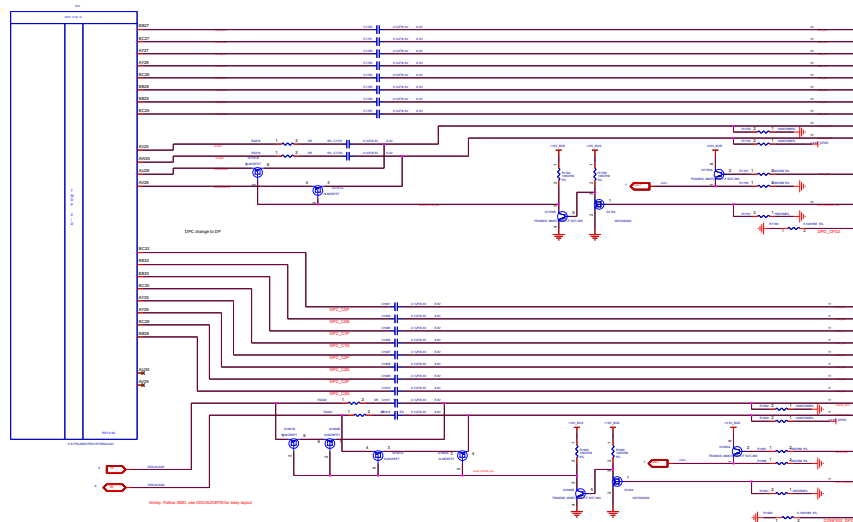
WFO ADDRESS	POSITION	OFFICE
NAME	EXT. TELEPHONE	ADDRESS

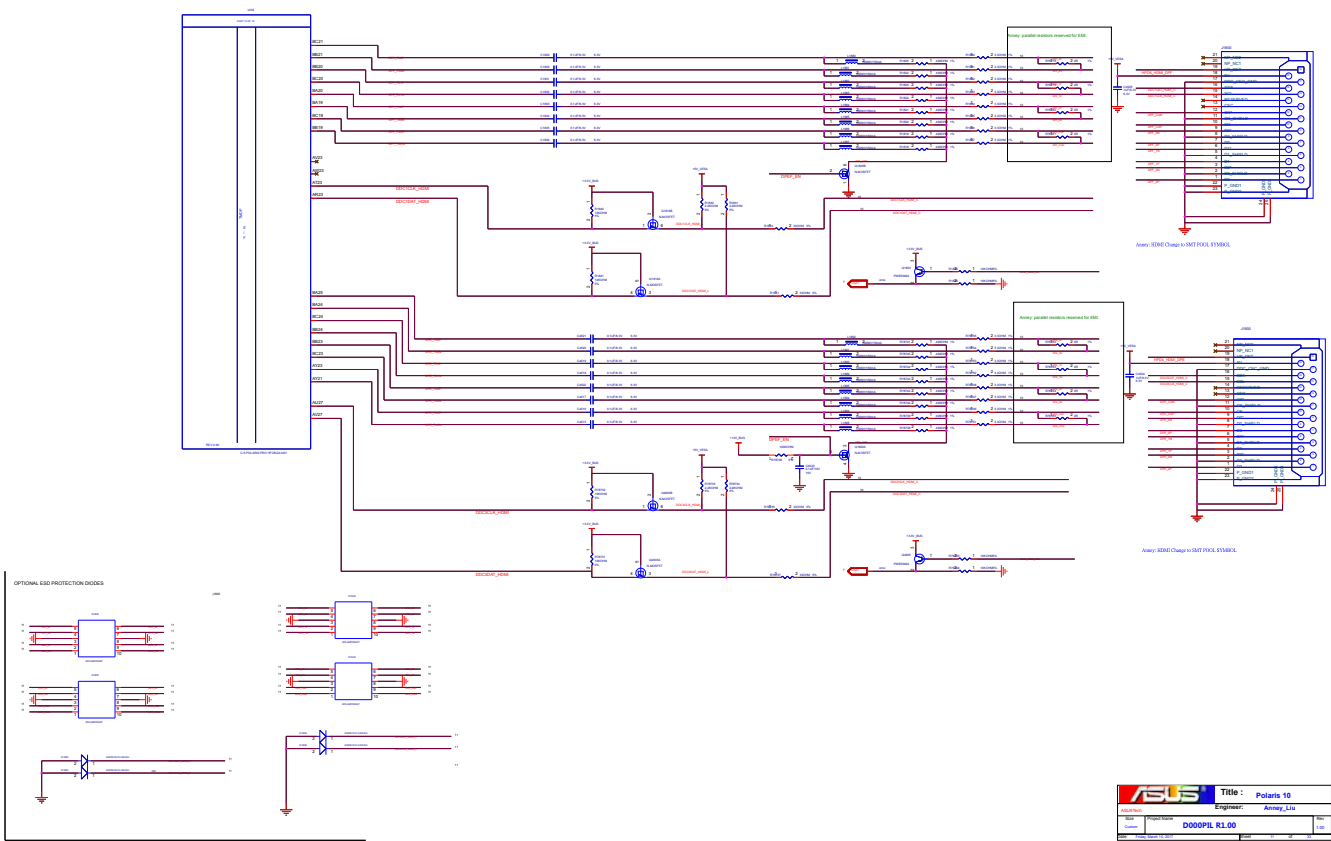
[illegible]

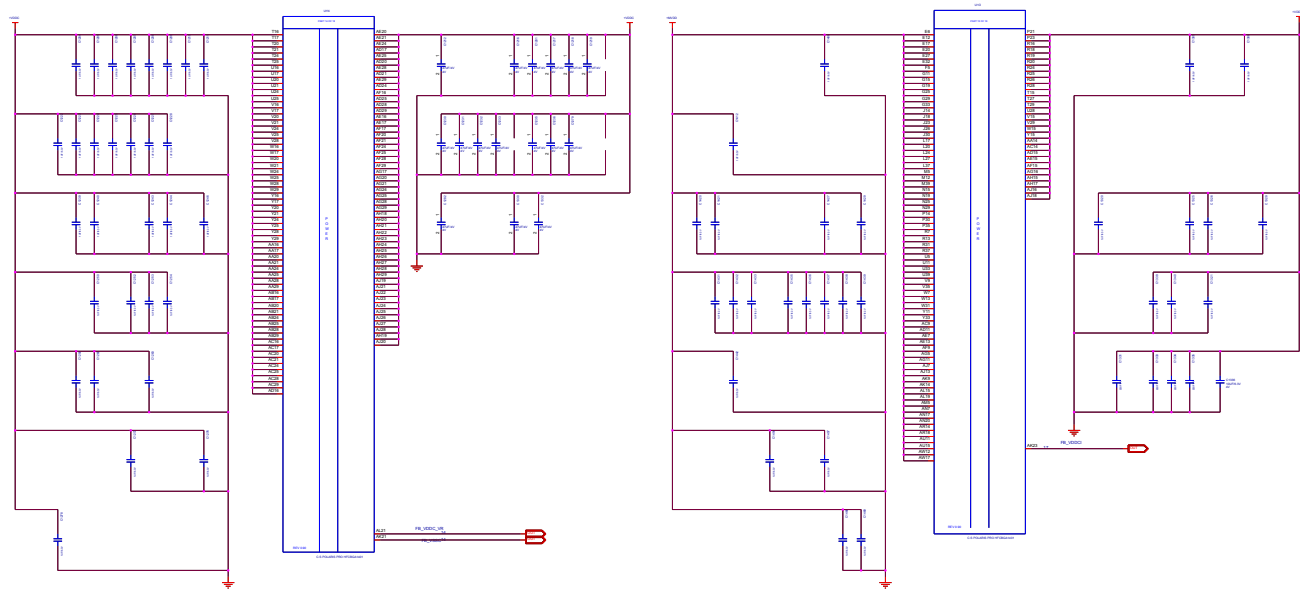
(8) ELLESMERE DAC1 LOCK

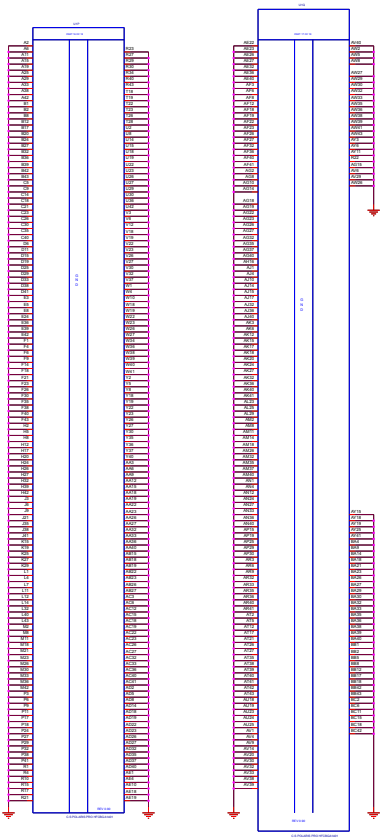


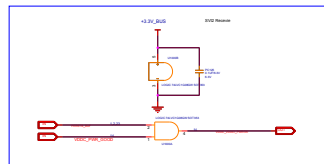
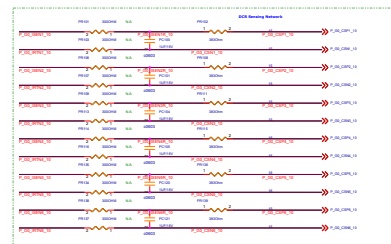




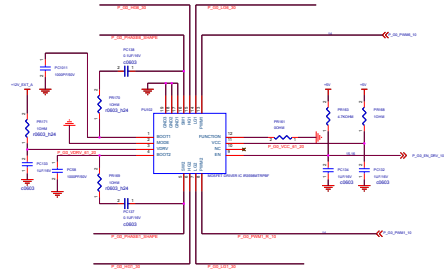




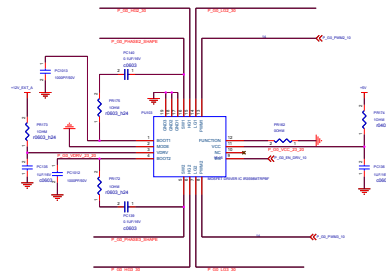




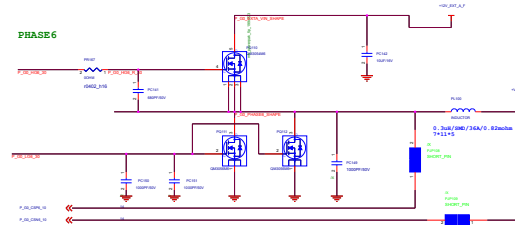
PHASE6_1



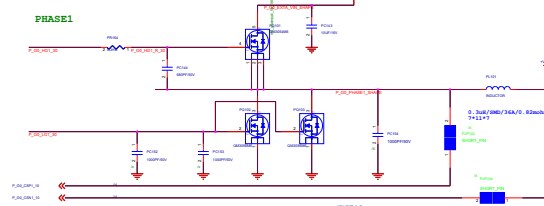
PHASE2-3



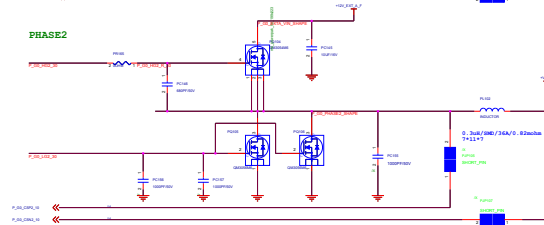
PHASE6



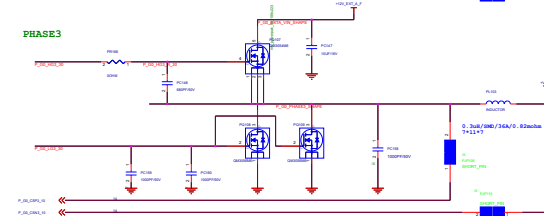
PHASE1



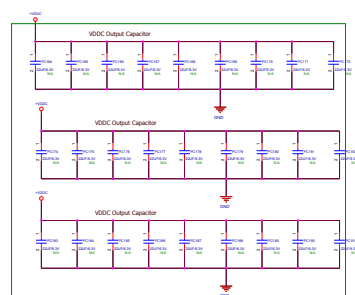
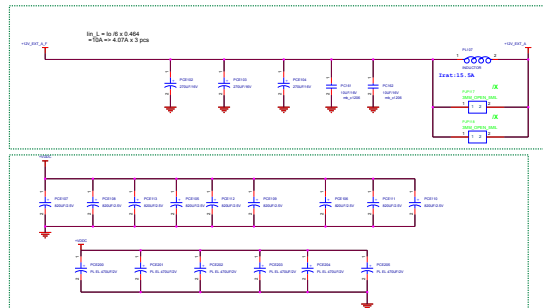
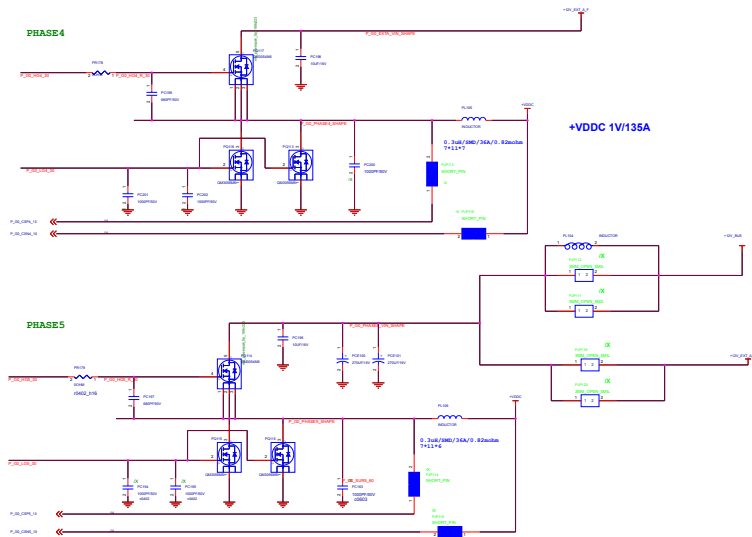
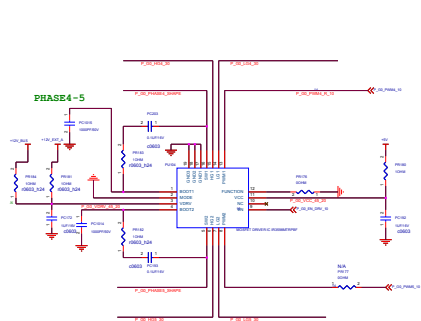
PHASE2



PHASE3

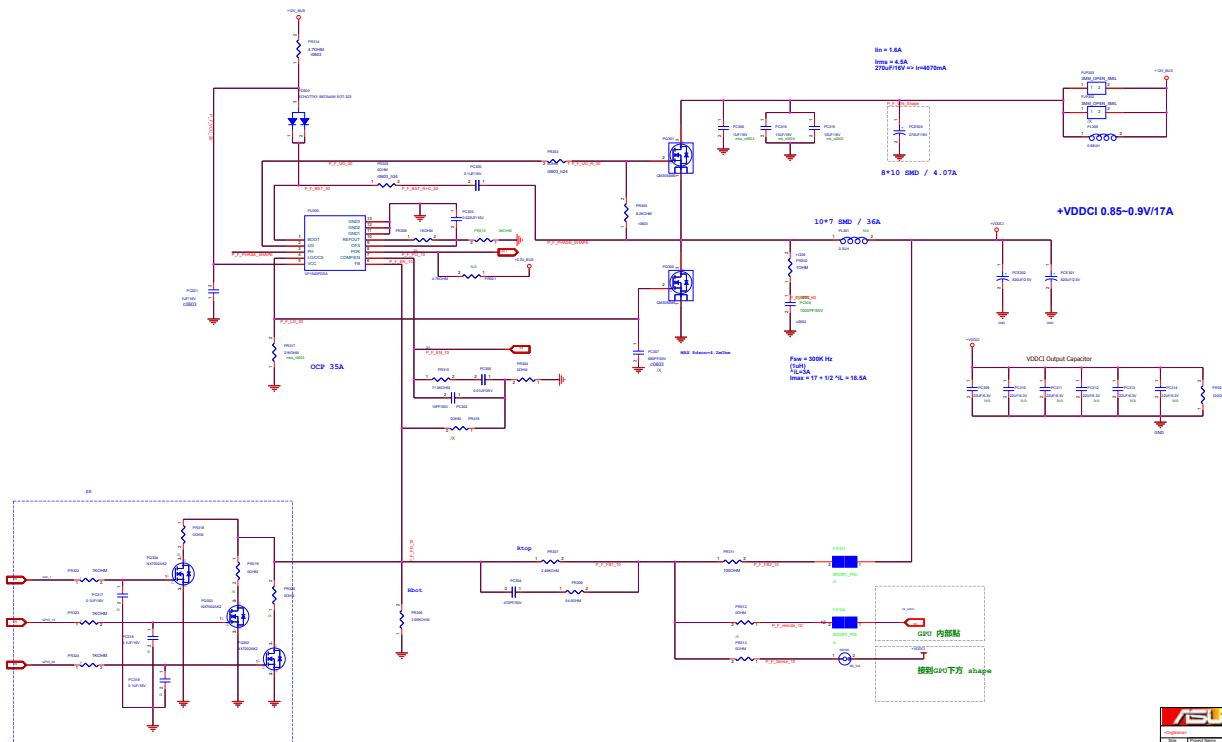


+VDDC 1V/135A



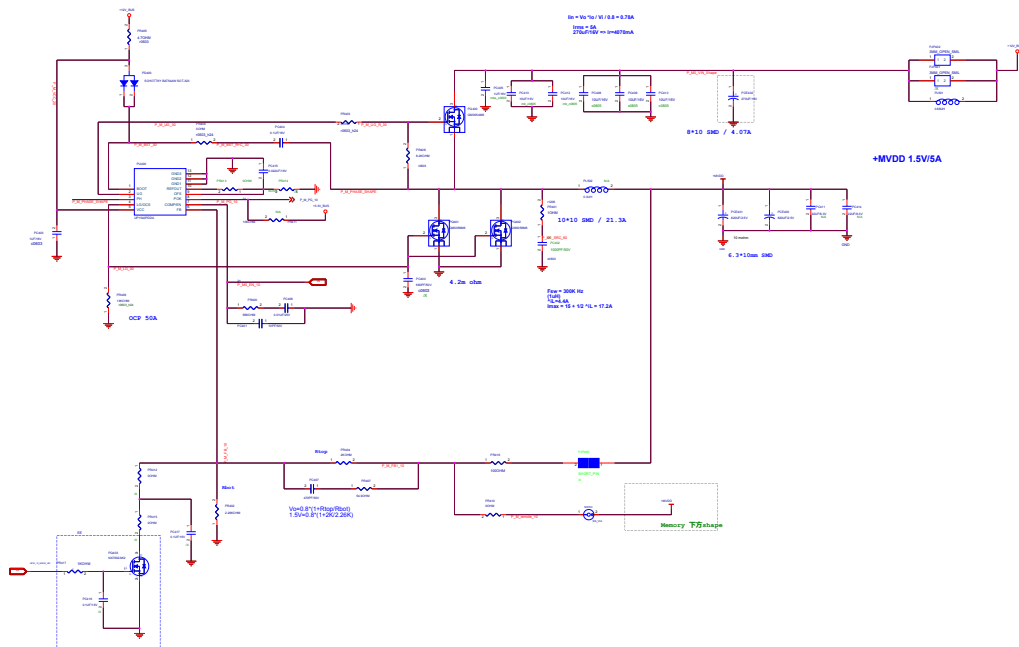
(17) VDDCI 1 Phase

+12V_EXT_A --> +VDDCI

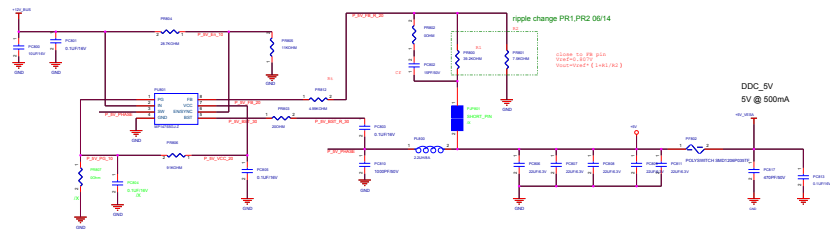


(19) MVDD 1 Phase

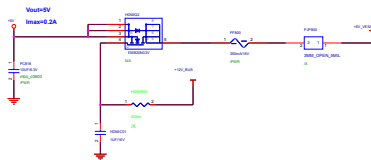
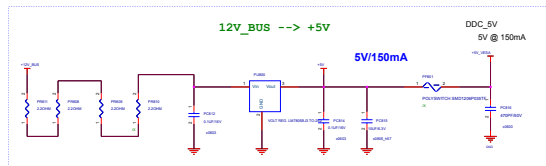
+12V_EXT_A--> +MVDD



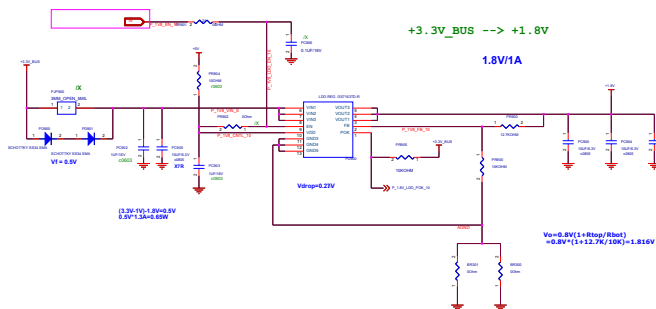
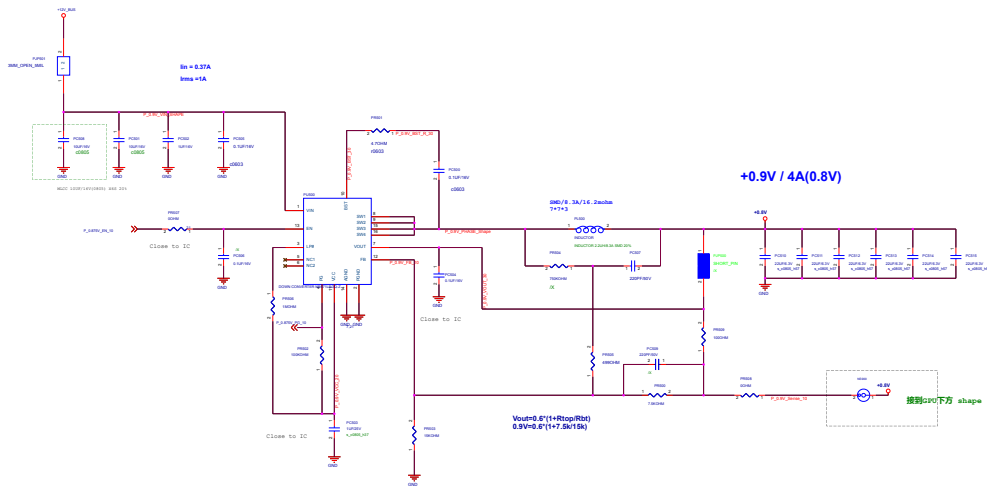
(20) Power 5V, 1V8,



Option



12V_PEX6 --> +0.9V



(21) POWER MANAGEMENT

Input source

PEX_3V3 INPUT - 10W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

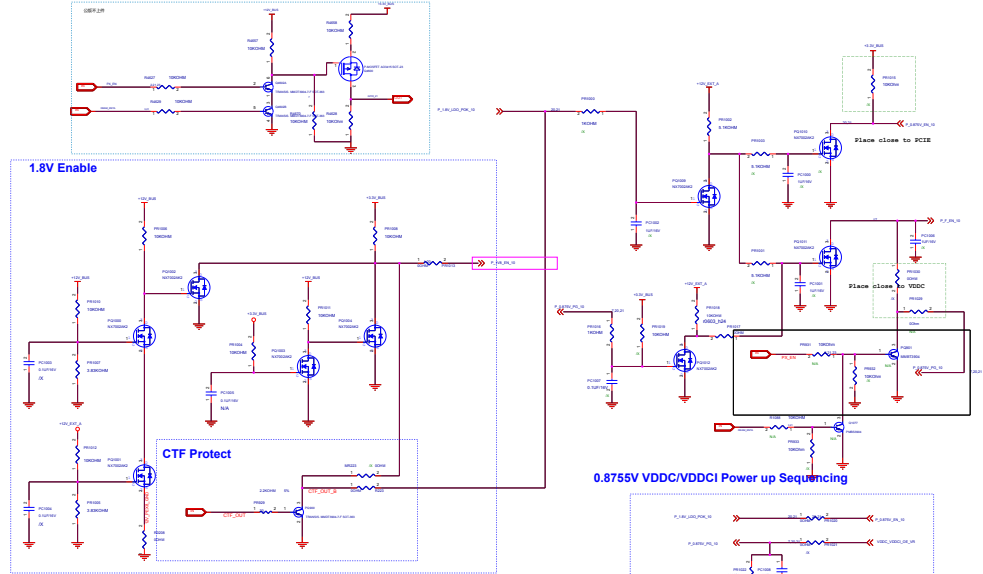
PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

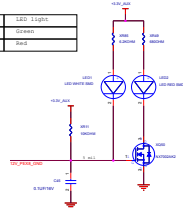
PEX_12V INPUT - 66W

PEX_12V INPUT - 66W

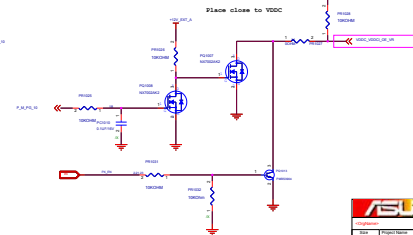
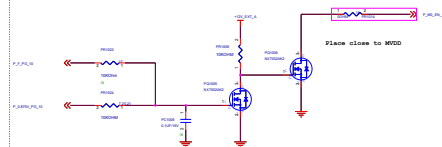


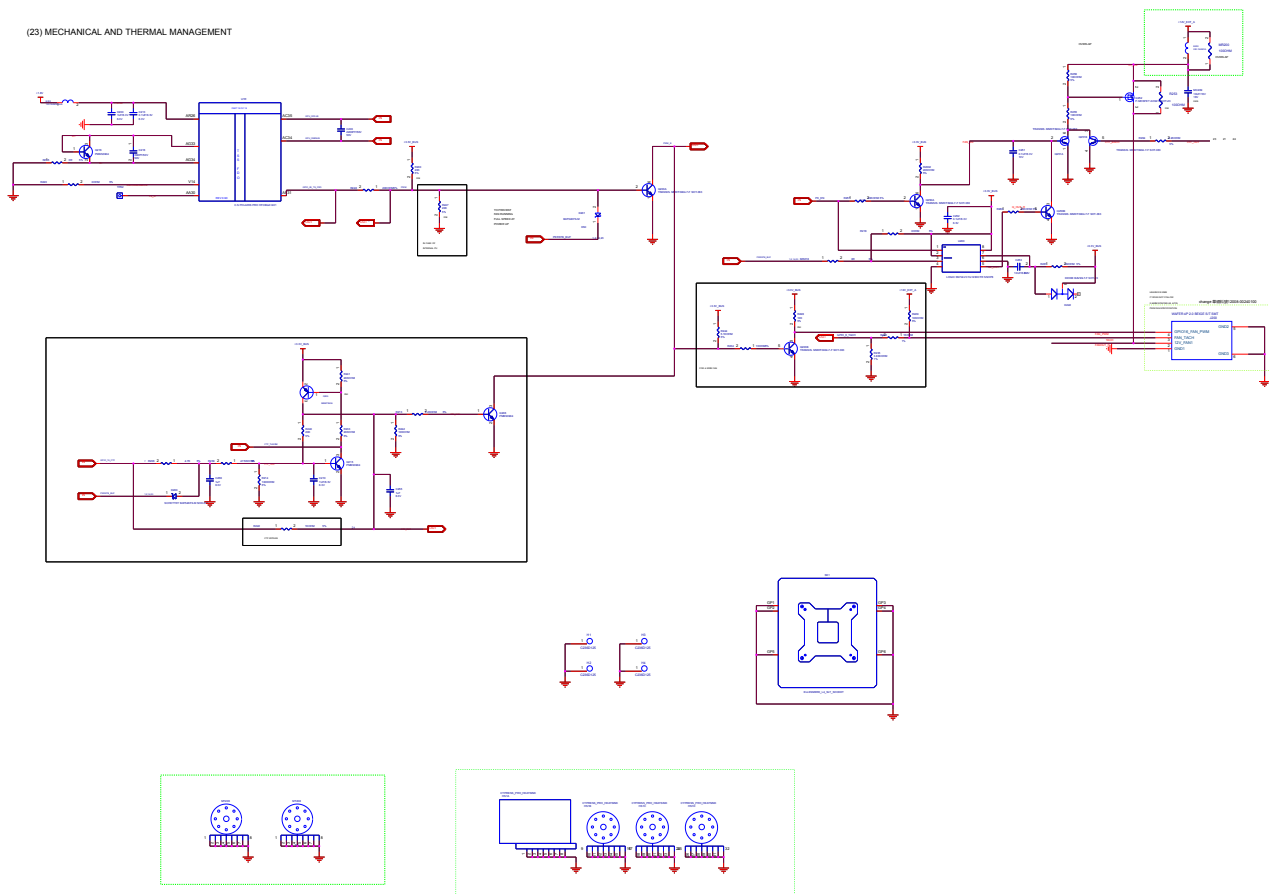
Ext Power detect

LED Light	Green
Cable Connected	Green
Cable Removed	Red

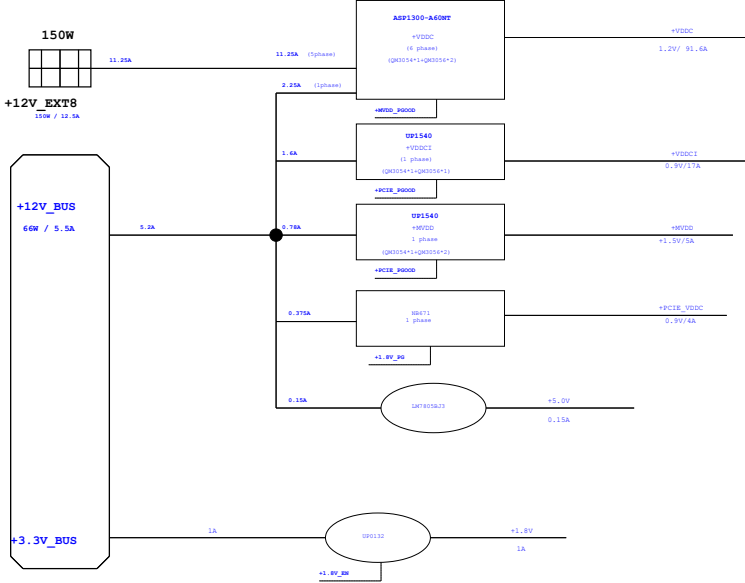
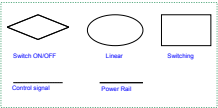


MVDD Power up Sequencing

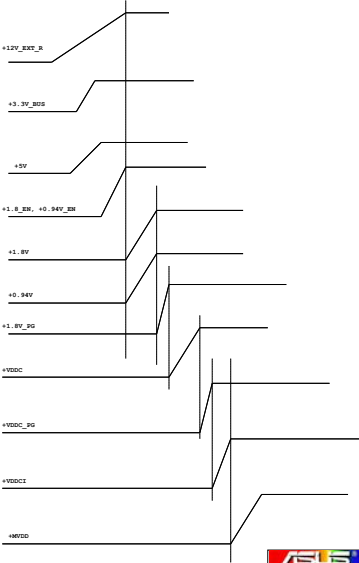
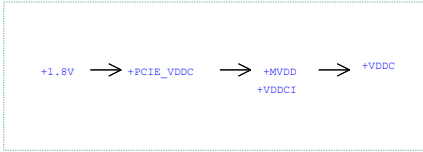




Power Flow



POWER SEQUENCE



(25) DEBUG CIRCUITS

