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MEMORY: GPU FB Partition A and B



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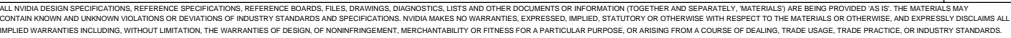


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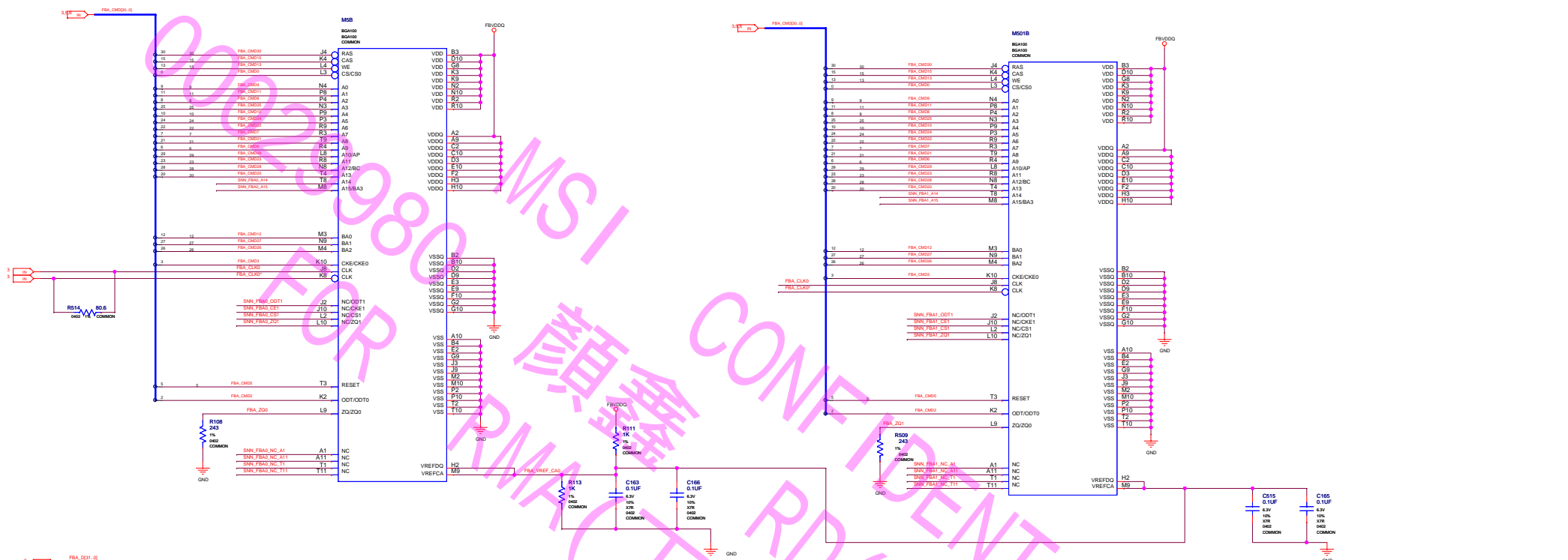
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6-APR-2010

## Memory Lower Partition A



	LOWER	UPPER
	0.31	32.43
CMS00	WZP	WZP
CMS01	CSP	CSP
CMS13	WE	WE
CMS15	CSP	NA
CMS16	NA	NA
CMS17	NA	CSP
CMS19	NA	NA
CMS21	NA	NA
CMS22	A1	A1
CMS23	A2	A2
CMS24	A3	A3
CMS25	A3	A3
CMS26	A4	A4
CMS27	A5	A5
CMS28	A7	A7
CMS29	A8	A8
CMS30	A9	A9
CMS39	A10	A10
CMS42	A11	A11
CMS48	A12	A12
CMS50	A13	A13
CMS51	A13	A13
CMS54	A15	A15
CMS58	BAG	BAG
CMS61	BAG	BAG
CMS62	BA1	BA1
CMS63	BA2	BA2
CMS64	CSE	NA
CMS65	NA	CSE
CMS69	OD1	NA
CMS71	NA	OD1
CMS75	RST	RST

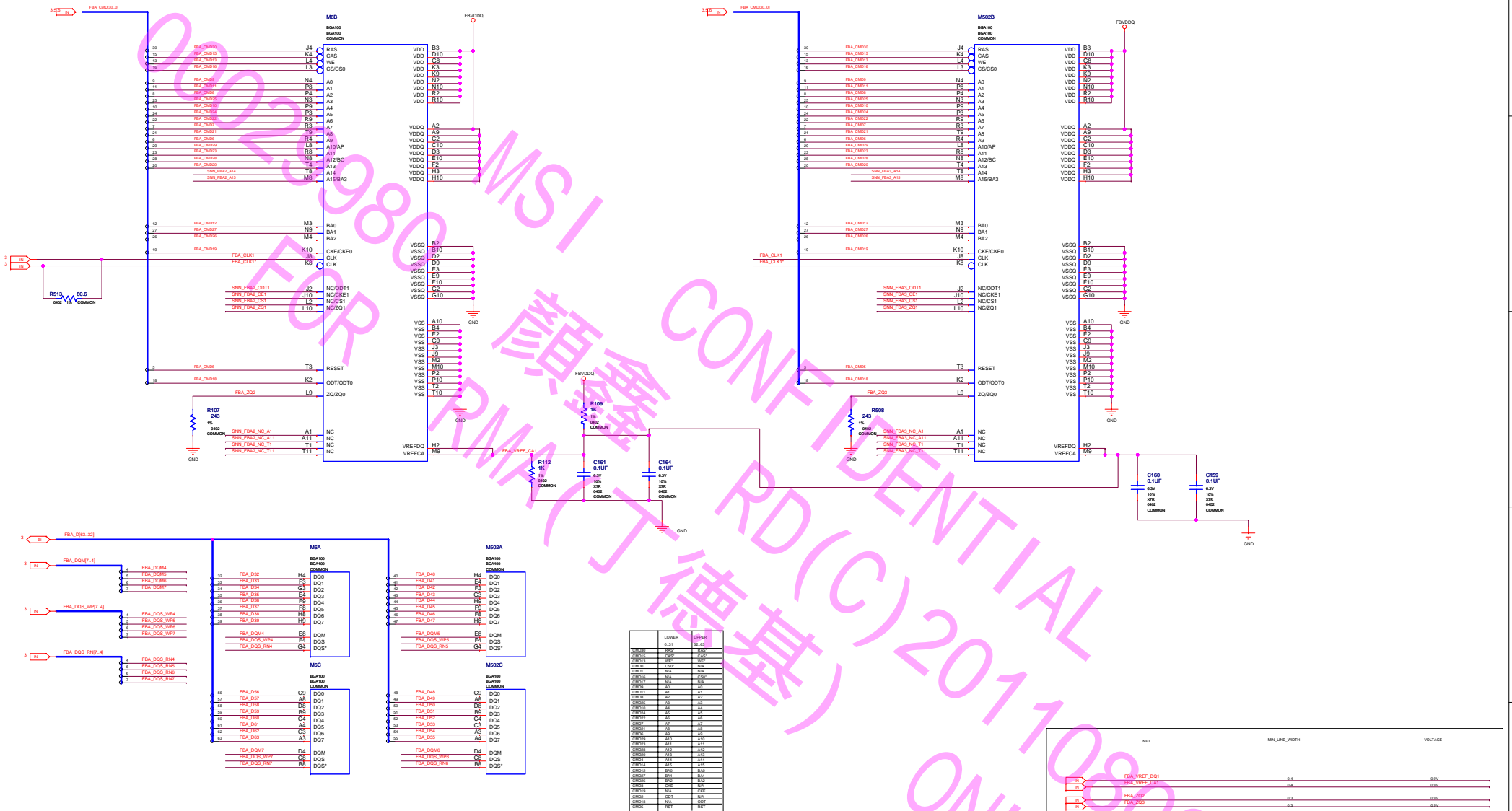
	NET	DIFFER	MM_LINE_WIDTH	CRITICAL	VOLTAGE
(X)	FBA_VREF_D00		0.4		0.9V
(X)	FBA_VREF_CAD		0.4		0.9V
(X)	FBA_200		0.3		0.9V
(X)	FBA_201		0.3		0.9V

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## Memory Upper Partition A

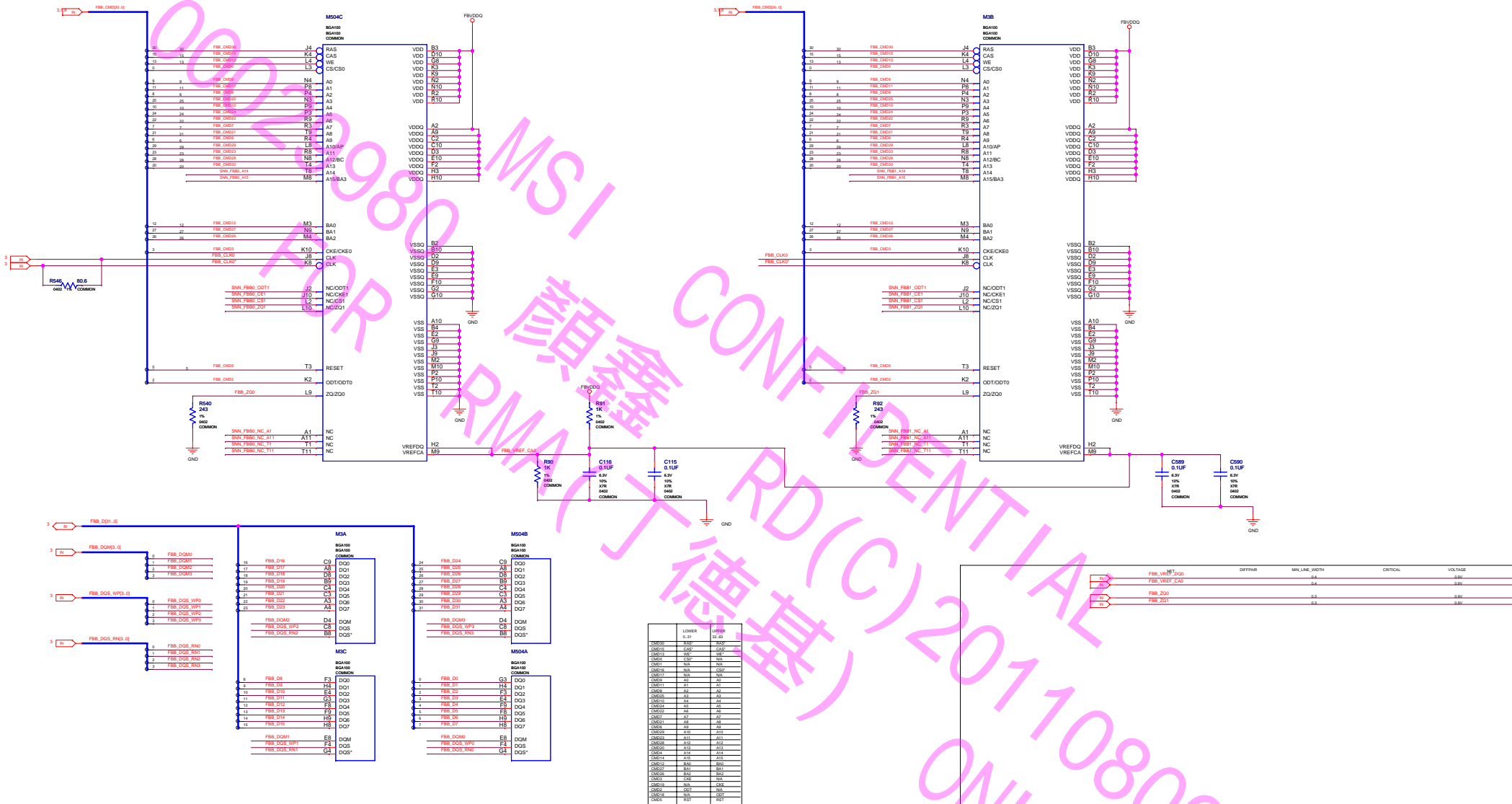


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Memory Lower Partition B



	LOWER	UPPER
MEM01	MEM01	MEM01
MEM02	MEM02	MEM02
MEM03	MEM03	MEM03
MEM04	MEM04	MEM04
MEM05	MEM05	MEM05
MEM06	MEM06	MEM06
MEM07	MEM07	MEM07
MEM08	MEM08	MEM08
MEM09	MEM09	MEM09
MEM10	MEM10	MEM10
MEM11	MEM11	MEM11
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MEM98	MEM98	MEM98
MEM99	MEM99	MEM99
MEM100	MEM100	MEM100

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PAGE DETAIL Memory Lower Partition B

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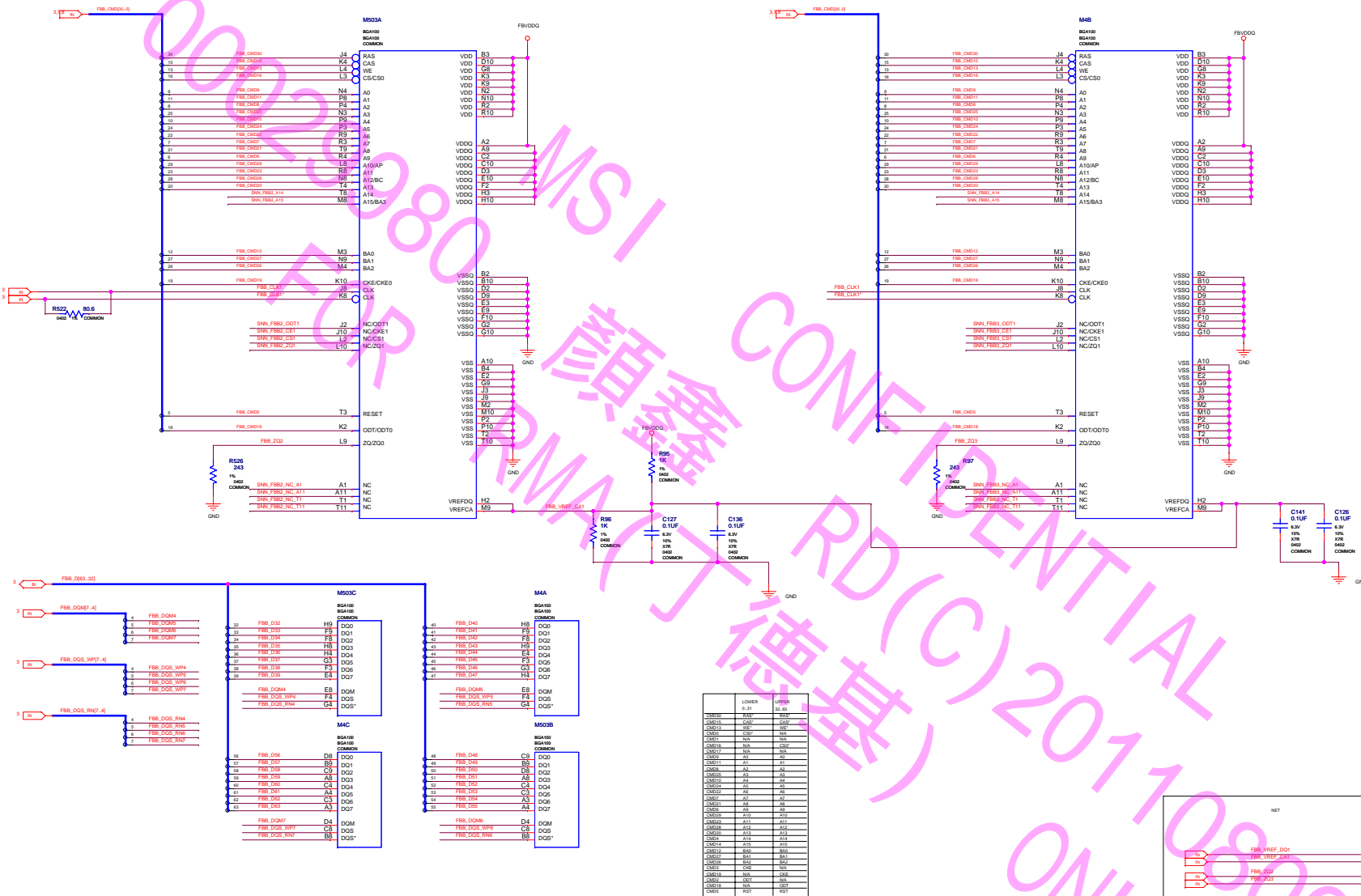
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DATE

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## Memory Upper Partition B



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C00000	000	000
C00010	CAS	CAS
C00020	WES	WES
C00030	WES	WES
C00040	N/A	N/A
C00050	N/A	N/A
C00060	N/A	CAS
C00070	N/A	N/A
C00080	A0	A1
C00090	A1	A1
C00100	A2	A2
C00110	A3	A3
C00120	A4	A4
C00130	A5	A5
C00140	A6	A6
C00150	A7	A7
C00160	A8	A8
C00170	A9	A9
C00180	A10	A10
C00190	A11	A11
C00200	A12	A12
C00210	A13	A13
C00220	A14	A14
C00230	A15	A15
C00240	A16	A16
C00250	B0	B0
C00260	B1	B1
C00270	B2	B2
C00280	C0	N/A
C00290	N/A	C0
C00300	001	001
C00310	002	002
C00320	003	003
C00330	004	004
C00340	005	005
C00350	006	006
C00360	007	007
C00370	008	008
C00380	009	009
C00390	010	010
C00400	011	011
C00410	012	012
C00420	013	013
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C00450	016	016
C00460	017	017
C00470	018	018
C00480	019	019
C00490	020	020
C00500	021	021
C00510	022	022
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C00530	024	024
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C00560	027	027
C00570	028	028
C00580	029	029
C00590	030	030
C00600	031	031
C00610	032	032
C00620	033	033
C00630	034	034
C00640	035	035
C00650	036	036
C00660	037	037
C00670	038	038
C00680	039	039
C00690	040	040
C00700	041	041
C00710	042	042
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C00770	048	048
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C00890	060	060
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C00940	065	065
C00950	066	066
C00960	067	067
C00970	068	068
C00980	069	069
C00990	070	070
C01000	071	071
C01010	072	072
C01020	073	073
C01030	074	074
C01040	075	075
C01050	076	076
C01060	077	077
C01070	078	078
C01080	079	079
C01090	080	080
C01100	081	081
C01110	082	082
C01120	083	083
C01130	084	084
C01140	085	085
C01150	086	086
C01160	087	087
C01170	088	088
C01180	089	089
C01190	090	090
C01200	091	091
C01210	092	092
C01220	093	093
C01230	094	094
C01240	095	095
C01250	096	096
C01260	097	097
C01270	098	098
C01280	099	099

	NET	MIN_LINE_WIDTH	VOLTAGE
	FMS_VREF_D01	0.4	0.0V
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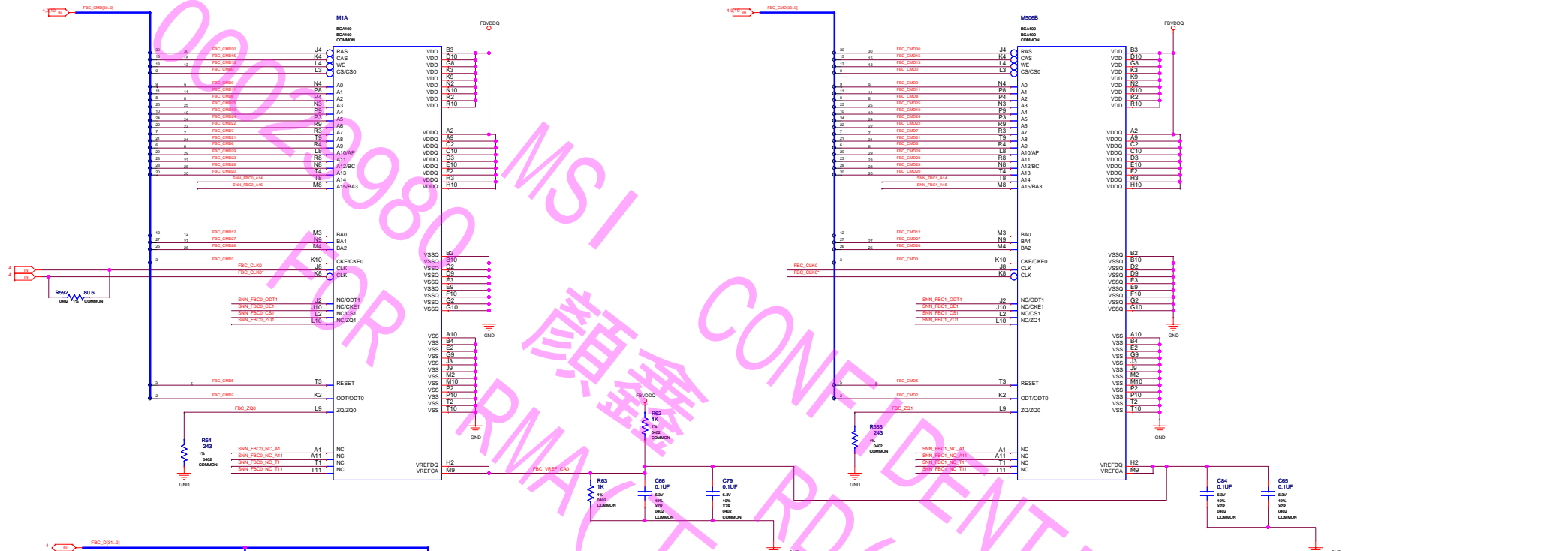
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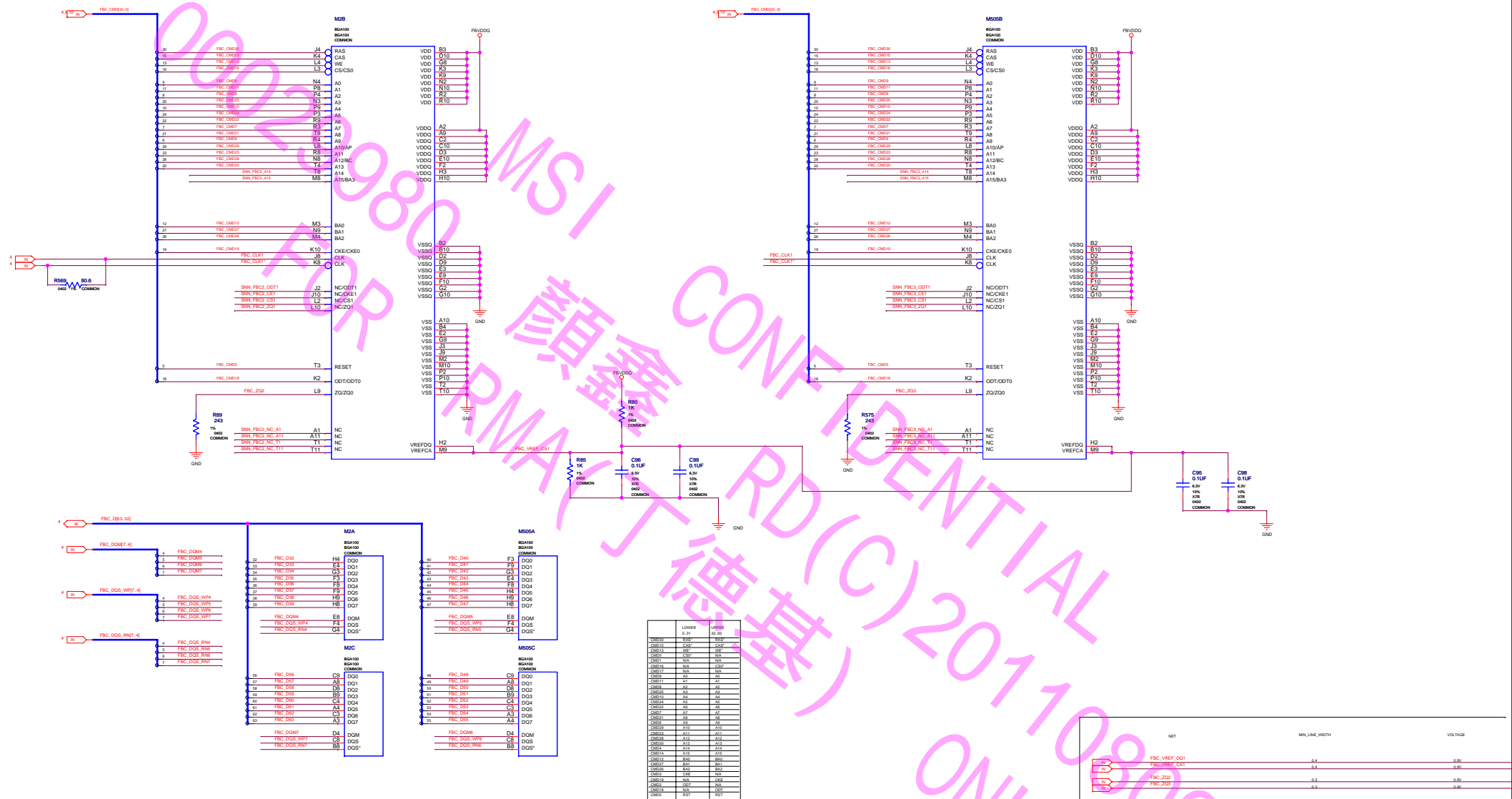
Memory Lower Partition C



	LOWER	UPPER
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CS2	CS2	CS2
CS3	CS3	CS3
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CS95	CS95	CS95
CS96	CS96	CS96
CS97	CS97	CS97
CS98	CS98	CS98
CS99	CS99	CS99
CS100	CS100	CS100




## Memory Upper Partition C



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## Memory FBVDD/Q Decoupling



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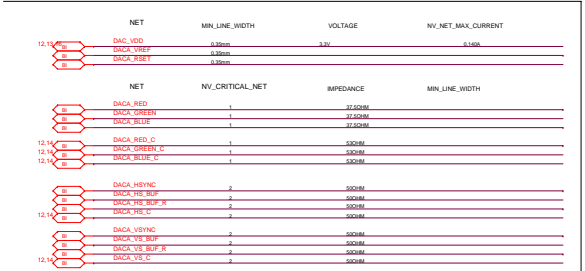
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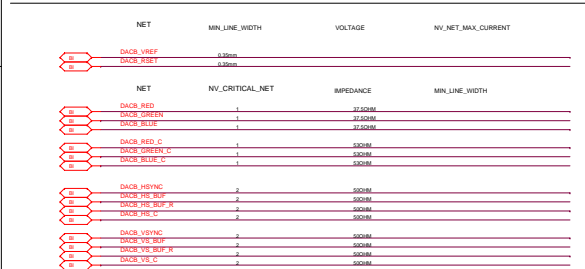
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## DACA RGB-FILTER



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## DACB RGB-FILTER



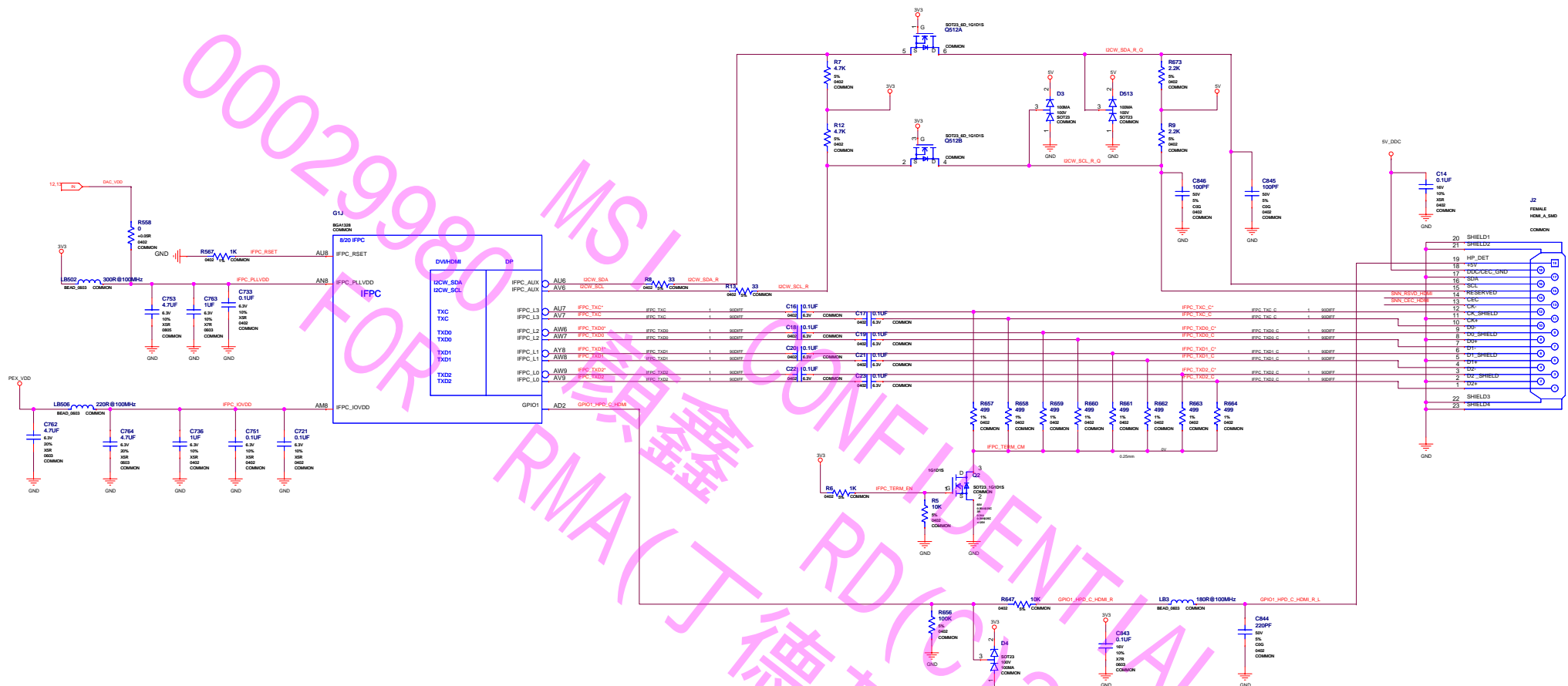
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## IFP C Interface -- HDMI, North



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PAGE DETAIL	IFPC Interface -- HDMI

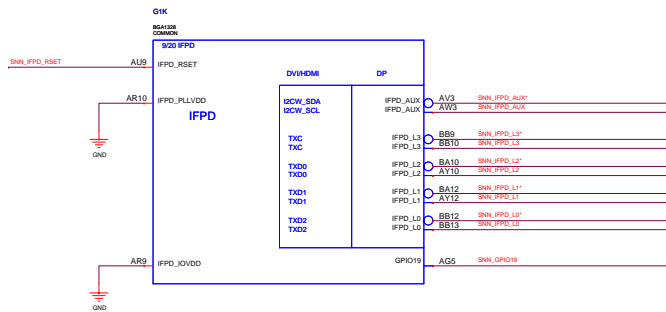
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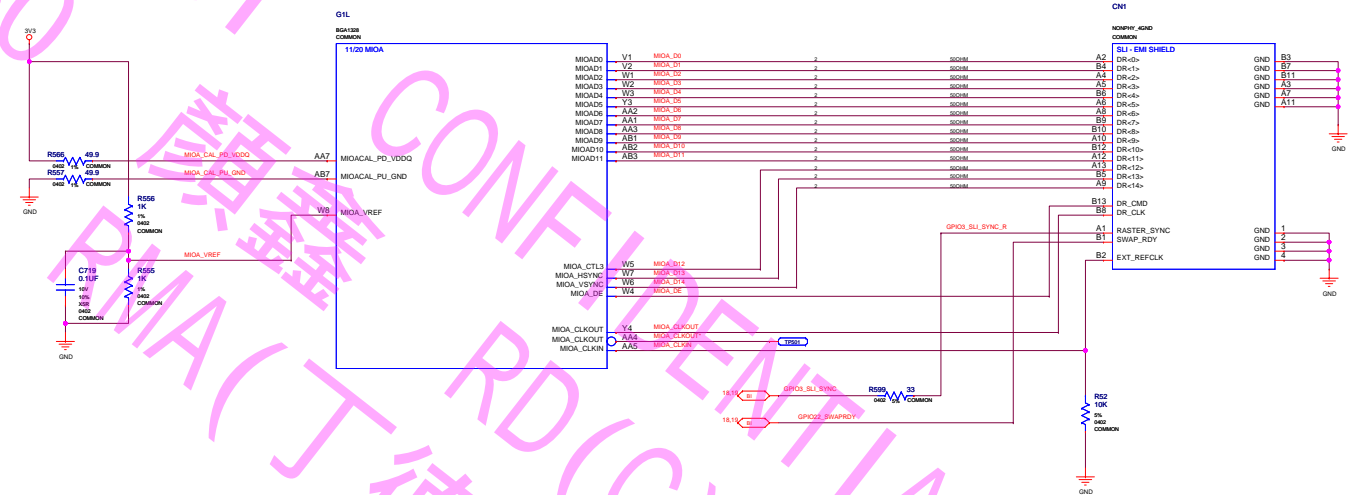
IFP D INTERFACE (UNUSED)



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NET	NV_CRITICAL_NET	IMPEDANCE	DIFFPAIR
B	M004_CU1IN	1	500mV
B	M004_CU2OUT	1	500mV
B	M004_SE	2	500mV
B		2	500mV
B	GPO3_B1_SYNC_R	2	500mV
B	GPO3_B1_SYNC	2	500mV
16, 19	GPO3_SYNCPROV	2	500mV
B		2	500mV
NET	MIN_LINE_WIDTH	VOLTAGE	NV_NET_MAX_CURRENT
B	M004_VREF	12M $\Omega$	1.6V
B	M004_CAL_PD_1000	12M $\Omega$	0.3V
B	M004_CAL_PT1_000	12M $\Omega$	0.3V

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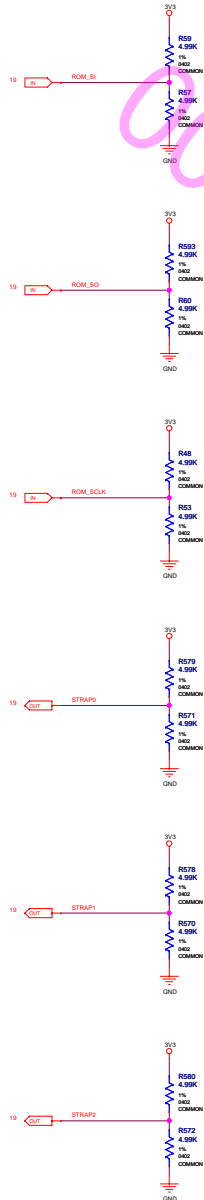
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Strap Configuration

GF106-300: 0x0DC0



ROM_SI	1: 10A DEVICE 2: 0A0E(POR) 3: 0A0E(POR) 4: 0A0E(POR)	PU	PD
ROM_SO	RAM_CONFIG	488A/16 100-bit Sensing 488A/16 100-bit Memory 488A/16 100-bit Winbond 488A/16 100-bit Hynix	20A PD 10A PD 10A PD 5A PD
ROM_SCLK	PER_FIL_EN_TERMINO SLOT_CLK_CONFIG SUB_TERMINO POL_DEVICE_EXT	0: DISABLED (POR) 1: GPU and MCH COMMON REFCLK 1: 100MHz (MCH) PRESENT 0: POCDEVICE	35A
STRAP0	USER_BIT0	0A0 DEFAULT	5A
STRAP1	3G0_PNOCTG_LUT_A0R0	0A0 Default High Imp	5A
STRAP2	POL_DEVICE0	0A0 POC DEVICE0/0DC0	5A

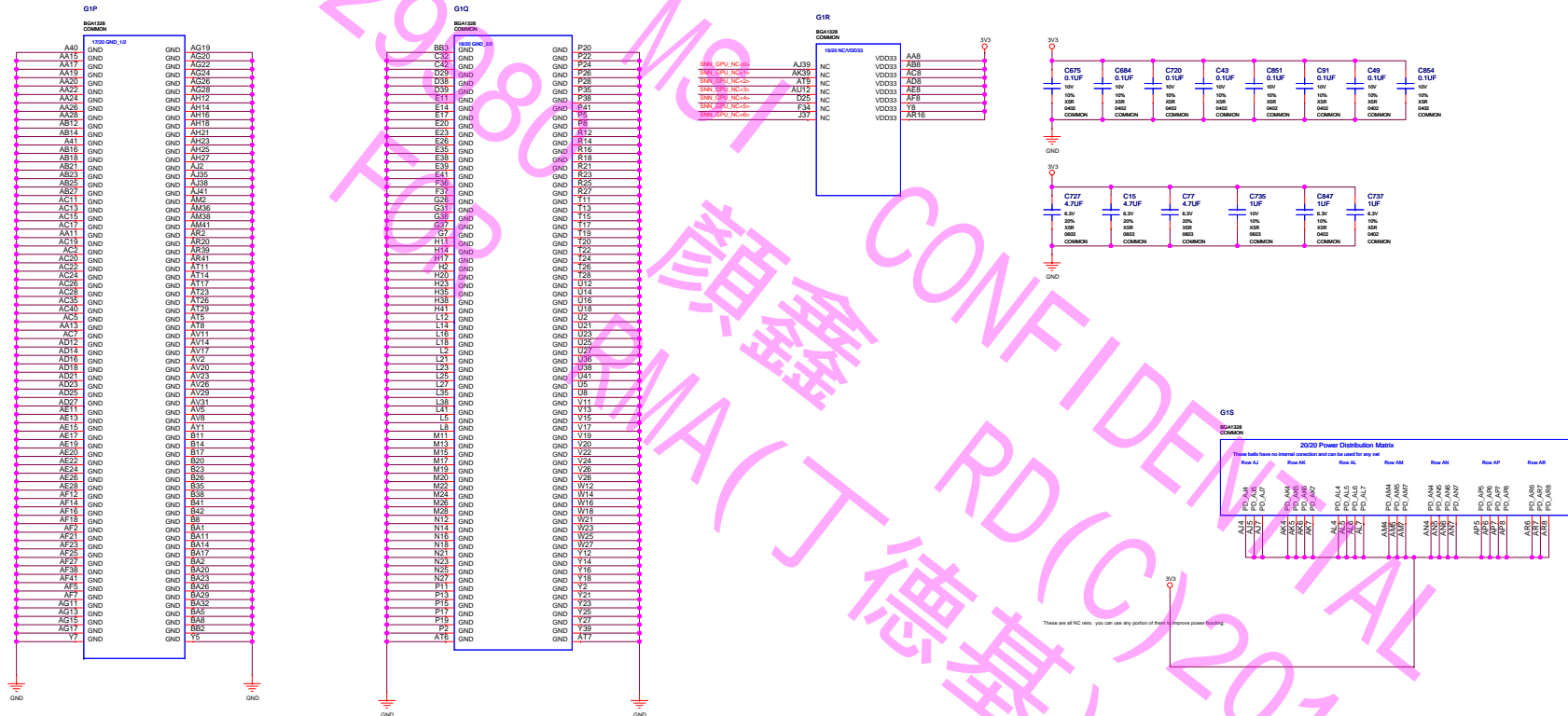
Multi-level Strap Resistor Values
Strap PU/PD (0A0/5A0)
0 PU 5
1 PU 10
2 PU 15
3 PU 20
4 PU 25
5 PU 30
6 PU 35
7 PU 40
8 PU 45
9 PU 50
10 PU 55
11 PU 60
12 PU 65
13 PU 70
14 PU 75
15 PU 80

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PAGE DETAIL	Strap Configuration


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## PWR and GND Signals



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PAGE DETAIL	PWR and GND Signals

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# NVDD Decoupling

Pin	Pin Name	Pin Name	Pin Name
AA12	VDD	AA12	VDD
AA13	VDD	AA13	VDD
AA14	VDD	AA14	VDD
AA15	VDD	AA15	VDD
AA16	VDD	AA16	VDD
AA17	VDD	AA17	VDD
AA18	VDD	AA18	VDD
AA19	VDD	AA19	VDD
AA20	VDD	AA20	VDD
AA21	VDD	AA21	VDD
AA22	VDD	AA22	VDD
AA23	VDD	AA23	VDD
AA24	VDD	AA24	VDD
AA25	VDD	AA25	VDD
AA26	VDD	AA26	VDD
AA27	VDD	AA27	VDD
AA28	VDD	AA28	VDD
AA29	VDD	AA29	VDD
AA30	VDD	AA30	VDD
AA31	VDD	AA31	VDD
AA32	VDD	AA32	VDD
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AA94	VDD	AA94	VDD
AA95	VDD	AA95	VDD
AA96	VDD	AA96	VDD
AA97	VDD	AA97	VDD
AA98	VDD	AA98	VDD
AA99	VDD	AA99	VDD
AA100	VDD	AA100	VDD

0805 0603 0402

Sites placed under GPU: 8x 10uF 2x 4.7uF 18x 1uF 1x 22uF (2x unused) 18x 0.1uF (17x unused)



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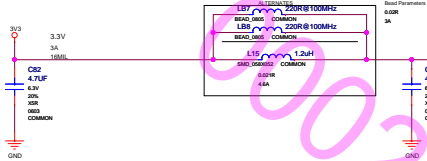
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# PS I: 3V3, 12V, and 12V\_EXT Power Supply Filter, 5V, DDC VOLTAGE

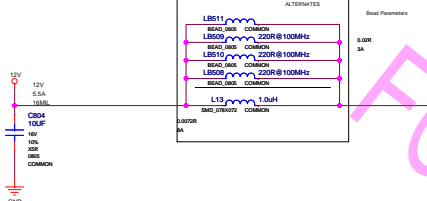
## 3V3 Power Supply Filter

3V3\_F = 3.3V @ 2.4A

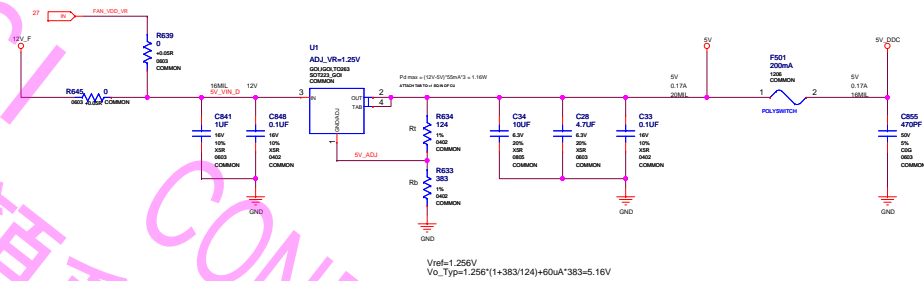


## 12V Power Supply Filter

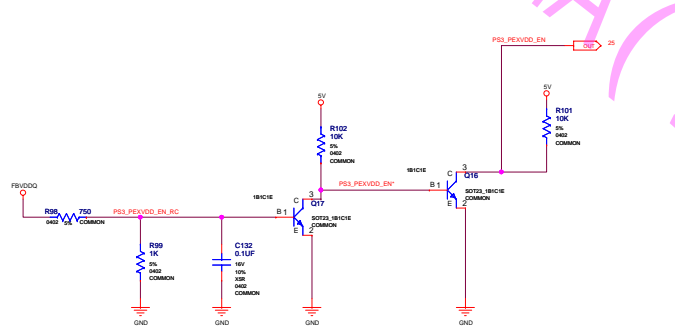
12V\_F = 12V @ 5.5A



## 5V POWER SUPPLY, DDC POWER



## PEXVDD ENABLE

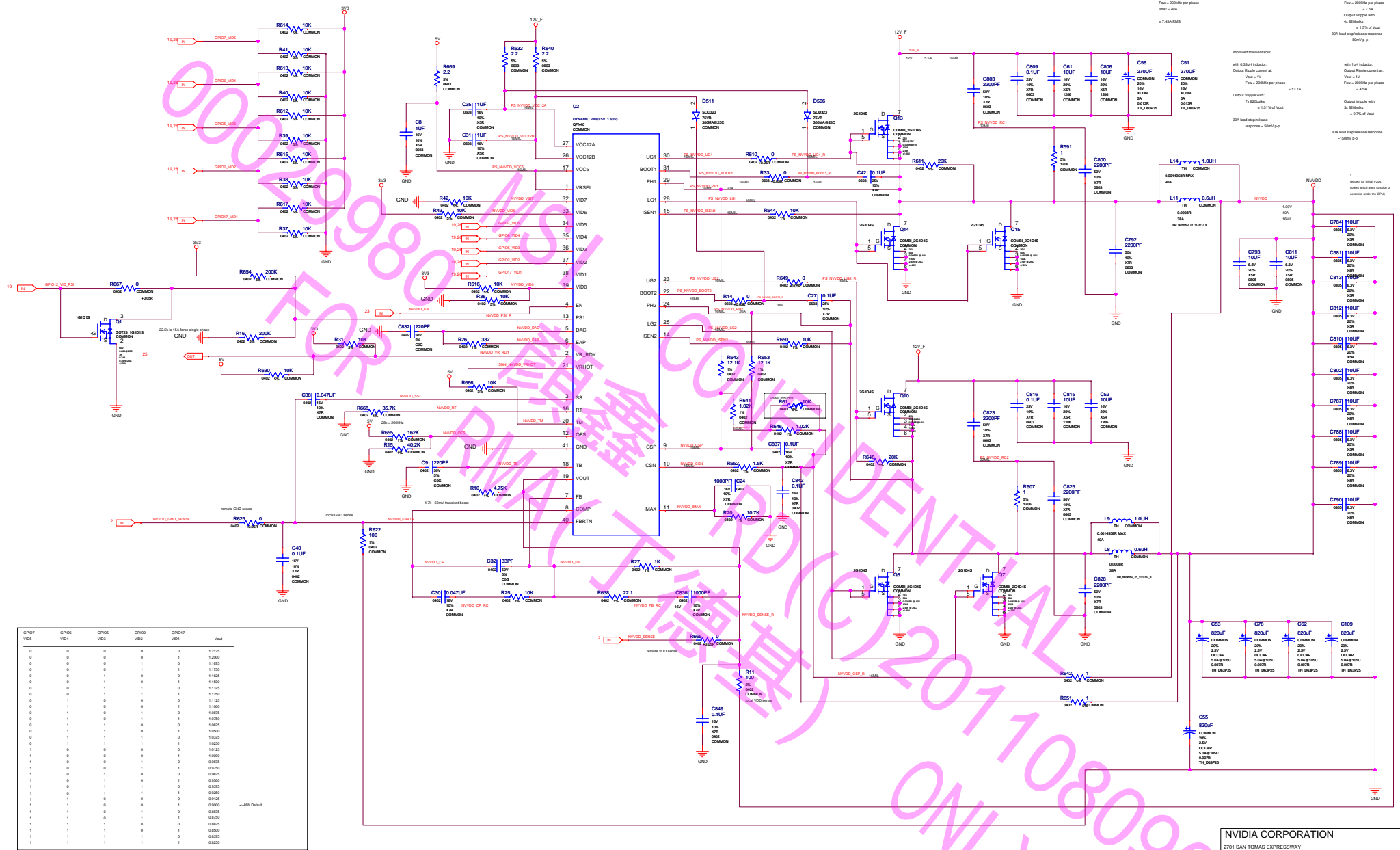


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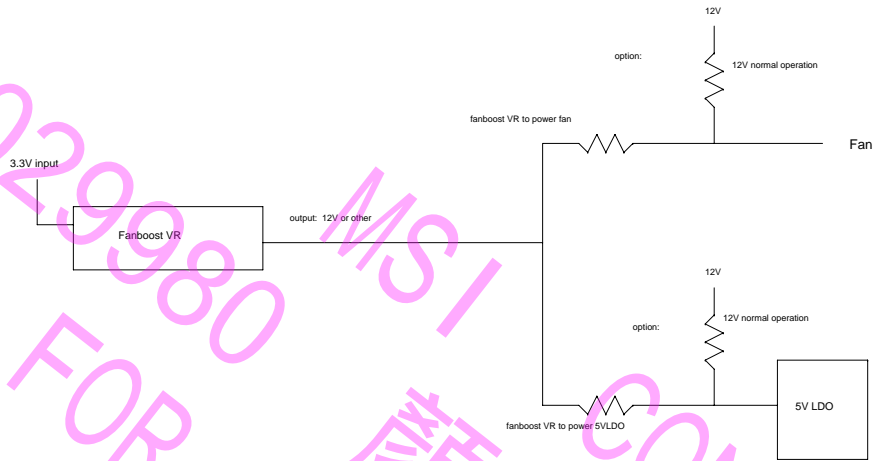








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PAGE DETAIL	Block diagram of 12V options for fan and 5V LDO

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