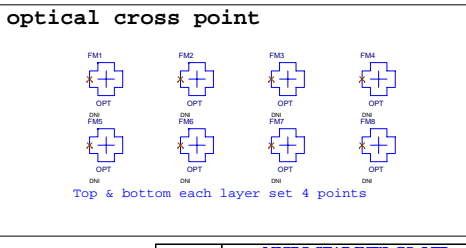
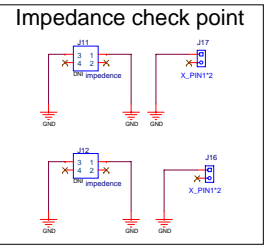
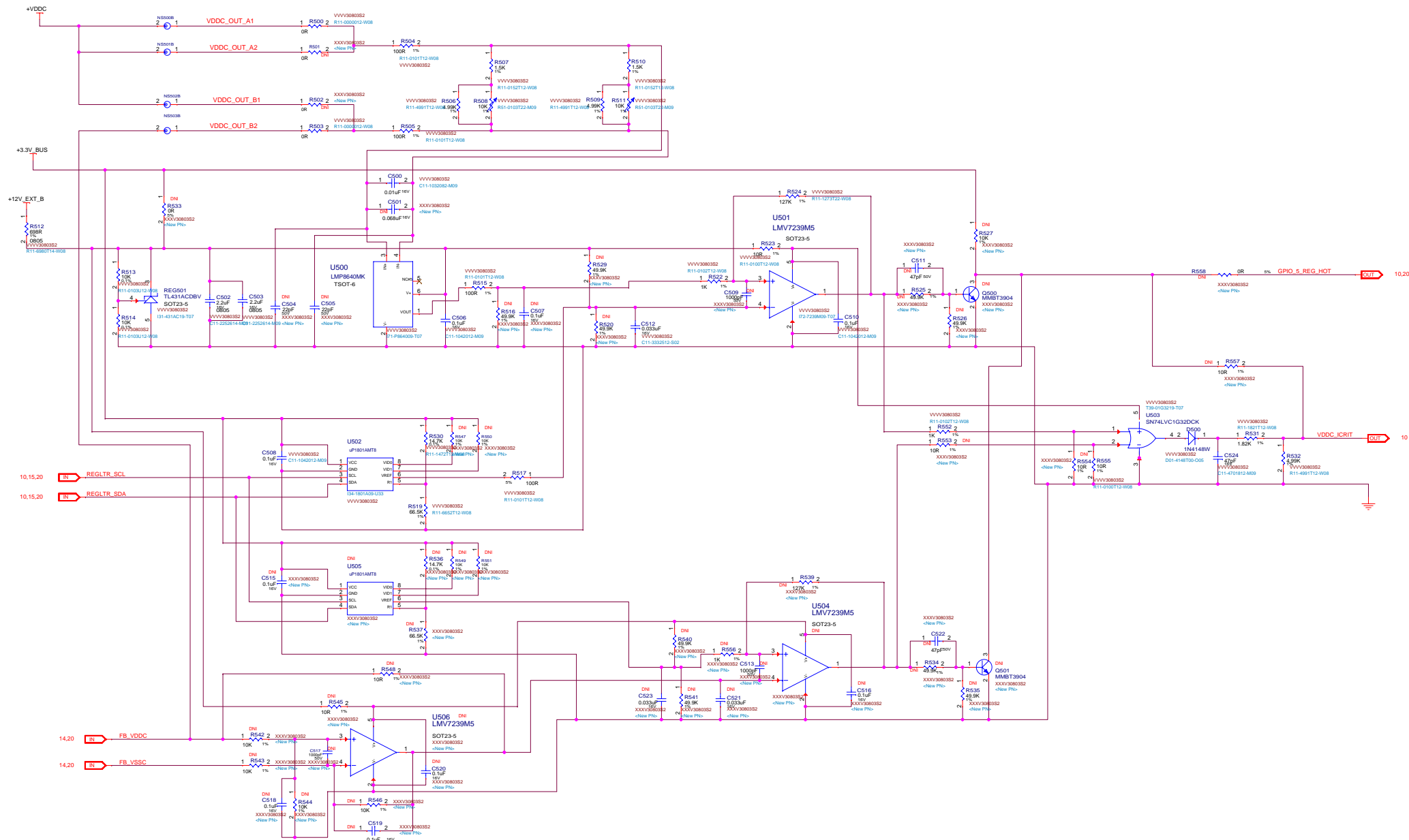
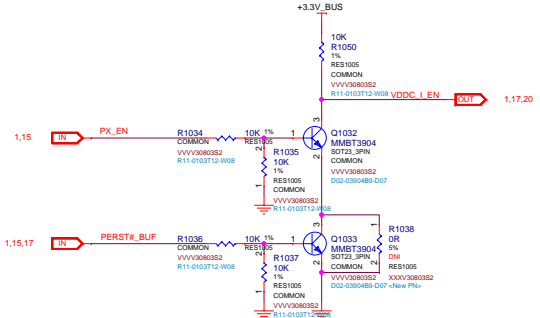
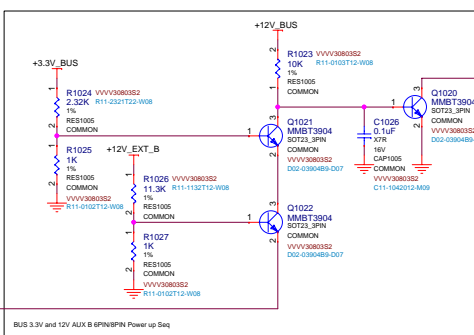
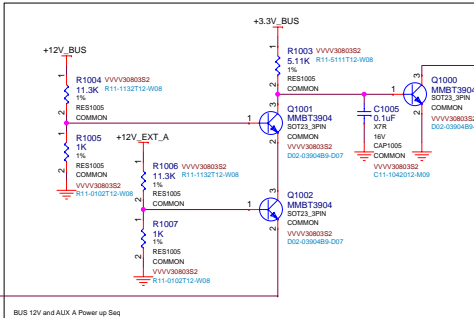
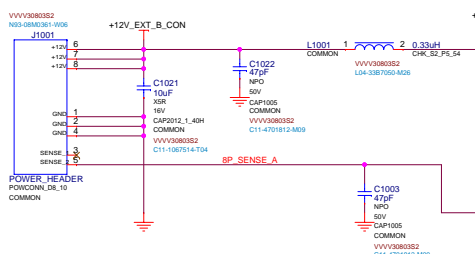
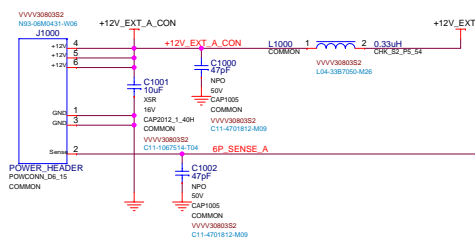
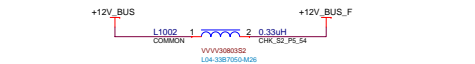


USE Q203 2020023005, R506, R549, D11719
AND C3003 FOR CHEAPER FAN CONTROLLER.
REQUIRES BIG CHANGE IN FAN TABLE.

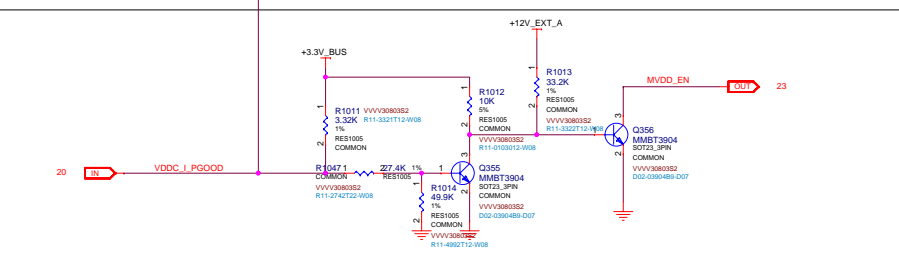
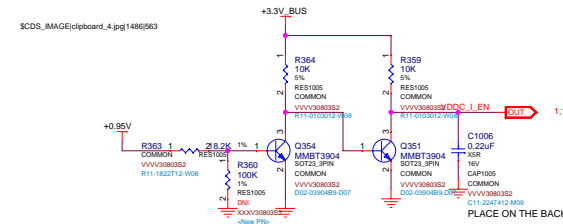
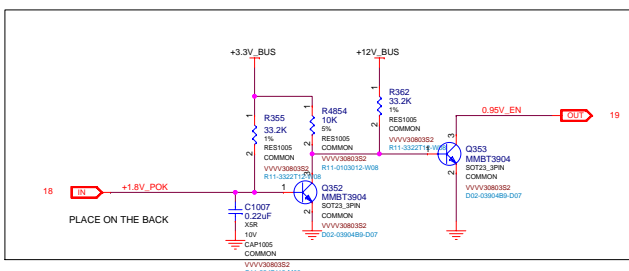
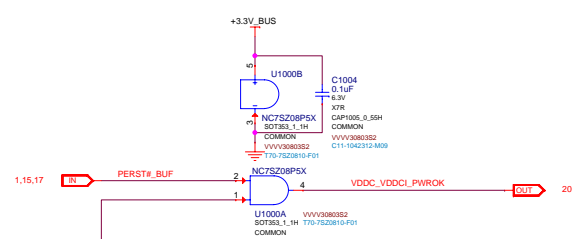
Change net form +0.95_EN to INPUT_RAILS_UP



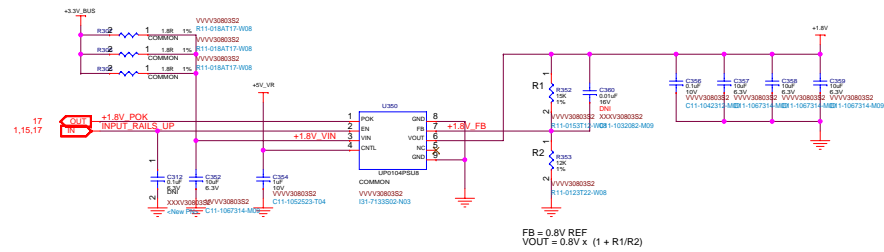




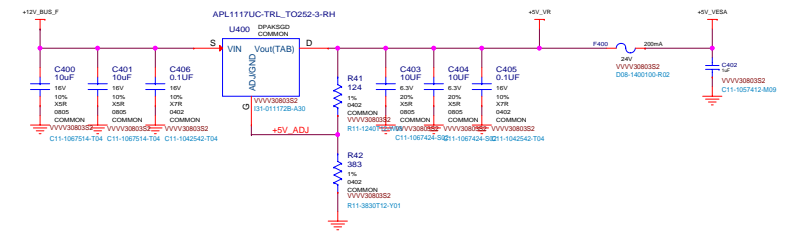
POWER UP SEQUENCING

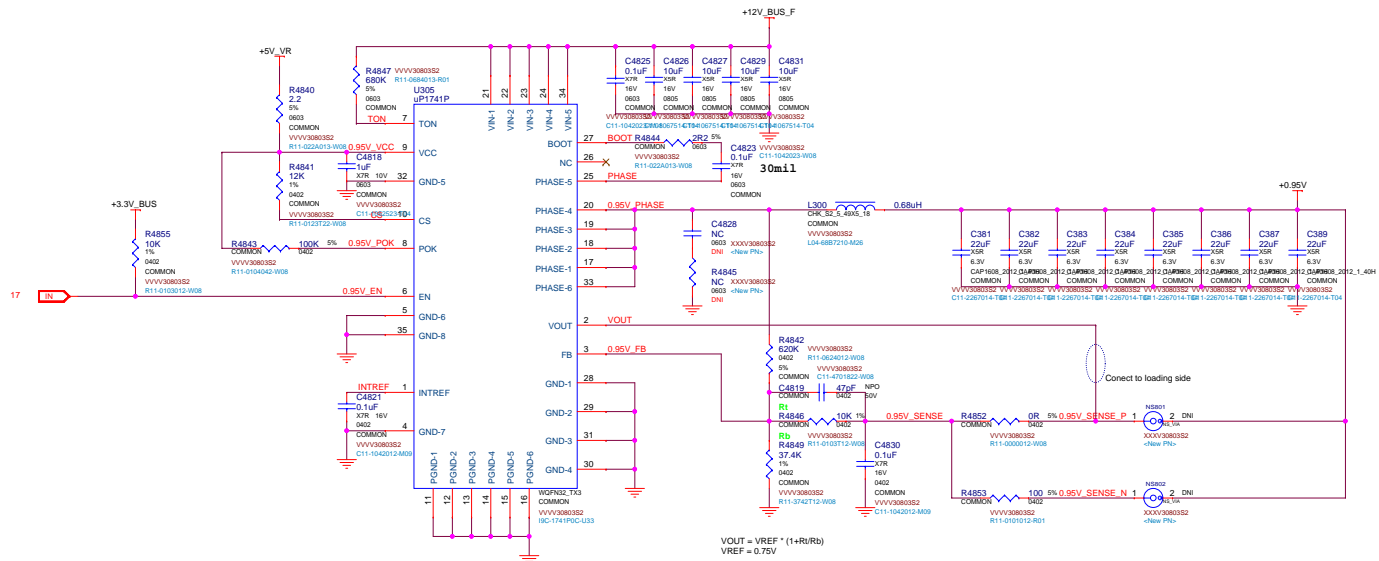


Regulators for +1.8V



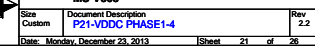
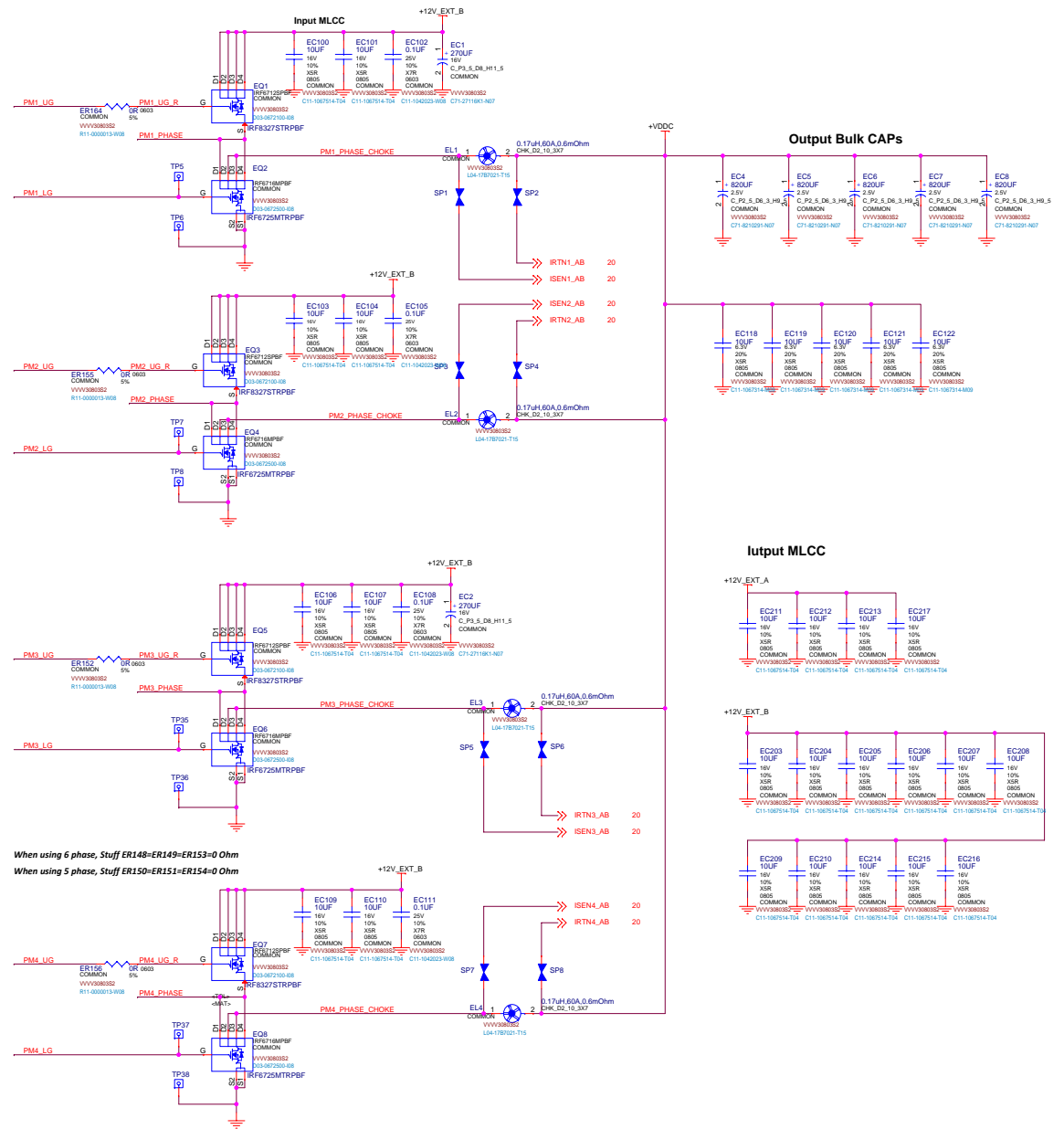
Regulators for +5V, and +5V_VESA



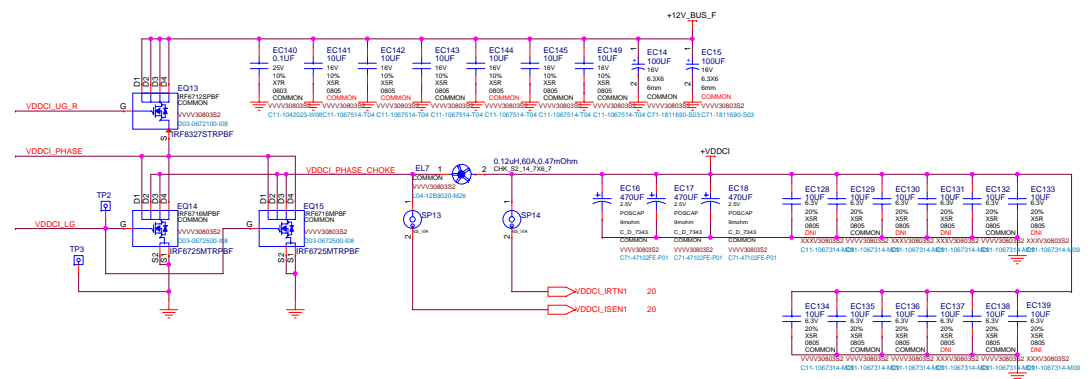
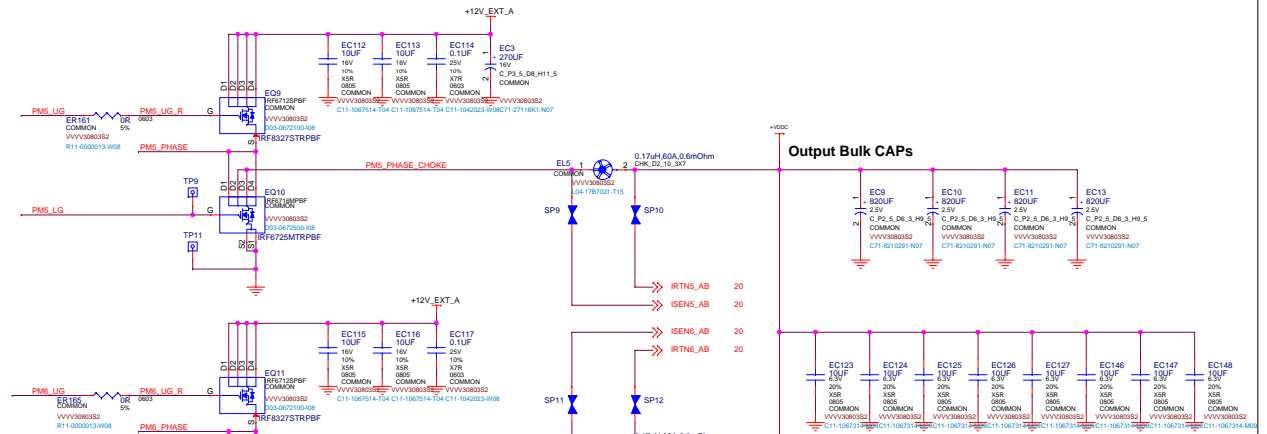
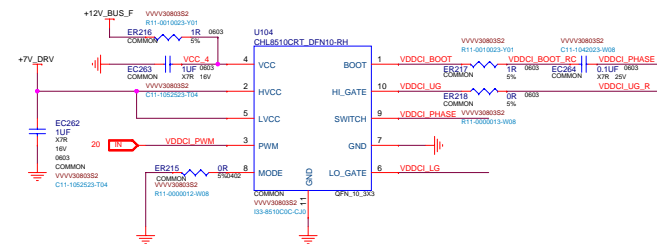
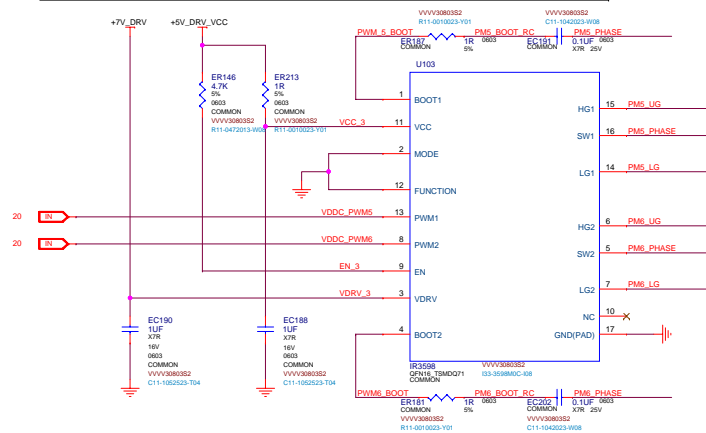


5V POWER SUPPLY FOR DRIVER VCC

The diagram shows a 5V power supply circuit for the driver VCC. It starts with a +12V_EXT_B input connected to a 100uF capacitor (C180). This is followed by a voltage divider consisting of resistors R1 (124 ohms) and R2 (383 ohms). The output of the divider is connected to a 5V regulator (REG1) which is also connected to a 100uF capacitor (C181). The output of the regulator is connected to a 100uF capacitor (C182) and a 10uF capacitor (C183). The output of the 10uF capacitor is connected to a 100uF capacitor (C184) and a 10uF capacitor (C185). The output of the 10uF capacitor is connected to a 100uF capacitor (C186). The output of the 100uF capacitor is connected to the +5V_DRV_VCC output.



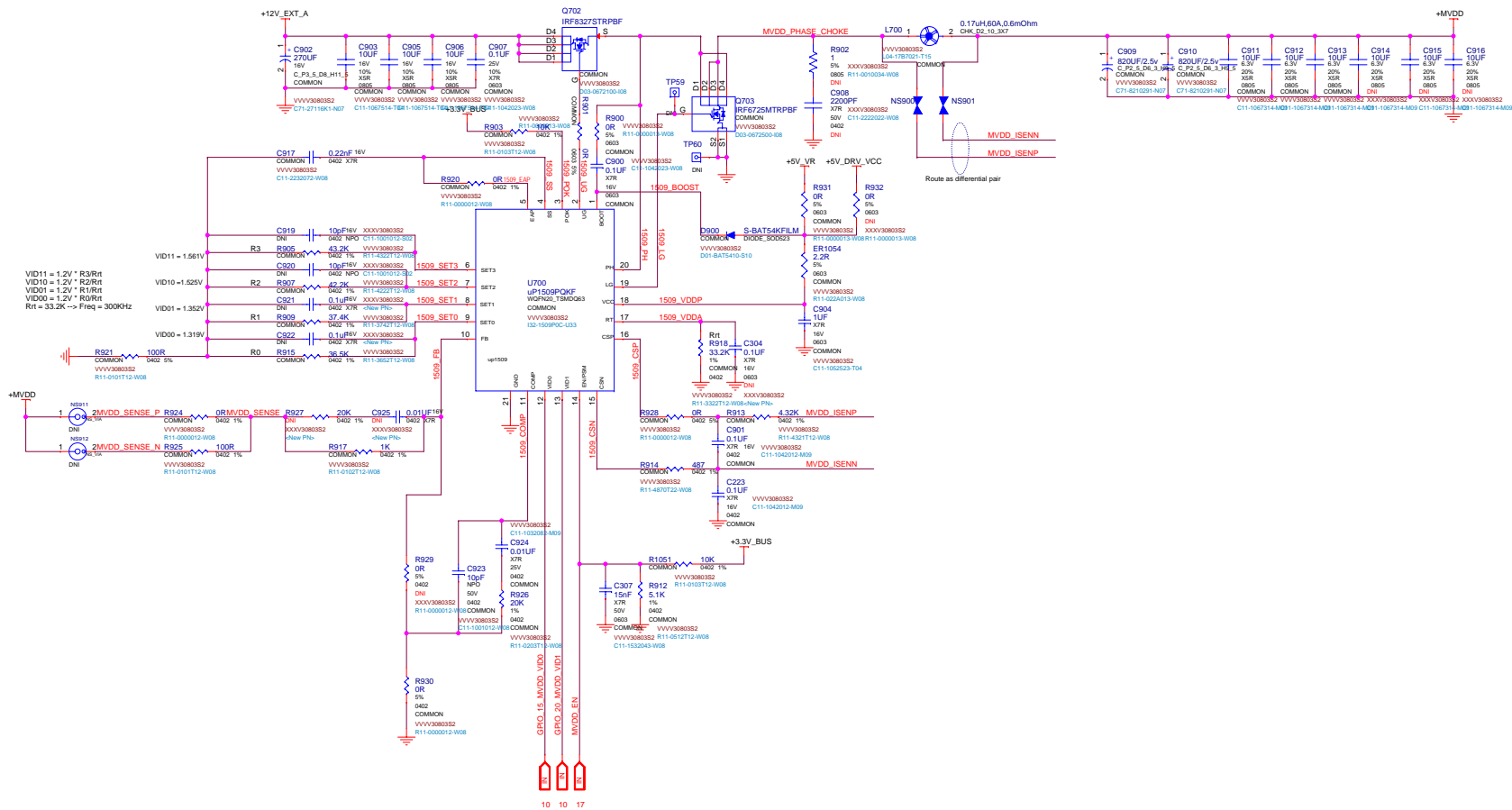
7V POWER SUPPLY FOR DRIVER



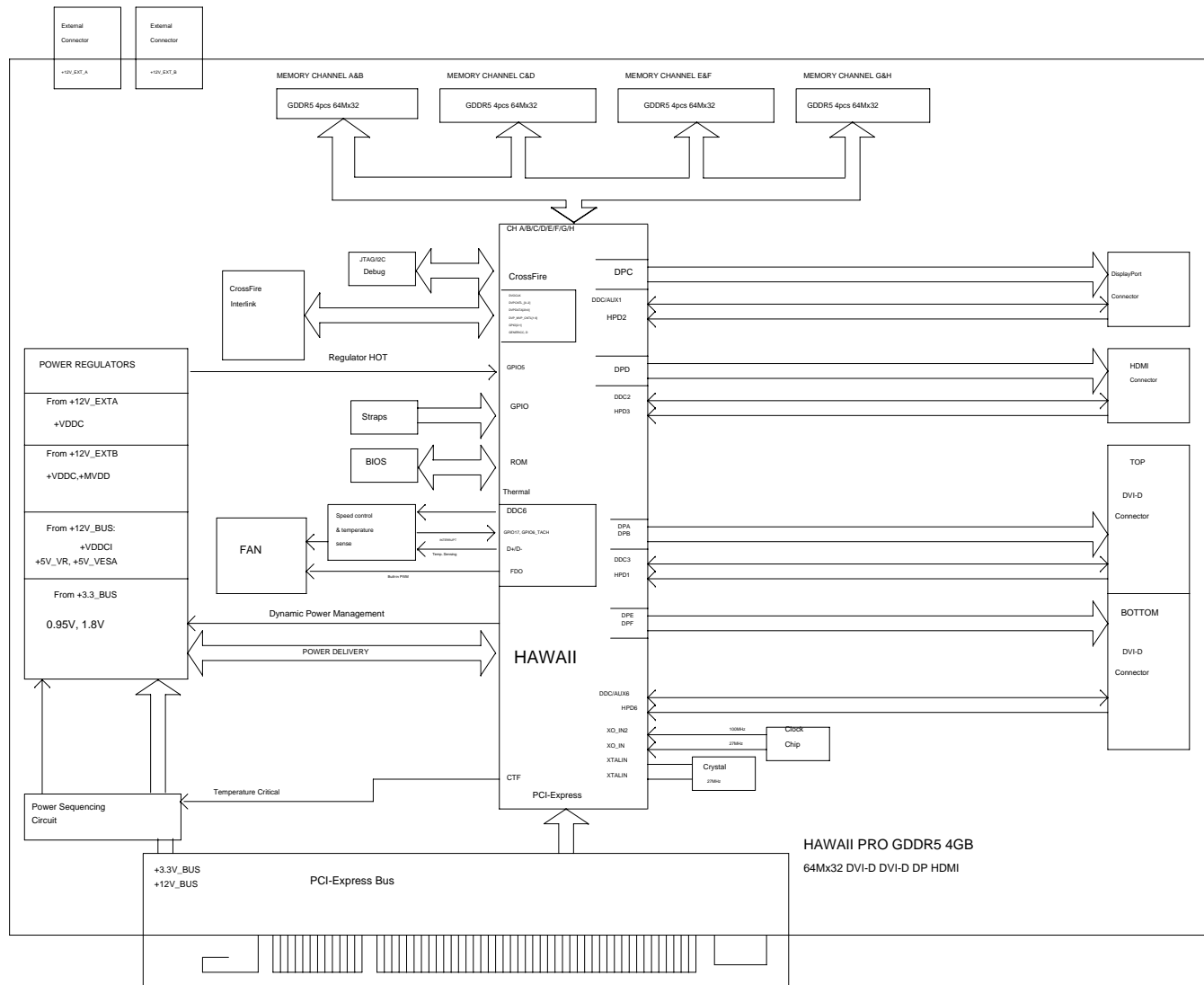
MICRO-STAR INT'L CO.,LTD

MS-V308

Size Custom	Document Description 22_VDDC Phase 5-6 & VDDCI	Rev 2.2
Date: Monday, December 23, 2013		Sheet 22 of 26







1 00A Feb01 Initial release for engineering review.
2013

2 00B Apr12 1) Added provision to combine Ext_A and Ext_B on MVDD input.
2013 2) Added filter on 12V_BUS.
3) "Fixed" CTF_DIS connection.
4) MVDDPWRGOOD pull-up changed to 1.8V.
5) Removed diode from 1.8V_POK.
6) Improved power sequence for MVDD_EN.

3 00C Jul24 1) WAKEB enable changed to VDDC_I_EN.
2013 2) Changed polarity of PCC comparator and added the circuitry to provide fast rising and delayed falling edges
3) Added filter on MVDD enable input
4) Added provision to connect 1.8V_DAC directly to PCC comparator.
5) Added extra MLCCS and polymers for whining noise reduction.

4 00D Sep30 1) Added option for 300W board power.
2013 2) Added option of PCC circuitry from C671 board.
3) Added VR_HOT option from C673.
4) Added extra filtering options for VDDC current measuring.
5) Added more caps on DP_VDDR.

5	V308-20	Page01 : 1. MVDD_PWRGD change to INPUT_RAILS_UP 2. Remove JTAG Page10 : Remove CF Test Point Page11 : 1. Change DVI footprint and library 2. Add ESD Page12 : 1. Change DP/HDMI footprint and library 2. Add ESD Page13 : 1. Change DVI footprint and library 2. Add ESD Page15 : 0.95V_EN change to INPUT_RAILS_UP Page16 : Copy from C671 PCC circuit Page17 : Copy from C671 sequence circuit Page18 : 1. Change 1.8V LDO 2. 5V change to 1117 Page19 : 0.95V Change to I9C-1741P0C-U33 Page20 : VDDC/VDDCI controller change to IR3567B Page23 : MVDD PWM change to I32-1509P0C-U33 Page24 : Remove Debug circuit	V308-21	Page17 : 1. L1000/L1001/L1002/J1000/J1001 change footprint 2.Remove R1020/R1021/MJ1001 Page19 : Add 0.95_En pull-high resistor Page21 : Add Ext_12V input MLCC
			V308-22	No change