

PG174-A01

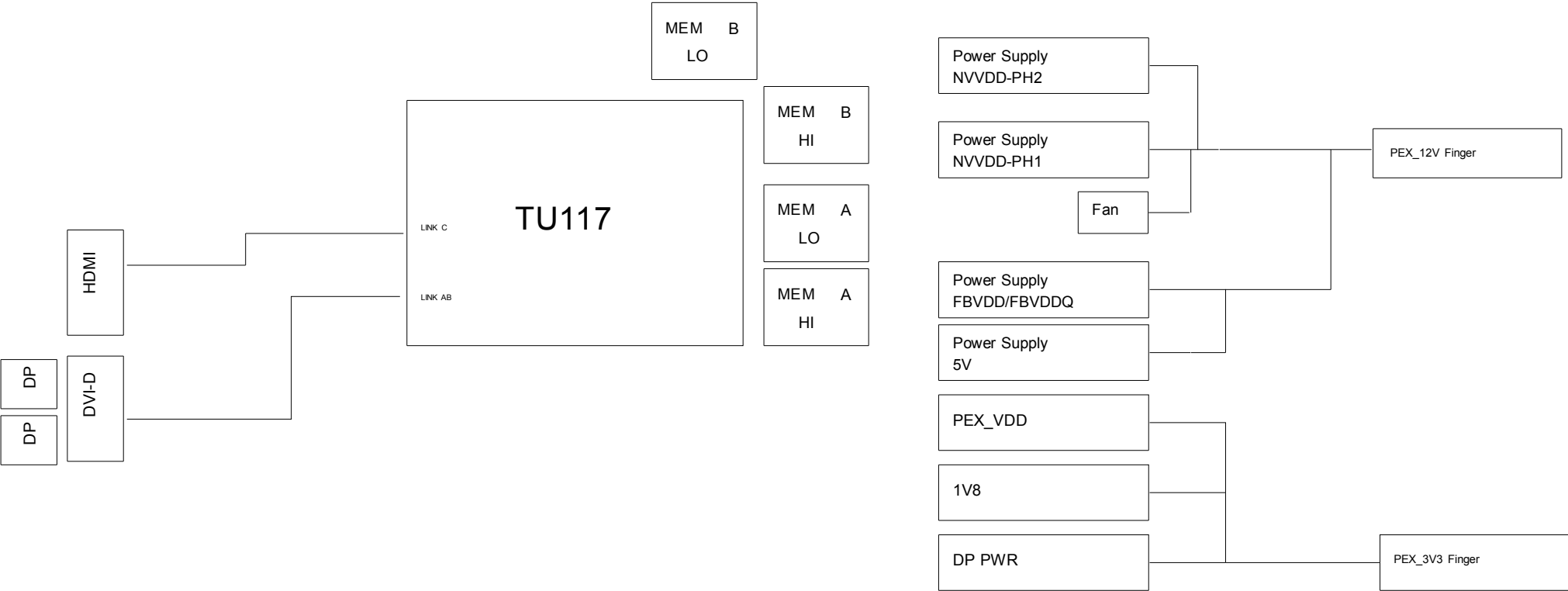
TU117, 128b GDDR5 x32, 75W  
DVI-D DL/DUAL DP + HDMI

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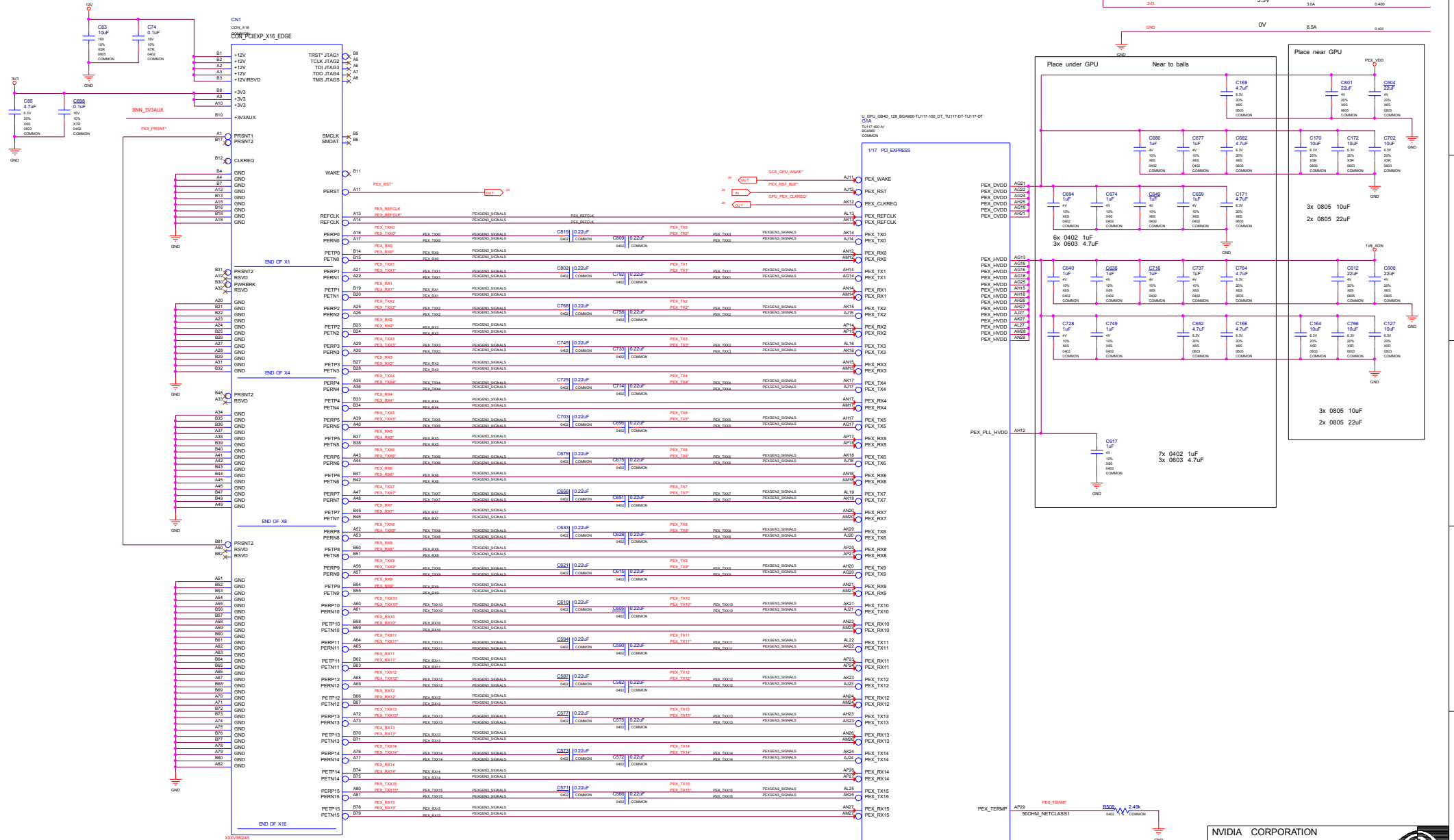
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Block Diagram



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PCI Express



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PAGE DETAIL		PG Express		NV_PN		600-1G174-BASE-A01	
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UNRECORDED				A		10-DEC-2018	

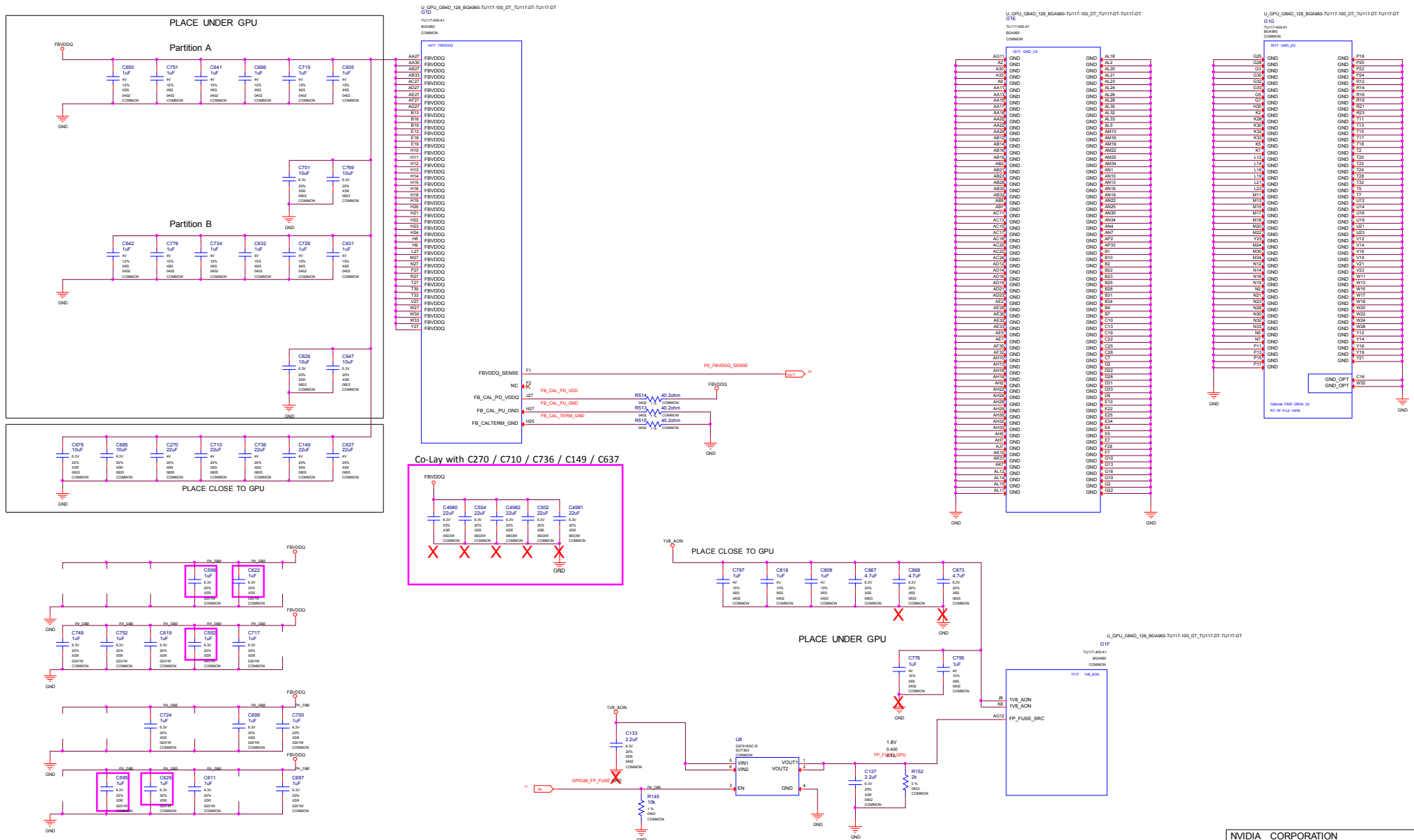
[illegible]

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STANDARDS.



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## GPU FBVDDQ & 1V8 Decoupling



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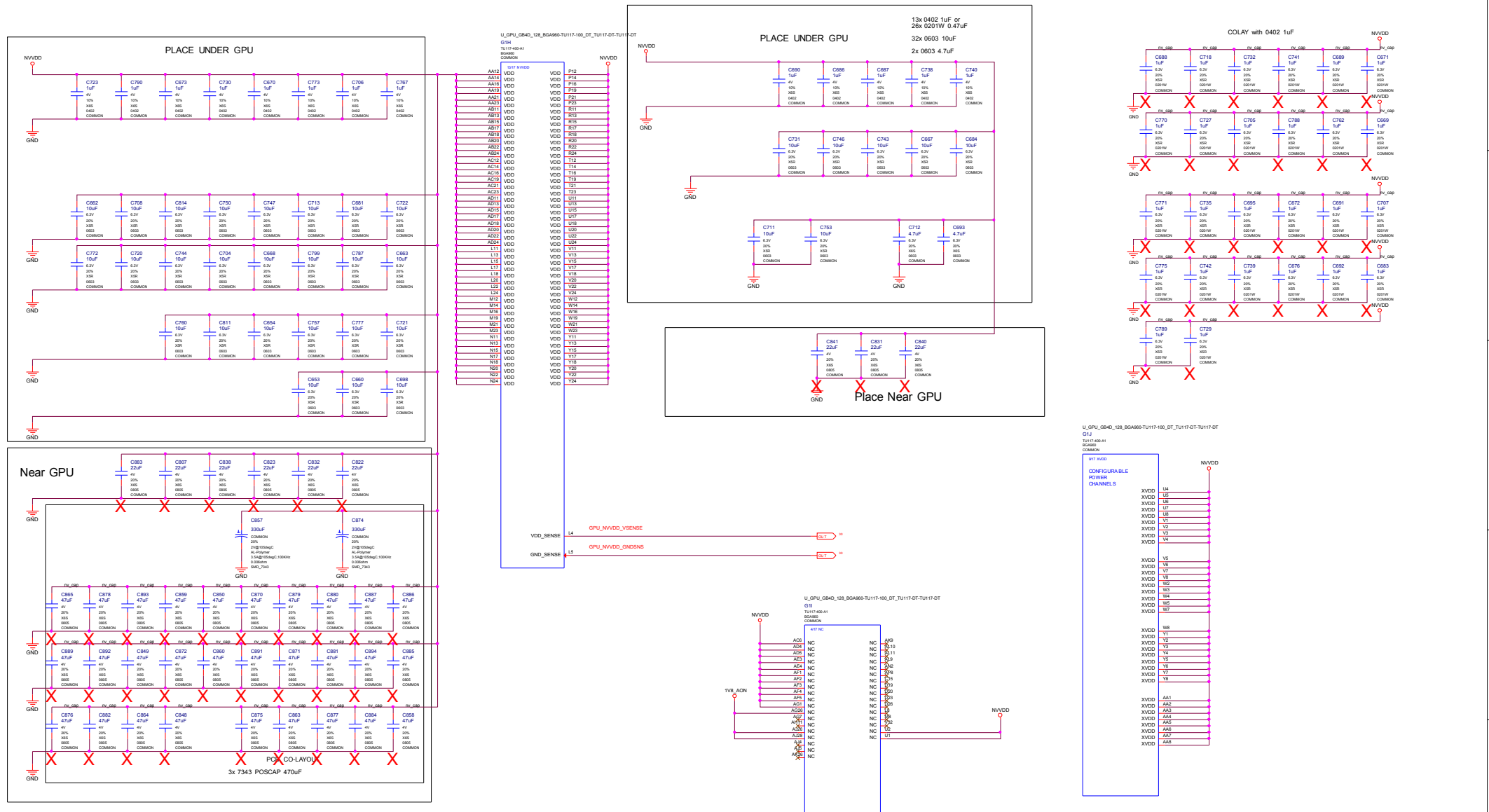
NV_PN	600-1G174-BASE-A01
-------	--------------------

PCB REV	PG174-A01
---------	-----------

BOM REV	A	DATE	10-DEC-2018
---------	---	------	-------------

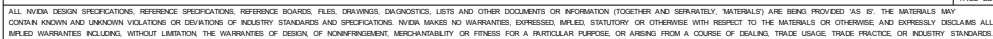
[illegible]

## GPU NVVDD Decoupling

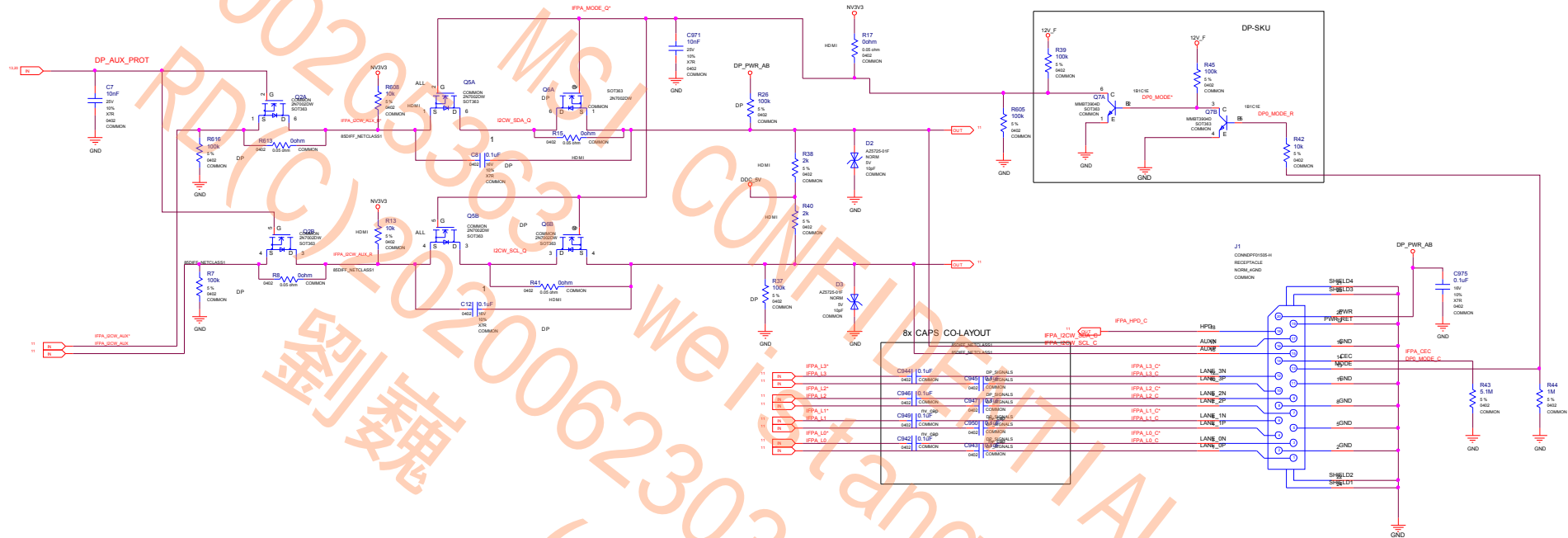


NV_PN	600-1G174-BASE-A01		
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# IFPA DP CO-LAYOUT WITH IFPAB DVI-D-DL

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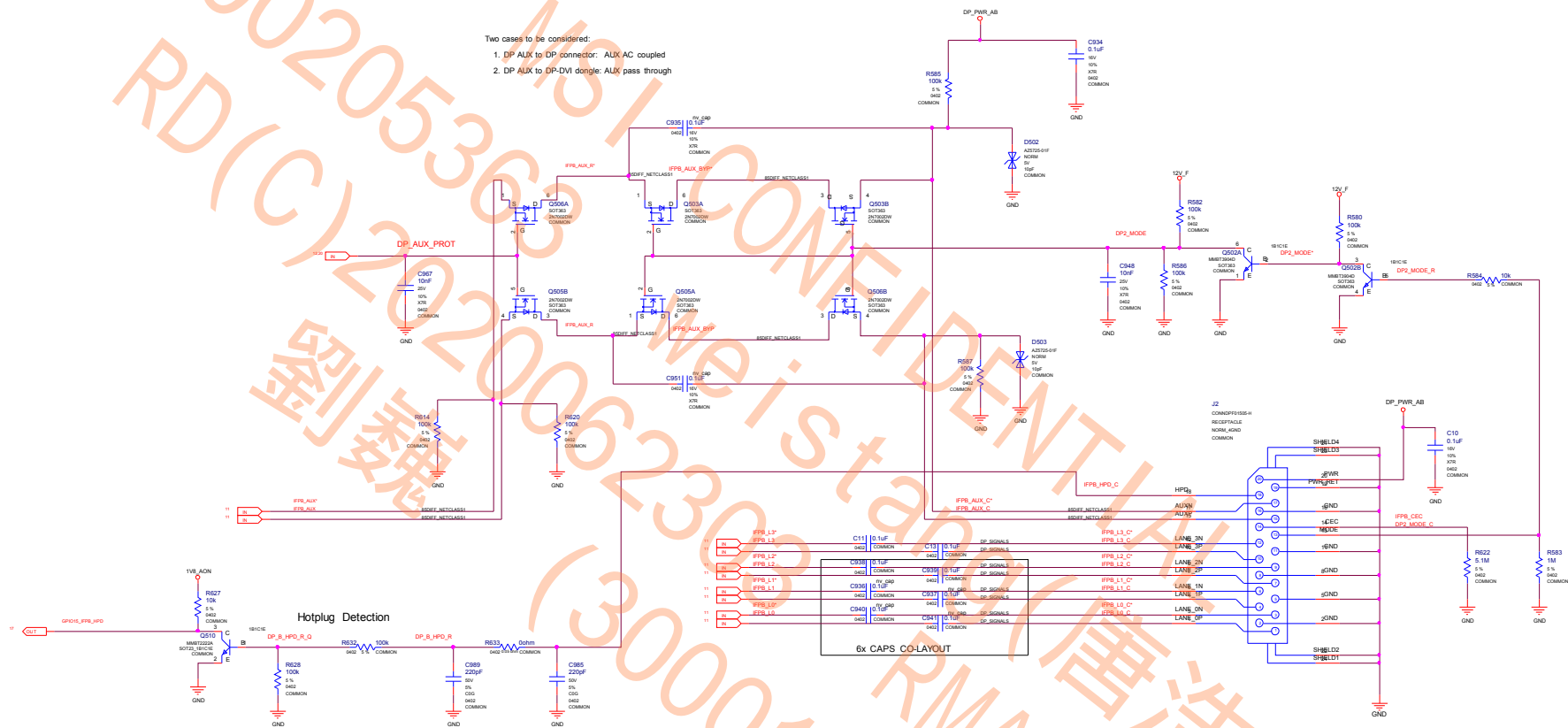
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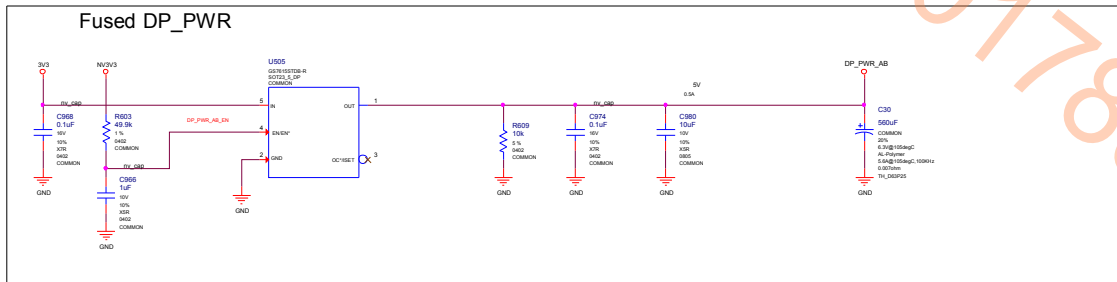
## Page13: IFPB DP

Two cases to be considered:

1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through



Fused DP\_PWR



ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	IFB DP

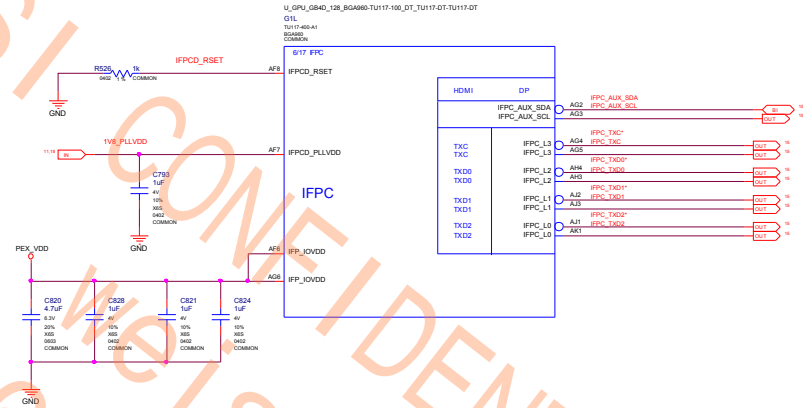
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NV_PN	600-1G174-BASE-A01
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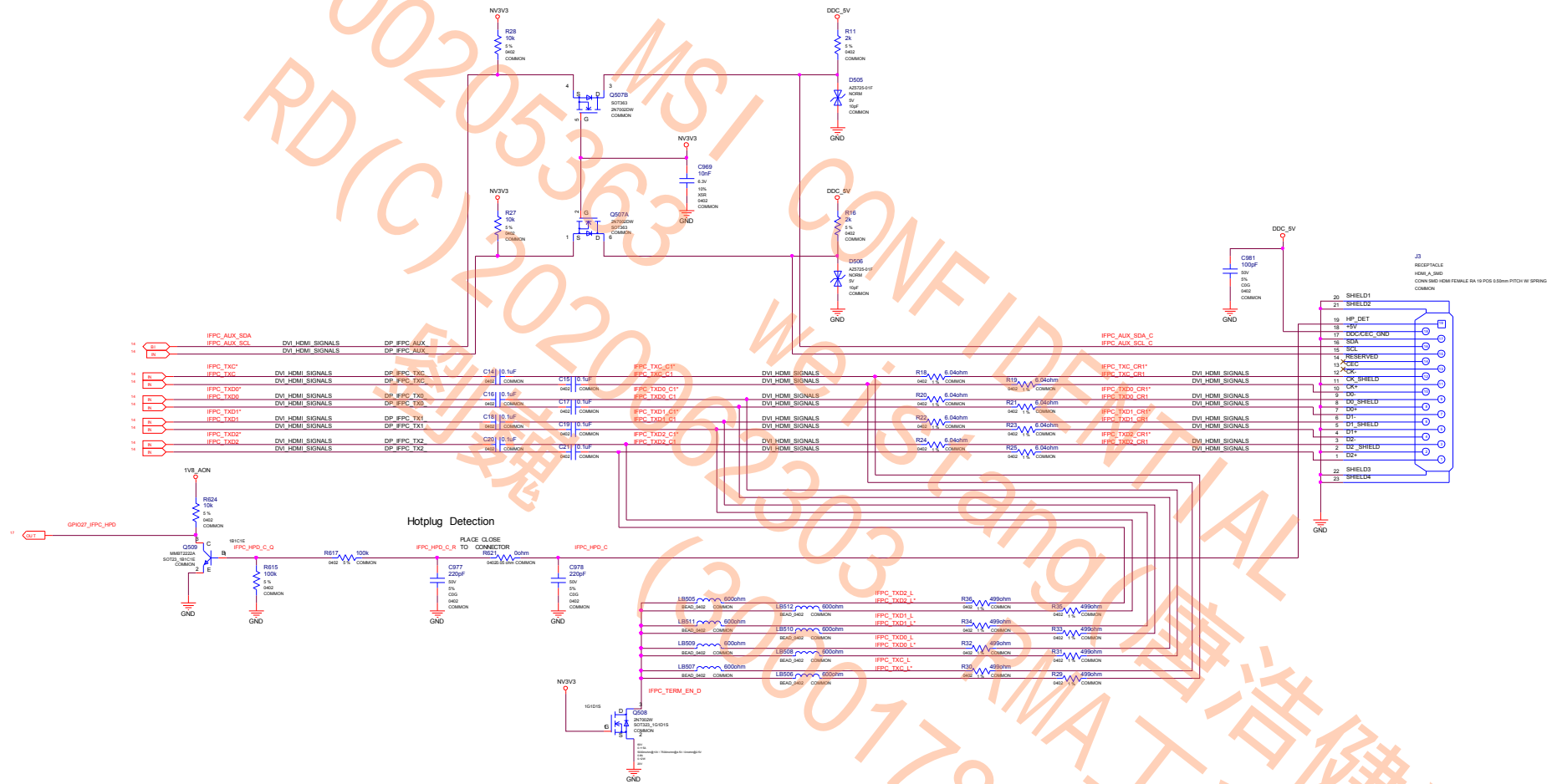
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# IFPC HDMI



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ASSEMBLY: IFPC\_HDMI  
PAGE DETAIL: IFPC\_HDMI

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NV\_PN: 600-1G174-BASE-A01

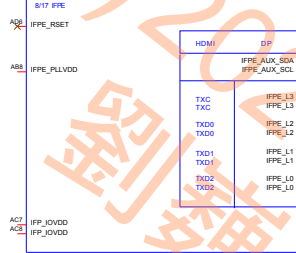
PCB REV: PG174-A01  
BOM REV: A

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DATE: 10/DEC/2018

IFPDEF UNUSED

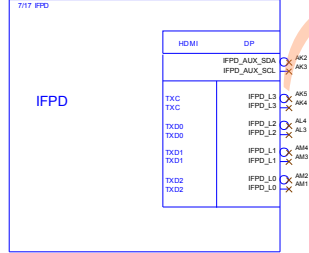
U\_GPU\_GB40\_128\_BOA965-TU117-100\_DT\_TU117-DT-TU117-DT

G1M  
TU117-40A1  
BOMREV  
COMMON



U\_GPU\_GB40\_128\_BOA965-TU117-100\_DT\_TU117-DT-TU117-DT

G1M  
TU117-40A1  
BOMREV  
COMMON



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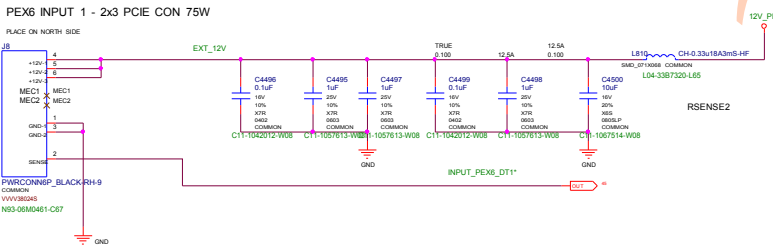
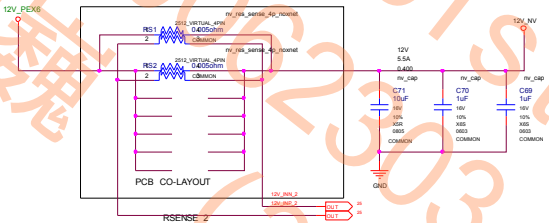
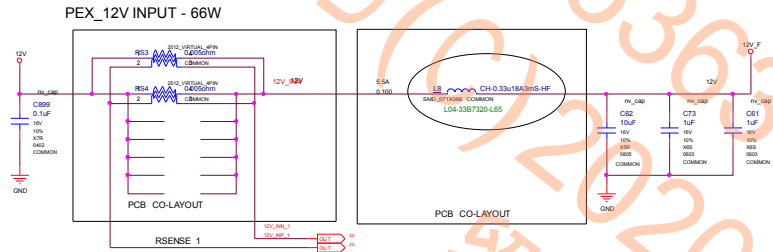
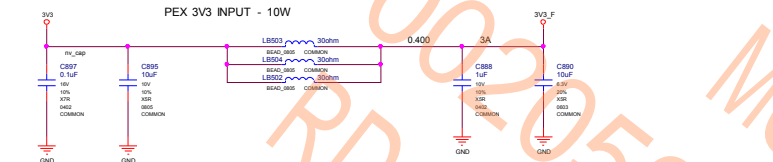
NV\_PN 600-1G174-BASE-A01

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BOM REV A DATE 10/DEC/2018

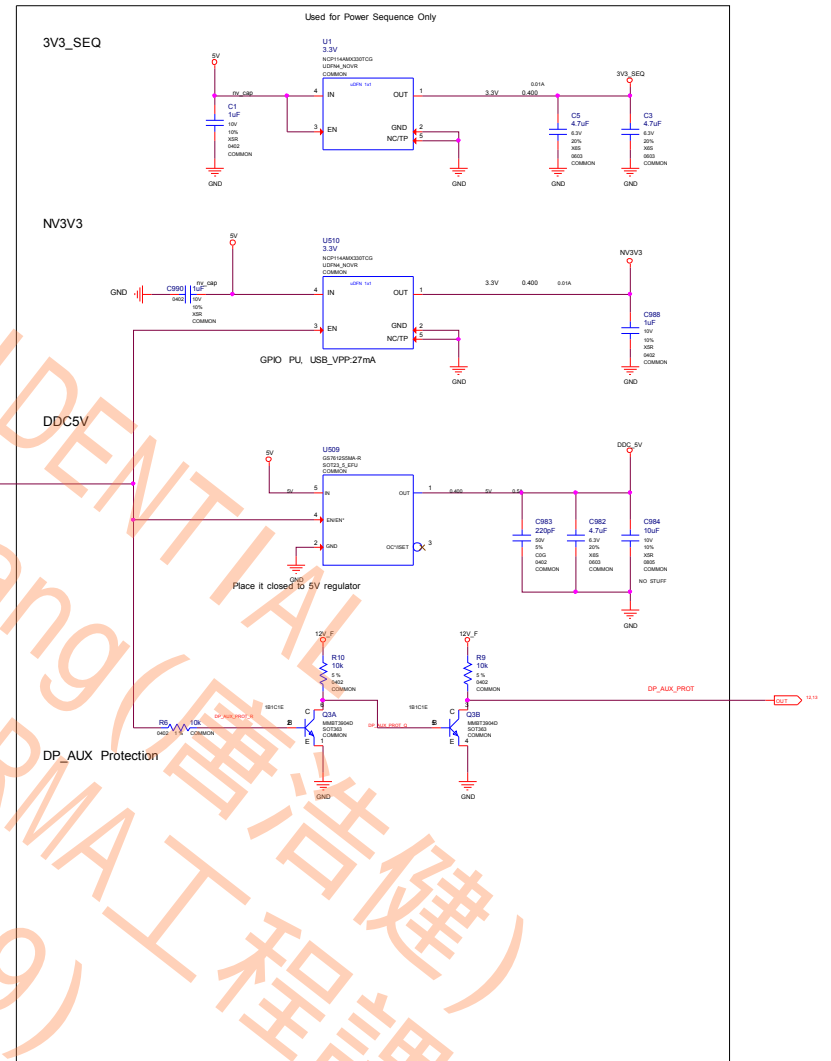
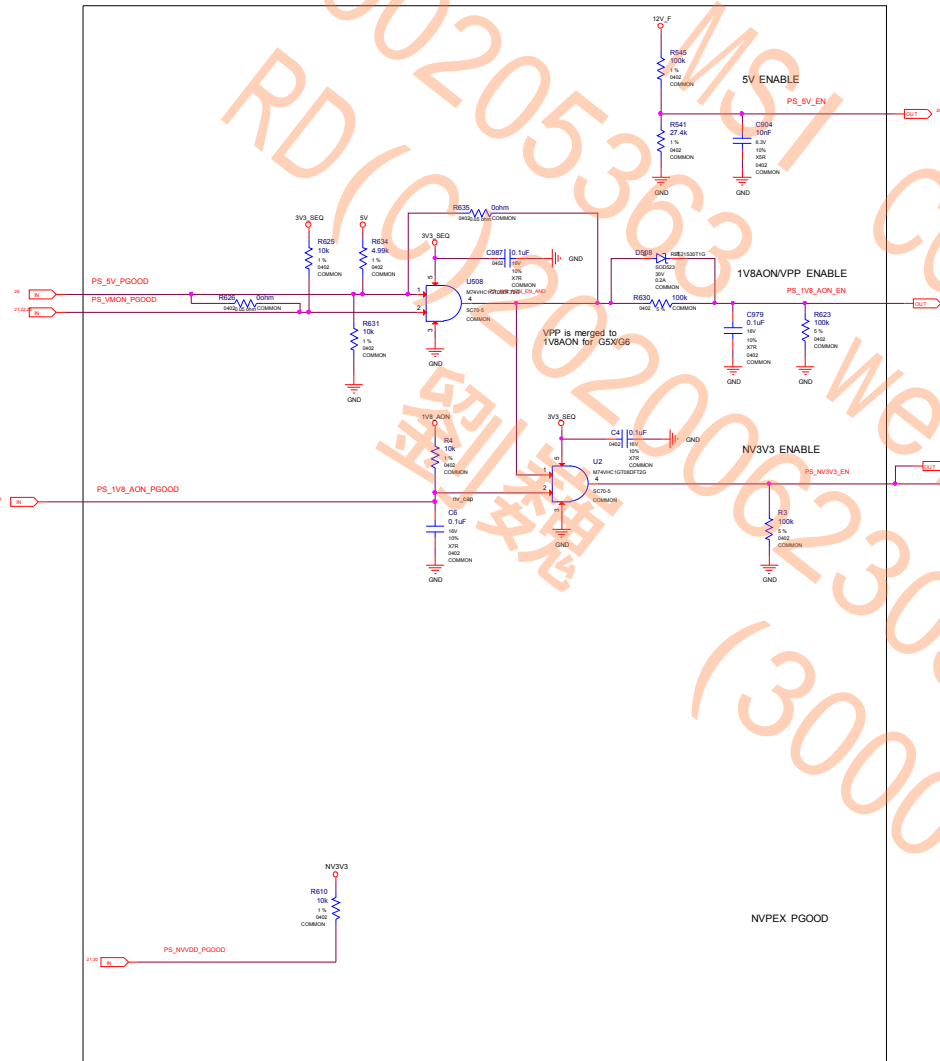





Power Inputs and Filtering



SEQ:5V,1V8,NV3V3 ENABLE



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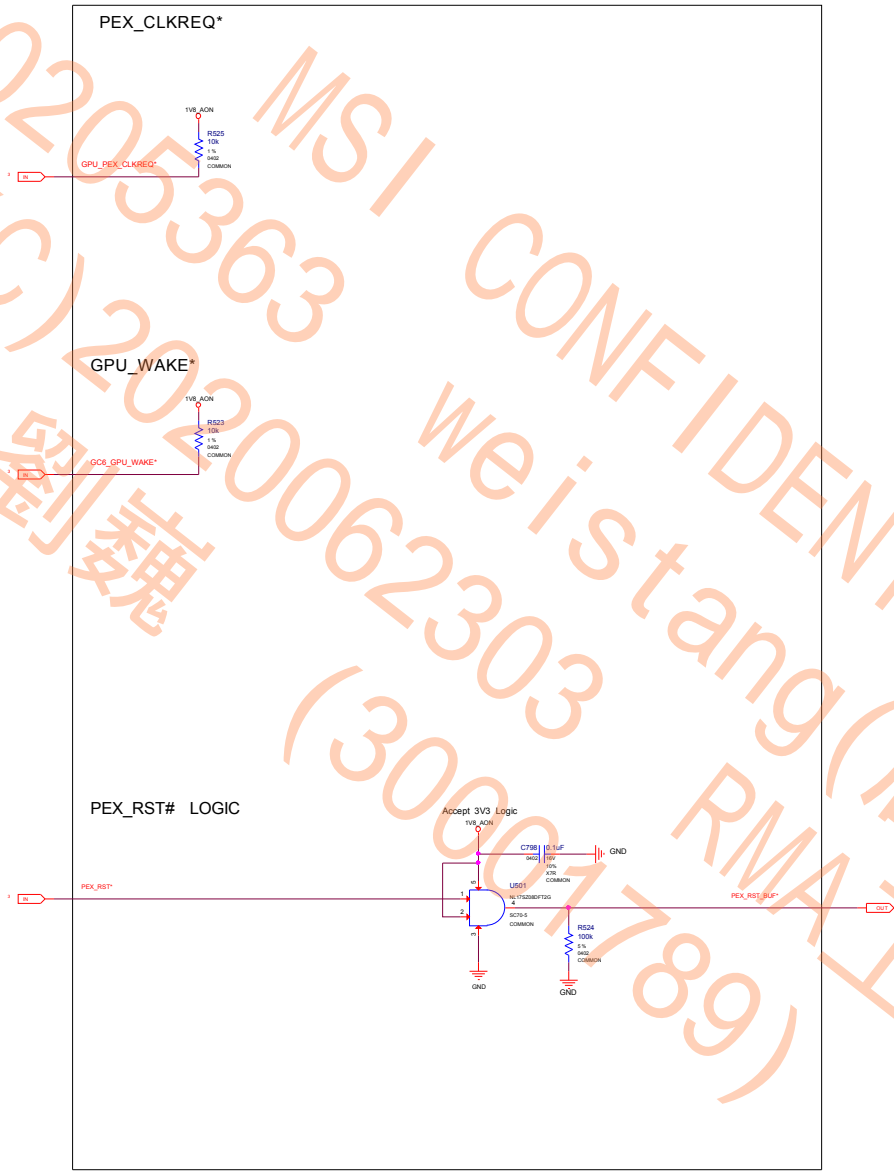








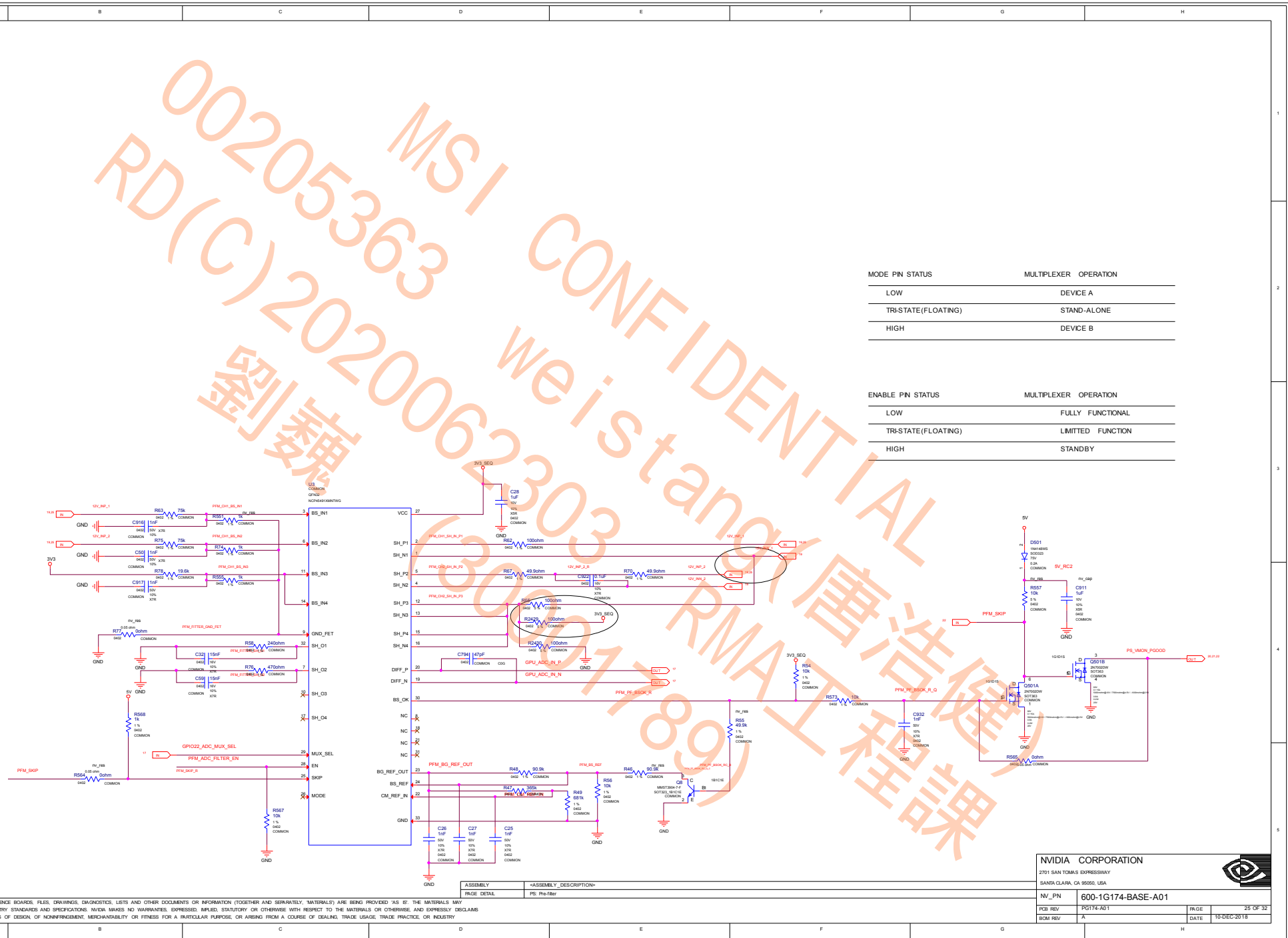
SEQUENCE:MISC



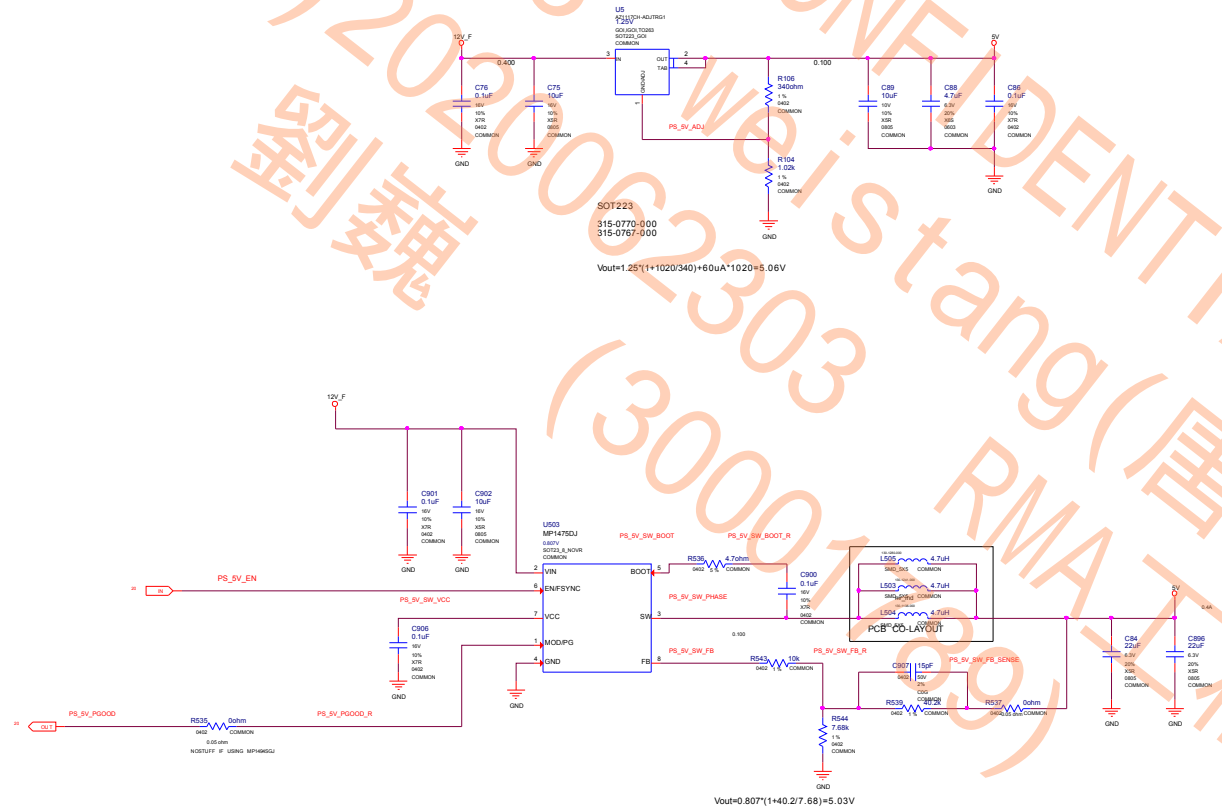
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NV_PN	600-1G174-BASE-A01		
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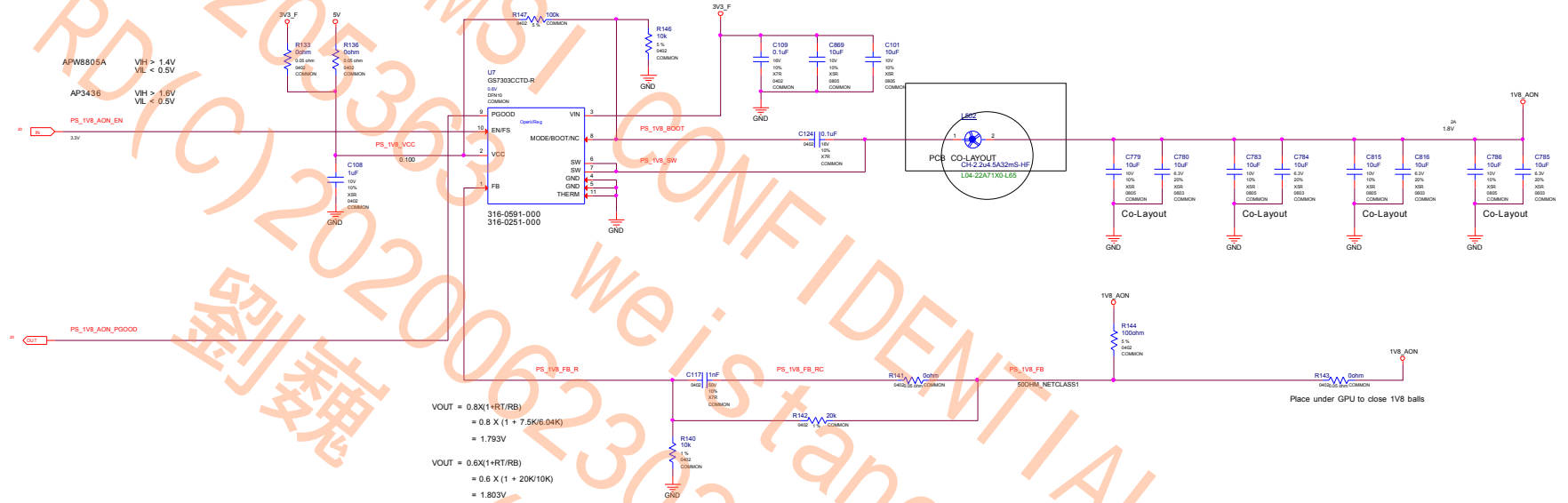
PWR SENSE



PS: 5V



PS: 1V8



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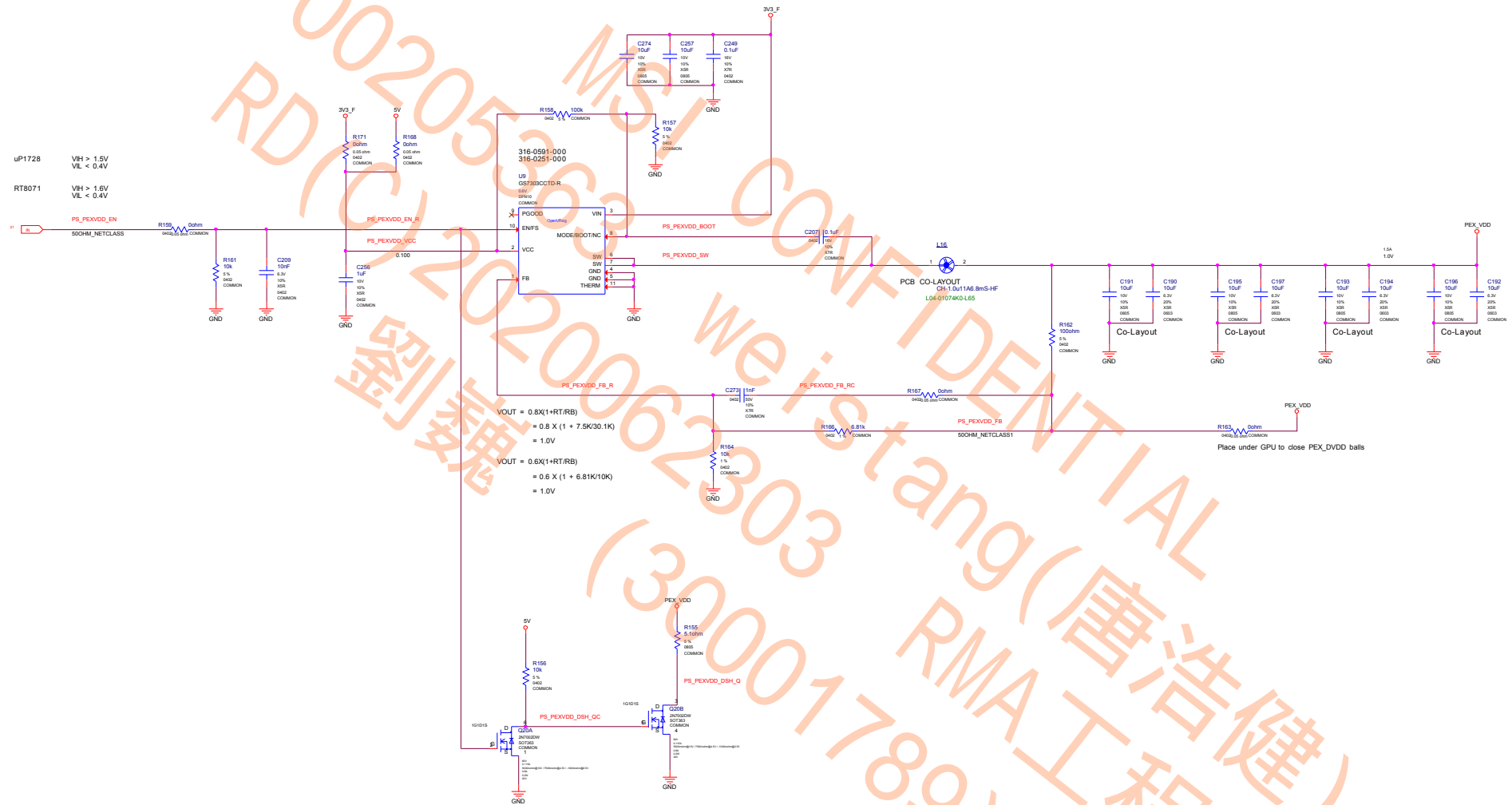
ASSEMBLY	<ASSEMBLY_DESCRIPTION
PAGE DETAIL	PS: 1/8


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## PS: PEXVDD



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<b>NV_PN</b>		<b>600-1G174-BASE-A01</b>		
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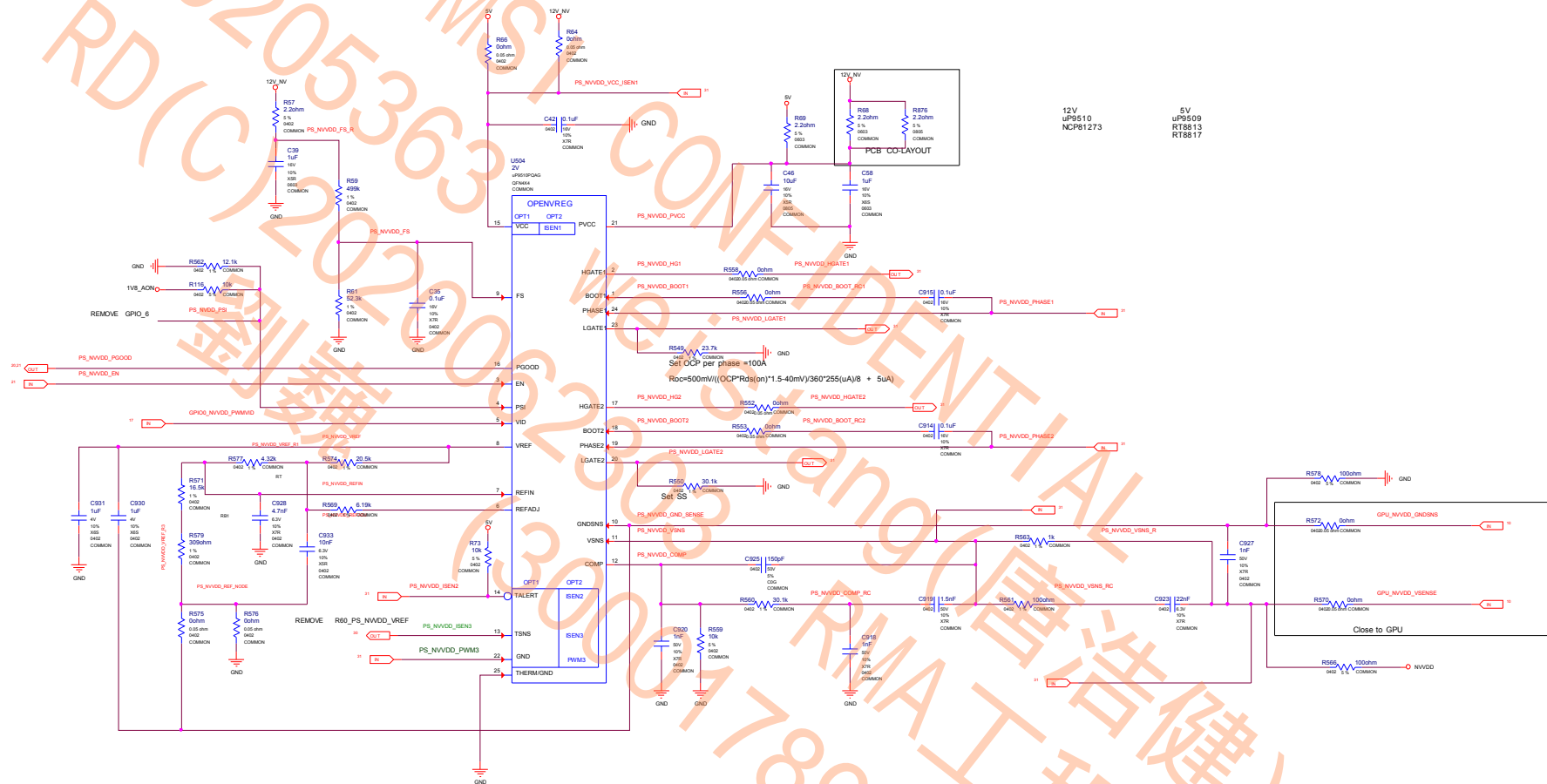






## PS: NVVDD OVR2+1

NCP81273	EN VIH >1.5V, VIL < 0.7V
UP9509/10	EN VIH >1.2V, VIL < 0.6V
RT8817A	EN VIH >1.2V, VIL < 0.6V



NVPN	Name	PVCC(21)	VCC(15)
78-000	NCP81273	12v	LDO_OUT
76-000	up9510	12v	ISENSE
			NC

ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: NVDD OVR2+1

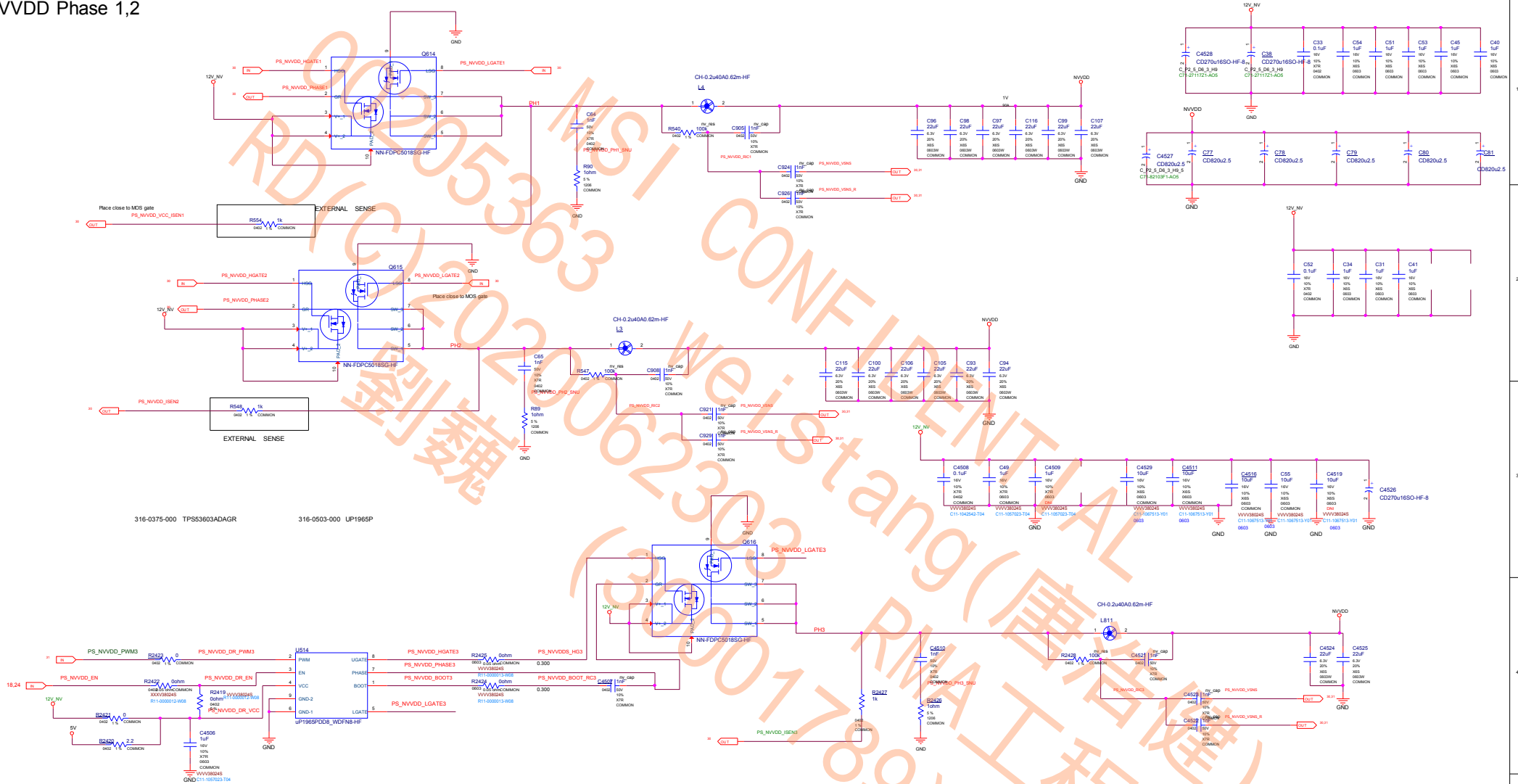
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## PS: NVVDD Phase 1,2



ASSEMBLY	<ASSEMBLY_DESCRIPTION
PAGE DETAIL	PS: NVDD Phase 1,2

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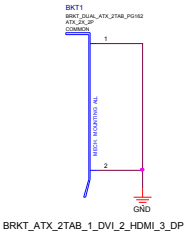


	NV_PN	600-1G174-BASE-A01
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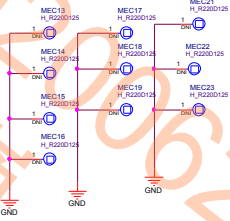
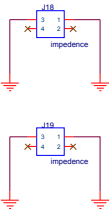
PCB REV	PG174-A01	PAGE	31 OF 32
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Mechanical: Bracket/Thermal Solution

Brackets:



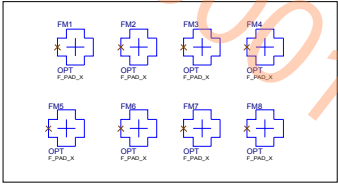
BRKT\_ATX\_2TAB\_1\_DVI\_2\_HDMI\_3\_DP



COOLING SOLUTION

4 connected mounting pins

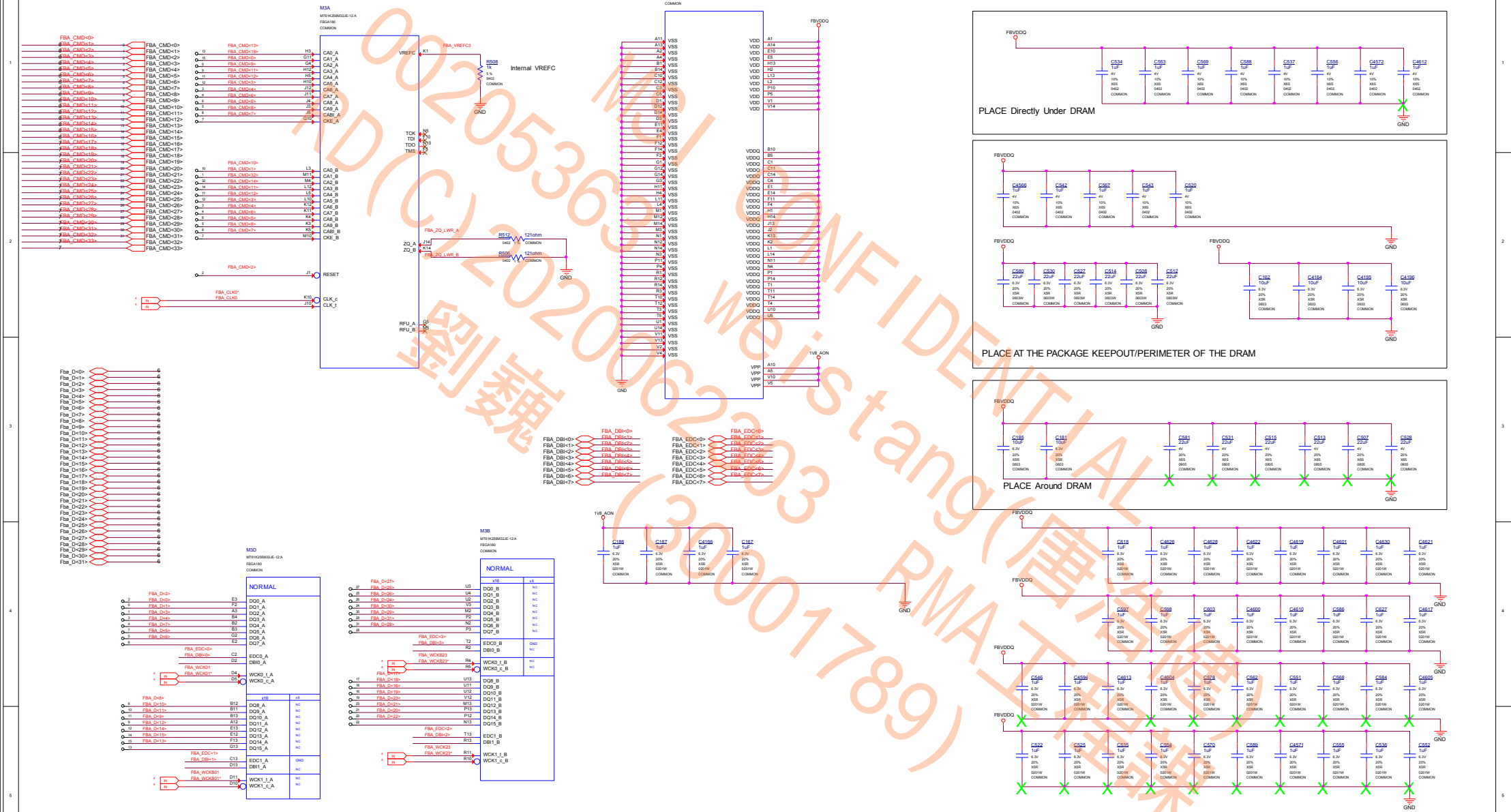
GND



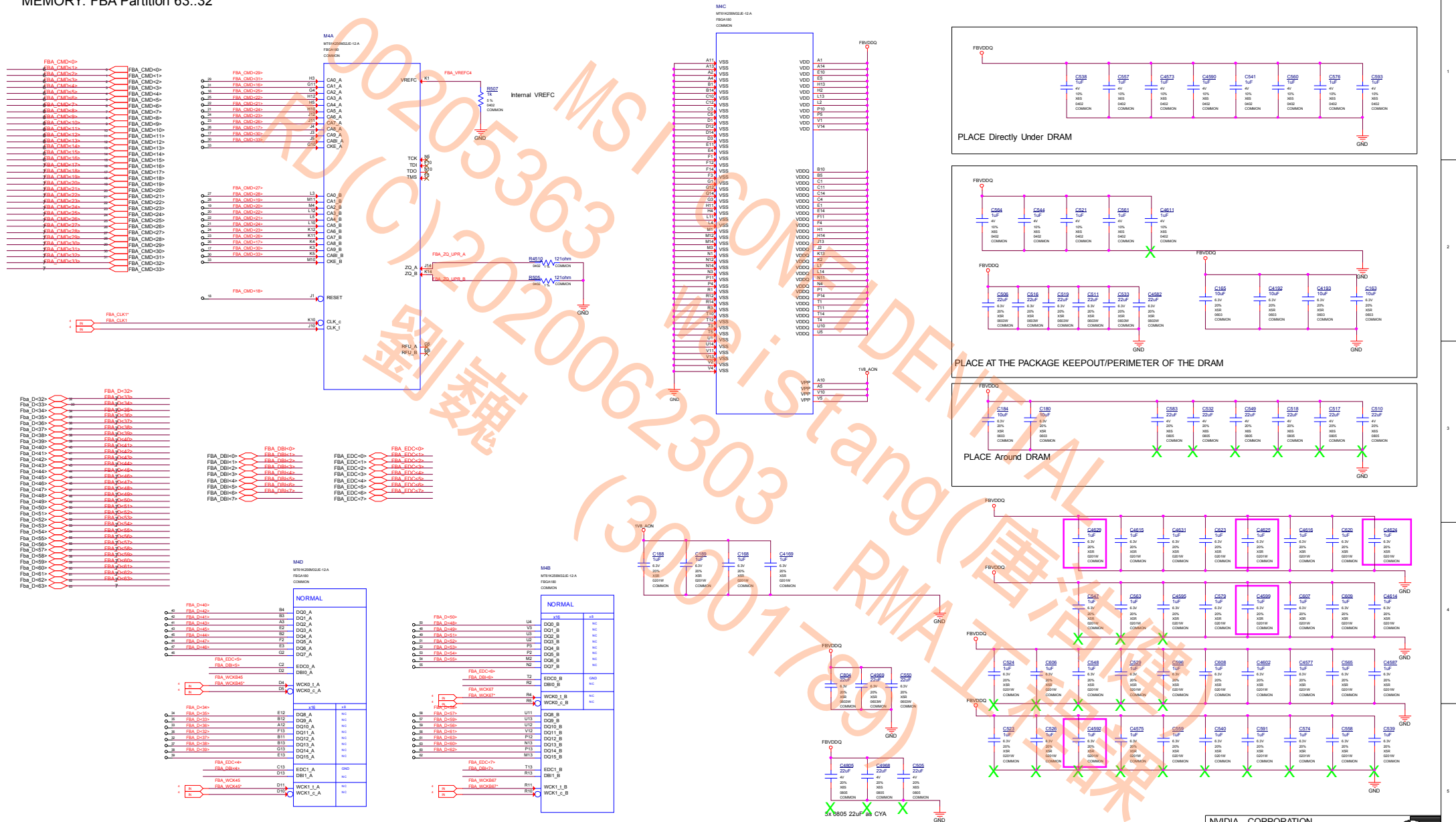
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
# MEMORY: FBA Partition 31..0



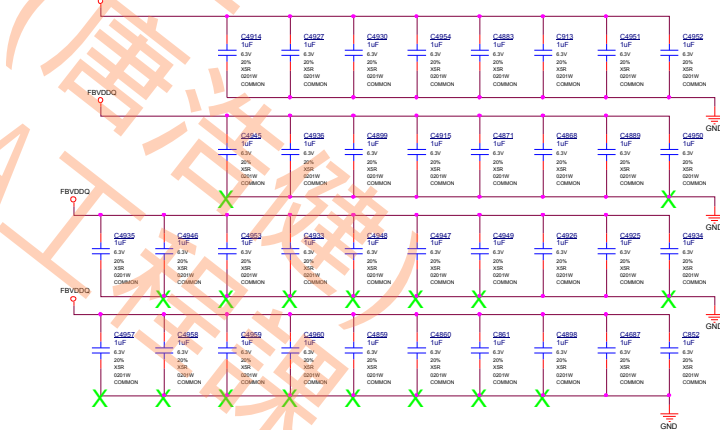
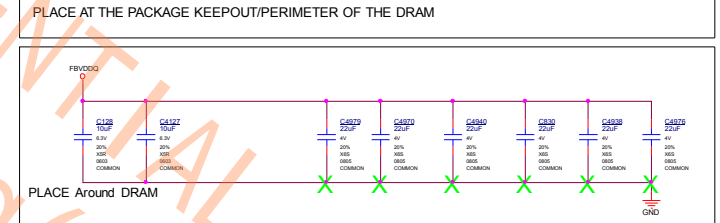
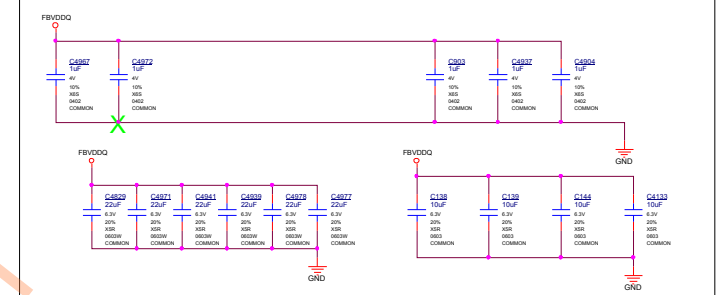
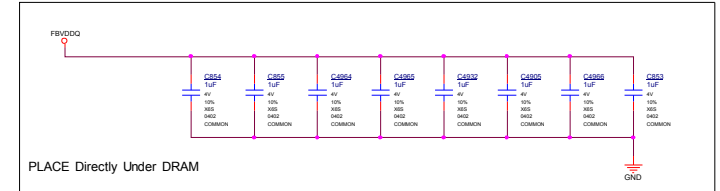
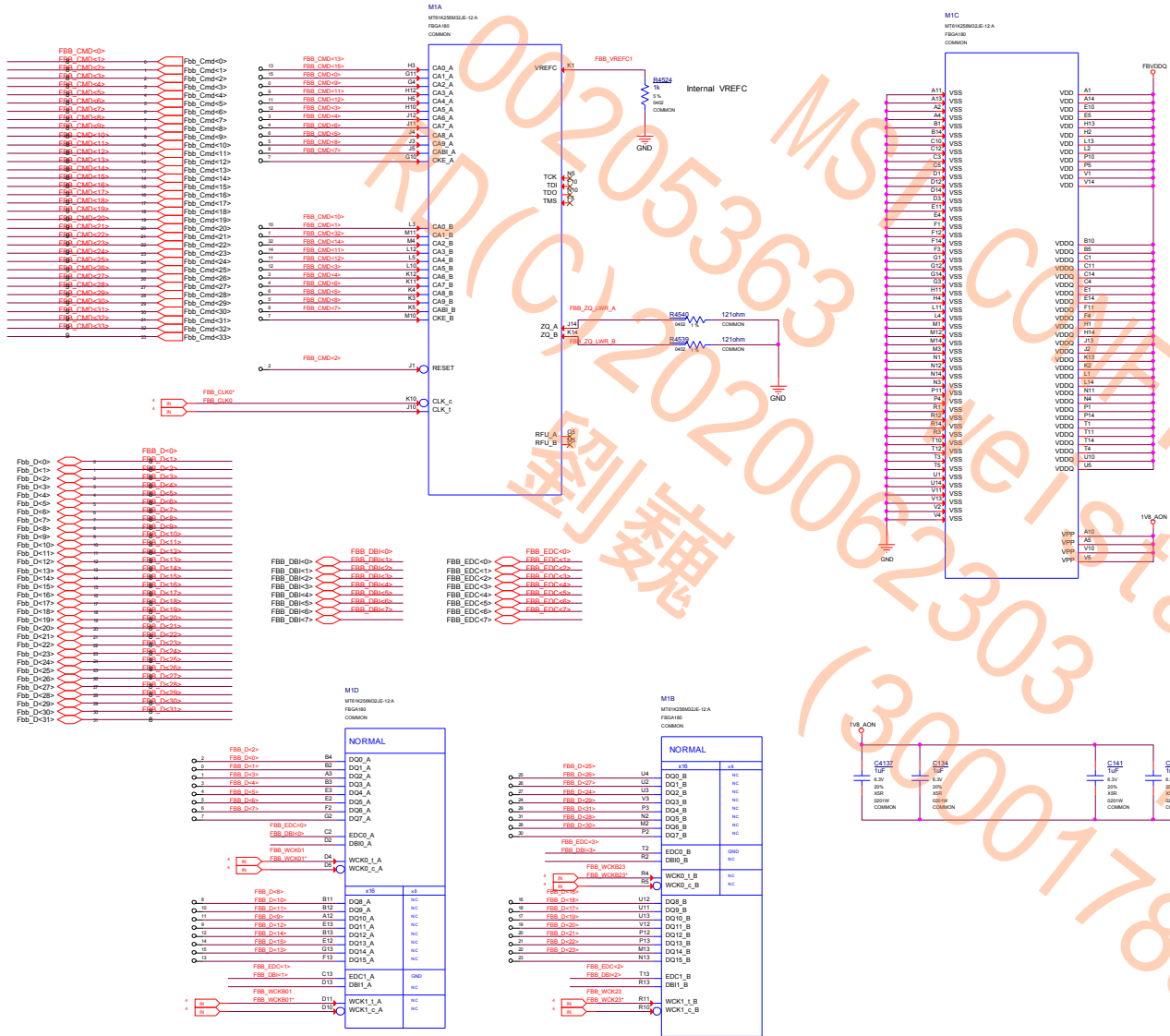
MEMORY: FBA Partition 63..32



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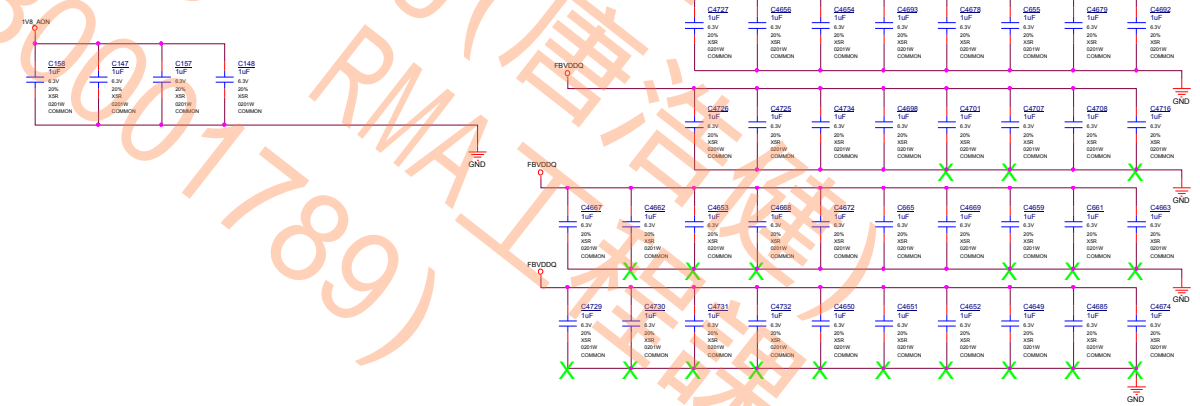
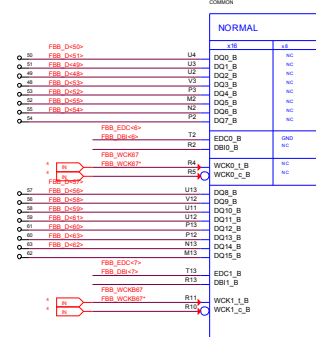
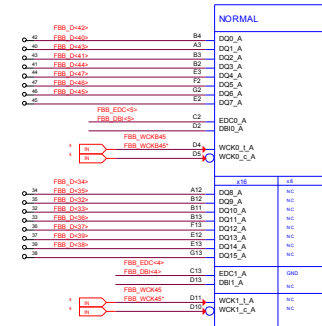
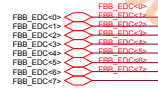
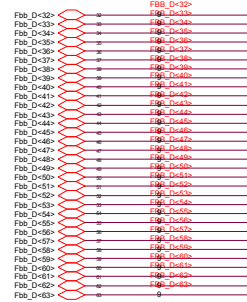
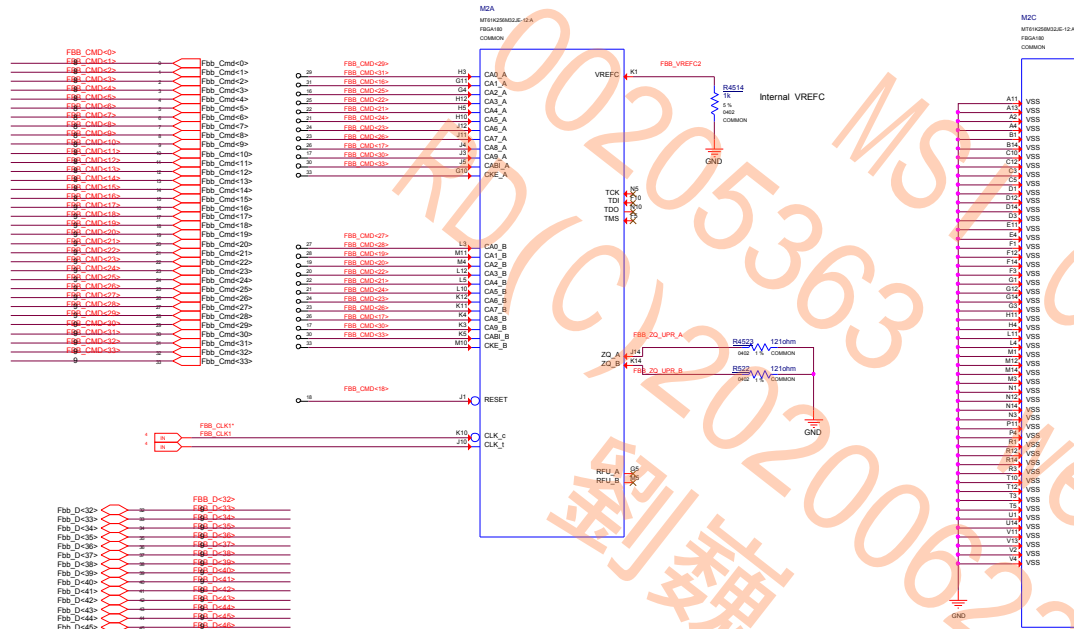
ASSEMBLY		ASSEMBLY DESCRIPTION		SANTA CLARA, CA 95050, USA			
PCB DETAIL		MEMORY: FBA0332		NV_PN		600-1G177-BASE-000	
REV		ICLAMS ALL		PCB REV		P5177-70/0	
STANDARD		REV		PCB REV		A	
				DATE		20-FEB-2018	

MEMORY: FBB Partition 31..0






MEMORY: FBB Partition 63..32



PLACE Directly Under DRAM

PLACE AT THE PACKAGE KEEPOUT/PERIMETER OF THE DRAM

PLACE Around DRAM

<b>NVIDIA CORPORATION</b> 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA				
<b>NV_PN</b>		<b>600-1G177-BASE-000</b>		
<b>PCB REV</b>		<b>PG177-A00</b>		<b>PAGE</b>
<b>QRM REV</b>		<b>A</b>		<b>20-FEB-2019</b>
				<b>8 OF 36</b>

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