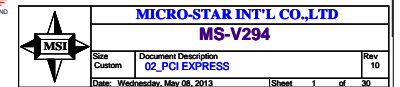
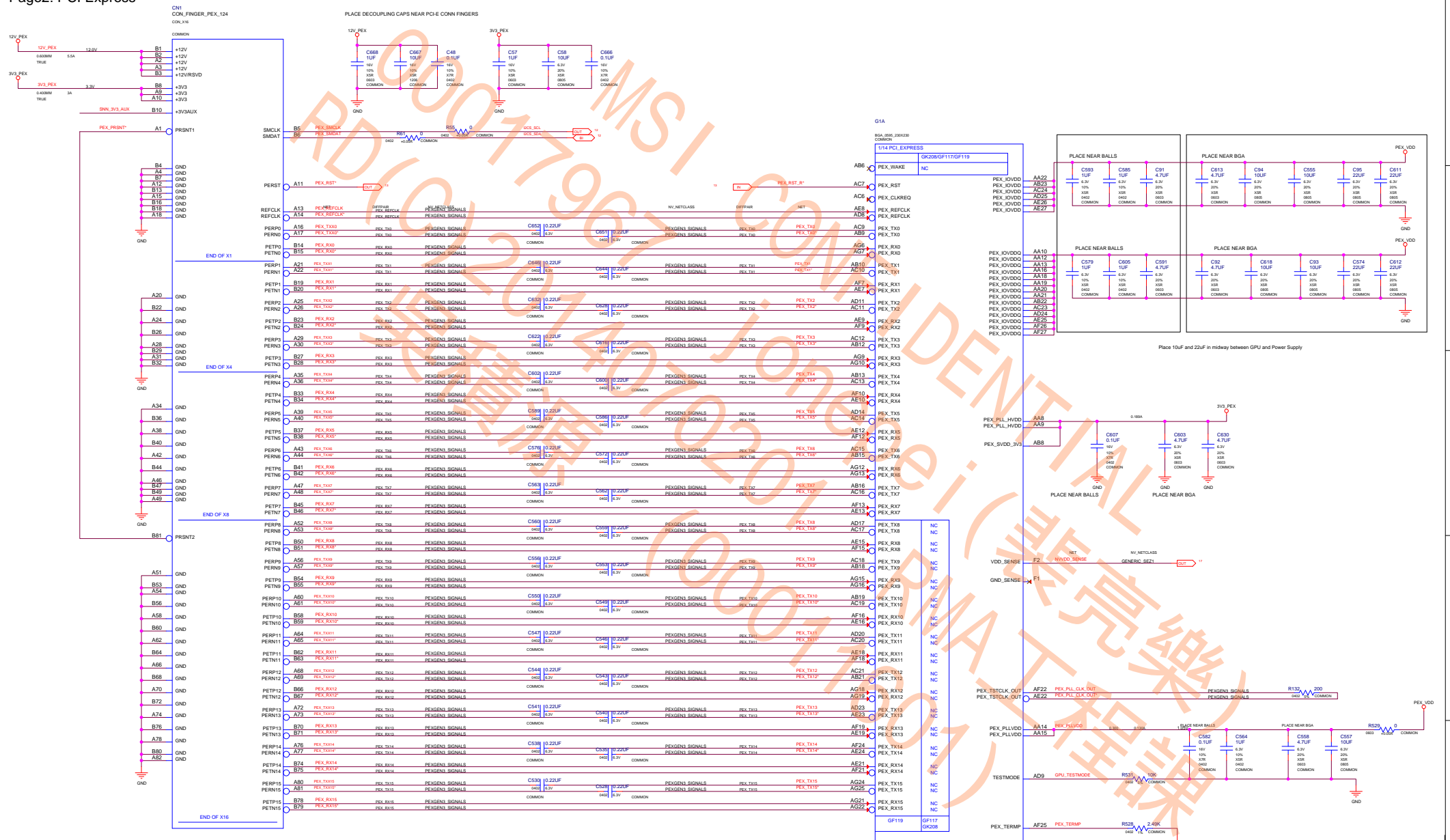
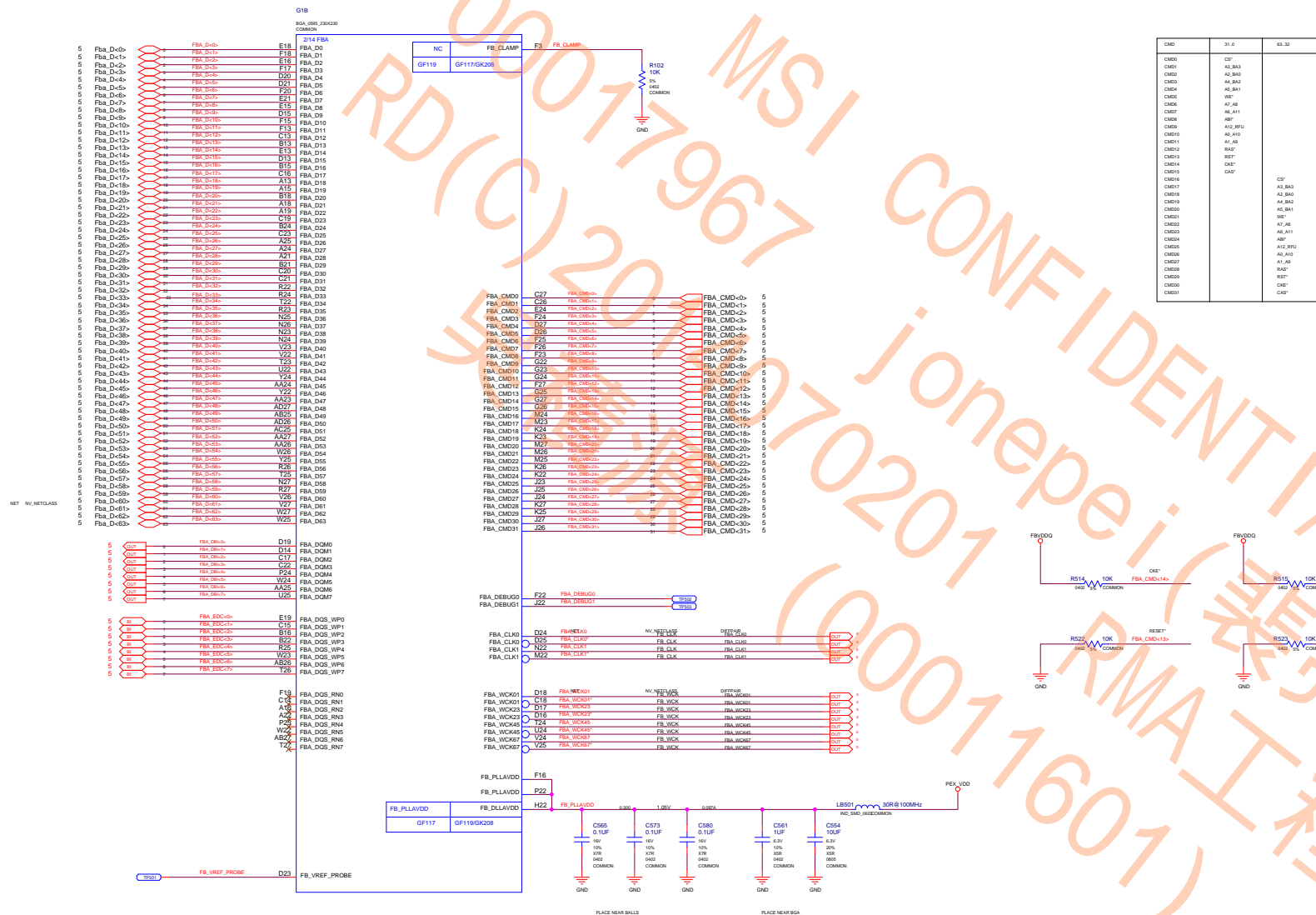


GK208 GDDR5 X16 64BIT
DVI-I/DVI-D/SlimVGA + HDMI/DP + VGA

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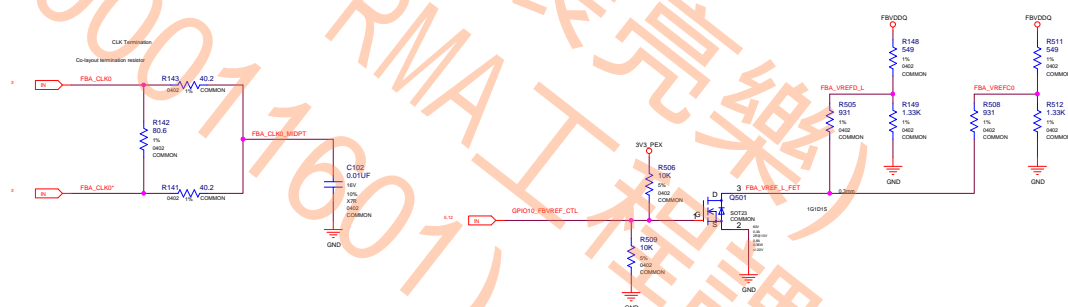
Page	Description
1	Table of Contents
2	PCI EXPRESS
3	GPU MEMORY FBA
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5	FB Partition A bits 63..32
6	DECOUPLING for DRAMs
7	POWER/DECOUPLING: NVVDD, FBVDDQ, VDD33, GND
8	IFPA/B TMD5 DUAL LINK
9	IFPC HDMI/DP, DP_PWR
10	UNUSED IFPD, IFPE & IFPF
11	DACA VGA
12	BIOS, JTAG, GPIO & XTAL
13	PCI-E Reset, STRAPS & FAN
14	PS: 5V, PEXVDD
15	PEX_VDD: Open VR Type0
16	PS: FBVDDQ
17	PS: NVVDD



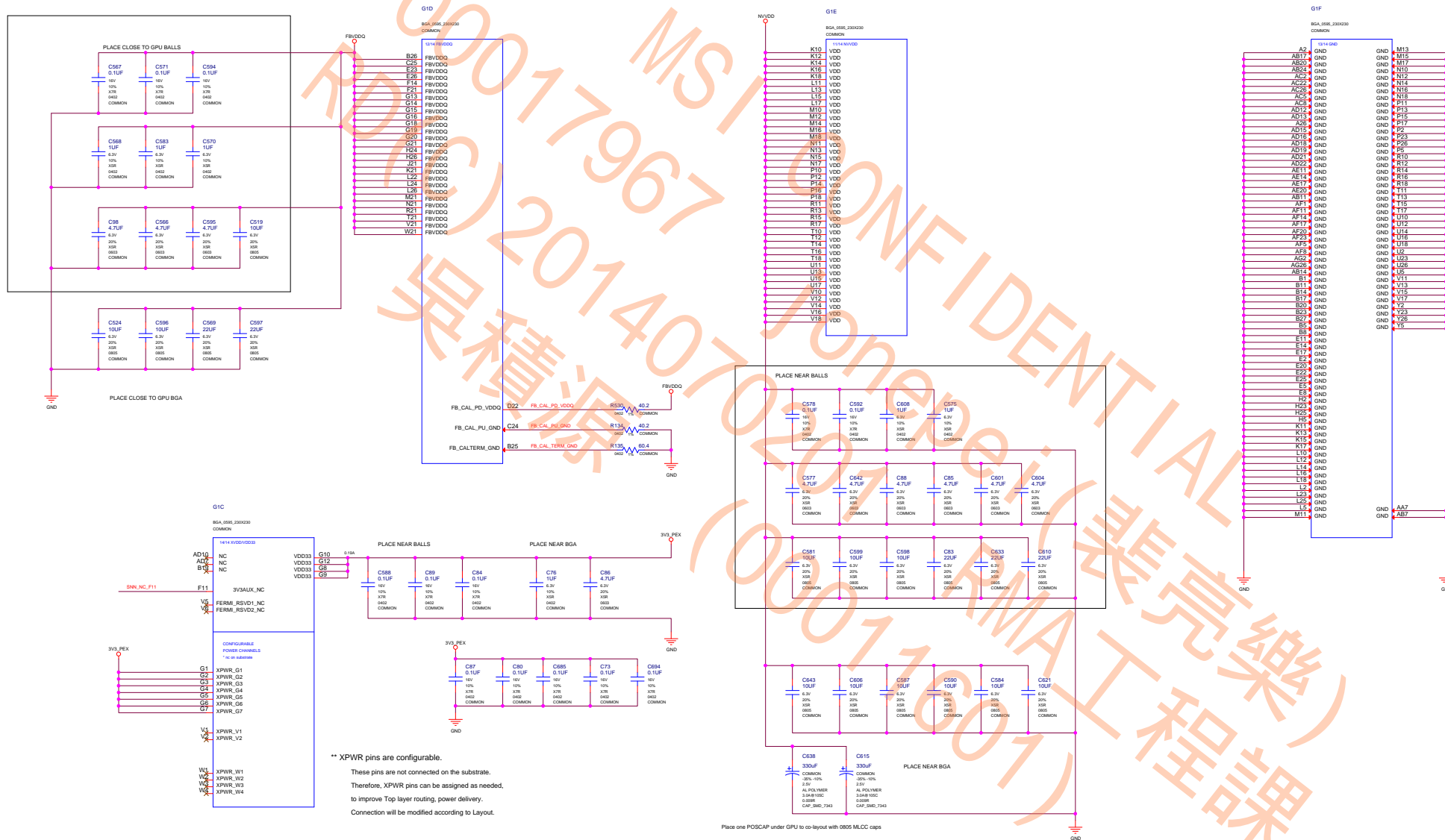


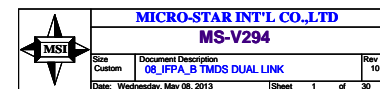
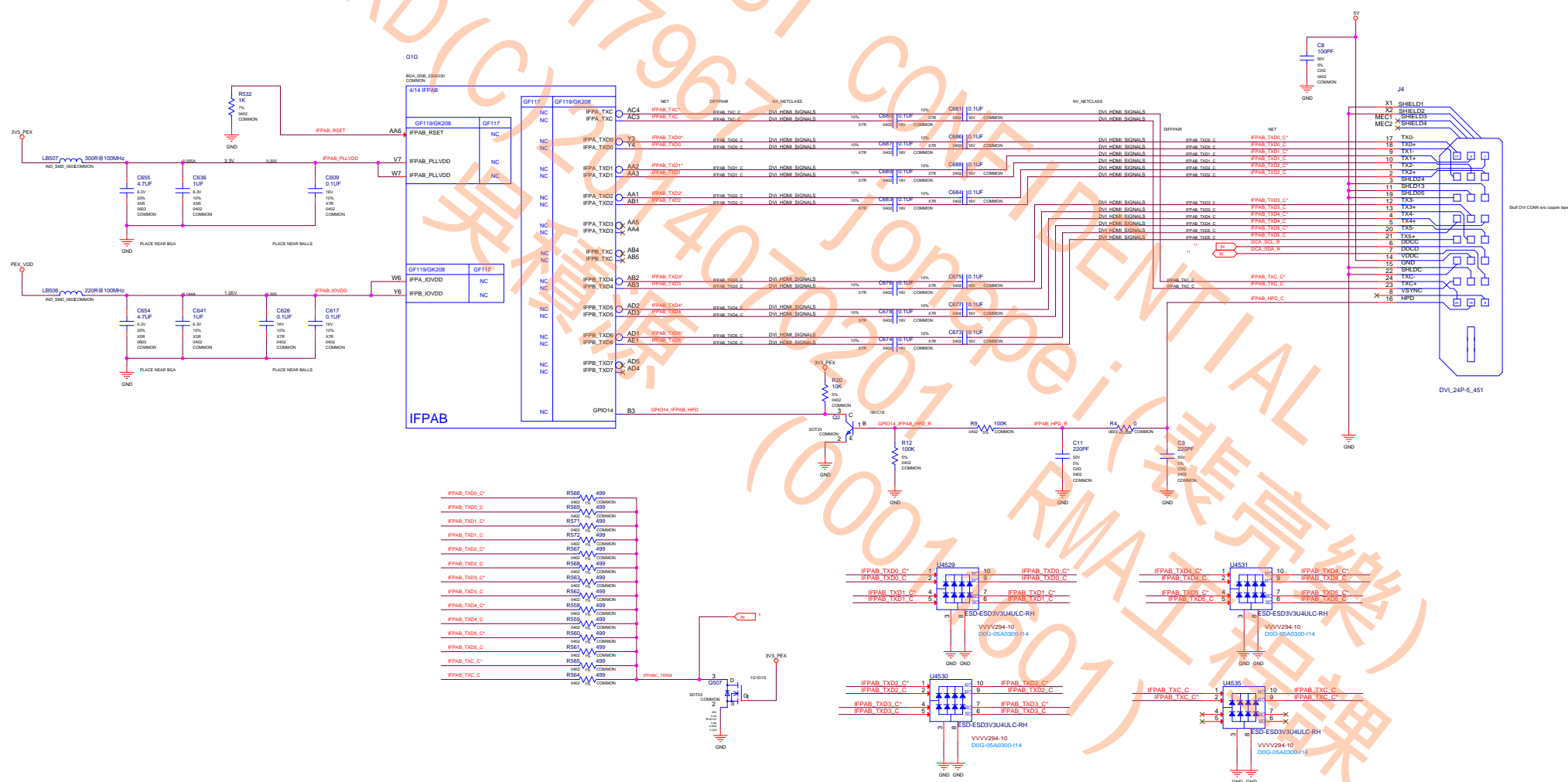


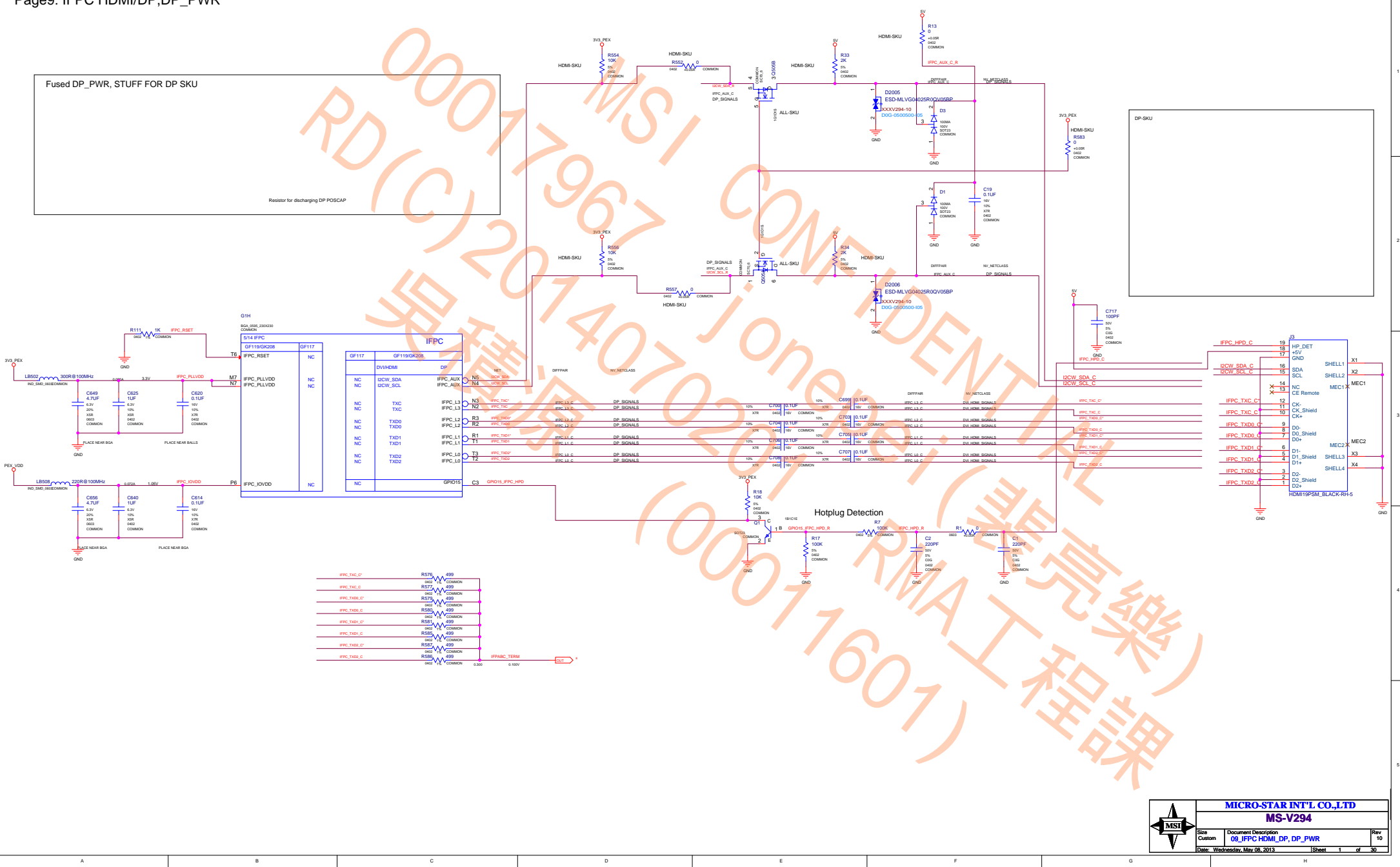
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CM001	CS*	
CM011	A3, BA3	
CM021	A2, BA2	
CM031	A4, BA2	
CM041	AB, BA1	
CM051	BB*	
CM061	A2, A5	
CM071	AB, A11	
CM081	AB*	
CM091	A2, A7A1	
CM101	A4, A13	
CM111	A1, A5	
CM121	BB*	
CM131	BB*	
CM141	CA1A	
CM151	CA5*	
CM161		CS*
CM171		A3, BA3
CM181		A2, BA2
CM191		A4, BA2
CM201		AB, BA1
CM211		BB*
CM221		A2, A5
CM231		A4, A11
CM241		AB*
CM251		A2, A7A1
CM261		AB, A13
CM271		A1, A5
CM281		BB*
CM291		BB*
CM301		CA5*

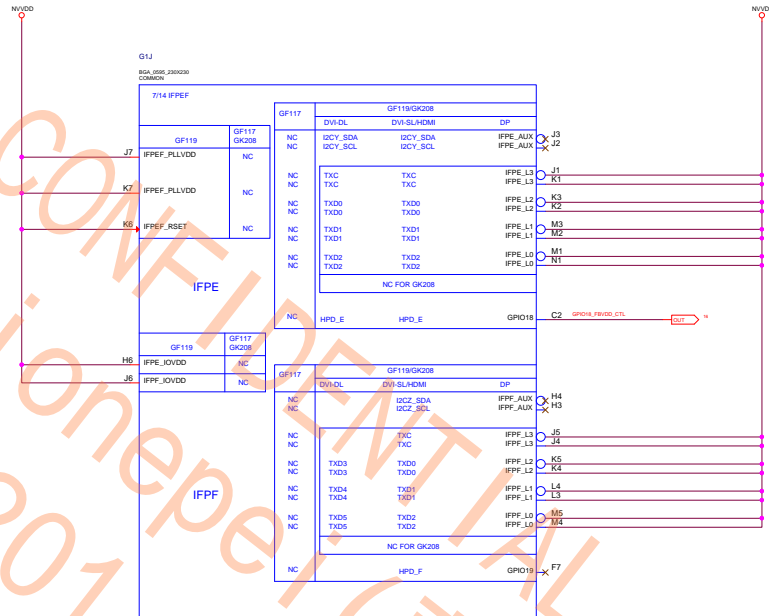


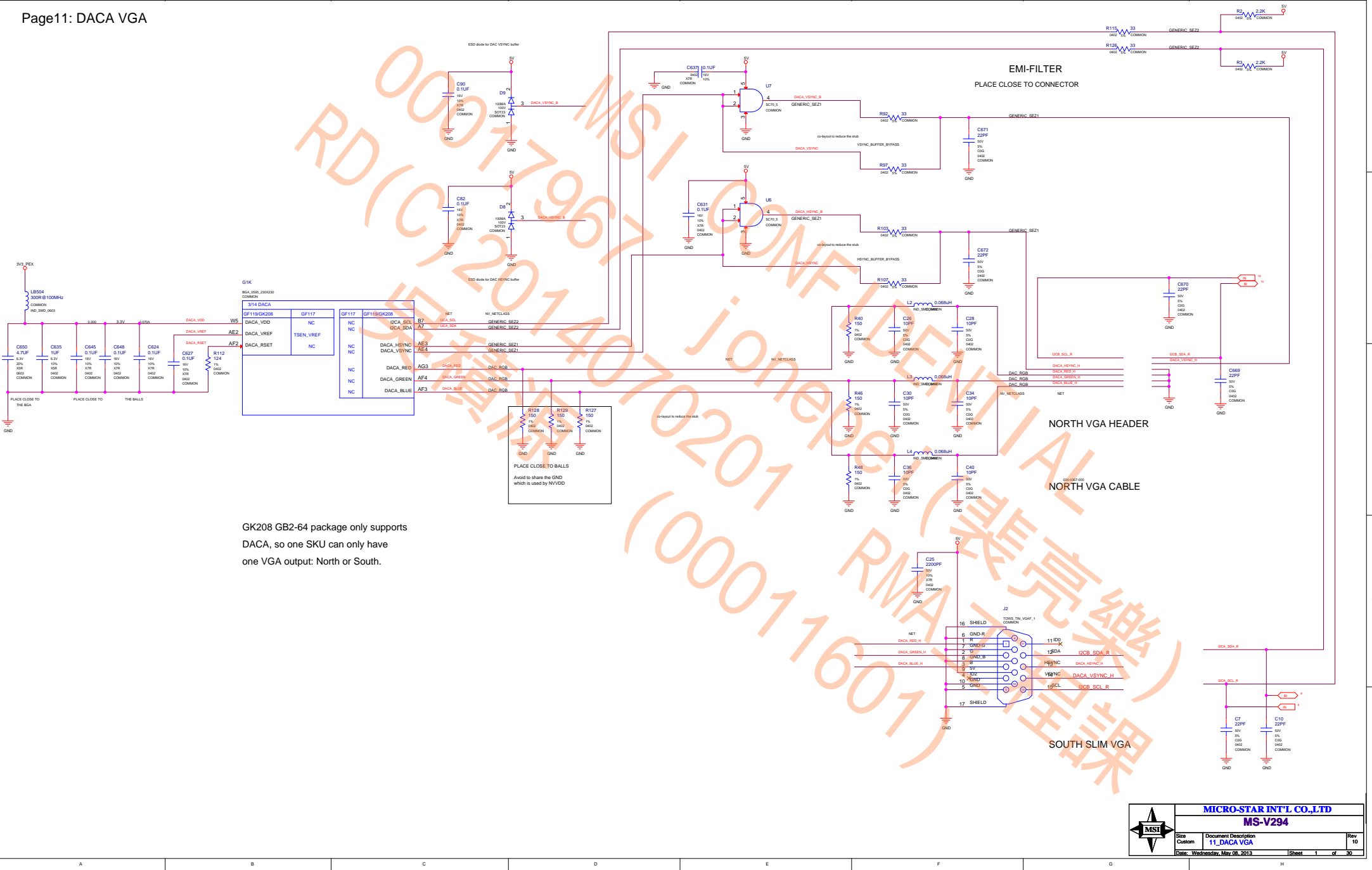
Size Custom	Document Description 04_FB Partition A bits 31..0	Rev 10
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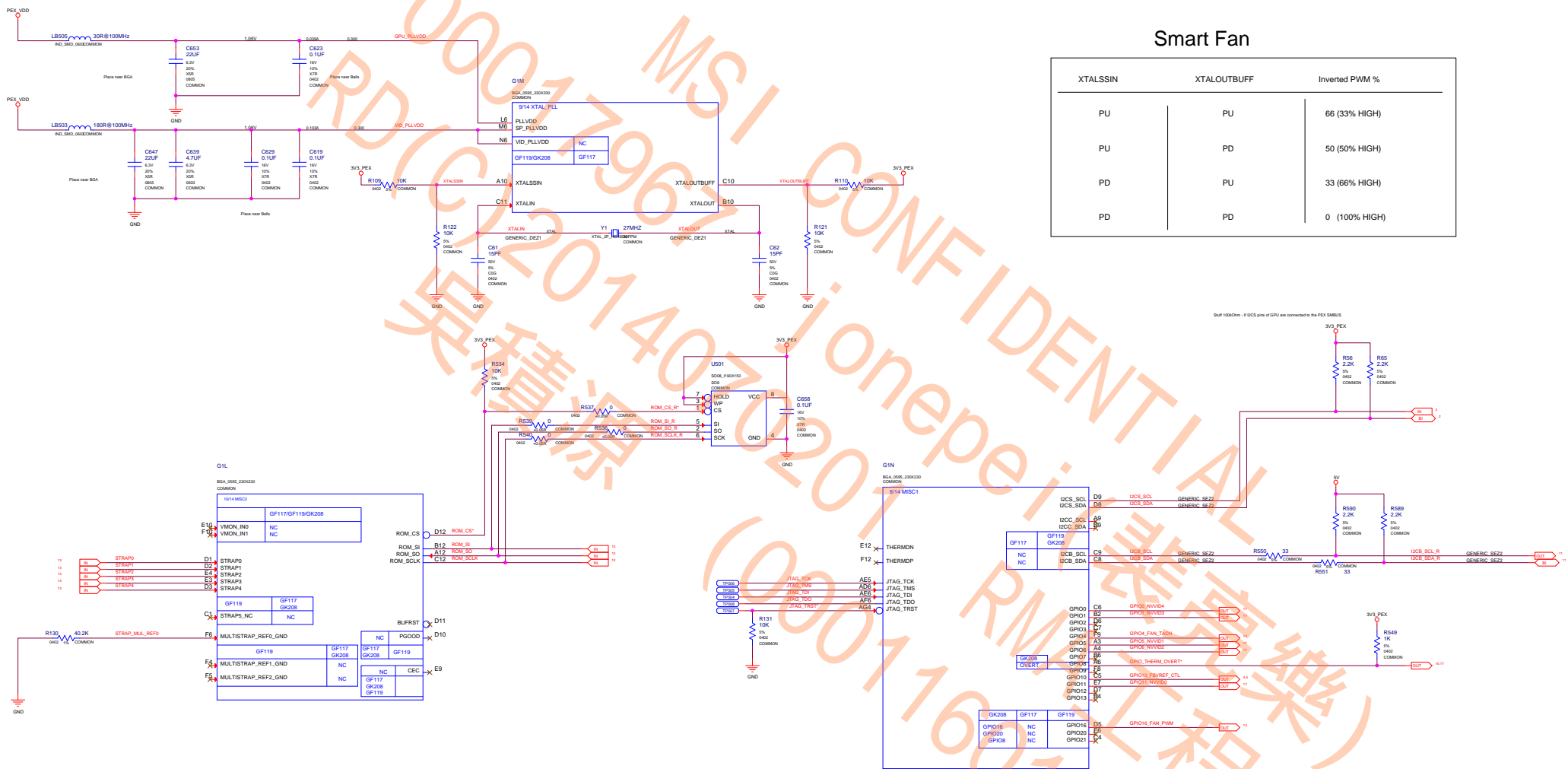




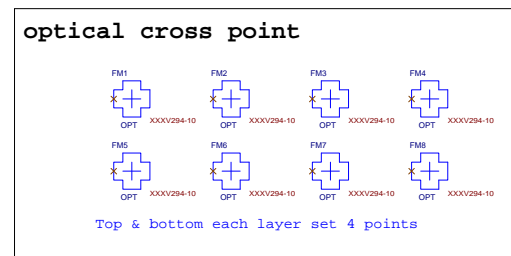
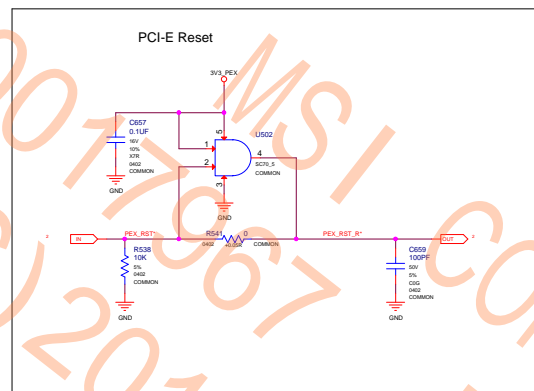






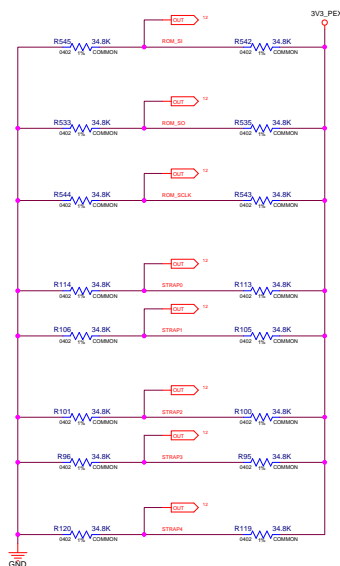


XTALSSIN	XTALOUTBUFF	Inverted PWM %
PU	PU	66 (33% HIGH)
PU	PD	50 (50% HIGH)
PD	PU	33 (66% HIGH)
PD	PD	0 (100% HIGH)

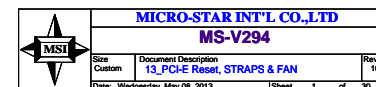
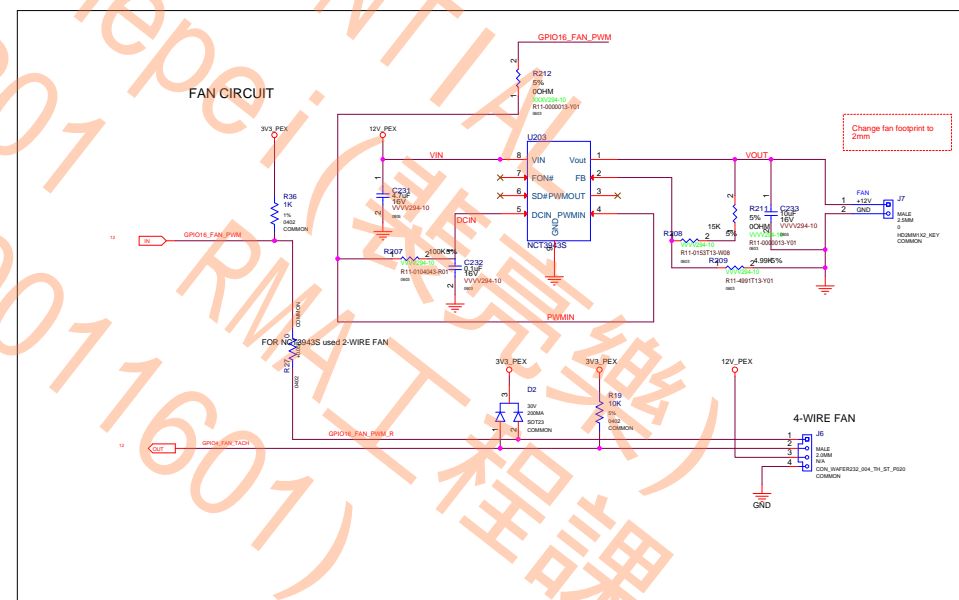


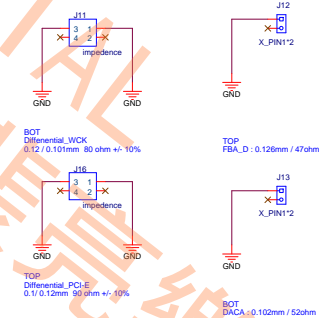
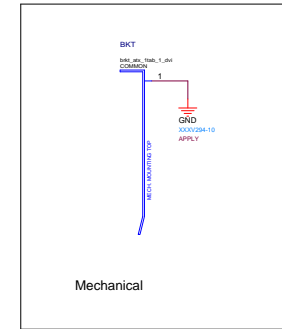
0000	5.1K to GND	1000	5.1K to VCC
0001	10K to GND	1001	10K to VCC
0010	15K to GND	1010	15K to VCC
0011	20K to GND	1011	20K to VCC
0100	24.9K to GND	1100	24.9K to VCC
0101	30.1K to GND	1101	30.1K to VCC
0110	34.8K to GND	1110	34.8K to VCC
0111	45.3K to GND	1111	45.3K to VCC


GK208 Straps





Bit Signal		Values				
03	RAMCFG[2]	0000	Reserved			ROM_SI
01	RAMCFG[2]	0001	UDMA-H GDDR3 X16 64bit ECC			
01	RAMCFG[2]	0010	UDMA-H GDDR3 X16 64bit Parity A-bits			
01	RAMCFG[2]	0011	UDMA-H GDDR3 X16 64bit Samsung D-die			
03	FB[1]	01	UDMA8			ROM_SO
02	FB[0]	10	UDMA9			
01	SMBL_ALT_ADDR	0	DISABLED	1	ENABLED	ROM_SCL
01	VGA_DEVICE	0	DISABLED	1	ENABLED	
03	PCI_DEV0_EXT	SW0	0	SW1	780	
02	SUB_VENDOR	0	NO_BIOS	1	BIOS	
01	PCI_DEV0_EXT_BITS	SW0	0	SW1	780	
00	PEX_FUL_EN_TERM100	0	DISABLED	1	ENABLED	
00	USE[0]	0000	REF			Strap0
01	USE[0]					
01	USE[1]					
01	USE[2]					
01	3G0_PNDCFG_LUT_ADDR[2]	0000	CGSTOP_DEFAULT	1000	CGSTOP_HTRHS	Strap1
01	3G0_PNDCFG_LUT_ADDR[1]	0001	MOBILE_DEFAULT	1001	MOBILE_HTRHS_XNAMP	
01	3G0_PNDCFG_LUT_ADDR[0]	0010	MOBILE_HTRHS_LAMP	1010	MOBILE_HTRHS_LAMP	
01	3G0_PNDCFG_LUT_ADDR[3]	0011	MOBILE_HTRHS_LAMP	1011	MOBILE_HTRHS_LAMP	
01	3G0_PNDCFG_LUT_ADDR[4]	0100	MOBILE_HTRHS_XNAMP	1100	MOBILE_HTRHS_XNAMP	
01	3G0_PNDCFG_LUT_ADDR[5]	0101	MOBILE_HTRHS_XNAMP	1101	MOBILE_HTRHS_XNAMP	
01	3G0_PNDCFG_LUT_ADDR[6]	0110	MOBILE_HTRHS_XNAMP	1110	MOBILE_HTRHS_XNAMP	
01	3G0_PNDCFG_LUT_ADDR[7]	0111	MOBILE_HTRHS_XNAMP	1111	MOBILE_HTRHS_XNAMP	
00	PCI_DEV[0]	SW0	0010			Strap2
01	PCI_DEV[0]					
01	PCI_DEV[1]					
01	PCI_DEV[0]	SW1	780			
00	S0R0_EXPOSED	0	DISABLED	1	ENABLED	Strap3
00	S0R0_EXPOSED	0	DISABLED	1	ENABLED	
01	S0R1_EXPOSED	0	DISABLED	1	ENABLED	
00	S0R0_EXPOSED	0	DISABLED	1	ENABLED	
02	PCR_SPEED_CHANGE_GEN0	0	DISABLED	1	ENABLED	Strap4
01	PCR_MAX_SPEED	0	GEN0	1	GEN23	
01	DP_APL_VDD_SVS	0	USE_V8	1	USE_V9	
01	DP_APL_VDD_SVS	0	USE_V8	1	USE_V9	






MEC25
H₂ NR 16201/210
COMMON
MEC25 

MEC26
H₂ NR 16201/210
COMMON
MEC26 

MEC27
H₂ NR 16201/210
COMMON
MEC27 

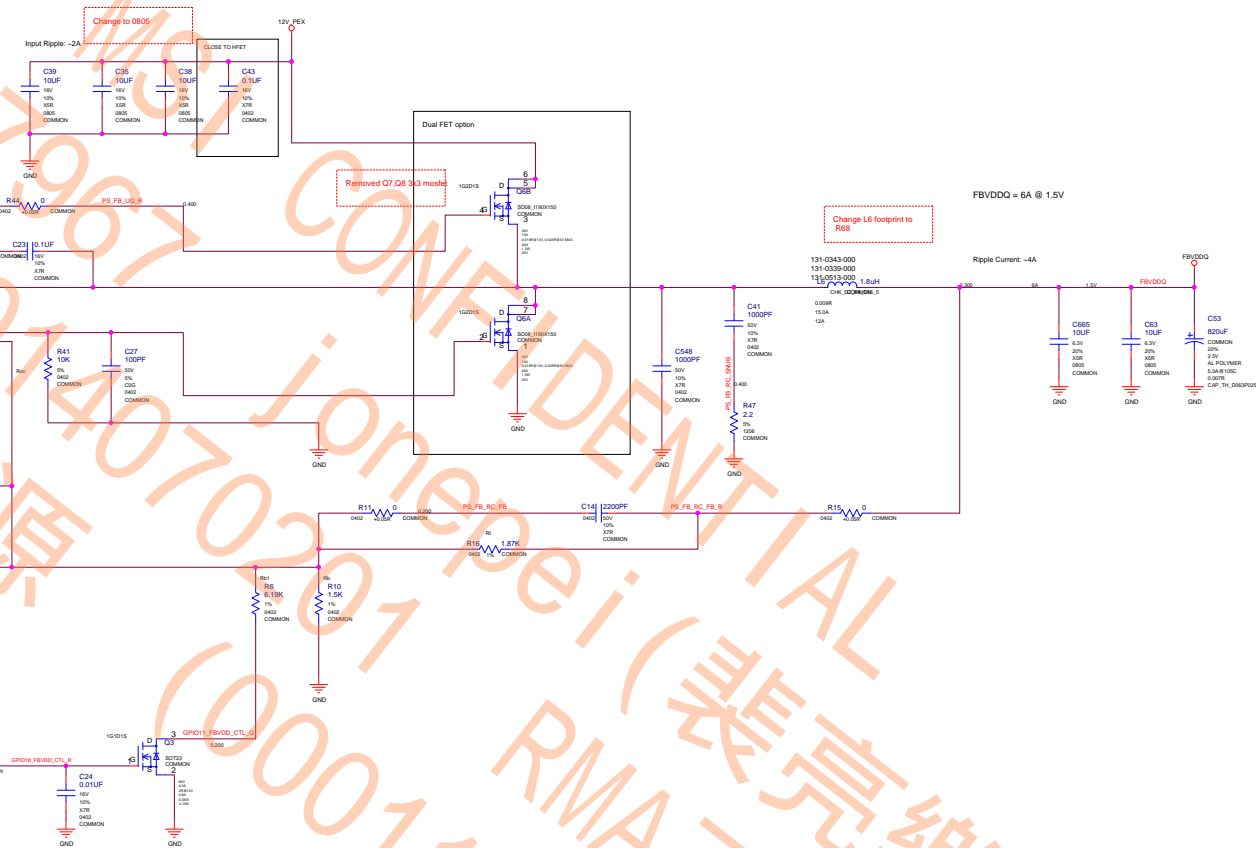
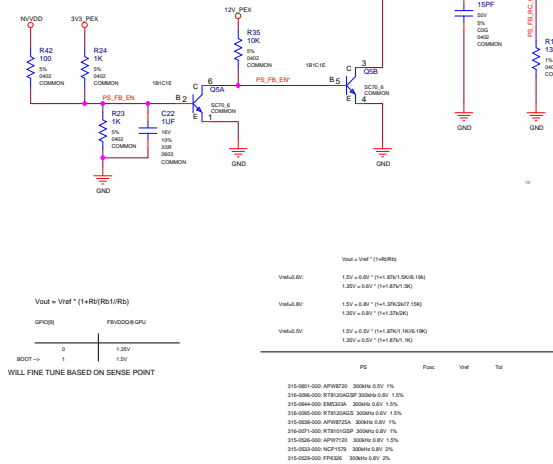
MEC28
H₂ NR 16201/210
COMMON
MEC28 


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	MS-V294		
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