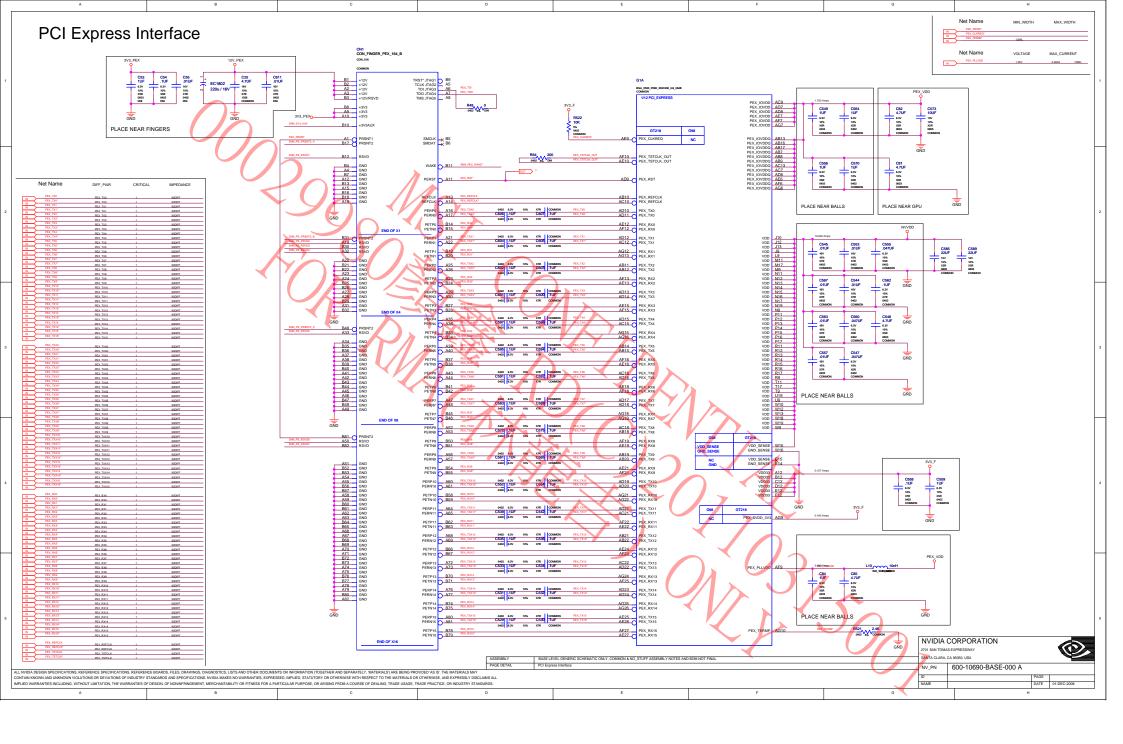
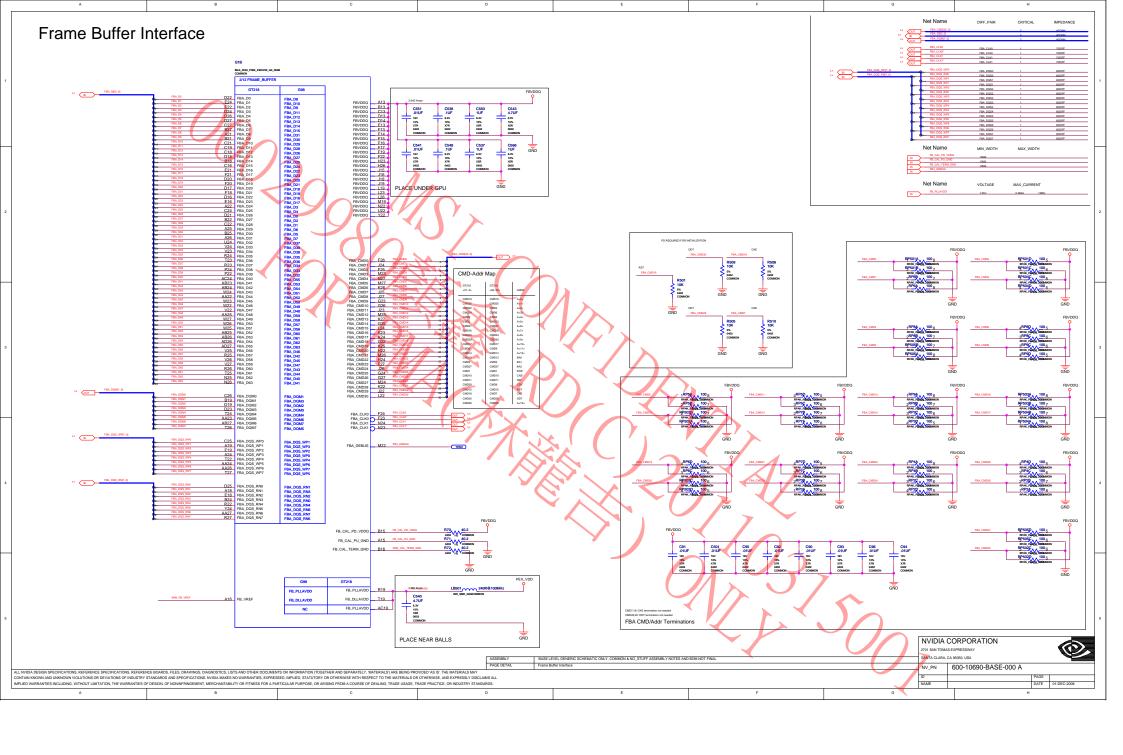
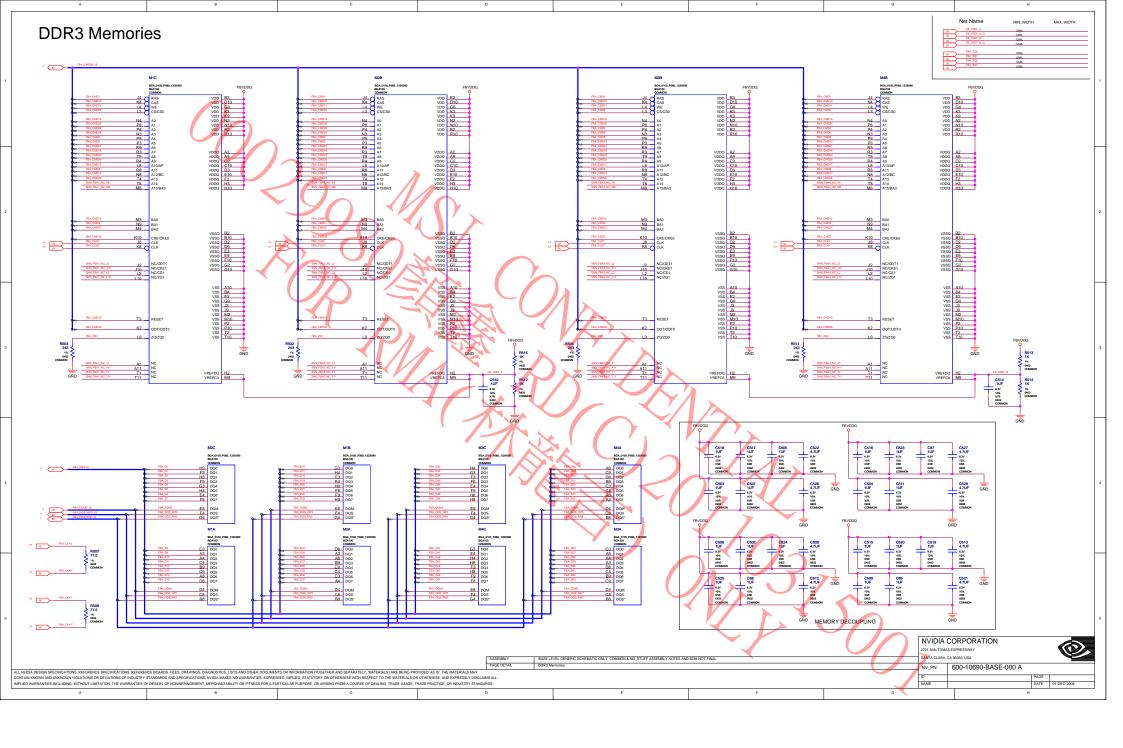
P690: GT218, DDR3 MEMORY 64MX16/32MX16 V183 2.0 pcb change list Page 1: P690 Overview Page 2: Add EC1802 for 12V_PEX use, G1.E15 pin NC Page 2: PCI Express Interface Page 3: Add R72 Page 3: Frame Buffer Interface Page 5: ESD diose move to close connector side Page 4: DDR3 Memories Page 7: Add EMI suggestion Page 5: DAC A Slim VGA Page 8: Add HDMI function Page 6: DAC B VGA Header Page 8 ; G1.H6 pin connector to GND Page 7: TMDS Interface Page 9 G1 P6 D7 pin connector to GND Page 8: DisplayPort Connector Page 10 : Del JTAG · I2C SCH, U503 pin 3 connector to ROM_VCC Page 9: IFPC, IFPE Interface, Fan, Mechanical Page 12 : Change FBVDDQ PWM sch , Add C99 for 3V3_PEX Page 10: XTAL, ROM, SPDIF, JTAG Page 12 : Change PEX_VDD . 5V Page 11: Thermal Protection, IFP_IOVDD, Straps Page 13: Change NVVDD PWM sch Page 12: Power Supply I: FBVDD/Q, PEX_VDD, 5V, 3V3_F Page 13 Del PEX_PLL sch Page 13: Power Supply II: PLLVDD, NVVDD GT218-300, 550/1375/800, 512MB/64bit, 64Mx16 DDR3, DVI-DL+DP+VGA, D1 NVIDIA CORPORATION NTA CLARA, CA 95050, USA

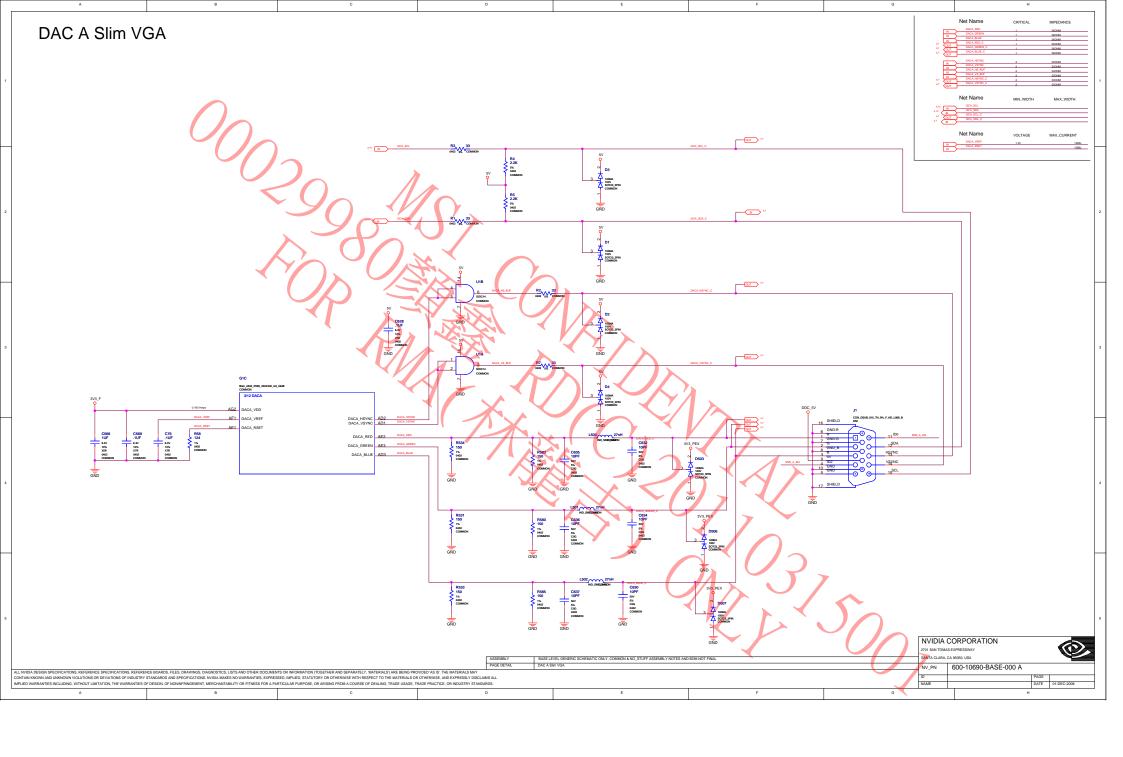
ALL LYMEA SEGN SPECIPICATIONS, EFFERENCE SPECIFICATIONS, EFFERENCE BOARDES, FLES, DAMINISS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS ON INFORMATION, (TOGETHER AND SEPARATELY, MATERIALS) ARE BEIND PROVIDED AS IS THE MATERIALS MAY CONTRIBUTED AND THE DATE OF THE MATERIALS OF THE MATERIALS AND THE DATE OF THE MATERIALS OF THE MATERI

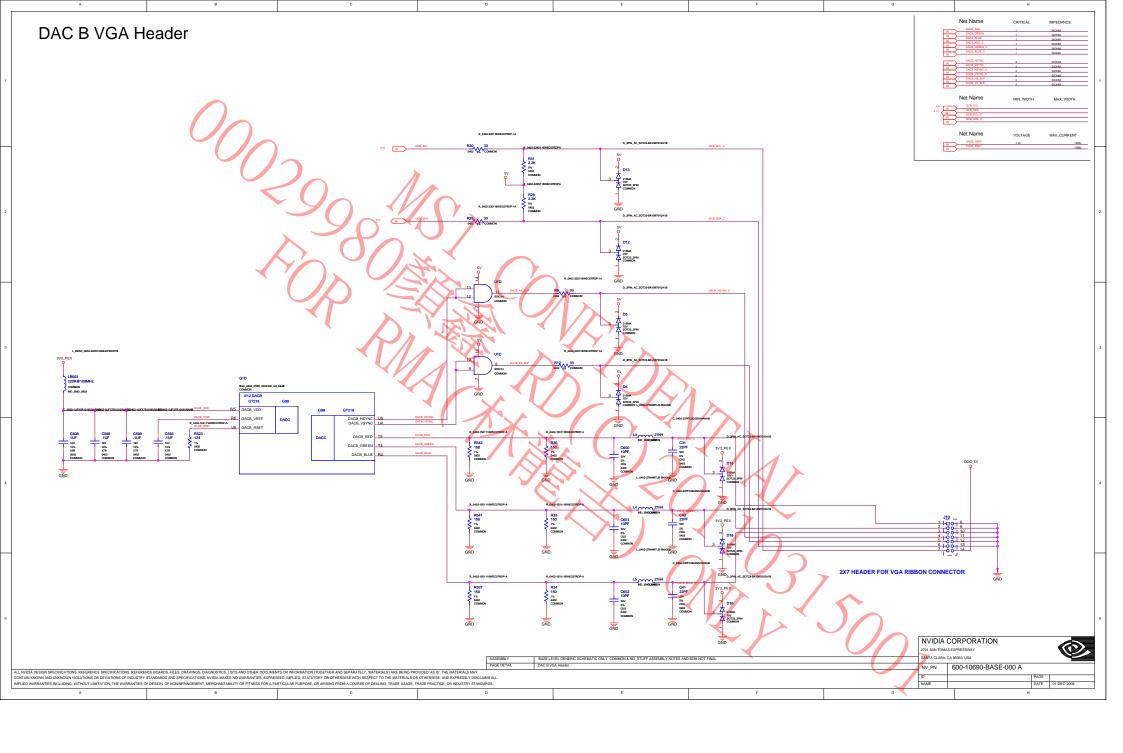
600-10690-BASE-000 A

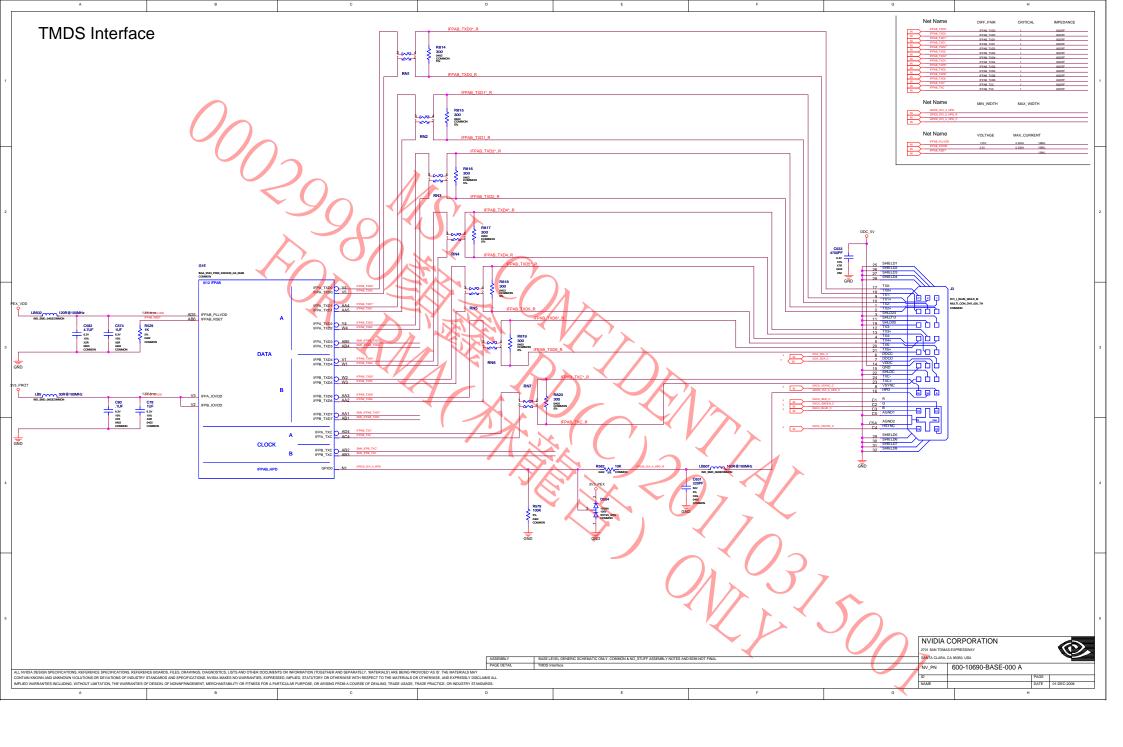


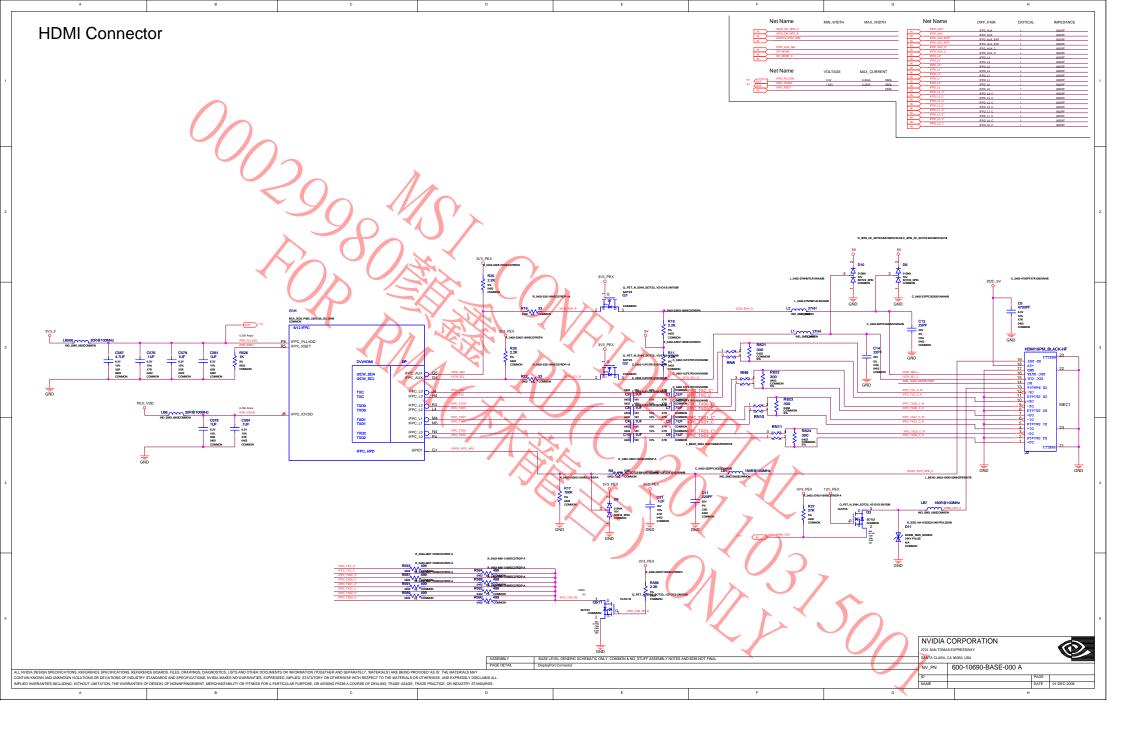


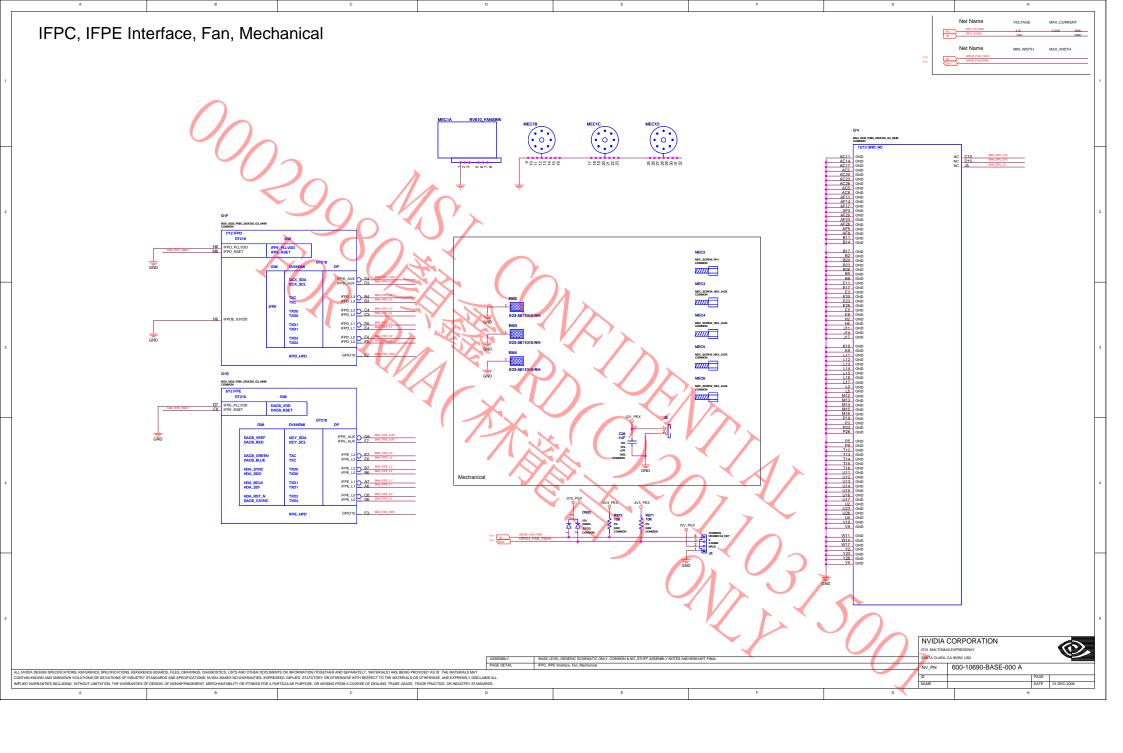


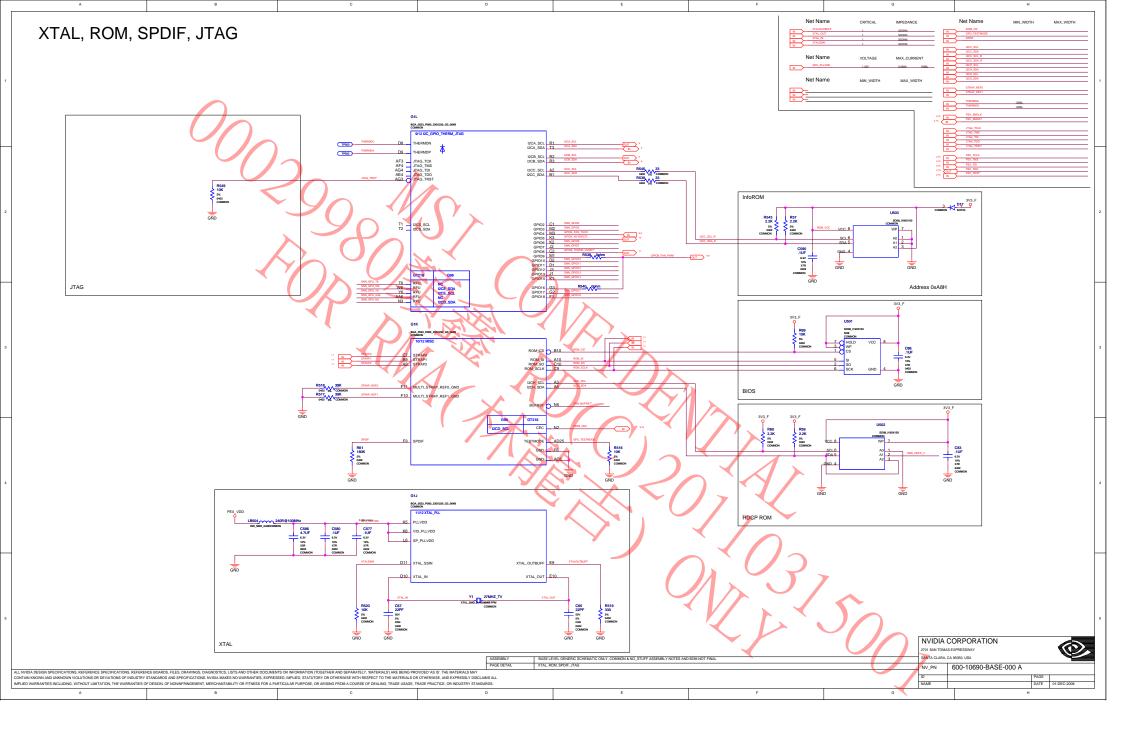


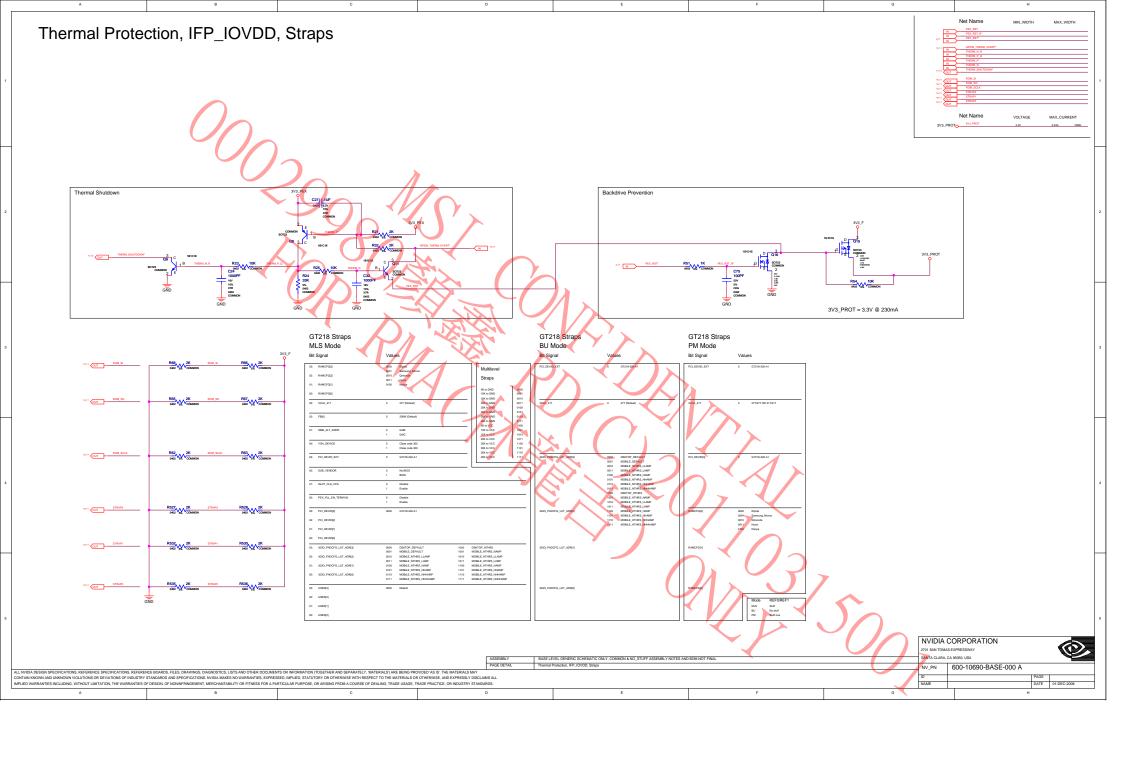


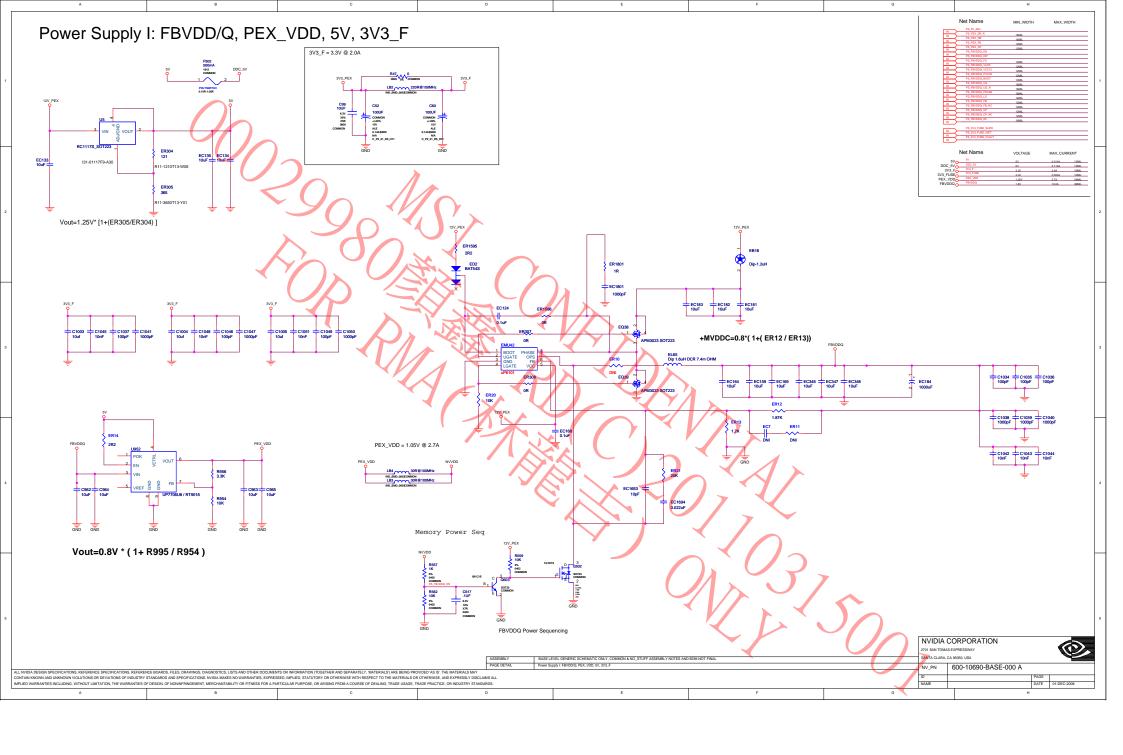


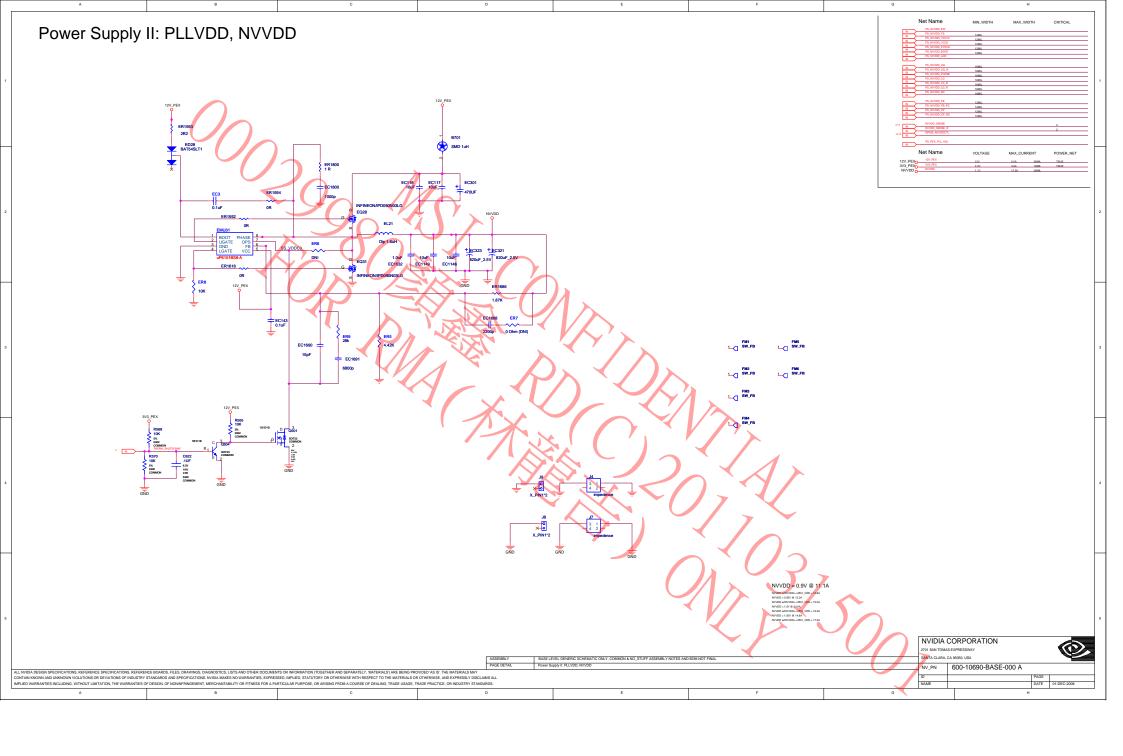












Title: Basenet Report	FBA_CMD<28> 3.9C 3.4H 4.2A 4.2C	FBA_DQS_WP-4> 3.1G 3.4B 4.4D	NVDD 13.2G	PEX_TXX3* 2.3A<2.3D	SNN_FBA2_NC_A11 4.3C	STRAP2 10.3Cc 11.1Gc 11.4Ac
Design: design Date: Dec 1 21:48:15 2008	4.2E 4.2F FBA_CMD<27> 3.3C 3.4H 4.2A 4.2C	FBA_DQS_WPc5> 3.1G 3.4B 4.5D FBA_DQS_WPc6> 3.1G 3.4B 4.4E	NVVDD_SENSE 2.4G> 13.1G< 13.4G< NVVDD_SENSE_R 13.1G< 13.4F	PEX_TXX4	SNN_FBA2_NC_J2 4.2C SNN_FBA2_NC_J10 4.2C	11.48 STRAP_REF0 10.1G<10.3C
	4.2E 4.2F	FBA_DQS_WP<7> 3.1G 3.4B 4.5E	PEX_CLKREQ* 2.1E 2.1G c	PEX_TXX5 2:3A: 2:3D	SNN_FBA2_NC_L2 42C	STRAP_REF1 10.1G< 10.3C
Base nets and synonyms for design_lib.design(sch	FBA_CMD<28> 3.9C 3.9E 4.9E 4.3F FBA_CMD<28> 3.9C 3.4H 4.1A 4.1C	FBA_Z00 4.1G< 4.3A FBA_Z01 4.1G< 4.3C	PEX_PLL 13.2G PEX_PLLVDD 2.1G<2.5F	PEX_TXX5* 2:3A-2:3D PEX_TXX6 2:3A-2:3D	SNN_FBA2_NC_L10 42C SNN_FBA2_NC_M8 42C	THERMDA 10.1G<10.2C THERMDC 10.1C 10.1G<
(i) design_sc.unv(@design_sc.design;scn	FBA_CMD<29> 3.9C 3.4H 4.1A 4.1C FBA_CMD<30> 3.2E 3.9C 4.3A 4.9C	FBA_ZQ1 4.1G<4.3C FBA_ZQ2 4.1G<4.3E	PEX_PREVIOD 2.1G<2.5F PEX_PRSNT 2.1C.2.1G<	PEX_TXX6 2:3A<2:3D PEX_TXX6* 2:3A<2:3D	SNN_FBA2_NC_M8 42C SNN_FBA2_NC_T1 4.3C	THERMOC 10.1C 10.1G- THERM_N 11.1G-11.2C
Signal Location([Zona](dirl)	FBA_D<0> 3.18 4.48	FBA_ZQ3 4.1G<4.3F	PEX_REFCLK 2:2D 2:5Ac	PEX_TXX7 2.3A<2.3D	SNN_FBA2_NC_T8 4.2C	THERM_N_R 11.1G<11.2B
12.2H	FBA_De83.05 3.1Ac> 3.1Gc> 4.4Ac> FBA_De1> 3.1B 4.4B	FBVDDQ 12:2H FB_CAL_PD_VDDQ 3:2G<3:4C	PEX_REFCLK* 2.2D 2.5A< PEX_RST 11.1G< 11.3C	PEX_TXX7* 2:3D 2:4A< PEX_TXX8 2:4A <c 2:4d<="" td=""><td>SNN_FBA2_NC_T11 4:3C SNN_FBA3_NC_A1 4:3E</td><td>THERM_P* 11.1G<11.2C THERM_P_Q 11.1G<11.2B</td></c>	SNN_FBA2_NC_T11 4:3C SNN_FBA3_NC_A1 4:3E	THERM_P* 11.1G<11.2C THERM_P_Q 11.1G<11.2B
ISE 12.2H	FBA_D<2> 3.18 4.48	FB_CAL_PU_GND 32Gc 3.4C	PEX_RST* 2.2D> 11.1Gc 11.2Ec	PEX_TXX8* 2.4A<2.4D	SNN_FBA3_NC_A11 4.3E	THERM_SHUTDOWN* 11.1G> 11.2A> 13.4A<
EX 13.2G	FBA_D<3> 3.18 4.48	FB_CAL_TERM_GND 3.2G<	PEX_RST_R* 11.1G<11.2F	PEX_TXX9 2.4Ac 2.4D	SNN_FBA3_NC_J2 4.2E	XTALOUTBUFF 10.1F< 10.5E
ROT 11.1H 12.2H	FBA_Doto 31B 4.4B FBA_Doto 3.1B 4.4B	FB_PLIAVDD 3.2G<3.5C FB_VREF_A 4.1G<4.3D	PEX_RX0 2.2D 2.4Ac PEX_RX0* 2.2D 2.4Ac	PEX_TXX9" 2.4A<2.4D PEX_TXX10 2.4A<2.4D	SNN_FBA3_NC_110 4.2E SNN_FBA3_NC_12 4.2E	XTALSSIN 10.1F< 10.5C XTAL_IN 10.1F< 10.5C
12.2H EX 13.2G	FBA_D-db 3.18 4.48	FB_VREF_A_Q 4.1G<	PEX_RX1 2.2D 2.4A<	PEX_TXX10° 2.4Ac 2.4D	SNN_FBA3_NC_L10 42E	XTAL_OUT 10.1F<10.5D
SLUE 5.1G<5.4C SLUE_C 5.1G>5.4F>7.3F<	FBA_D<7> 3.18 4.48 FBA_D<8> 3.18 4.48	FB_VREF_B 4.1Q< 4.3H FB_VREF_B_Q 4.1Q<	PEX_RX1* 2.2D 2.4A< PEX_RX2 2.2D 2.4A<	PEX_TXX11	SNN_FB43_NC_M8 4.2E SNN_FB43_NC_T1 4.3E	
REEN 5.1G<5.4C	FBA_Dob 3:18 4:48	FB_VREF_B_Q 4.1G< GPIO0_DVI_A_HPD 7.1G< 7.4D	PEX_RX2 2.3D.2.4A<	PEX_TXX11* 2.4A<2.4D PEX_TXX12 2.4A<2.4D	SNN_FBA3_NC_T1 4.3E SNN_FBA3_NC_T8 4.2E	
REEN_C 5.1G> 5.4F> 7.3F<	FBA_D<10> 3.1B 4.5B	GPI00_DVI_A_HPD_C 7.1G< 7.3F	PEX_RX3 2.3D 2.4A<	PEX_TXX12* 2.4A<2.4D	SNN_FBA3_NC_T11 4:3E	
SYNC 5.1G<5.4C	FBA_D<11> 328 458 FBA_D<12> 328 458	GPIO0_DVI_A_HPD_R 7.1G< 7.4E GPIO4_FAN_TACH _ 9.1G< 9.4D> 10.2E<	PEX_RX3* 2.3D 2.4Ac PEX_RX4 2.3D 2.4Ac	PEX_TXX13 2.4A<2.5D PEX_TXX13* 2.4A<2.5D	SNN_FBA4_NC_A1 4.3F SNN_FBA4_NC_A11 4.3F	
8_BUF 5.1G<5.3D	FBA_D<13> 3.28 4.58	GPIO5_NVVDDCTL 10:2E> 13:1G< 13:5D<	PEX_RX4* 2.3D.2.4A<	PEX_TXX14	SNN_FBA4_NC_J2 4.2F	
ED 5.1G< 5.4C	FBA_D<14> 328 4.58	GPIOS_NVVDDCTL_R 13.4E	PEX_RX5 2.3D 2.4A<	PEX_TXX14* 2.4A<2.5D	SNN_FBA4_NC_J10 4.2F	
NED_C 5.1G> 5.4F> 7.3F< NSET 5.2G< 5.4B	FBA_D<15> 3.28.4.5B FBA_D<16> 3.28.4.4C	GPIO8_THERM_OVERT* 10.2E> 11.1G< 11.2D< GPIO9_FAN_PWM 9.1G> 9.4D< 10.2E>	PEX_RX5" 2.3D 2.4Ac PEX_RX6 2.3D 2.5Ac	PEX_TXX15 2.4A<2.5D PEX_TXX15* 2.4A<2.5D	SNN_FBA4_NC_L2	
REF 5.2Gc 5.4B	FBA_D<17> 3.28 4.4C	GPIO19_IFPD_HPD 8.1F<8.4D	PEX_RX6* 2.3D.2.5A<	PEX_VDD 12.2H	SNN_FBA4_NC_M8 4.2F	
SYNC 5.10<5.4C	FBA_D<18> 3.28 4.4C	GPIO_DP_HPD_C 8.1F<8.4F	PEX_RX7 2.3D 2.5A<	PS_3V3_FUSE_FAULT 12.10<-12.3F	SNN_FBA4_NC_T1 4.3F SNN_FBA4_NC_T8 4.2F	
PYNC_C 5.10×5.3F×7.3F< 8_BUF 5.16×5.3D	FBA_D<19> 3.28 4.4C FBA_D<20> 3.28 4.4C	GPIQ_GP_HPQ_R 8.1F< 8.4E GPU_PLLVDD 10.1F< 10.4C	PEX_RX7* 2.4D 2.5A < PEX_RX8 2.4D 2.5A <	PS_3V3_FUSE_ISET 12:10<:12:3F PS_3V3_FUSE_SLEW 12:10<:12:3F	SNN_FBA4_NC_TS 4.2F SNN_FBA4_NC_T11 4.3F	
.UE 6.1G< 6.4C	FBA_D<21> 328 4.4C	GPU_TESTMODE 10.1G< 10.4E	PEX_RX8* 2.4D 2.5A <	PS_5V_ADJ 12.1G< 12.28	SNN_FB_VREF 3.5B	
UE_C 6.1G<6.5E REEN 6.1G<6.4C	FBA_D<22> 3.28 4.4C FBA_D<23> 3.28 4.4C	12CA_SCL	PEX_RX9 2.4D 2.5Ac PEX_RX9* 2.4D 2.5Ac	PS_FBVDDQ_BOOT 12.1G< 12.3E PS_FBVDDQ_CP 12.1G< 12.4E	SNN_GPIO2 10.2E SNN_GPIO3 10.2E	
REEN_C 8.1G< 8.4E	FBA_D<24> 32844C	12CA SDA 5.1G-5.2C-0.10.2E-0	PEX_RX10 24D25A<	PS_FBVDDQ_CP_RC 12.10<12.4E	SNN_GPI06 10.2E	
SYNC 6.1G<6.4C	FBA_D-d5> 328.45C	12CA_SDA_C 5.10 5.2F 5.7.3F 5	PEX_RX10* 2.4D 2.5Ac	PS_FBVDDQ_EN 12.1G< 12.9C	SNN_GPI07 10.2E	
YNC_C 6.1G<6.3E BUF 6.1G<6.3D	FBA_D-28> 3.28 4.5C FBA_D-27> 3.28 4.5C	12CB_SCL	PEX_RX11 2.4D.2.5Ac PEX_RX11* 2.4D.2.5Ac	PS_FBVDDQ_EN* 12.1G<12.5C PS_FBVDDQ_FB 12.1G<12.4E	SNN_GPI010 10.2E SNN_GPI011 10.2E	
ED 6.1G< 6.4C	FBA_D<28> 3.28 4.5C	12CB_SDA 6.1G→62C→102E→	PEX_RX12 2.4D 2.5Ac	PS_FBVDDQ_FB_RC 12.1G<12.4G	SNN_GPI012 10.2E	
ED_C 6.1G-6.4E	FBA_D<29> 328 45C	12CB_SDA_C	PEX_RX12* 2.54x2.50	PS_FBVDDQ_FS 12.1G< 12.4D	SNN_GPI013 10.2E	
ET 6.2G< 6.4B :EF 6.2G< 6.4B	FBA_D<30> 3.28.4.5C FBA_D<31> 3.28.4.5C	2CC_SCL 10.1Gc 10.2E 12CC_SCL_R 10.1Gc 10.2E	PEX_RX13	PS_FBVDDQ_LG 12.1G<12.4E PS_FBVDDQ_PHASE 12.1G<12.4E	SNN_GPI014 10.2E SNN_GPI017 10.3E	
YNC 6.1G< 6.4C	FBA_D<32> 3.28 4.4D	12CC_80A 10.1Gc 10.2E	PEX_RX14 25A<25D	PS_FBVDDQ_PVCC5 12:1G<12:3E	SNN_GPIO18 10.3E	
3YNC_C 6.1G< 6.3E 3_BUF 6.1G< 6.3D	FBA_D<33> 3.28 4.4D FBA_D<34> 3.28 4.4D	12CC_5DA_R 10.1Gc 10.2F 2CH_SCL 10.1Gc 10.3E	PEX_RX14* 2.5Ac.2.5D PEX_RX15 2.5Ac.2.5D	PS_FBVDDQ_RC 12.1G<12.4G PS_FBVDDQ_UG 12.1G<12.4E	SNN_GPU_A46 10.3C SNN_GPU_C15 9.2H	
12.2H	FBA_D<35> 3.28 4.4D	120H, SDA 10.1Gc 10.3E	PEX_RX15* 2.5Ax:2.5D	PS_FBVDDQ_UG_R 12:1G< 12:3F	SNN_GPU_D15 9.2H	
E* 8.1F<8.2F	FBA_D<38> 3.28 4.4D	12CS_SCL 10.1Gc 10.2C	PEX SMCLK 2.1D> 10.1Ge 10.38<	PS_FBVDDQ_VCC5 12.1Gc 12.3D	SNN_GPU_16 9:2H	
E_C 8.1Fc 8.3G 0 3.1G> 3.4D> 4.2Ac	FBA_D<37> 3.28 4.4D FBA_D<38> 3.28 4.4D	12CS_SDA 10.1G<10.2C IFPAB IOVDD 7.2G<7.3C	PEX_SMDAT 2.2D ⇔ 10.1G ⇔	PS_FBVDDQ_VCC12 12.1G<12.3E PS_NVVDD_BOOT 13.1G<13.3C	SNN_GPU_NS 10.3C SNN_GPU_T6 10.3C	
4.28< 4.4Ac	FBA_D<30> 3.28 4.4D	IFPAB_PLLVDD 7:2G<7:3C	PEX_TOLK 2.1D> 10.2Ac 10.2Gc	PS_NV/DD_CP 13.1G< 13.3C	SNN_GPU_W6 10.9C	
" 3.1G> 3.4D> 4.2Ac	FBA_D<40> 328 4.4D	IFPAB_RSET 72G<73C	PEX_TD) 2.10> 10.2A< 10.2G<	PS_NVVDD_CP_RC 13.1G<13.4D	SNN_GPU_Y8 10.9C	
428< 458< 3.1G> 3.4D> 4.2D<	FBA_D<41> 3.38 4.5D FBA_D<42> 3.38 4.5D	IFPAB_TXC 7.1G<7.4D IFPAB_TXC* 7.1G<7.4D	PEX_TDQ 2.1Dc.10.2As 10.2Qs PEX_TERMP 2.1Dc.2.5F	PS_NVVDD_EN* 13.1G< 13.4B PS_NVVDD_FB 13.1G< 13.9C	SNN_HDCP_2 10.4G SNN_IFPAB_TXD3 7.3D	
4.2F< 4.5A<	FBA_D<43> 3.38 4.5D	IFPAB_TXD0 7.1Gc 7.30	PEX_TMS 2.1D> 10.2Ac 10.2Gc	PS_NVVDD_FB_RC 13.1G<13.4F	SNN_IFPAB_TXD3* 7.3D	
1* 3.1G> 3.4D> 4.2Dc 4.2Fc 4.5Ac	FBA_Do44> 3.38.4.5D FBA_Do45> 3.38.4.5D	IFPAB_TXD0* 7.1Gc7.3D IFPAB_TXD1 7.1Gc7.3D	PEX_TRS1* 2.10> 10.20< 10.20 PEX_TSTCLK 2.5A<	PS_NV/DD_ES 13.1G< 13.9C PS_NV/DD_LDO 13.1G< 13.9C	SNN_IFPAB_TXD7 7.4D SNN_IFPAB_TXD7* 7.3D	
D-0> 32C 32G 4.1A 4.1C	FBA_D+46> 3.38 4.5D	IFPA8_TXD1* 7.1G< 7.3D	PEX_TSTCLK* 2.5A<	PS_WVDD_LG 13.1G< 13.3C	SNN_IFPB_TXC 7.4D	
D<30.0> 3.1G>3.2D>4.1A<>	FBA_D+47> 3.38 4.5D	IFPAB_TXD2 7.1G<7.3D	PEX_TSTCLK_OUT 2.2E	PS_NV\DD_LG_D 13.1G< 13.30	SNN_IFPB_TXC* 7.4D	
4.1E 4.1F	FBA_D-48> 3.38 4.4E FBA_D-49> 3.38 4.4E	IFPAB_TXD2* 7.1Gc 7.3D IFPAB_TXD4 7.1Gc 7.3D	PEX_TSTCLK_OUT* 2.2E PEX_TX0	PS_NV/DD_LG_R 13.1Gc13.4E PS_NV/DD_PHASE 13.1Gc13.9C	SNN_IFPC_AUX 9.9C SNN_IFPC_AUX 9.2C	
ID<2> 32C 32H 4.1A 4.1C	FBA_D<50> 3.38 4.4E	IFPAB_TXD4* 7.1G<7.3D	PEX_TX0* 2.2A<2.2E	PS_NV/DD_PVCC5	SNN_IFPC_HPD 9.3C	
ID-3> 32C 32H 42A 42C	FBA_D<51> 338 4.4E	IFPAB_TXD5 7.1G<7.3D	PEX.TX1 22A-c22E	PS_NV/0D_RC 13.1G< 13.4F	SNN_IFPC_LO 9.9C	
4.2E 4.2F IDolo 3.2C 3.3G 4.1E 4.1F	FBA_D<52> 3.38 4.4E FBA_D<53> 3.38 4.4E	IFPAB_TXD8* 7.1G<7.3D IFPAB_TXD8 7.1G<7.3D	PEX_TXs: 2.2A<2.2E PEX_TX2 2.2A<2.2E	PS_NVVDD_UG_13.1G<13.9C PS_NVVDD_UG_R 13.1G<13.3E	SNN_IFPC_L0* 9.3C SNN_IFPC_L1 9.3C	
D-5> 3.3C 3.3G 4.1E 4.1F	FBA_D<54> 3.38 4.4E	IFPAB_TXD6* 7.1G<7.3D	PEX_TX2* 2:2A<2:2E	PS_NVVDD_VCC5 13.1G 13.3C	SNN_IFPC_L1* 9.3C	
Dob 33C 33H 4.1E 4.1F Dc/> 33C 33F 4.2E 4.2F	FBA_D<55> 3.38 4.4E FBA_D<56> 3.38 4.4E	IFPC_IOVDD 9.1G<9.38 IFPC_PLLVDD 9.1G<9.28	PEX_TX3	PS_NVVDD_VCC12 13.1Gc13.3C PS_PEX_CP 12.1Gc12.4C	SNN_IFPC_L2 9.9C SNN_IFPC_L2* 9.9C	
D-85 3.3C 3.3H 4.1E 4.1F	FBA_D<57> 3.38 4.5E	IFPD_AUX 8.1G< 8.4D	PEX TX4 2:24x 2:5	PS_PEX_DR 12.1G< 12.3D	SNN_IFPC_L3 9.9C	
Delb 33C 33E 4.2A 4.2C	FBA_D<58> 3.38 4.5E	IFPD_AUX* 8.1G< 8.4D	PEX_TX4* 2.24< 2.5E	PS_PEX_DR_R 12.1G< 12.38	SNN_IFPC_L3* 9.3C	
4.2E 4.2F 0<10> 3.3C 3.3E 4.1A 4.1C	FBA_D<50> 3.38.4.5E FBA_D<60> 3.38.4.5E	IFPD_AUX_BYP 8.1G<8.3D IFPD_AUX_BYP* 8.1G<8.2D	PEX_TXS	PS_PEX_PB 12:10<12:40 PS_PEX_PLL_ADJ 13:1E 13:20<	SNN_IFPC_RSET 9.2B SNN_IFPE_AUX 9.4C	
4.1E 4.1F	FBA_D<61> 3.38 4.5E	IFPD_AUX_C 8.1G< 8.4F	PEX_TX8 2.2A< 2.3E	ROM_CS* 10.1G< 10.3E	SNN_IFPE_AUX* 9.4C	
Ne11> 33C 33F 4.1A 4.1C	FBA_D-62> 3.38 4.5E	IFPD_AUX_C* 8.1G< 8.4F	PEX_TX6* 2.2A< 2.3E	ROM_SCLK 10.3E<11.1G>11.4A>	SNN_IFPE_HPD 9.4C	
4.1E 4.1F <12> 3.3C 3.3F 4.2A 4.2C	FBA_D<83> 3.38 4.5E FBA_DEBUG 3.2G<3.4C	IFPD_AUX_SEL 8.1F<8.2D IFPD_IOVDD 8.1F>8.4C>9.3A<	PEX_TX7 2.2A<2.3E PEX_TX7* 2.2A<2.3E	11.4B ROM_SI 10.3E<11.1Q=11.3A>	SNN_IFPE_LO 9.4C SNN_IFPE_LO 9.4C	
4.2E.4.2F	FBA_DQM-0> 3.38 4.48	IFPD_L0 8.1G< 8.4D	PEX_TX8 2.2A<2.4E	11.38	SNN_IFPE_L1 9.4C	
<13> 3.3C 3.3G 4.1E 4.1F <14> 3.3C 3.3G 4.2A 4.2C	FBA_DQM<7.65 3.10>3.34>4.44 FBA_DQM<1> 3.38 4.58	IFPD_L0" 8.1G< 8.4D IFPD_L0_C 8.1G< 8.4F	PEX_TX8* 2.2A<2.4E PEX_TX9 2.3A<2.4E	ROM_SO 10.3Ec 11.1Go 11.3As 11.3B	SNN_IFFE_L1* 9.4C SNN_IFFE_L2 9.4C	
4.2E 4.2F	FBA_DQM<2> 3.38 4.40	IFPD_LD_C* 8.1G< 8.4F	PEX_TX9 2.3A<2.4E PEX_TX9* 2.3A<2.4E	11.3B ROM_VCC 10.2F	SNN_FPE_L2 9.4C SNN_FPE_L2 9.4C	
:15> 3.2E 3.3C 4.3A 4.3C	FBA_DQM<3> 3.38 4.5C	IFPO_L1 8.1Gc 8.4D	PEX_TX10 2:3A::24E	SNN_SVS_AUX 2.1C	SNN_IFPE_L3 9.4C	
4.3E 4.3F <16> 3.3C 3.3H 4.2A 4.2C	FBA_DQM-4> 3.38 4.4D FBA_DQM-5> 3.48 4.5D	IFPD_L1* 8.1G< 8.4D IFPD_L1_C 8.1G< 8.4F	PEX_TX10* 2.3Ac.2.4E PEX_TX11 2.3Ac.2.4E	SNN_ADZ_7 12:3G SNN_AJD0 5:4G	SNN_IFPE_US* 9.4C SNN_IFPE_RSET 9.38	
4.2E 4.2F	FBA_DQM<6> 3.48 4.4E	IFPD_L1_C* 8.1G< 8.4F	PEX_TX11* 2.3A<2.4E	SNN_A_ID2 5.4F	SNN_PEX_WAKE* 2:20	
<17> 3.9C 3.3H 4.2A 4.2C	FBA_DQM<7> 3.4B 4.5E	IFPD_L2 8.1Gc 8.4D	PEX_TX12	SNN_BUFRST* 10.3E	SNN_PE_PRSNT2_A 2.1C	
4.2E 4.2F :18> 3.2F 3.3C 4.2A 4.2C	FBA_DQS_RN-0> 3.1G 3.4B 4.4B FBA_DQS_RN-7.0> 3.1G \ightarrow 3.4A \ightarrow 4.4A \ightarrow	IFPD_L2* 8.1G< 8.4D IFPD_L2_C 8.1G< 8.4F	PEX_TX12* 2.3Ac 2.4E PEX_TX13 2.3Ac 2.5E	SNN_CAL_TERM_GND_3.4C SNN_CEC 10.4E	SNN_PE_PRSNT2_B 22C SNN_PE_PRSNT2_C 23C	
<19> 3.3C 3.4E 4.1A 4.1C	FBA_DQS_RNc1> 3.1G 3.4B 4.5B	IFPD_L2_C* 8.1G< 8.4F	PEX_TX13* 2.3A< 2.5E	SNN_DP_CEC 8.4G	SNN_PE_RSVD1 2.2C	
4.1E 4.1F :20> 3.3C 3.4E 4.2A 4.2C	FBA_DQS_RN-2> 3.1G 3.4B 4.4C FBA_DQS_RN-3> 3.1G 3.4B 4.5C	IFPD_L3 8.1G<8.4D IFPD_L3* 8.1G<8.4D	PEX_TX14 2.3A<2.5E PEX_TX14* 2.3A<2.5E	SNN_FBA1_NC_A1 4.3A SNN_FBA1_NC_A11 4.3A	SNN_PE_RSVD2 2.2C SNN_PE_RSVD3 2.2C	
20> 33C 34E 42A 42C 42E 42F	FBA_DQS_RN<3> 3.1G 3.4B 4.5C FBA_DQS_RN<4> 3.1G 3.4B 4.4D	IFPO_L3" 8.1G< 8.4D IFPO_L3_C 8.1G< 8.4F	PEX_TX14" 2.3A<2.5E PEX_TX15 2.3A<2.5E	SNN_FBA1_NC_A11 4.3A SNN_FBA1_NC_J2 4.2A	SNN_PE_RSVD4 22C SNN_PE_RSVD4 22C	
21> 3.3C 3.4F 4.2A 4.2C	FBA_DQS_RN<5> 3.1G 3.4B 4.5D	IFPD_L3_C* 8.1G< 8.4F	PEX_TX15* 2:3A<2:5E	SNN_FBA1_NC_J10 42A	SNN_PE_RSVD6 2.3C	
4.2E 4.2F 225 3.3C 3.4F 4.1A 4.1C	FBA_DQS_RN-6> 3.1G 3.4B 4.4E FBA_DQS_RN-7> 3.1G 3.4B 4.5E	IFPD_PLLVDD 8.1F> 8.3C> 9.2A< IFPD_RSET 8.1F< 8.4B	PEX_TXXX 22D 2.3Ac PEX_TXXX 22D 2.3Ac	SNN_FBA1_NC_L2 4.2A SNN_FBA1_NC_L10 4.2A	SNN_PE_RSVD8 2.4C SNN_PE_RSVD7 2.4C	
23> 3.9C 3.4G 4.2A 4.2C	FBA_DQS_WP-0> 3.1G 3.4B 4.4B	JTAG_TCLK 10.1G< 10.2C	PEX_TXX1 2.2D.2.3A-c	SNN_FBA1_NC_M8 4:2A	SPDIF 10.1G< 10.4C	
4.2E 4.2F	FBA_DQ8_WP<7.0> 3.1G > 3.4A > 4.4A >	JTAG_TDI 10.1G< 10.2C	PEX_TXX1* 22D 2.3Ac PEX_TXX2 22D 2.3Ac	SNN_FBA1_NC_T1 4.3A	STRAPO 10.9C<11.1G>11.5A>	
1<26> 3.90 3.40 4.1A 4.10 1 2 1 2 1 2 1 2 1 2 1 2 1 2 <	FBA_DQS_WP<1> 3.1G 3.4B 4.5B FBA_DQS_WP<2> 3.1G 3.4B 4.4C	JTAG_TDO 10.2C 10.2G- JTAG_TMS 10.1G-10.2C	PEX_TXX2 2.2D.2.3Ac PEX_TXX2 2.2D.2.3Ac	SNN_FBA1_NC_T8 4.2A SNN_FBA1_NC_T11 4.3A	11.58 STRAP1 10.5Cc 11.1Go 11.4Ao	
4.1E 4.1F	FBA_DQS_WP<3> 3:1G 3:4B 4:5C	JTAG_TRST* 10.2C 10.2G<	PEX_TXX3 2:3A<2:3D	SNN_FBA2_NC_A1 4.9C	11.4B	
						I
						NVIDIA CORPORATION
						2701 SAN TOMAS EXPRESSWAY
			ASSEMBLY BASE LEVEL GENERIC SCHEMATIC ONLY, PAGE DETAIL <edit details<="" here="" insert="" page="" td="" to=""><td>COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL</td><td></td><td>NV_PN 600-10690-BASE-000 A</td></edit>	COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL		NV_PN 600-10690-BASE-000 A
IN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AN	LES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (D SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUT	ORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AN	MATERIALS MAY D EXPRESSLY DISCLAIMS ALL			ID PAGE
NAMES OF STREET OF STREET, STR	ONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, O	OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE,	OR INDUSTRY STANDARDS.			NAME <engineer> DATE 01-DEC-2008</engineer>
A	В	c	D	E F	G	н

Е

A

С

