

P2132-A01

NVG GK208 DDR3 64BIT

DVI-I/DVI-D + HDMI + VGA

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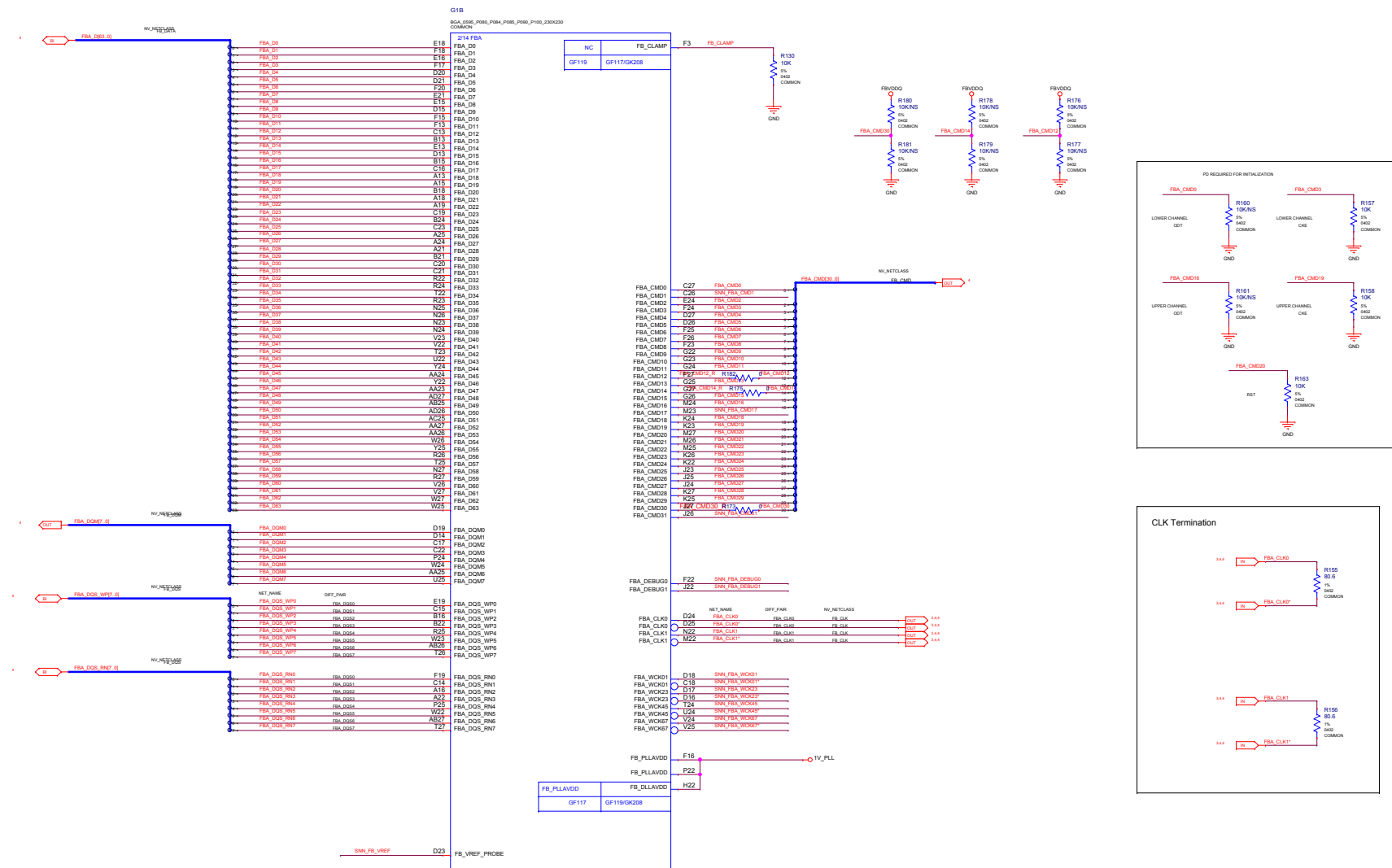
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## GPU FRAME BUFFER: PARTITION A



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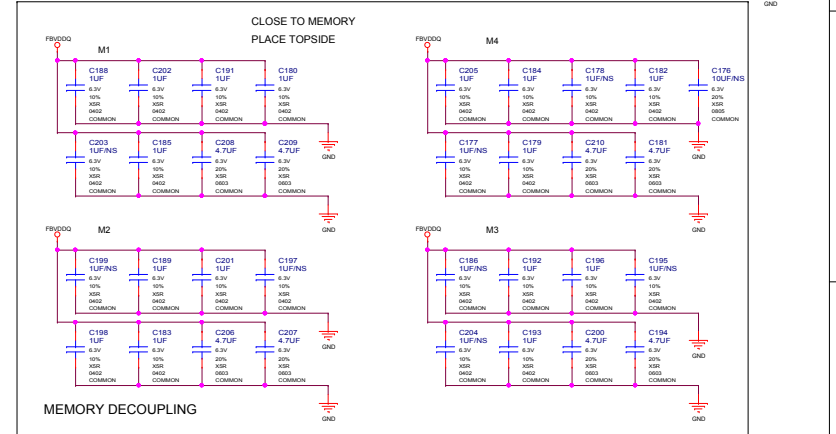
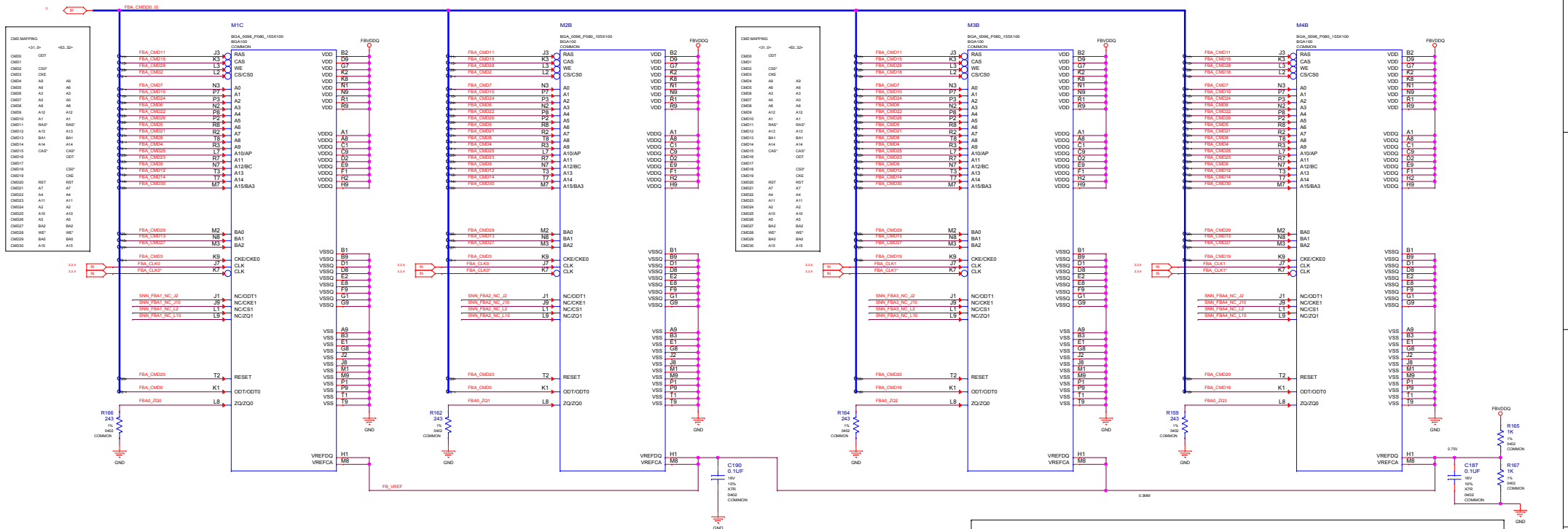
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## DDR3 Memories



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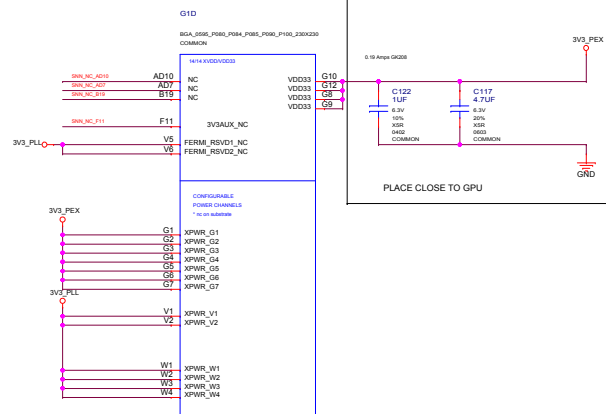
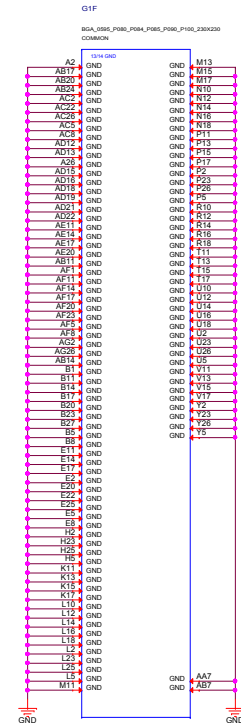
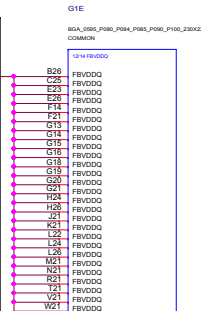
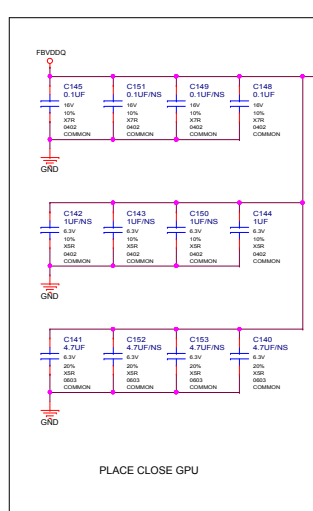
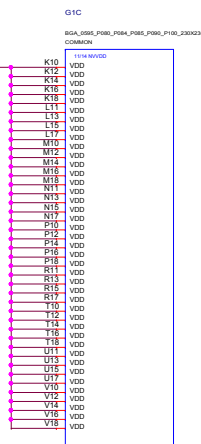
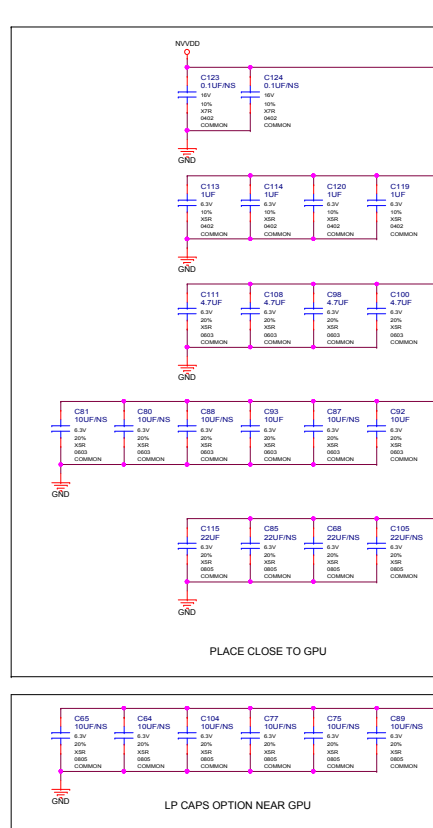
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## DECOUPLING FOR CORE POWER\_TOP



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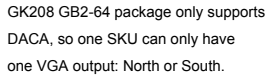



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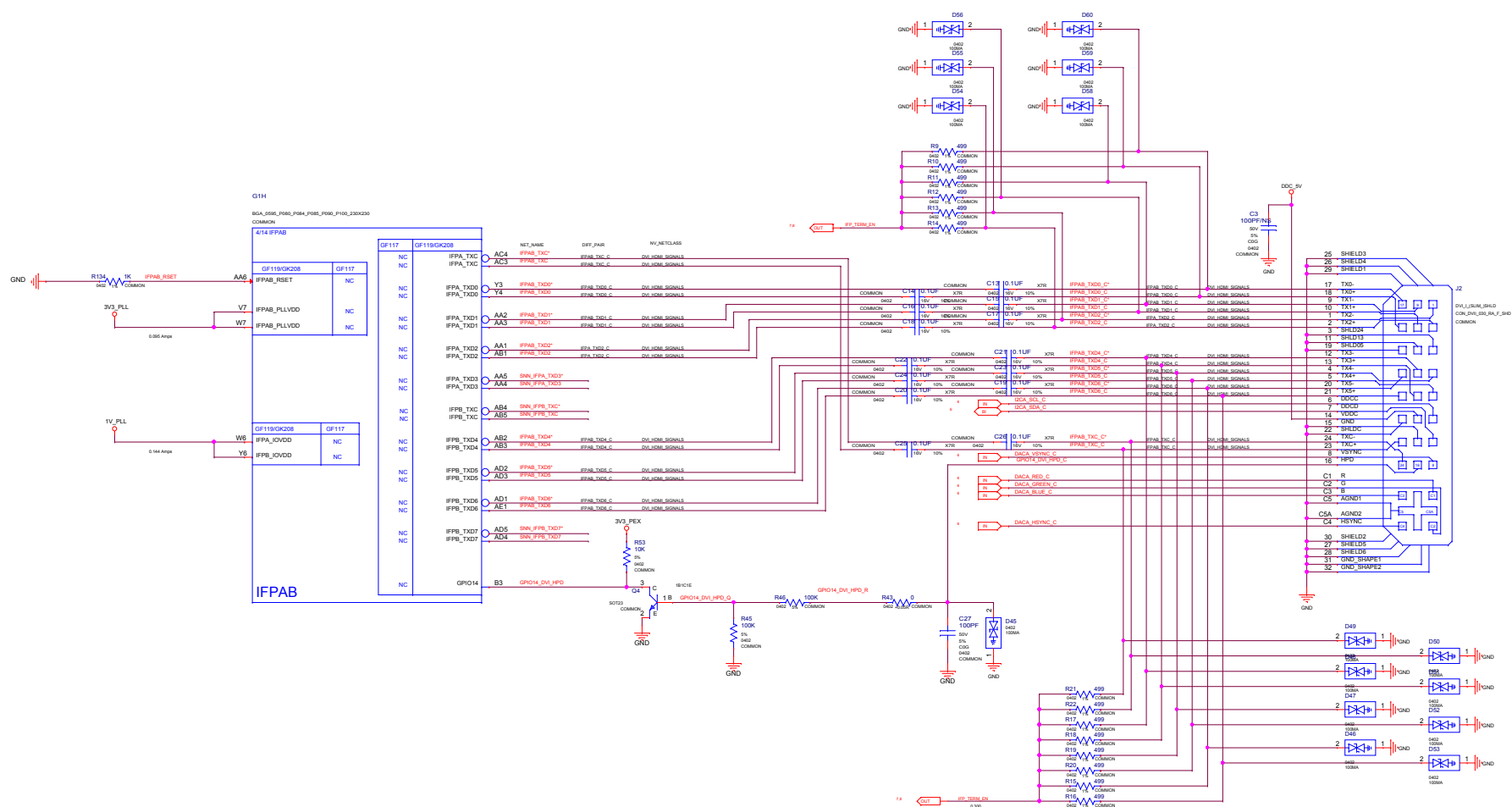
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## IFPAB TMDS



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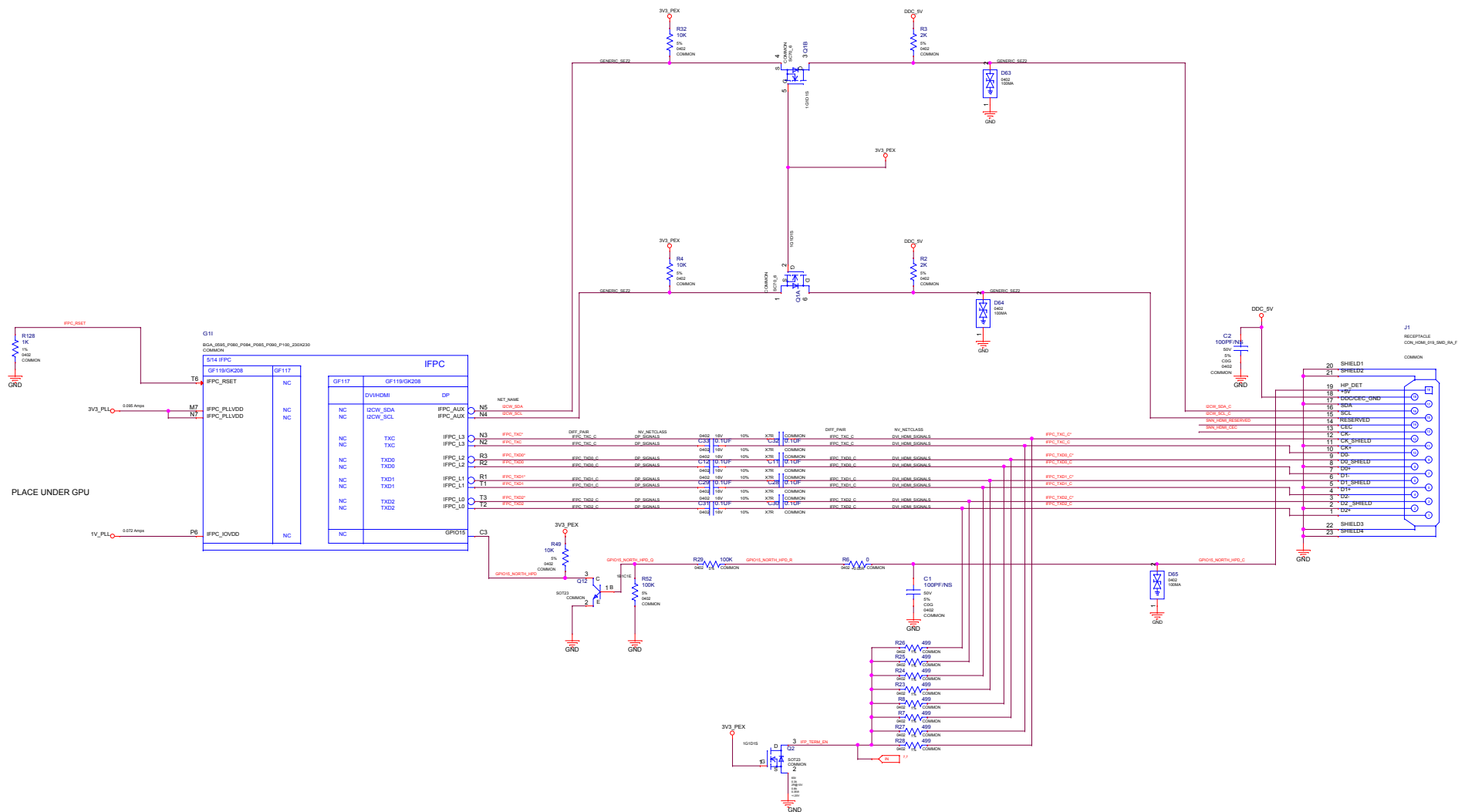
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A number line from 0 to 100 with tick marks every 10 units. A vertical line is drawn at 20, and a horizontal line is drawn at 60. The intersection is labeled 'H'.

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A horizontal number line with three tick marks. The first tick mark on the left is labeled with the letter 'H'.

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0000	5.1K to GND	1000	5.1K to VCC
0001	10K to GND	1001	10K to VCC
0010	15K to GND	1010	15K to VCC
0011	20K to GND	1011	20K to VCC
0100	24.9K to GND	1100	24.9K to VCC
0101	30.1K to GND	1101	30.1K to VCC
0110	34.8K to GND	1110	34.8K to VCC
0111	45.3K to GND	1111	45.3K to VCC

Bit Signal		Values				
00	ROM[CFG]	0000	Reserved			ROM_SI
01	ROM[CFG]	0001	Q288A10 DCCD3 X16 64KB Mtron			
02	ROM[CFG]	0010	Q288A10 DCCD3 X16 64KB Hynix			
03	ROM[CFG]	0011	Q288A10 DCCD3 X16 64KB Samsung			
04	ROM[CFG]	0100	Q288A10 DCCD3 X16 64KB Mtron			
		0101	Q288A10 DCCD3 X16 64KB Hynix			
		0111	Q288A10 DCCD3 X16 64KB Samsung			
00	FR[0]	01	128MB			ROM_SO
01	FR[0]	10	256MB			
01	SMBL_A1_T_ADDR	0	DISABLED	1	ENABLED	ROM_SCLK
00	VGA_DEVICE	0	DISABLED	1	ENABLED	
00	PCL_DEV[0_EXT]	0x00	0	0x01	TEST	ROM_SCLK
02	SUBV_VENDOR	0	NCL_BIOS	1	BIOS	
01	PCL_DEV[0_EXT_BITS]	0x00	0	0x01	TEST	
00	PCL_FLL_SA_TERRM00	0	DISABLED	1	ENABLED	
00	USER[0]	0000	INIT			Strap0
01	USER[0]					
02	USER[0]					
00	USER[0]					Strap1
00	SOP2_PNCDF0_LUT_ADDR0	0000	DIS_TOP_DEFAULT	1000	DIS_TOP_ATHRIVE	
		0001	MOBILE_DEFAULT	1001	MOBILE_ATHRIVE_NAMP	
02	SOP2_PNCDF0_LUT_ADDR0	0010	MOBILE_ATHRIVE_LLAAMP	1010	MOBILE_ATHRIVE_LLAAMP	
		0011	MOBILE_ATHRIVE_LAMP	1011	MOBILE_ATHRIVE_LAMP	
01	SOP2_PNCDF0_LUT_ADDR0	0100	MOBILE_ATHRIVE_NAMP	1100	MOBILE_ATHRIVE_NAMP	
		0101	MOBILE_ATHRIVE_NAMP	1101	MOBILE_ATHRIVE_NAMP	
00	SOP2_PNCDF0_LUT_ADDR0	0110	MOBILE_ATHRIVE_HHAMP	1110	MOBILE_ATHRIVE_HHAMP	
		0111	MOBILE_ATHRIVE_HHAMP	1111	MOBILE_ATHRIVE_HHAMP	
00	PCL_DEV[000]	0x00	0100			Strap2
01	PCL_DEV[001]					
02	PCL_DEV[002]					
03	PCL_DEV[003]					
00	SOP2_EXPOSED	0	DISABLED	1	ENABLED	Strap3
02	SOP2_EXPOSED	0	DISABLED	1	ENABLED	
01	SOP1_EXPOSED	0	DISABLED	1	ENABLED	
00	SOP0_EXPOSED	0	DISABLED	1	ENABLED	
00	PCIE_SPEED_CHANGING_GEN0	0	DISABLED	1	ENABLED	Strap4
01	PCIE_MAX_SPEED	0	GEN1	1	GEN3	
00	DP_FLL_VCO_010	0	USEV10	1	USEV20	

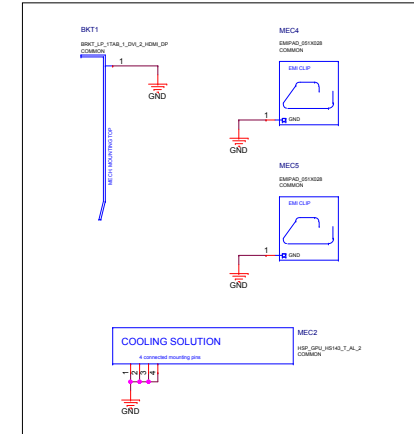
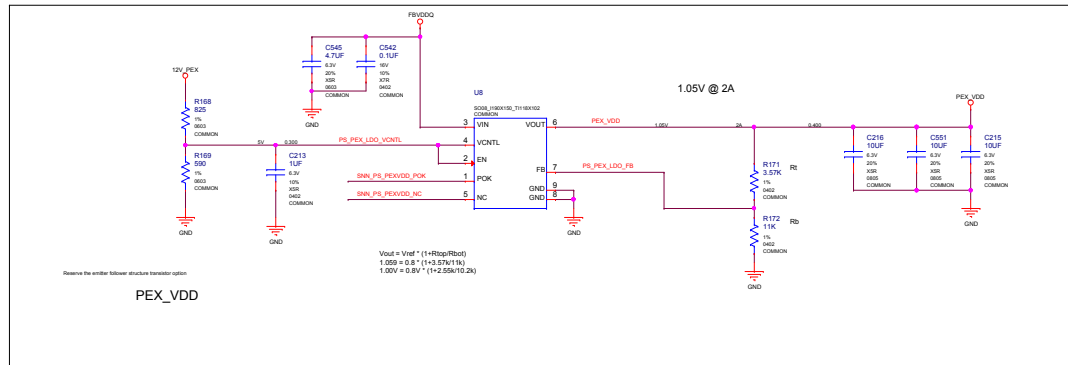
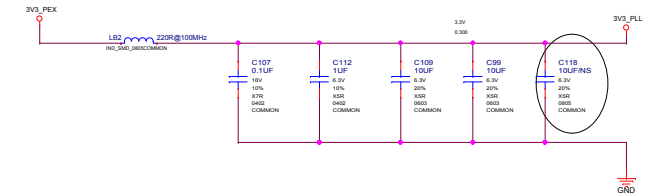
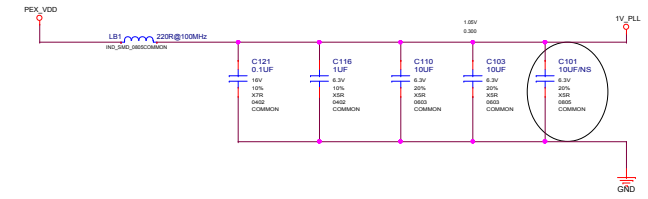
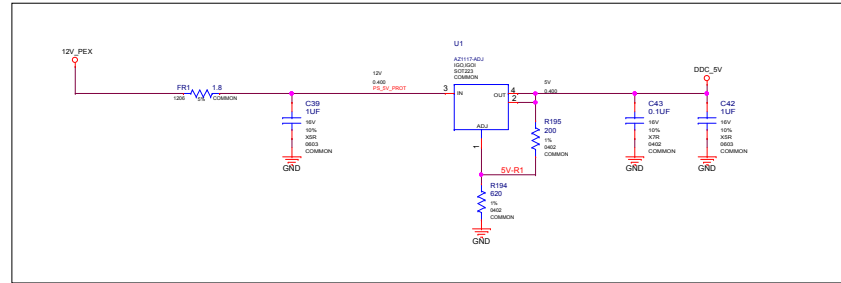


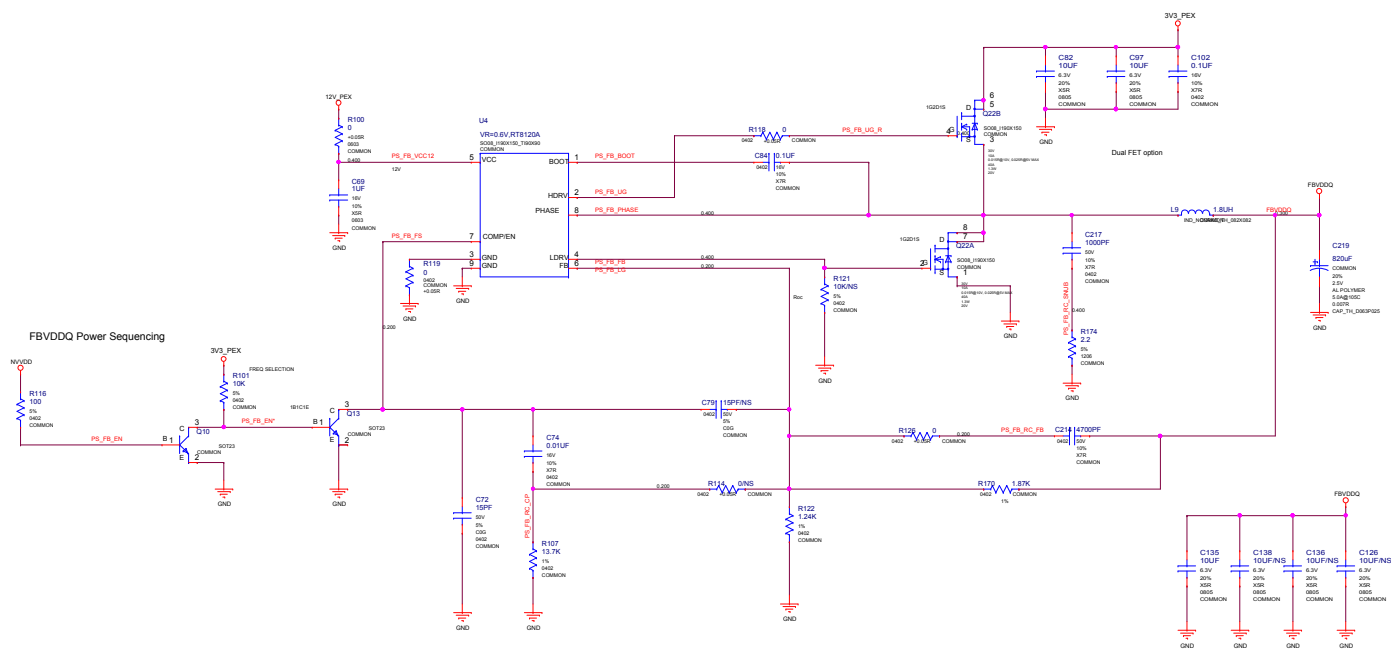
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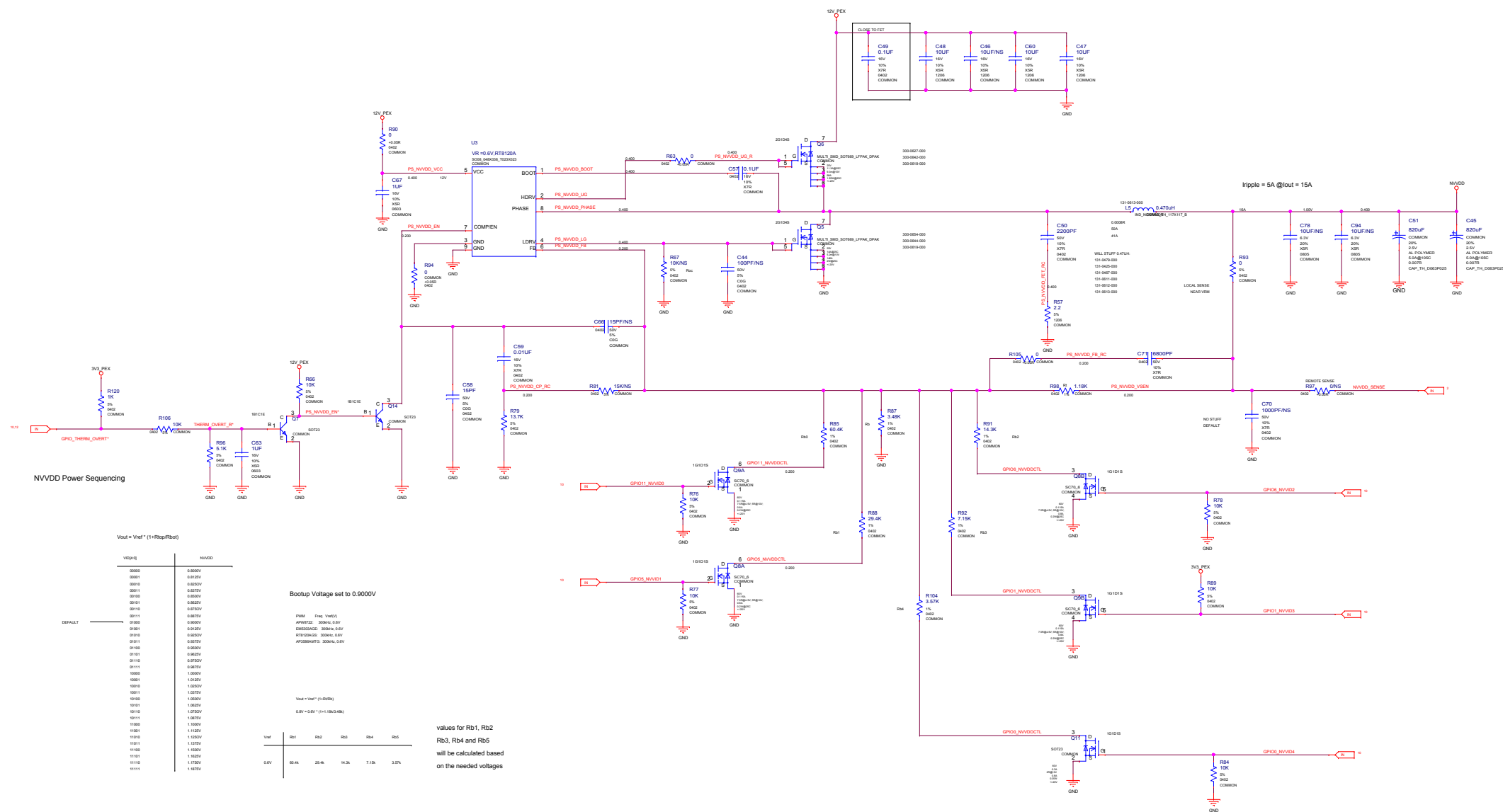
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# POWER SUPPLY I: PEXVDD, 5V





## POWER SUPPLY III: NVVDD



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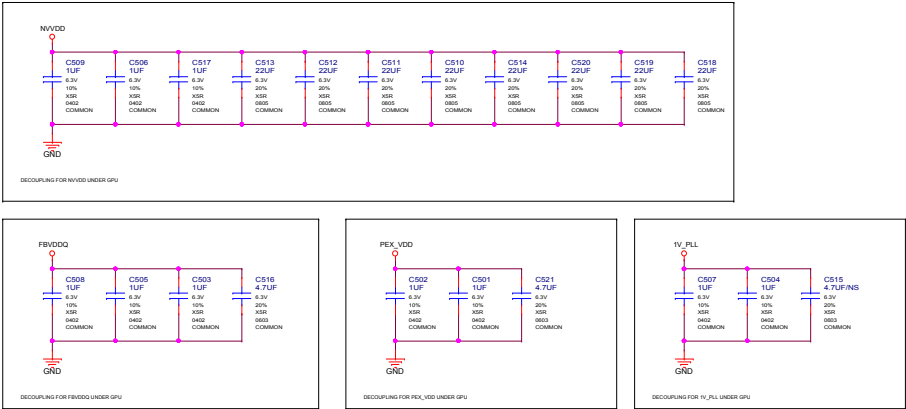
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