P693: GT218, GDDR3 MEMORY 16M/32MX32b

Page 1: P693 Overview

Page 2: PCI Express Interface

Page 3: Frame Buffer Interface

Page 4: GDDR3 Memories Rank0

Page 5: GDDR3 Memories Rank1

Page 6: DAC A VGA

Page 7: DAC B VGA D-Sub Connector

Page 8: TMDS Interface

Page 9: IFPC HDMI Connector

Page 10: IFPD & IFPE (NOT USED), Mechanical

Page 11: XTAL, ROM, JTAG

Page 12: Thermal Protection, Protected 3V3, Straps

Page 13: GPU/Memory decaps on bottom (Test option)

Page 14: Power Supply I: FBVDD/Q,5V,PEXVDD

Page 15: Power Supply II: NVVDD

Page 16: Basenet Report

Page 17: Basenet Report

V183 4.0 PCB change list - base on P693

Page 3: add FBA_VREF circuit & del DDR3 ODT terminator

Page 3: Enable FBA_DEBUG for CMD function

Page 4: GDDR3 (RANK0), Origin were DDR3 Circuit

Page 5: GDDR3 (RANK1)

Page 6: add ESD protect diode Page 7: add ESD protect diode

Page 7: VGA Header change to D-Sub Connector

Page 8: Remove Filter & add EMI bridge

Page 8: IFPAB_RSET(SNN_IFPAB_RSET) contact 1K ohm to GND

Page 9: IFPC connect to HDMI

Page 9 : remove FBVDD/Q for IFPC_PLLVDD

Page 9: remove HDMI_CEC function

Page 9: remove Bypass resister Page 10: add optical cross point

Page 10: add 4pin FAN Plug

Page 10: add impedance test trace

Page 11: SPDIF (SNN_SPDIF) contact 1K ohm to GND

Page 11: GPIO10 Connect to Control MOS for FBA_VREF* balance

Page 11: Enable GPIO4 & 9 for 4 pin FAN Control

Page 11: I2CC connect to NVVDD adjust Circuit

Page 12: Hynix GDDR3 set Strap ROM_SI 35K to GND (0110)

Page 14: FBVDD/Q & PEX_VDD copied from V183-3.0

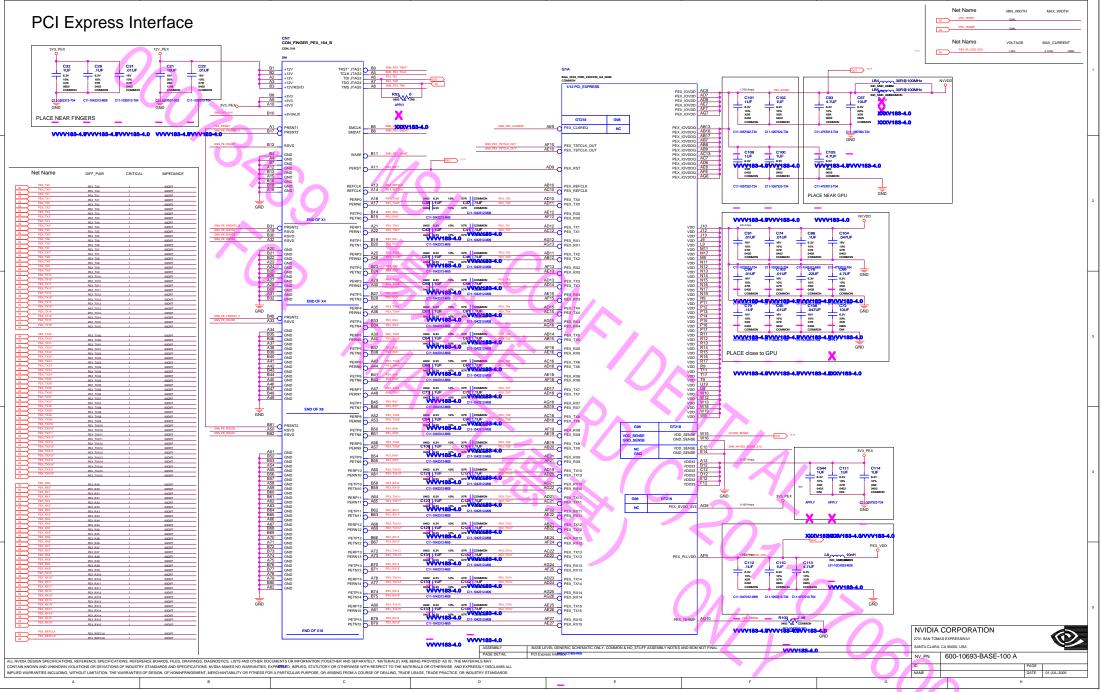
Page 15: NVVDD Copied from V183-3.0 Page 15 add NVVDD adjust Circuit

SKU	VARIANT	NVPN	ASSEMBLY	1
В	BASE	600-10693-BASE-100	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL	1
- 1	SKU0000	600-10693-0000-100	GT218-300, 589/1402/790, 512MB/64bit, 64Mx16 DDR3, DVI-DL+VGA, DT	
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600-10693-BASE-100 A



VVV188-4.0

