

G94-P545-A01 - GDDR3, DVI/VGA + DVI/VGA + Display port + HDMI

REV	VARIANT	NVPIN	ASSEMBLY
8	BASE	600-10545-5800-100	P545 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SRU0100	600-10545-5100-100	G94-400 6500MHz/1000MHz 512MB 16MA32 BGA136 GDDR3, DVI-I-OL+DVI-I-OL+HDTV-Out (Bring Up SRU)
2	SRU0200	600-10545-0000-100	G94-400 6500MHz/1000MHz 512MB 16MA32 BGA136 GDDR3, DVI-I-OL+DVI-I-OL+HDTV-Out
3	SRU0310	600-10545-0010-100	G94-300 5000MHz/800MHz 512MB 16MA32 BGA136 GDDR3, DVI-I-OL+DVI-I-OL+HDTV-Out
4	SRU0320	600-10545-0020-100	G94-300 5000MHz/800MHz 384MB 16MA32 BGA136 GDDR3, DVI-I-OL+DVI-I-OL+HDTV-Out
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V145-0A Base on V135-100 and P545


- 1.PAGE21: Remove HD Audio circuit.
- 2.PAGE19: Remove DACB HDTV circuit.
- 3.PAGE18: Modify DP_Power circuit and AUX channel circuit
- 4.PAGE 27:Phase adjustment by loading circuit
- 5.PAGE15: Remove DACC D-Sub connector
- 6.PAGE16,17: Add J5 Dual DVI connector
- 7.PAGE 25 add Optical Spidf in connector.
- 8.PAGE 28 Change FBVDD power solution to UP6205 and PEX_VDD to UP7706
- 9.PAGE 29 add Winbond W83L786G Hardware monitor
- 10.PAGE 30 Change NVVDD power solution to ISL6334

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ASSEMBLY	P545 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Overview



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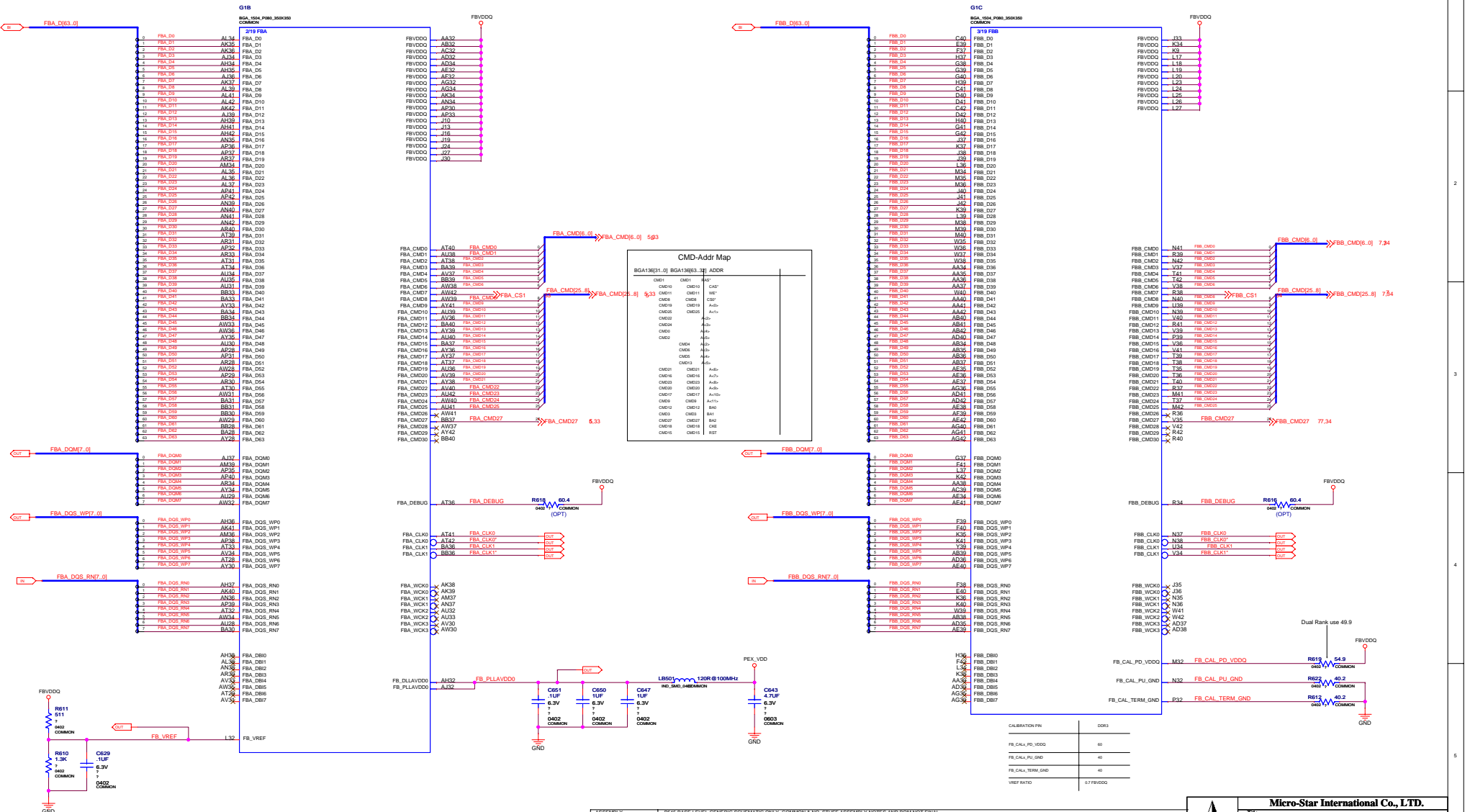
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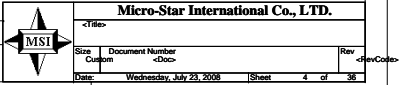
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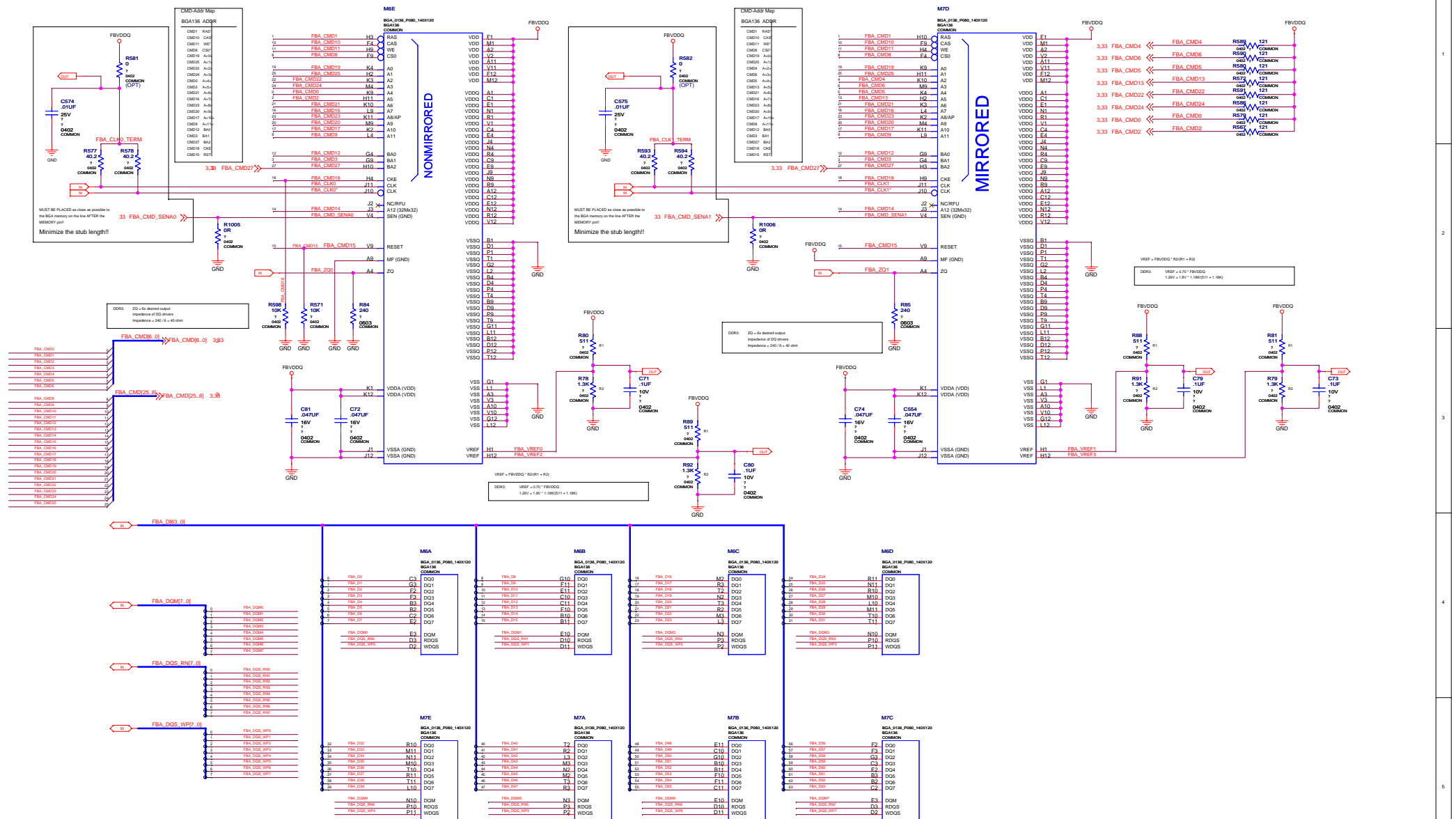
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MEMORY: GPU Partition A/B

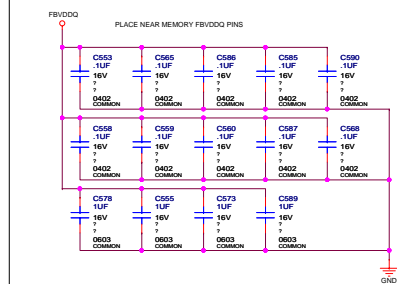
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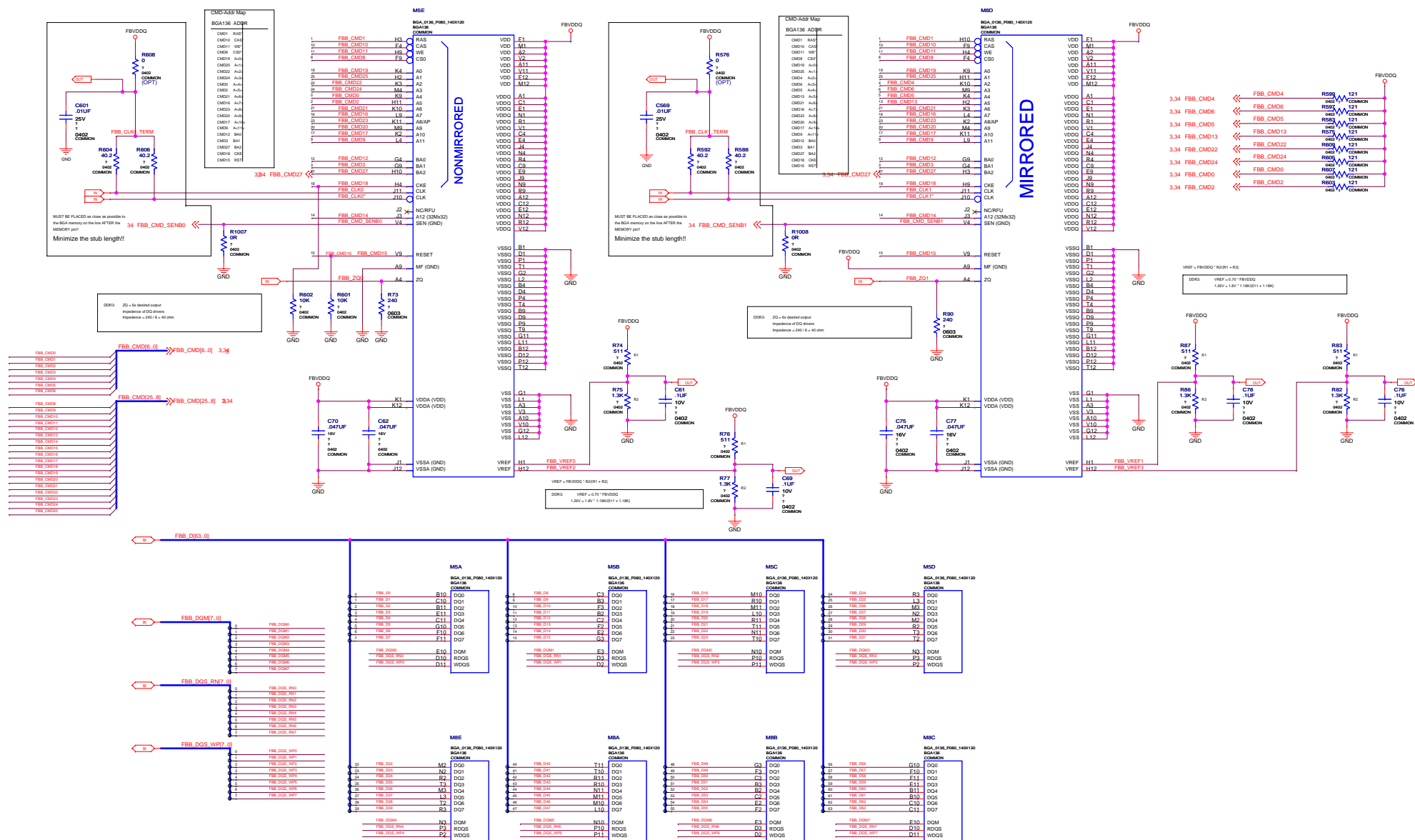
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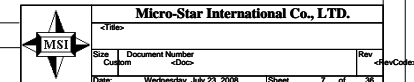


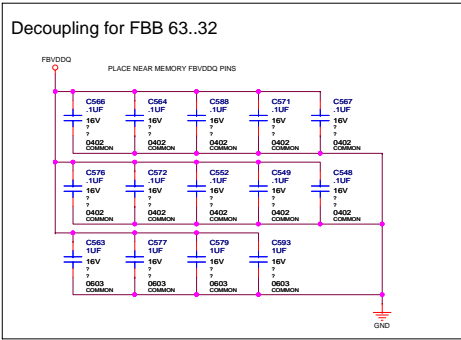
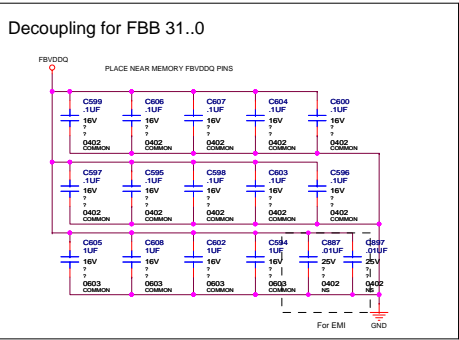


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PS4/PS5 LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO 3.1UFP ASSEMBLY NOTES AND BOM NOT FINAL
FBB Partition Decoupling

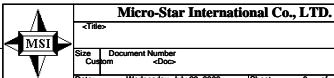


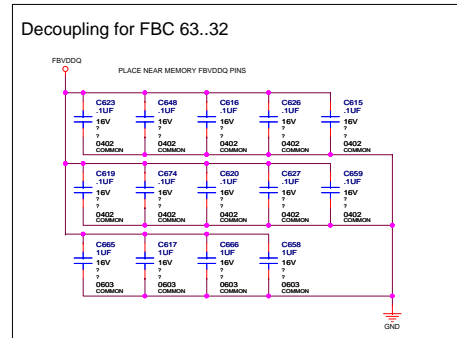
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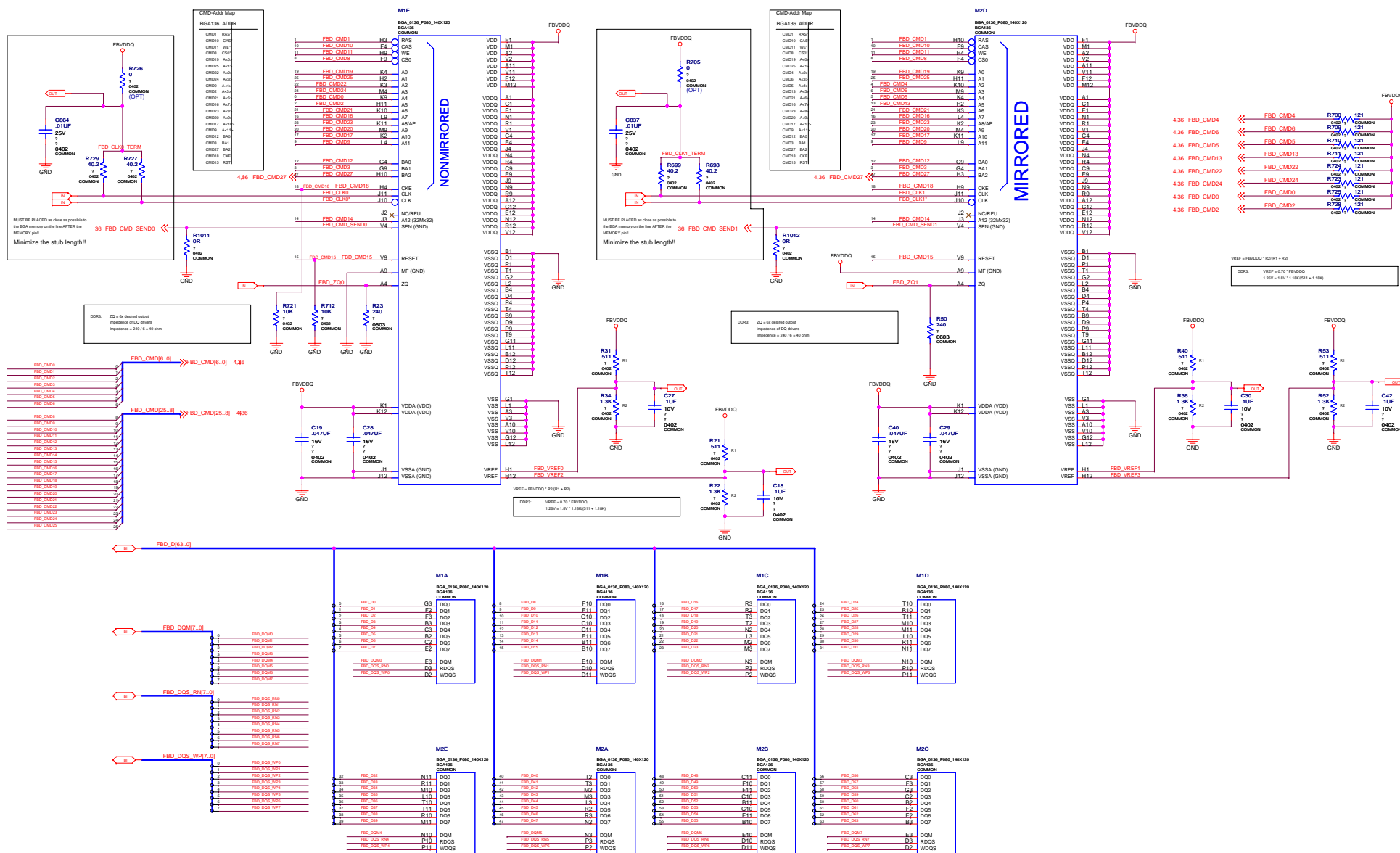
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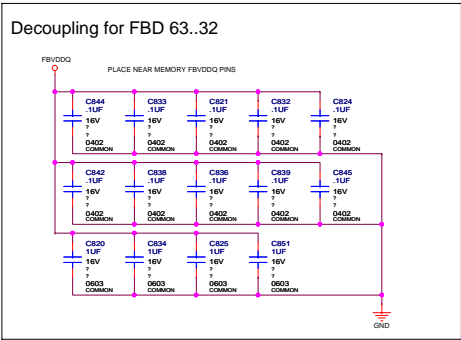
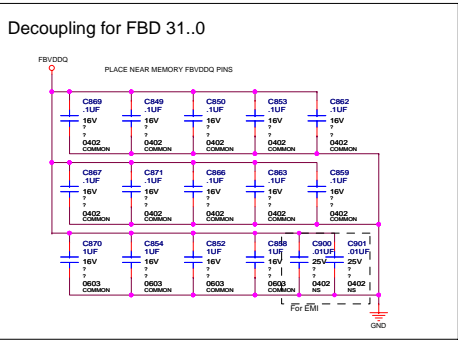
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ASSEMBLY PAGE DETAIL

PS4E BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO 3YUFF ASSEMBLY NOTES AND BOM NOT FINAL

FBD Partition Decoupling



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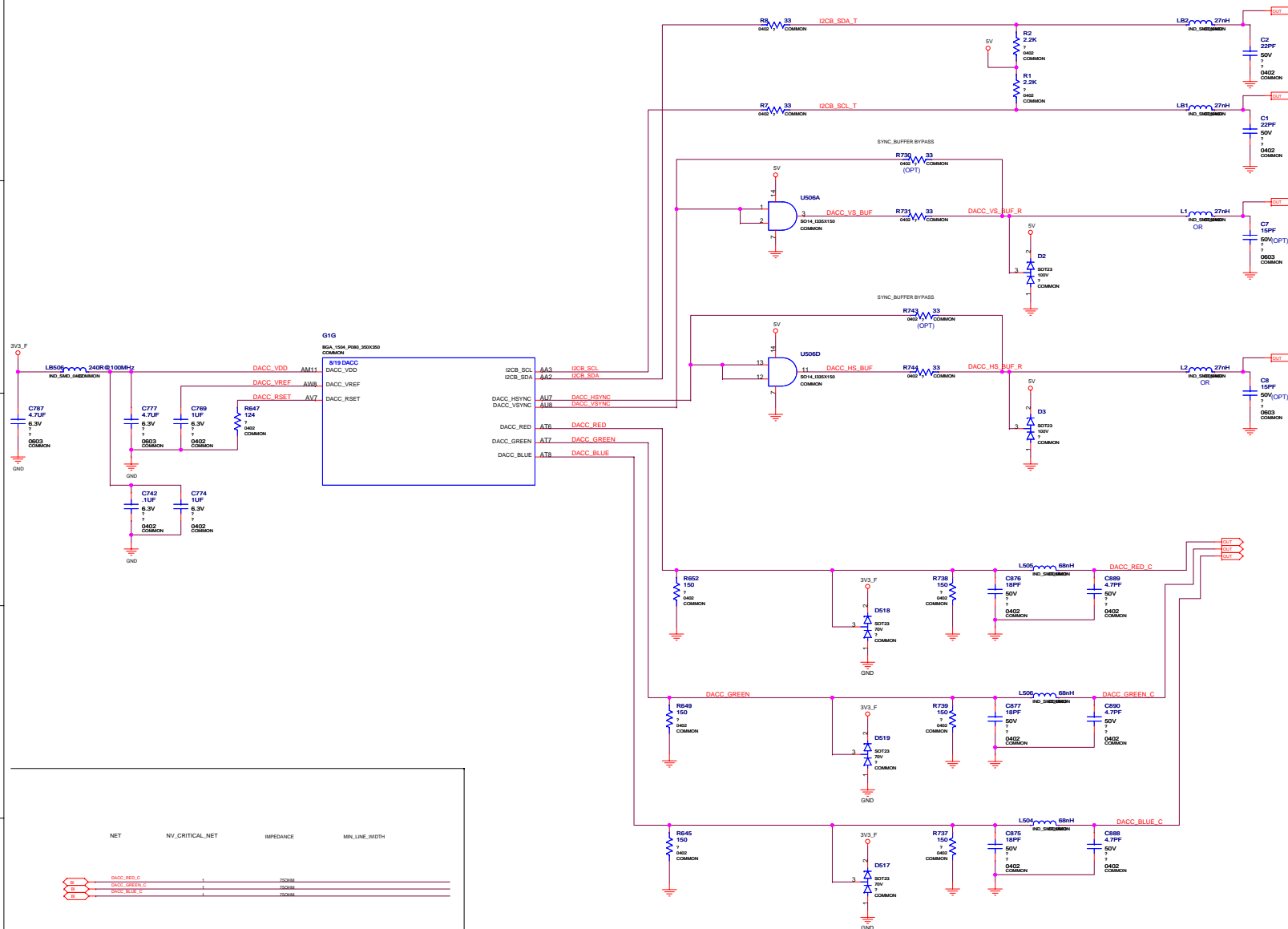
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20	FB_UREF	1.25%	1.20V	0.22A
20	FB_VREF0	1.25%	1.20V	0.22A
20	FB_VREF1	1.25%	1.20V	0.22A
20	FB_VREF2	1.25%	1.20V	0.22A
20	FB_VREF3	1.25%	1.20V	0.22A
20	FB_Z00	1.25%	1.20V	0.22A
20	FB_Z01	1.25%	1.20V	0.22A
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20	FB_Z01	1.25%	1.20V	0.22A
20	FB_VREF2	1.25%	1.20V	0.22A
20	FB_VREF3	1.25%	1.20V	0.22A
20	FB_Z00	1.25%	1.20V	0.22A
20	FB_Z01	1.25%	1.20V	0.22A

	NET	MIN_LINE_WIDTH	VOLTAGE	NV_NET_MAX_CURRENT
	PS_PLA1001	1250L	1.0V	0.00A
W	FBC_VREF0	1250L	1.20V	0.02A
W	FBC_VREF1	1250L	1.20V	0.02A
W	FBC_VREF2	1250L	1.20V	0.02A
W	FBC_VREF3	1250L	1.20V	0.02A
W	FBC_Z00	1250L	1.00V	0.02A
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W	FBC_Z01	1250L	1.00V	0.02A



DACC RGB-FILTER



	NET	NV_CRITICAL_NET	IMPEDANCE	MIN_LINE_WIDTH
D	DAC0_RED_C	1	500mΩ	
G	DAC0_GREEN_C	1	500mΩ	
B	DAC0_BLUE_C	1	500mΩ	
B	DAC0_HS_C	2	500mΩ	
V	DAC0_VS_C	2	500mΩ	

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PAGE DETAIL	DACC Interface



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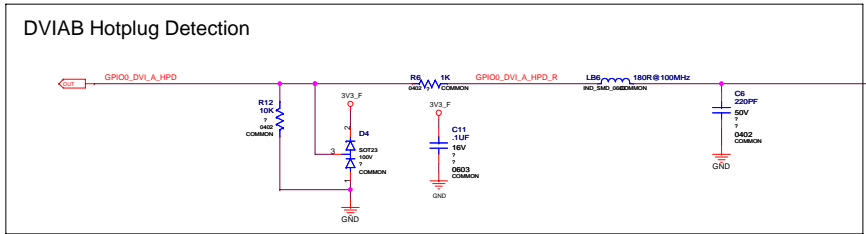
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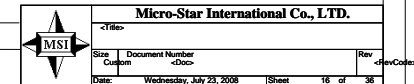
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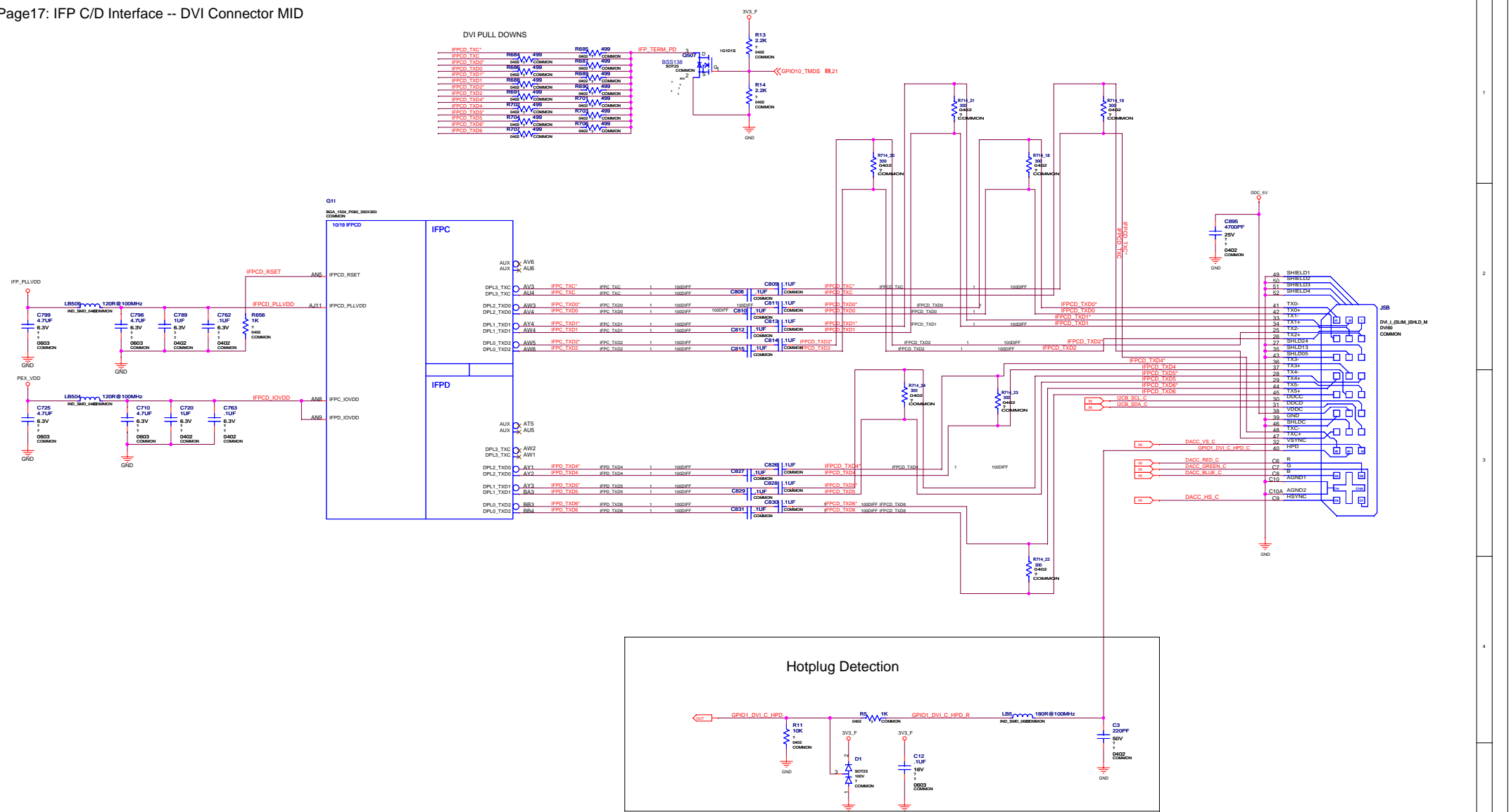
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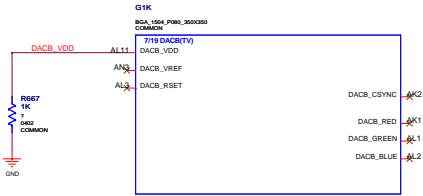


ASSEMBLY	P545 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	IFP A/B Interface -- DVI Connector South









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PAGE DETAIL	DACB and HDTV/SDTV-Out

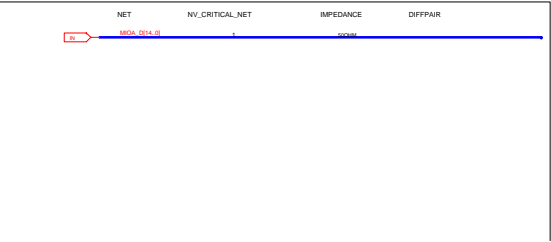
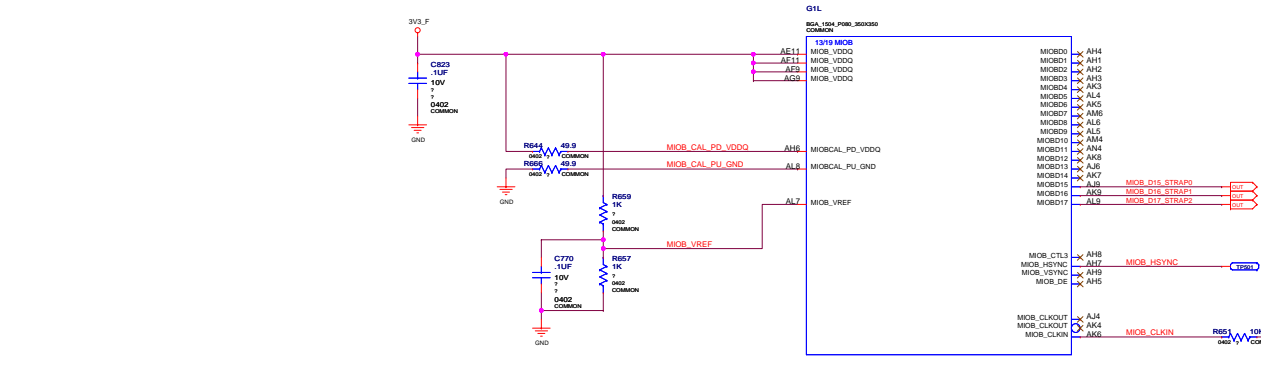
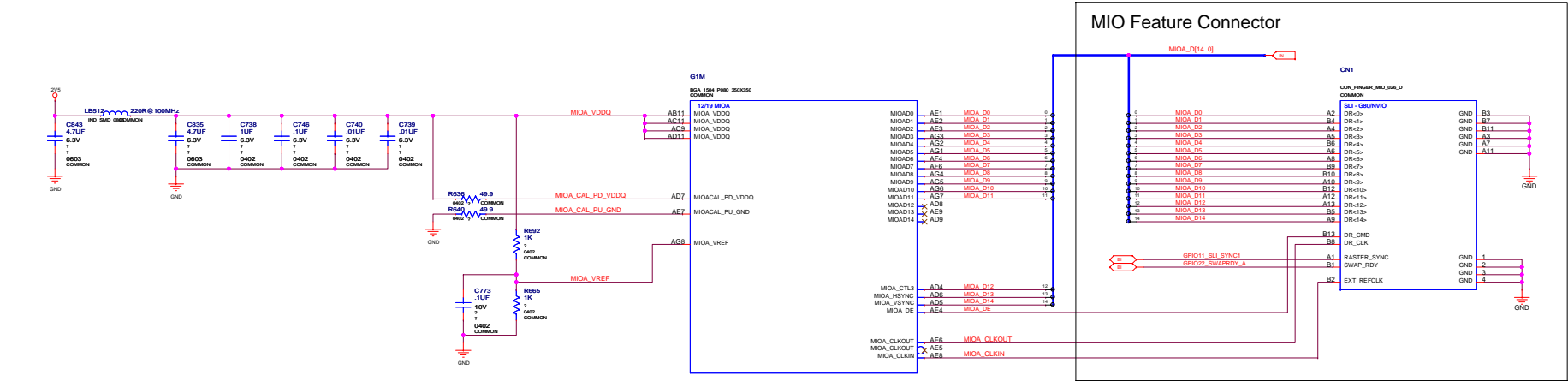


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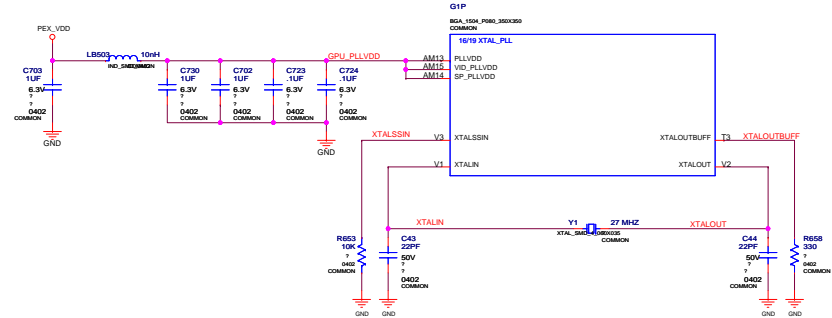
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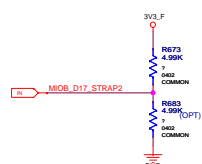
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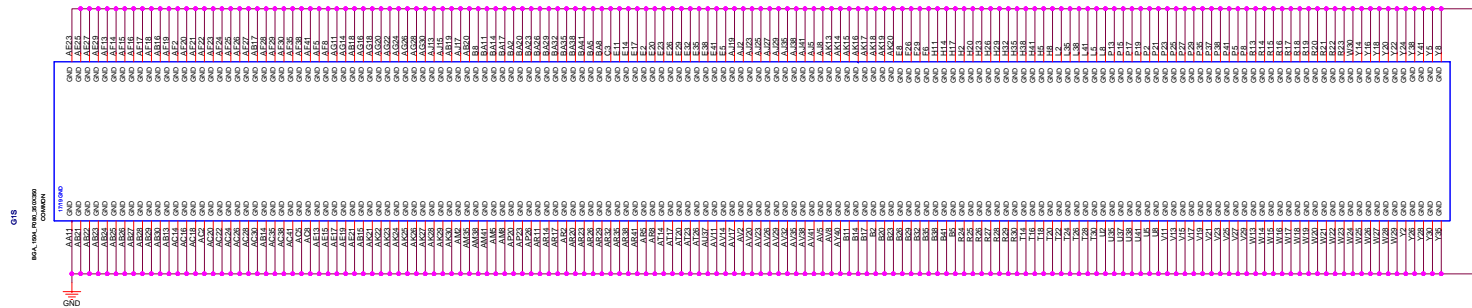
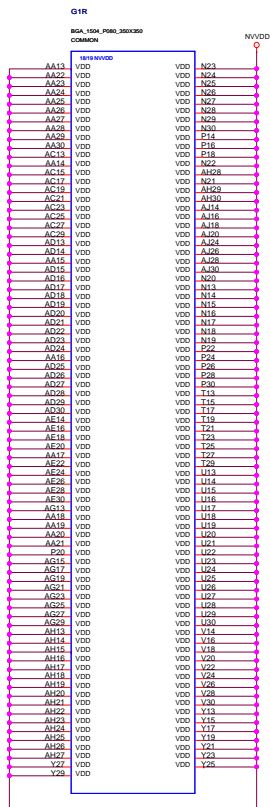


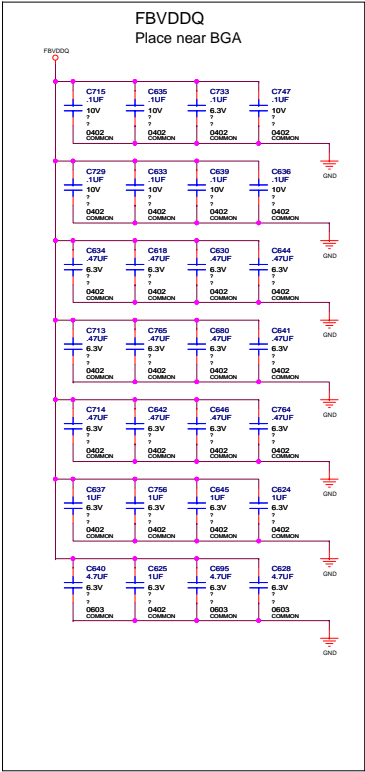
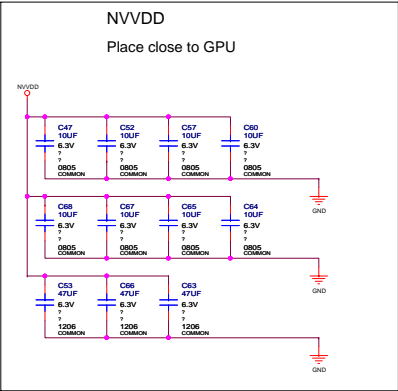
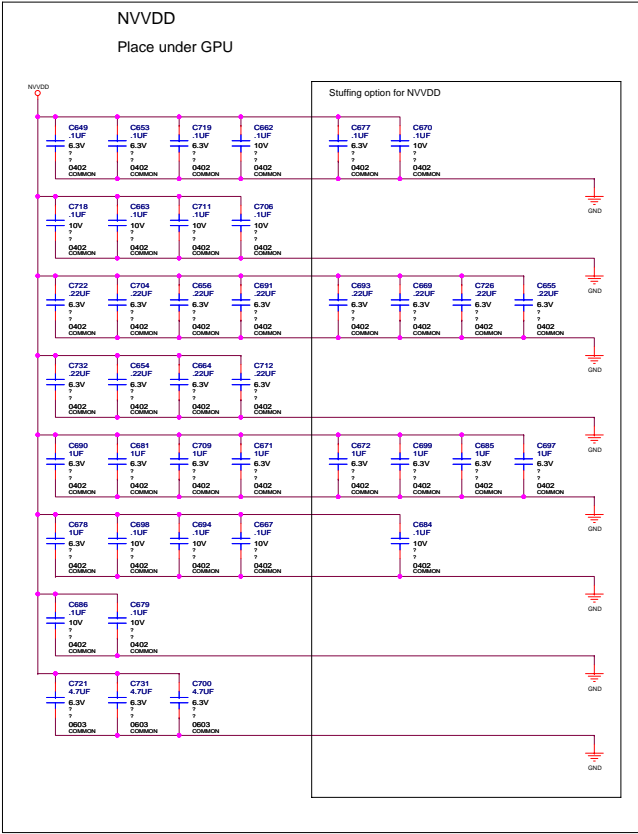
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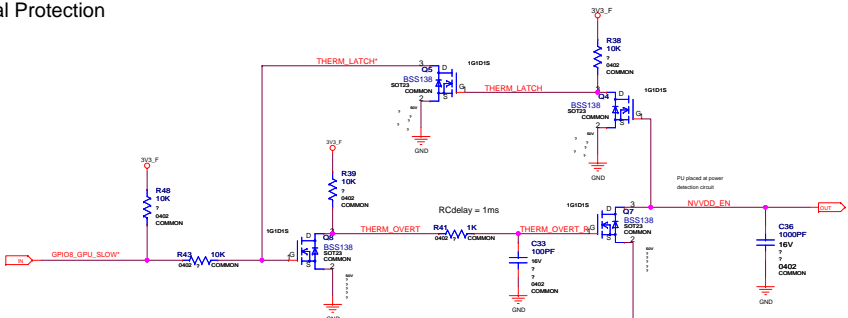


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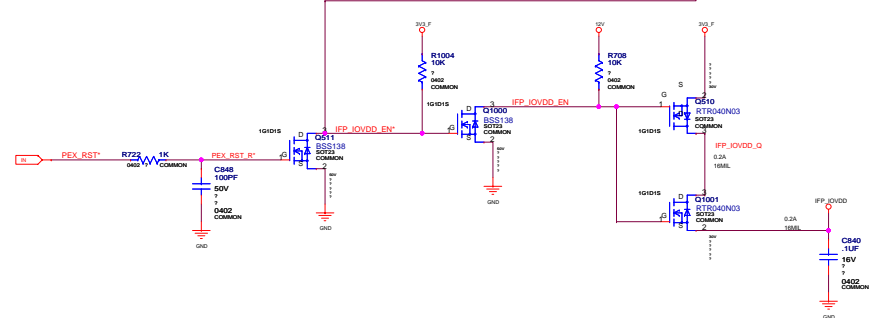
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Thermal Protection

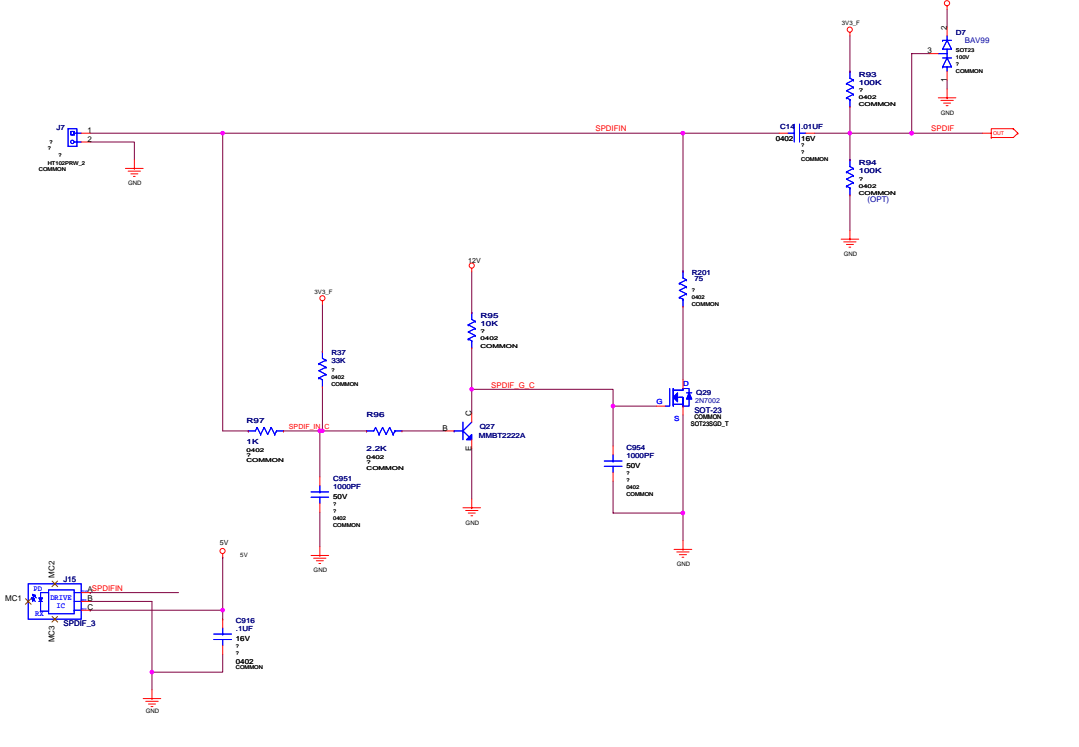


IFP_IOVDD Backdrive Prevention

Stiffing possibilities for thermal control and protection:




SPDIF Input



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ASSEMBLY PAGE DETAIL P5167 BASE LEVEL GENERIC SCHEMATIC ONLY COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL SPDIF Input, Backdrive Protection, and IFP_IOVDD Power Supply



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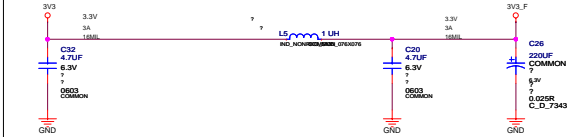
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3V3 Power Supply Filter

3V3_F = 3.3V @ 2.5A

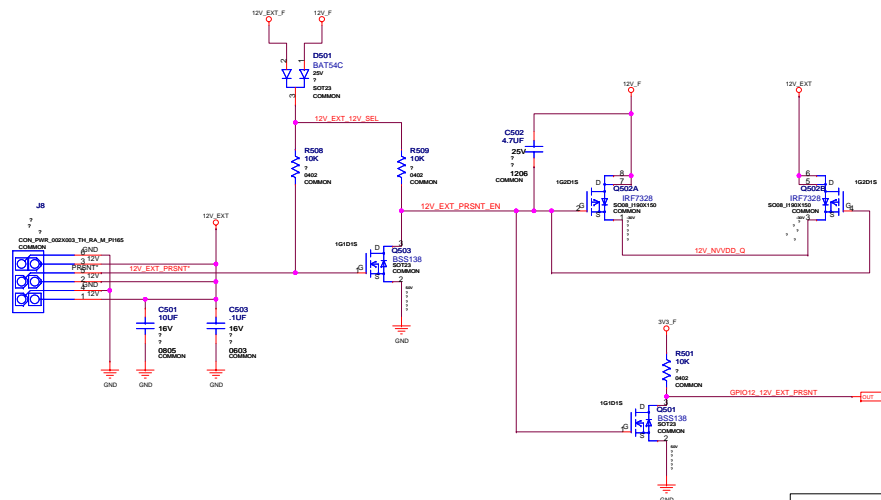


12V Power Supply Filter

12V_F = 12V @ 5.5A



INPUT POWER SELECTION for NVVDD



GPIO12	
EMERGENCY MODE (12V_EXT present)	0
150W POWER MODE (12V_EXT NOT present)	1

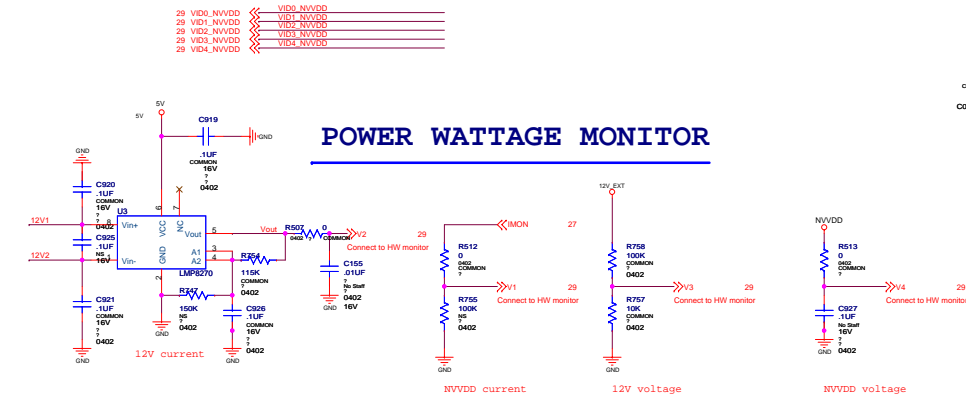
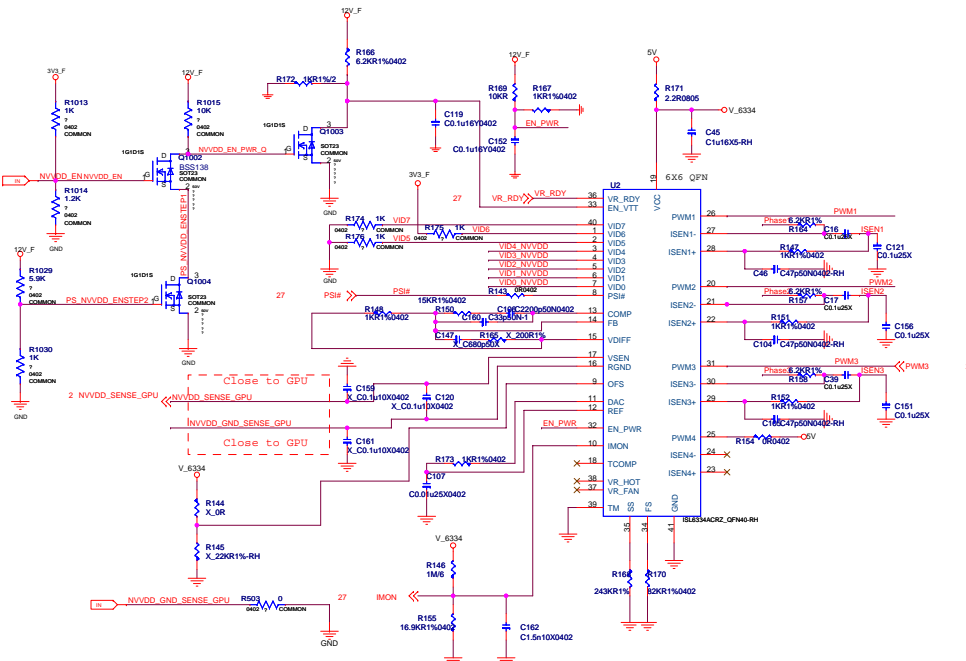
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ASSEMBLY	PSI4 BASE LEVEL GENERIC SCHEMATIC ONLY COMMON & NO 3V3UFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PS I: 3V3, 12V, and 12V_EXT Power Supply Filter

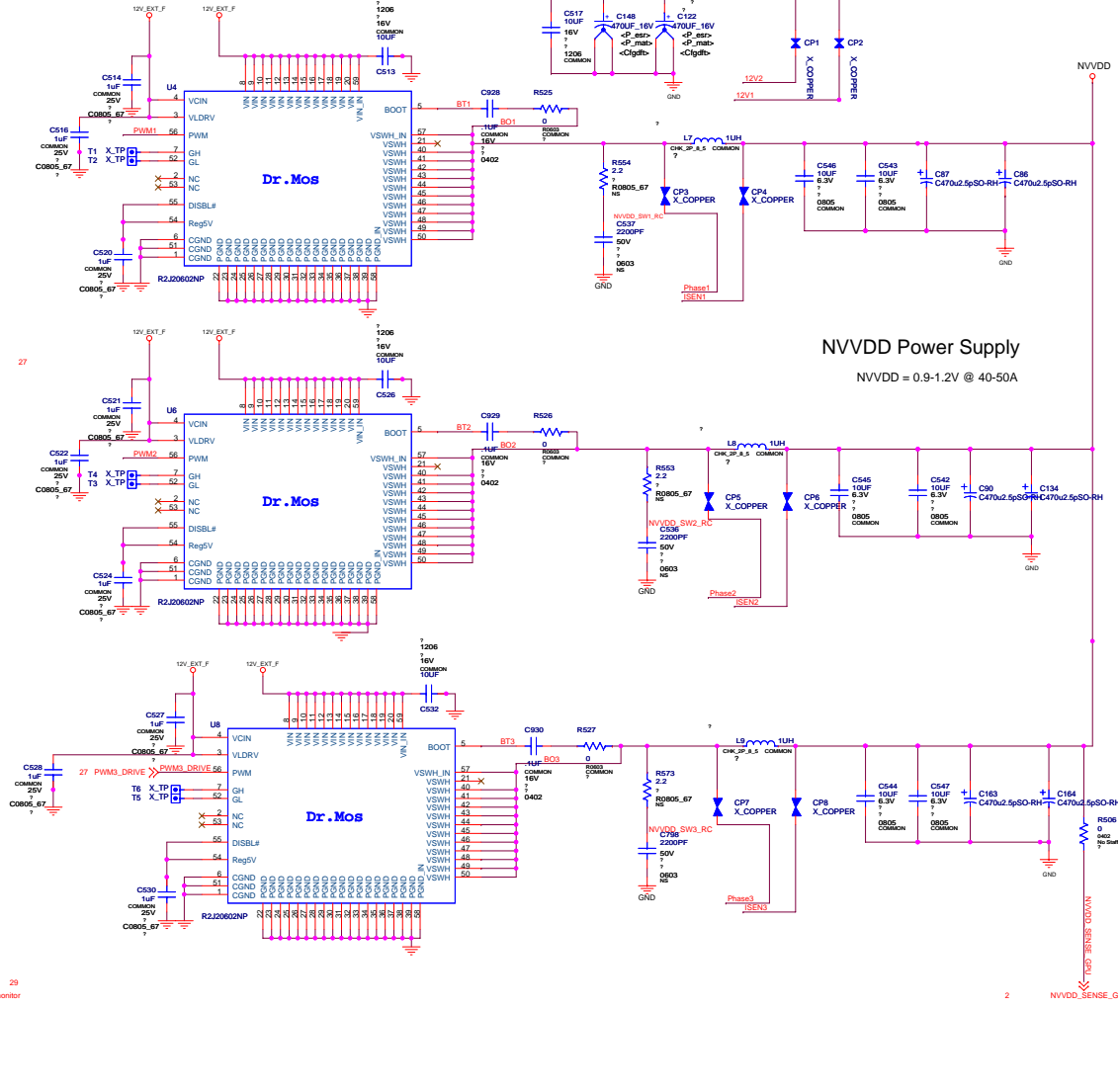


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12V1, 12V2 trace length不可拉太長,
LMV772MAX放在near PWM IC

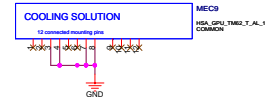


ASSEMBLY	PG45 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PS V: NVVDD Power Supply

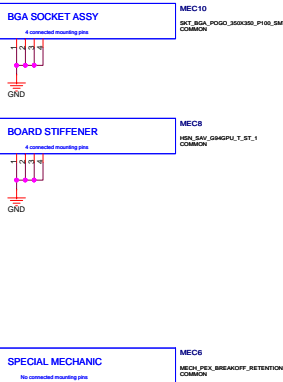
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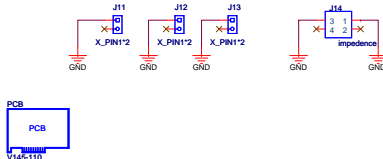
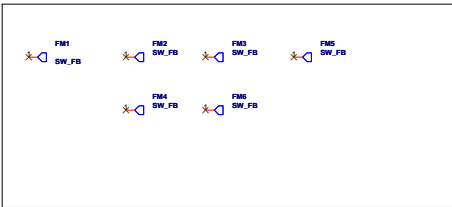
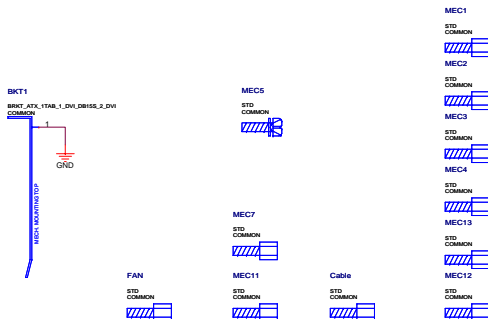
Thermal



Mechanical




Bracket



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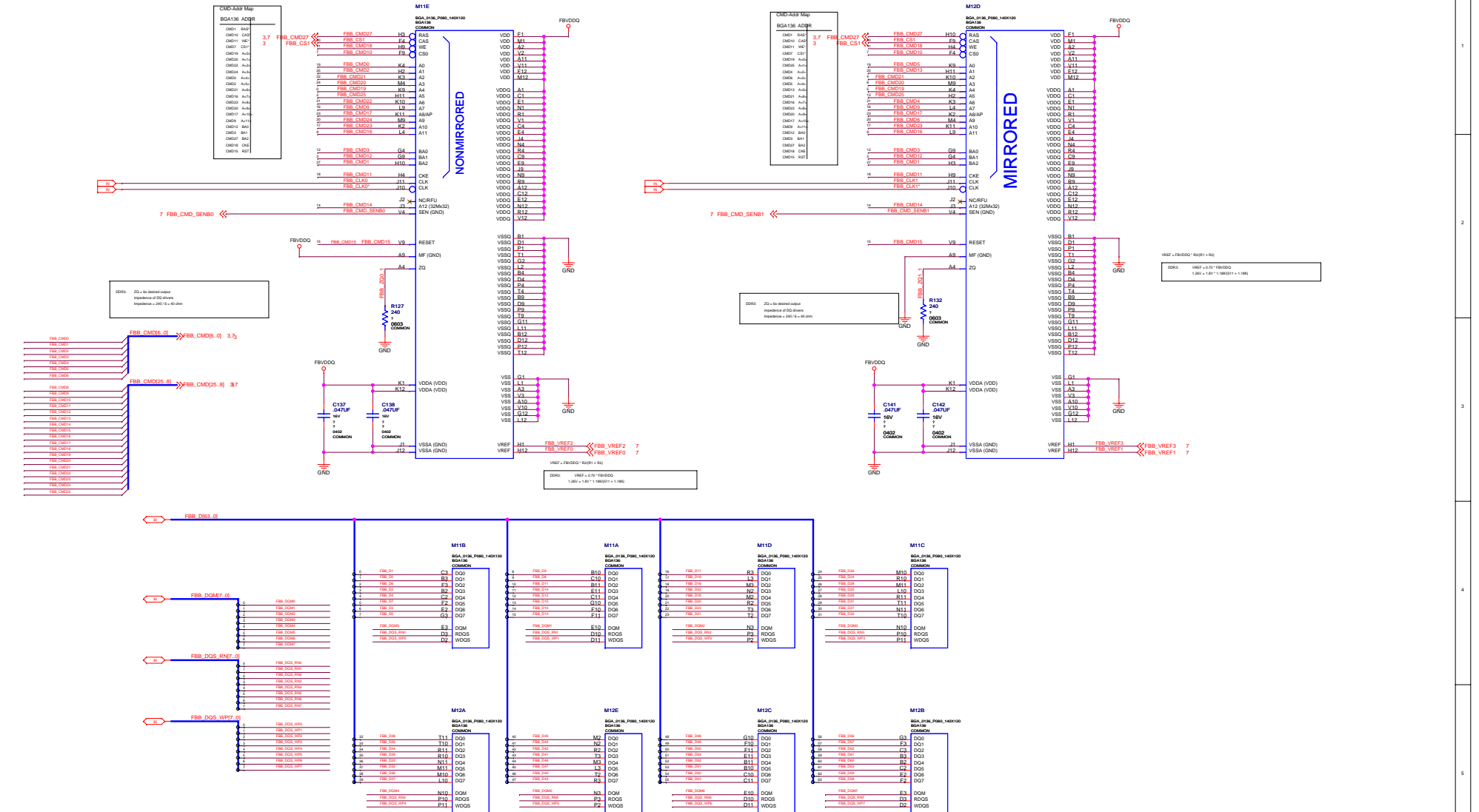
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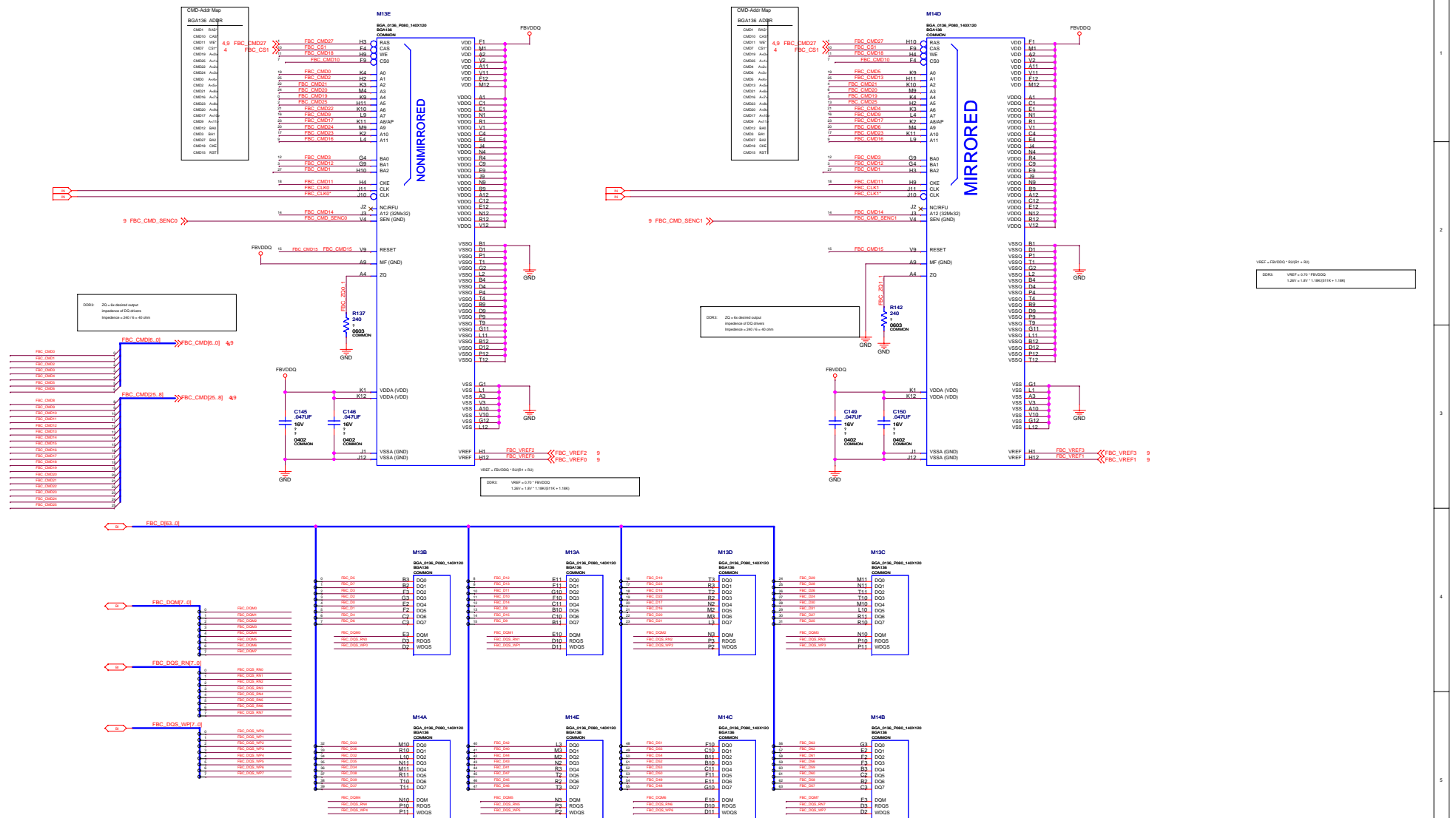


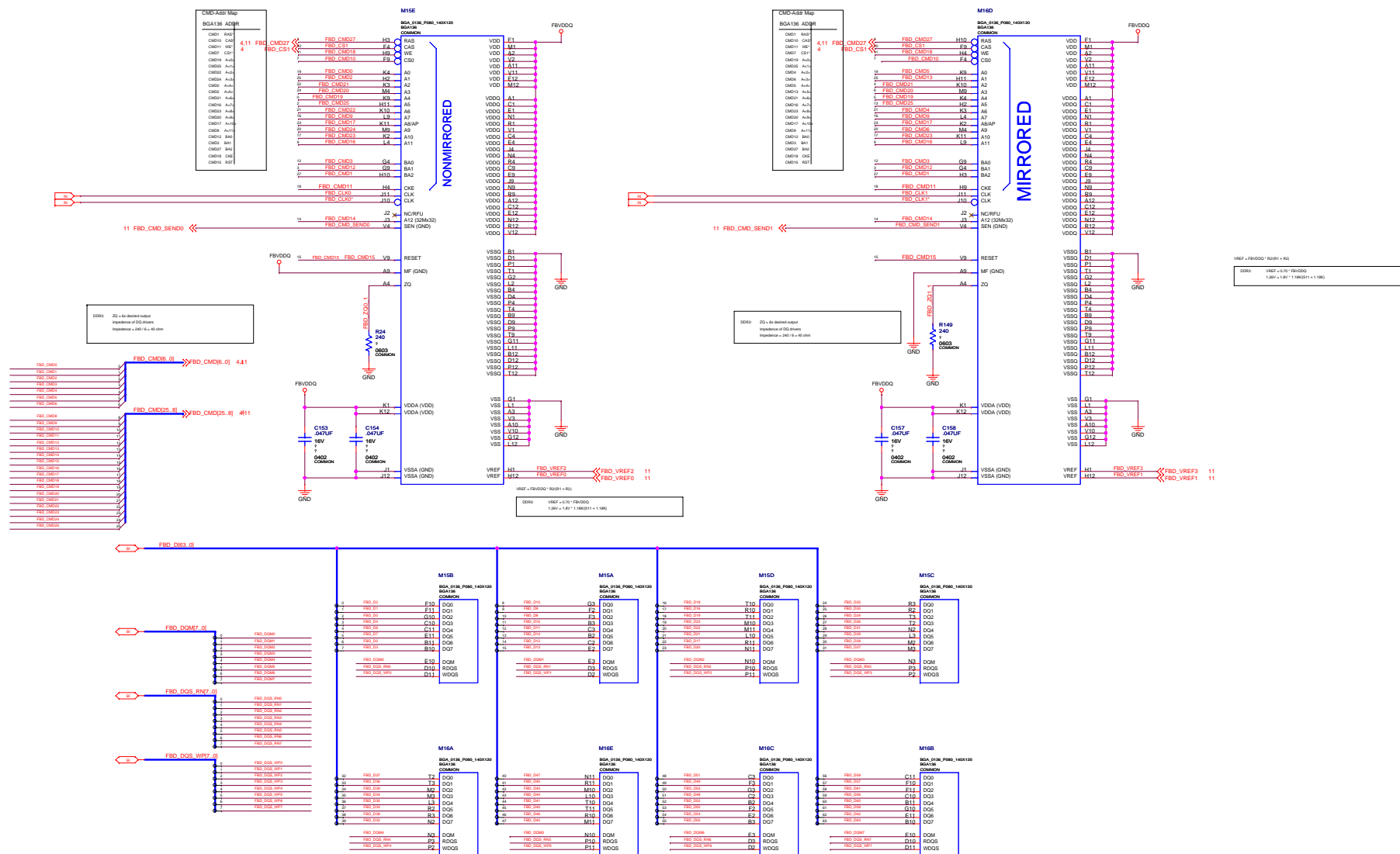
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