# C116, NV18, 8MX16DDR, 128MB, Video IN/OUT, DVI-I, VGA

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Filter and Connector

18 1394 TEXAS TSB41AB2, PowerRails,

I/O, Internal and external connector

19 POWER SUPPLY

NVVDD, MEM VDD, A3V3, TMDS3V3, TMDSPLL

20 MECHANICS, and FBVDD

#### HISTORY:

A00

INITIAL VERSION

Remove R117, C87, C131, C143 for CKE X01

Change C736, C743 to 18pf capacitors.

Remove C1099 - additional Capacitor for AGPVREFcg

Changed AGPVREFcg circuit.

### C116 Base on P112 to Modify.

1. Change page 4~7 & page 18 Reference.

2. Change J1 foot print from silm type to stand D-SUB.

Remove I1394 function.

4.Page 2, change voltage C75.1 & Q2.E from 3V3 to A3V3.

5. Page 8, Add Twin Bios for MSI function.

6.Page 16 ,replace INTERNAL VIDEO CAPTURE CONNECTOR.

7.Page 17 ,ADD C874 & C1098 ALE CAP. (DUAL-LAY)

8.Page 18 , Add H/W Monitor founction for MSI .

9.Page 2 change Q508, Q509 footprint from SOT23 to SOT-6 U200 package.

#### 00A change to version 100.

1. DACA and DACB signal swap.

600-10116-000X-A00





### AGP spacing rules

PCICBE[30]	20MIL	_
PCICLK	20MIL	
PCIRST*	10MIL	_
POREQ	10MIL	
PCIGNT	10MIL	
POFRAME	10MIL	
PCIRDY	10MIL	
PCITRDY	10MIL	
POSTOP	10MIL	
PCIDEVSEL	10MIL	
PCIPAR	10MIL	
PCIINTA	10MIL	
POINTB	10MIL	
AGPRBF AGPWBF	10MIL	
AGPUBLHI	10MIL 20MII	
AGPDBL LO	20MIL	
AGPDBI_LO	20MIL	
AGPST0	15MIL	
AGPST1	15MIL	_
AGPST2	15MIL	_
AGPADSTBOF	25MIL	_
AGPADSTB0S	25MIL	_
AGPADSTR1F	25MII	_
AGPADSTB1S	25MII	_
AGPSBSTBF	25MIL	_
AGPSBSTBS	25MIL	_
		_
AGPSBAI701	20MIL	
AGPVREFCG AGPVREFGC	10MIL 10MIL	
AGPMBDET	10MIL	
AGPSTOD	10MIL	























