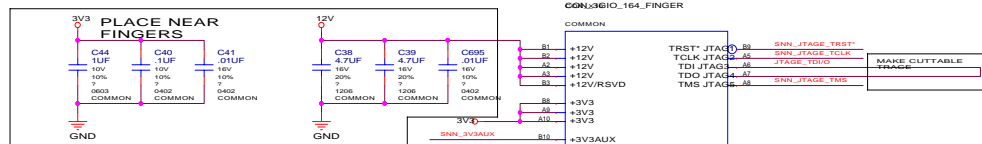
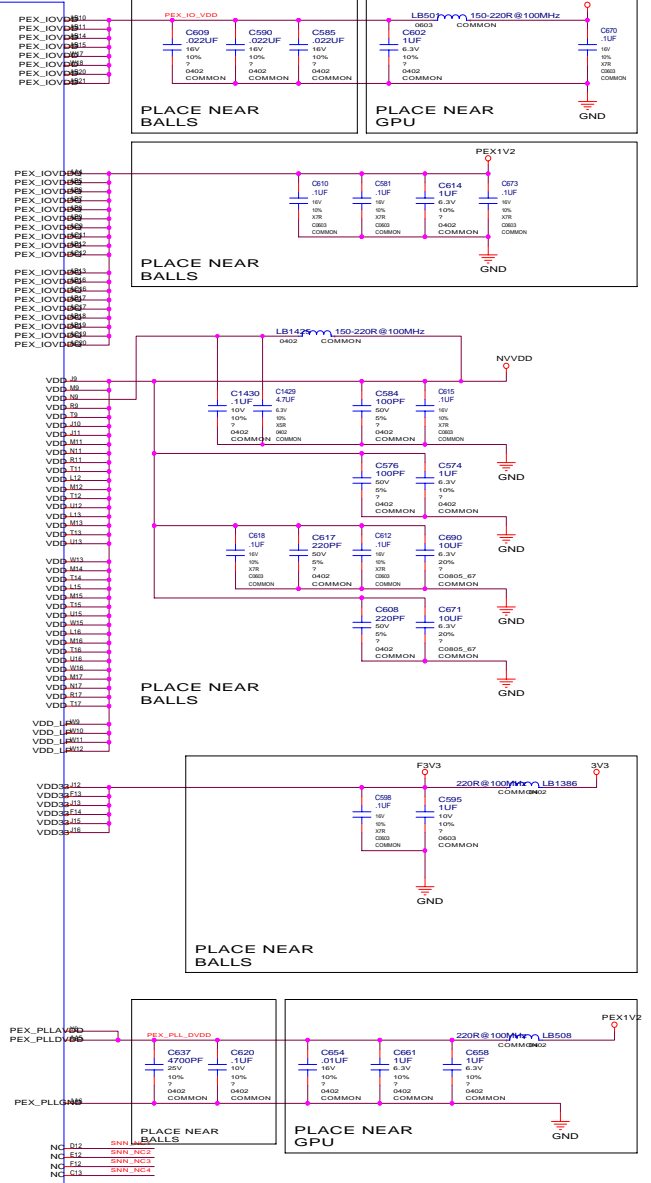
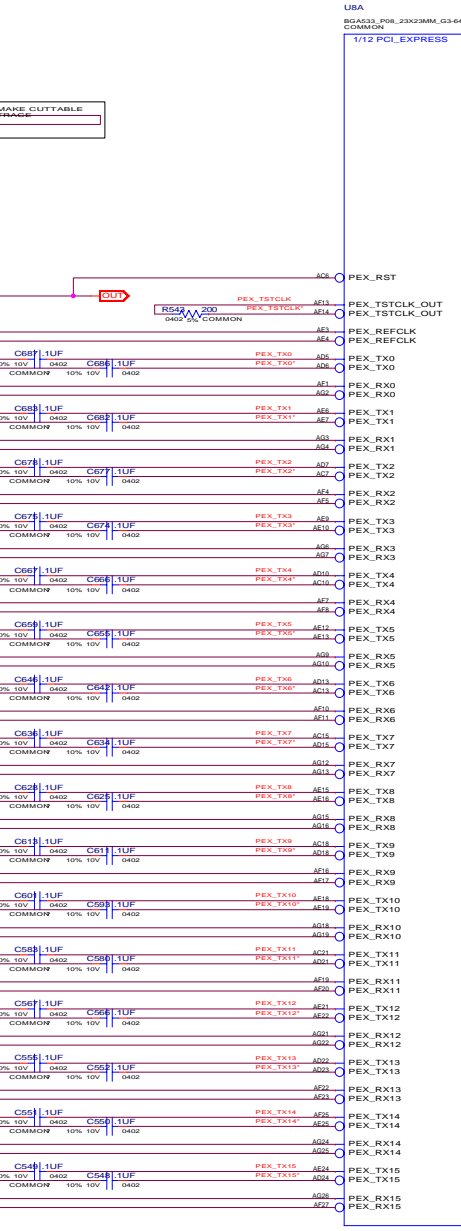
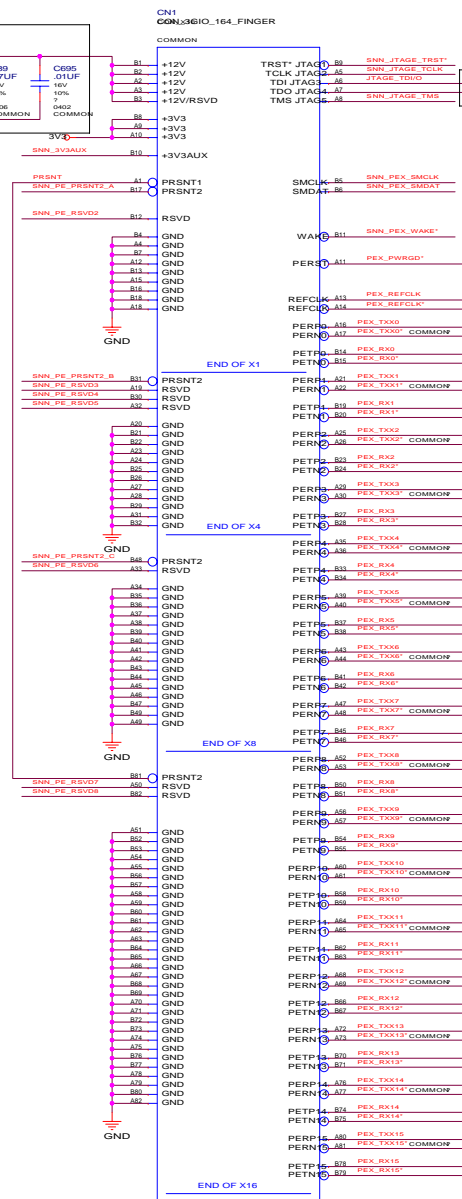




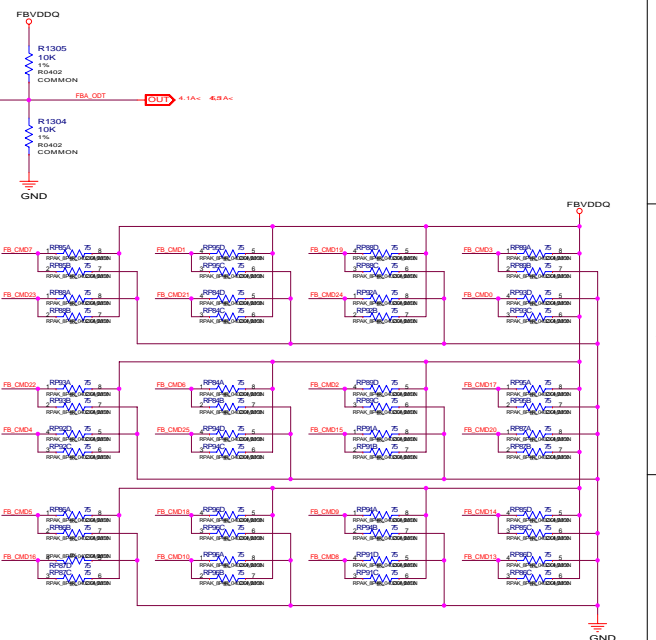
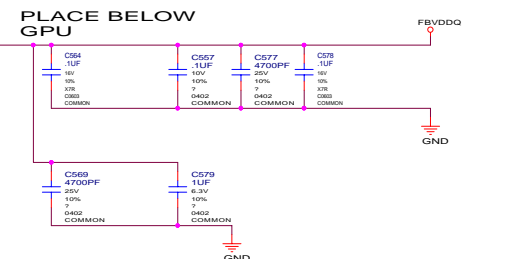
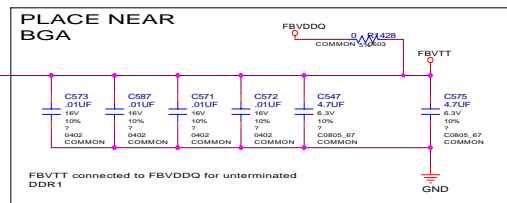
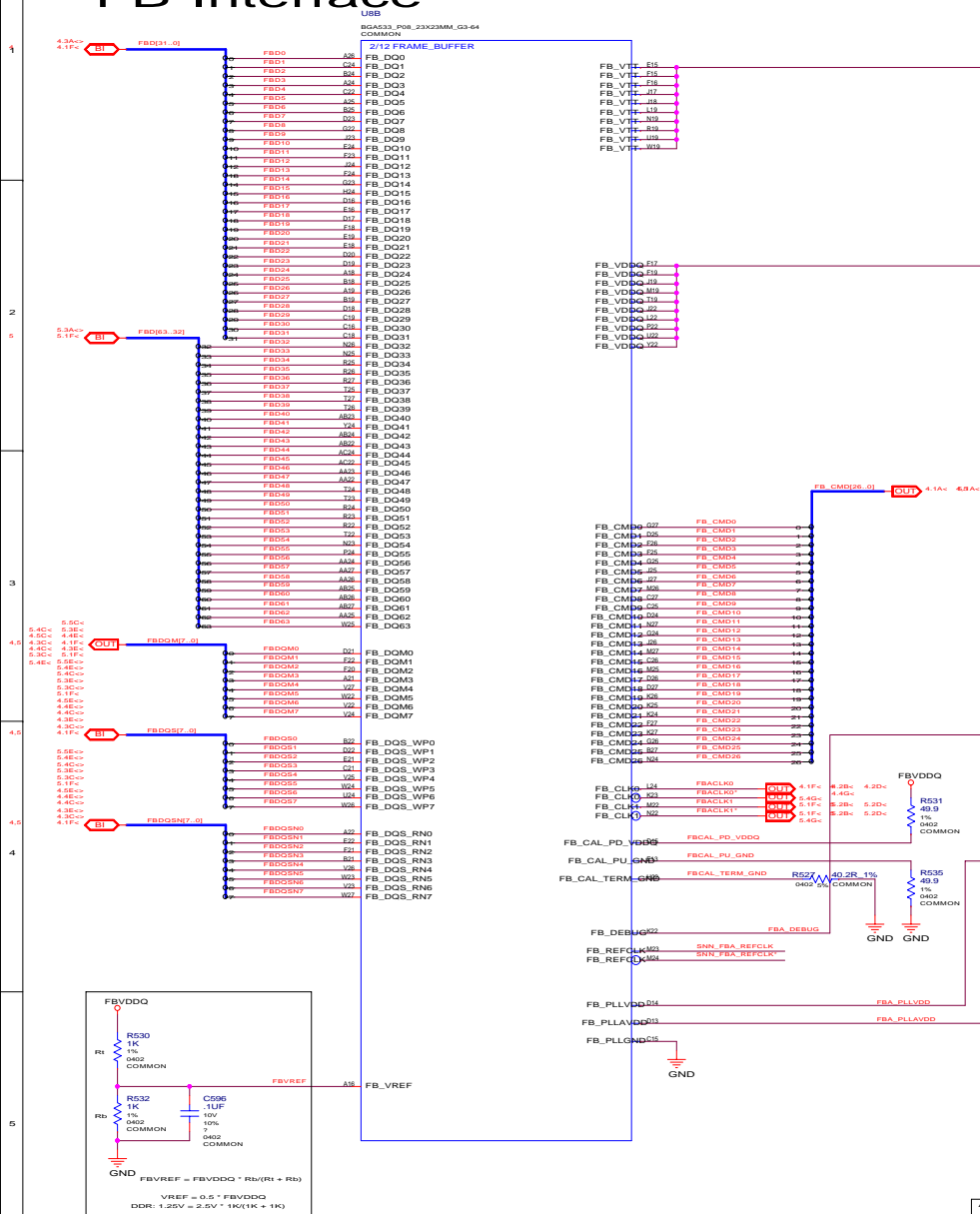
## PEX-Interface



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IN	PEX_T0TCLKN	PEX_T0T	25MM
IN	PEX_T0	PEX_T0	25MM
IN	PEX_T01	PEX_T01	25MM
IN	PEX_T02	PEX_T02	25MM
IN	PEX_T03	PEX_T03	25MM
IN	PEX_T04	PEX_T04	25MM
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IN	PEX_T07	PEX_T07	25MM
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IN	PEX_T11	PEX_T11	25MM
IN	PEX_T12	PEX_T12	25MM
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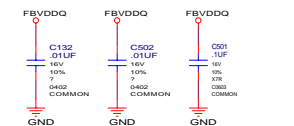


## GPU: FB-Interface



Net	NET_SPACING_RULE
IN Name	1.0MMIL
IN FPCAL_PU_GND	1.0MMIL
IN FPCAL_TIEIN_GND	1.0MMIL
IN FB_DLLVDD	1.0MMIL
IN FBA_PLLAVDD	1.0MMIL
IN FBVREF	1.0MMIL

## X-Caps for FB\_CMD BUS



## LABTESTPOINTS

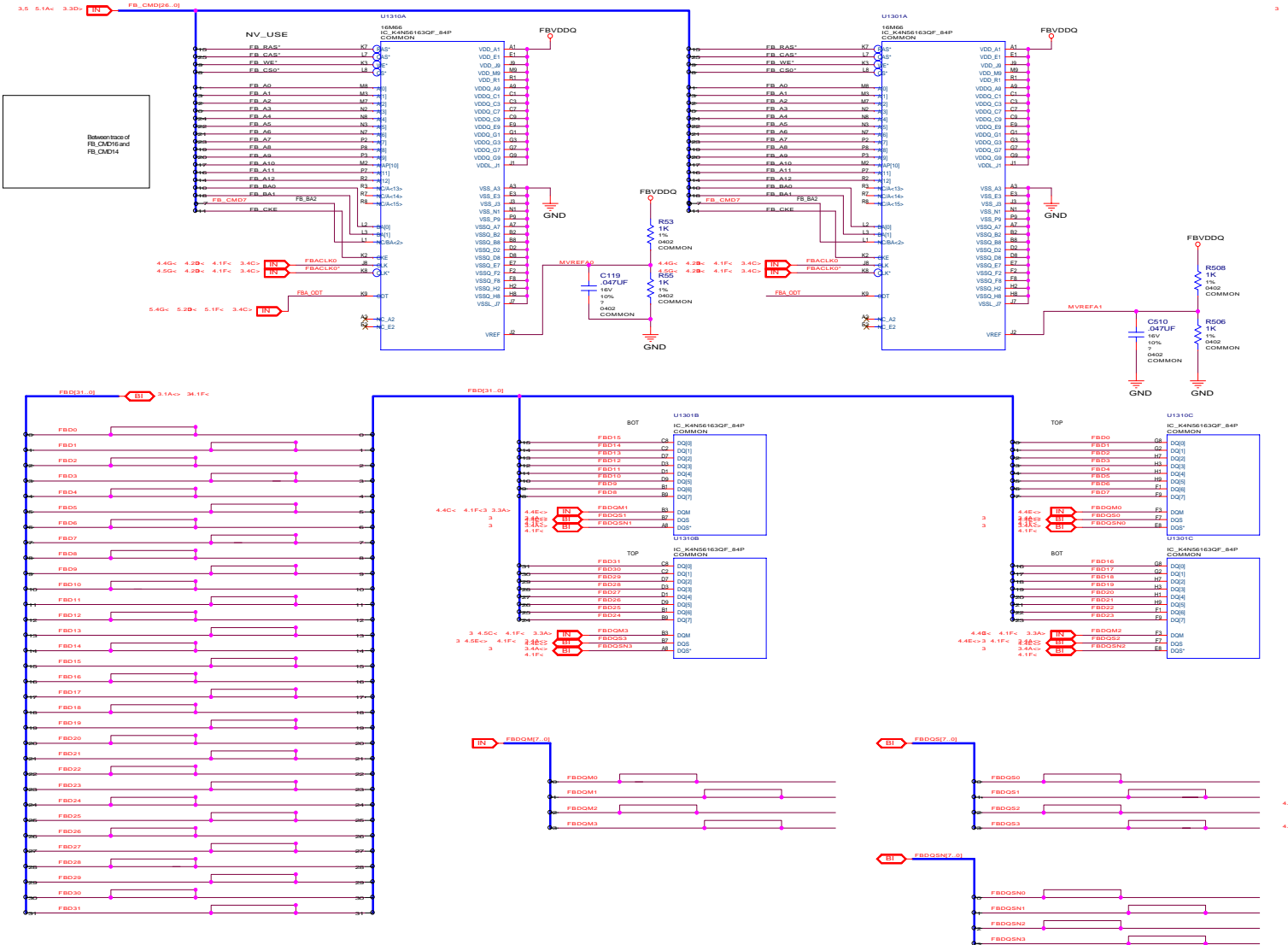


ASSEMBLY	BASE LEVEL GENERIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND
PAGE	BOM NOT FINAL Frame Buffer Interface

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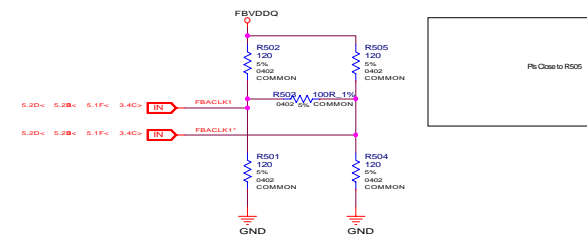
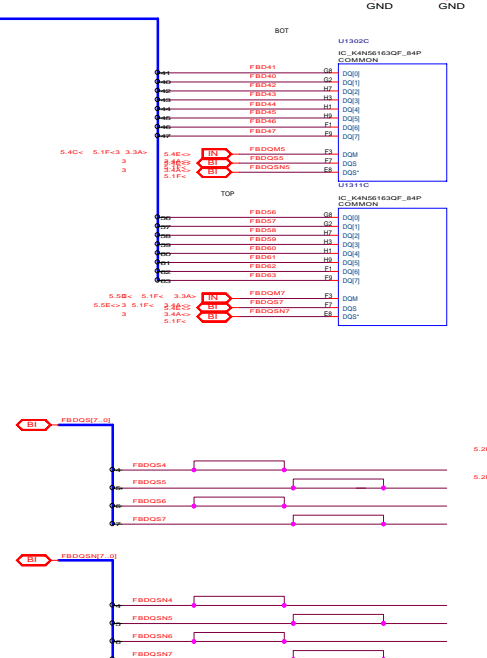
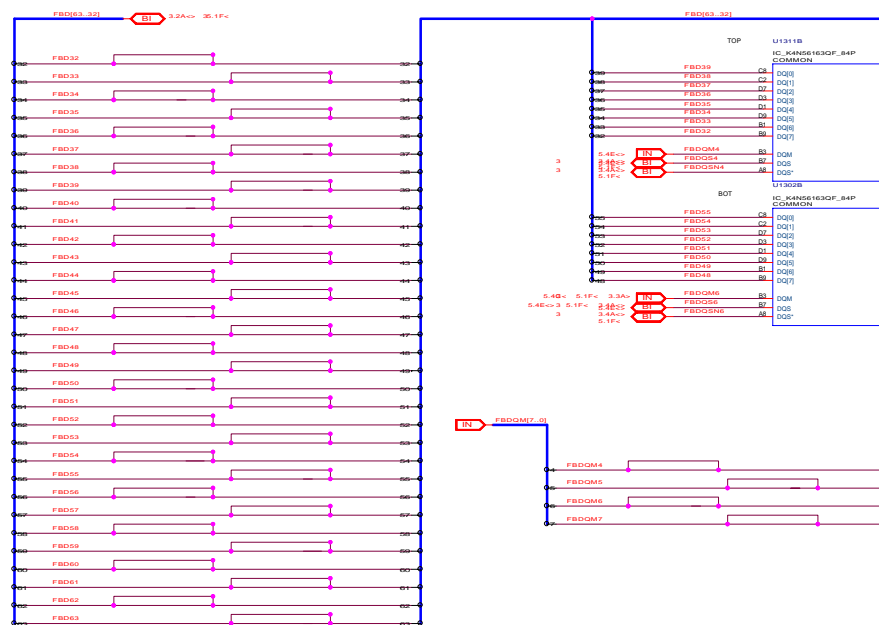
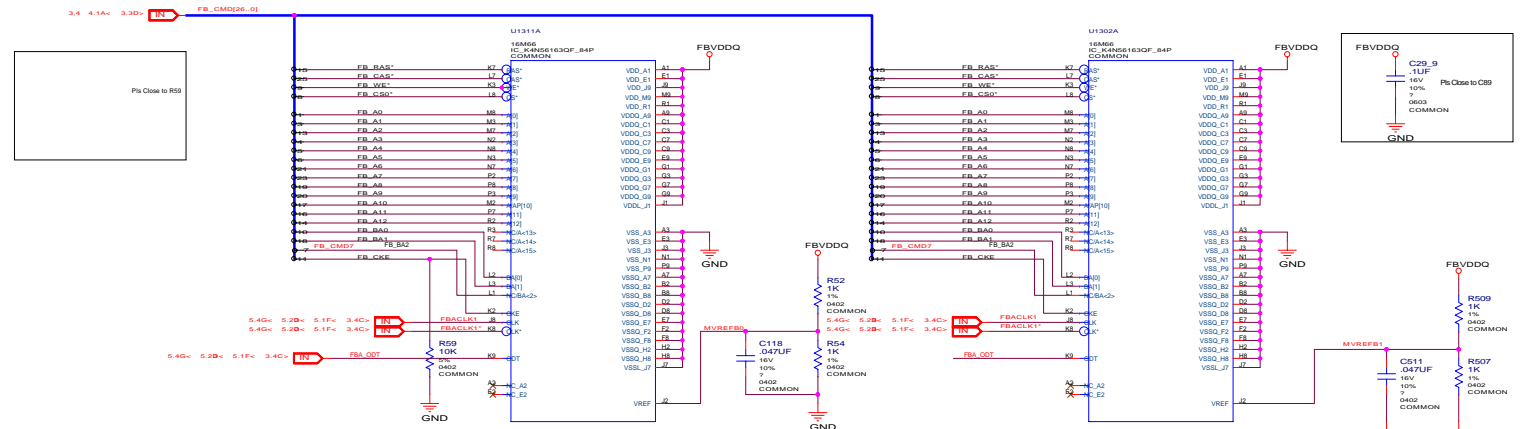
~~OR INDUSTRY STANDARDS~~

# Memory Bit 0..31

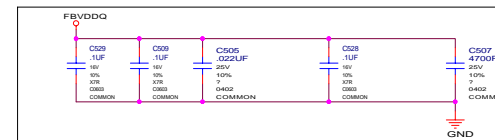
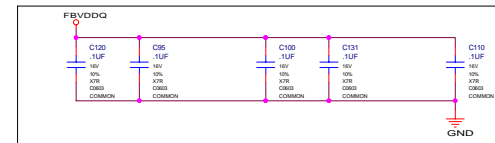


Net	Diffpair	NET_SPACING_RULE
FB_CMD[0..31]	FB_CMD[0..31]	25MIL
FB_CMD[32..63]	FB_CMD[32..63]	25MIL
FB_CMD[64..95]	FB_CMD[64..95]	25MIL
FB_CMD[96..127]	FB_CMD[96..127]	25MIL
FB_CMD[128..159]	FB_CMD[128..159]	25MIL
FB_CMD[160..191]	FB_CMD[160..191]	25MIL
FB_CMD[192..223]	FB_CMD[192..223]	25MIL
FB_CMD[224..255]	FB_CMD[224..255]	25MIL
FB_CMD[256..287]	FB_CMD[256..287]	25MIL
FB_CMD[288..319]	FB_CMD[288..319]	25MIL
FB_CMD[320..351]	FB_CMD[320..351]	25MIL
FB_CMD[352..383]	FB_CMD[352..383]	25MIL
FB_CMD[384..415]	FB_CMD[384..415]	25MIL
FB_CMD[416..447]	FB_CMD[416..447]	25MIL
FB_CMD[448..479]	FB_CMD[448..479]	25MIL
FB_CMD[480..511]	FB_CMD[480..511]	25MIL
FB_CMD[512..543]	FB_CMD[512..543]	25MIL
FB_CMD[544..575]	FB_CMD[544..575]	25MIL
FB_CMD[576..607]	FB_CMD[576..607]	25MIL
FB_CMD[608..639]	FB_CMD[608..639]	25MIL
FB_CMD[640..671]	FB_CMD[640..671]	25MIL
FB_CMD[672..703]	FB_CMD[672..703]	25MIL
FB_CMD[704..735]	FB_CMD[704..735]	25MIL
FB_CMD[736..767]	FB_CMD[736..767]	25MIL
FB_CMD[768..799]	FB_CMD[768..799]	25MIL
FB_CMD[800..831]	FB_CMD[800..831]	25MIL
FB_CMD[832..863]	FB_CMD[832..863]	25MIL
FB_CMD[864..895]	FB_CMD[864..895]	25MIL
FB_CMD[896..927]	FB_CMD[896..927]	25MIL
FB_CMD[928..959]	FB_CMD[928..959]	25MIL
FB_CMD[960..991]	FB_CMD[960..991]	25MIL
FB_CMD[992..1023]	FB_CMD[992..1023]	25MIL
FB_CMD[1024..1055]	FB_CMD[1024..1055]	25MIL
FB_CMD[1056..1087]	FB_CMD[1056..1087]	25MIL
FB_CMD[1088..1119]	FB_CMD[1088..1119]	25MIL
FB_CMD[1120..1151]	FB_CMD[1120..1151]	25MIL
FB_CMD[1152..1183]	FB_CMD[1152..1183]	25MIL
FB_CMD[1184..1215]	FB_CMD[1184..1215]	25MIL
FB_CMD[1216..1247]	FB_CMD[1216..1247]	25MIL
FB_CMD[1248..1279]	FB_CMD[1248..1279]	25MIL
FB_CMD[1280..1311]	FB_CMD[1280..1311]	25MIL
FB_CMD[1312..1343]	FB_CMD[1312..1343]	25MIL
FB_CMD[1344..1375]	FB_CMD[1344..1375]	25MIL
FB_CMD[1376..1407]	FB_CMD[1376..1407]	25MIL
FB_CMD[1408..1439]	FB_CMD[1408..1439]	25MIL
FB_CMD[1440..1471]	FB_CMD[1440..1471]	25MIL
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FB_CMD[1536..1567]	FB_CMD[1536..1567]	25MIL
FB_CMD[1568..1599]	FB_CMD[1568..1599]	25MIL
FB_CMD[1600..1631]	FB_CMD[1600..1631]	25MIL
FB_CMD[1632..1663]	FB_CMD[1632..1663]	25MIL
FB_CMD[1664..1695]	FB_CMD[1664..1695]	25MIL
FB_CMD[1696..1727]	FB_CMD[1696..1727]	25MIL
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FB_CMD[1760..1791]	FB_CMD[1760..1791]	25MIL
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FB_CMD[1920..1951]	FB_CMD[1920..1951]	25MIL
FB_CMD[1952..1983]	FB_CMD[1952..1983]	25MIL
FB_CMD[1984..2015]	FB_CMD[1984..2015]	25MIL
FB_CMD[2016..2047]	FB_CMD[2016..2047]	25MIL
FB_CMD[2048..2079]	FB_CMD[2048..2079]	25MIL
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FB_CMD[2112..2143]	FB_CMD[2112..2143]	25MIL
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FB_CMD[2240..2271]	FB_CMD[2240..2271]	25MIL
FB_CMD[2272..2303]	FB_CMD[2272..2303]	25MIL
FB_CMD[2304..2335]	FB_CMD[2304..2335]	25MIL
FB_CMD[2336..2367]	FB_CMD[2336..2367]	25MIL
FB_CMD[2368..2399]	FB_CMD[2368..2399]	25MIL
FB_CMD[2400..2431]	FB_CMD[2400..2431]	25MIL
FB_CMD[2432..2463]	FB_CMD[2432..2463]	25MIL
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FB_CMD[2976..3007]	FB_CMD[2976..3007]	25MIL
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FB_CMD[3072..3103]	FB_CMD[3072..3103]	25MIL
FB_CMD[3104..3135]	FB_CMD[3104..3135]	25MIL
FB_CMD[3136..3167]	FB_CMD[3136..3167]	25MIL
FB_CMD[3168..3199]	FB_CMD[3168..3199]	25MIL
FB_CMD[3200..3231]	FB_CMD[3200..3231]	25MIL
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FB_CMD[3264..3295]	FB_CMD[3264..3295]	25MIL
FB_CMD[3296..3327]	FB_CMD[3296..3327]	25MIL
FB_CMD[3328..3359]	FB_CMD[3328..3359]	25MIL
FB_CMD[3360..3391]	FB_CMD[3360..3391]	25MIL
FB_CMD[3392..3423]	FB_CMD[3392..3423]	25MIL
FB_CMD[3424..3455]	FB_CMD[3424..3455]	25MIL
FB_CMD[3456..3487]	FB_CMD[3456..3487]	25MIL
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FB_CMD[3520..3551]	FB_CMD[3520..3551]	25MIL
FB_CMD[3552..3583]	FB_CMD[3552..3583]	25MIL
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FB_CMD[3616..3647]	FB_CMD[3616..3647]	25MIL
FB_CMD[3648..3679]	FB_CMD[3648..3679]	25MIL
FB_CMD[3680..3711]	FB_CMD[3680..3711]	25MIL
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FB_CMD[3744..3775]	FB_CMD[3744..3775]	25MIL
FB_CMD[3776..3807]	FB_CMD[3776..3807]	25MIL
FB_CMD[3808..3839]	FB_CMD[3808..3839]	25MIL
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FB_CMD[3872..3903]	FB_CMD[3872..3903]	25MIL
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FB_CMD[3968..3999]	FB_CMD[3968..3999]	25MIL
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FB_CMD[4160..4191]	FB_CMD[4160..4191]	25MIL
FB_CMD[4192..4223]	FB_CMD[4192..4223]	25MIL
FB_CMD[4224..4255]	FB_CMD[4224..4255]	25MIL
FB_CMD[4256..4287]	FB_CMD[4256..4287]	25MIL
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FB_CMD[4320..4351]	FB_CMD[4320..4351]	25MIL
FB_CMD[4352..4383]	FB_CMD[4352..4383]	25MIL
FB_CMD[4384..4415]	FB_CMD[4384..4415]	25MIL
FB_CMD[4416..4447]	FB_CMD[4416..4447]	25MIL
FB_CMD[4448..4479]	FB_CMD[4448..4479]	25MIL
FB_CMD[4480..4511]	FB_CMD[4480..4511]	25MIL
FB_CMD[4512..4543]	FB_CMD[4512..4543]	25MIL
FB_CMD[4544..4575]	FB_CMD[4544..4575]	25MIL
FB_CMD[4576..4607]	FB_CMD[4576..4607]	25MIL
FB_CMD[4608..4639]	FB_CMD[4608..4639]	25MIL
FB_CMD[4640..4671]	FB_CMD[4640..4671]	25MIL
FB_CMD[4672..4703]	FB_CMD[4672..4703]	25MIL
FB_CMD[4704..4735]	FB_CMD[4704..4735]	25MIL
FB_CMD[4736..4767]	FB_CMD[4736..4767]	25MIL
FB_CMD[4768..4799]	FB_CMD[4768..4799]	25MIL
FB_CMD[4800..4831]	FB_CMD[4800..4831]	25MIL
FB_CMD[4832..4863]	FB_CMD[4832..4863]	25MIL
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FB_CMD[4896..4927]	FB_CMD[4896..4927]	25MIL
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FB_CMD[4992..5023]	FB_CMD[4992..5023]	25MIL
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FB_CMD[5056..5087]	FB_CMD[5056..5087]	25MIL
FB_CMD[5088..5119]	FB_CMD[5088..5119]	25MIL
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FB_CMD[5184..5215]	FB_CMD[5184..5215]	25MIL
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FB_CMD[5408..5439]	FB_CMD[5408..5439]	25MIL
FB_CMD[5440..5471]	FB_CMD[5440..5471]	25MIL
FB_CMD[5472..5503]	FB_CMD[5472..5503]	25MIL
FB_CMD[5504..5535]	FB_CMD[5504..5535]	25MIL
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FB_CMD[5728..5759]	FB_CMD[5728..5759]	25MIL
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FB_CMD[5952..5983]	FB_CMD[5952..5983]	25MIL
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FB_CMD[6112..6143]	FB_CMD[6112..6143]	25MIL
FB_CMD[6144..6175]	FB_CMD[6144..6175]	25MIL
FB_CMD[6176..6207]	FB_CMD[6176..6207]	25MIL
FB_CMD[6208..6239]	FB_CMD[6208..6239]	25MIL
FB_CMD[6240..6271]	FB_CMD[6240..6271]	25MIL
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FB_CMD[6304..6335]	FB_CMD[6304..6335]	25MIL
FB_CMD[6336..6367]	FB_CMD[6336..6367]	25MIL
FB_CMD[6368..6399]	FB_CMD[6368..6399]	25MIL
FB_CMD[6400..6431]	FB_CMD[6400..6431]	25MIL
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FB_CMD[6496..6527]	FB_CMD[6496..6527]	25MIL
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FB_CMD[7072..7103]	FB_CMD[7072..7103]	25MIL
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FB_CMD[7264..7295]	FB_CMD[7264..7295]	25MIL
FB_CMD[7296..7327]	FB_CMD[7296..7327]	25MIL
FB_CMD[7328..7359]	FB_CMD[7328..7359]	25MIL
FB_CMD[7360..7391]	FB_CMD[7360..7391]	25MIL
FB_CMD[7392..7423]	FB_CMD[7392..7423]	25MIL
FB_CMD[7424..7455]	FB_CMD[7424..7455]	25MIL
FB_CMD[7456..7487]	FB_CMD[7456..7487]	25MIL
FB_CMD[7488..7519]	FB_CMD[7488..7519]	25MIL
FB_CMD[7520..7551]	FB_CMD[7520..7551]	25MIL
FB_CMD[7552..7583]	FB_CMD[7552..7583]	25MIL
FB_CMD[7584..7615]	FB_CMD[7584..7615]	25MIL
FB_CMD[7616..7647]	FB_CMD[7616..7647]	25MIL
FB_CMD[7648..7679]	FB_CMD[7648..7679]	25MIL
FB_CMD[7680..7711]	FB_CMD[7680..7711]	25MIL
FB_CMD[7712..7743]	FB_CMD[7712..7743]	25MIL
FB_CMD[7744..7775]	FB_CMD[7744..7775]	25MIL
FB_CMD[7776..7807]	FB_CMD[7776..7807]	25MIL
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FB_CMD[7872..7903]	FB_CMD[7872..7903]	25MIL
FB_CMD[7904..7935]	FB_CMD[7904..7935]	25MIL
FB_CMD[7936..7967]	FB_CMD[7936..7967]	25MIL
FB_CMD[7968..7999]	FB_CMD[7968..7999]	25MIL
FB_CMD[8000..8031]	FB_CMD[8000..8031]	25MIL
FB_CMD[8032..8063]	FB_CMD[8032..8063]	25MIL
FB_CMD[8064..8095]	FB_CMD[8064..8095]	25MIL
FB_CMD[8096..8127]	FB_CMD[8096..8127]	25MIL
FB_CMD[8128..8159]	FB_CMD[8128..8159]	25MIL
FB_CMD[8160..8191]	FB_CMD[8160..8191]	25MIL
FB_CMD[8192..8223]	FB_CMD[8192..8223]	25MIL
FB_CMD[8224..8255]	FB_CMD[8224..8255]	25MIL
FB_CMD[8256..8287]	FB_CMD[8256..8287]	25MIL
FB_CMD[8288..8319]	FB_CMD[8288..8319]	25MIL
FB_CMD[8320..8351]	FB_CMD[8320..8351]	25MIL
FB_CMD[8352		

## Memory Bit 32..63

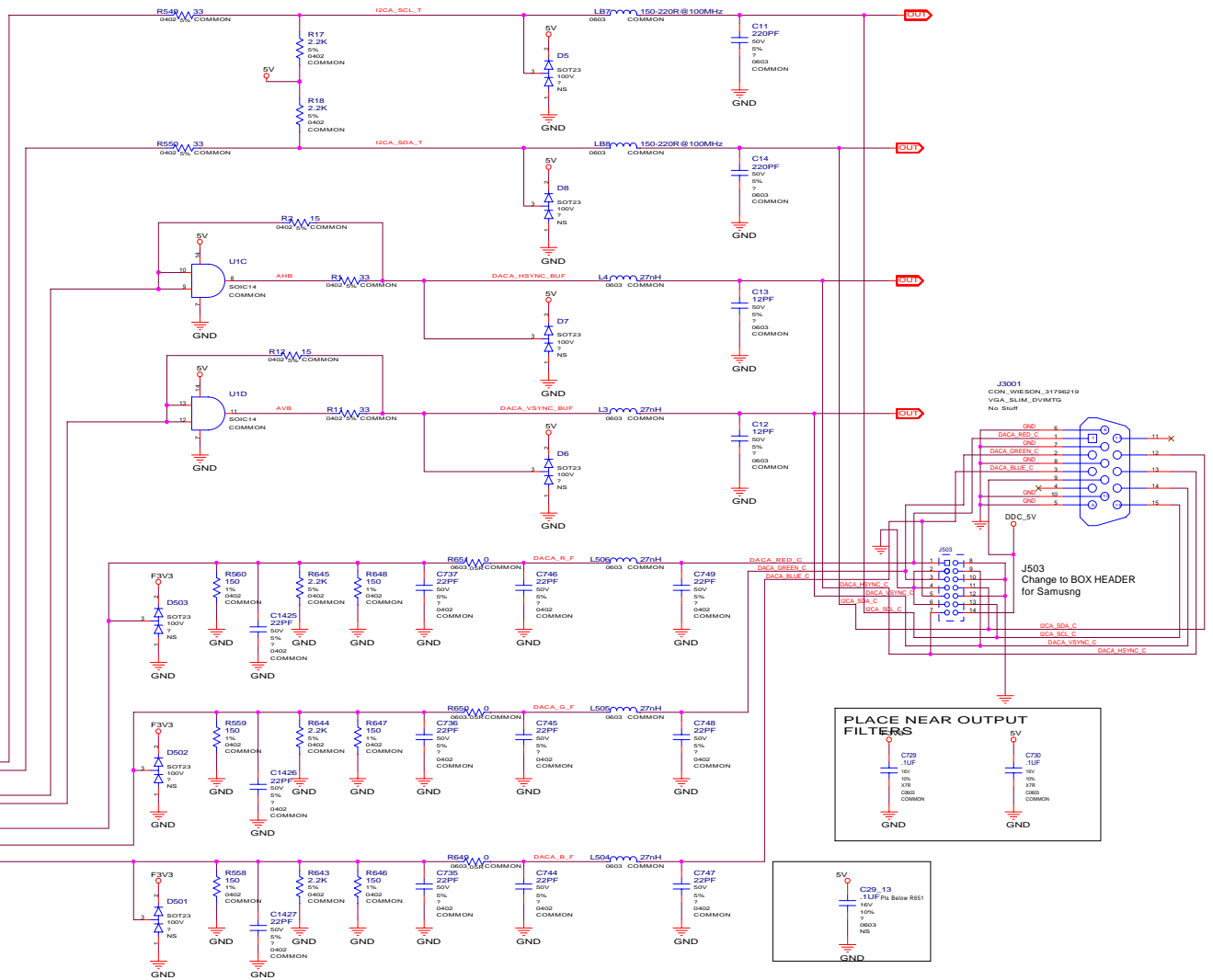
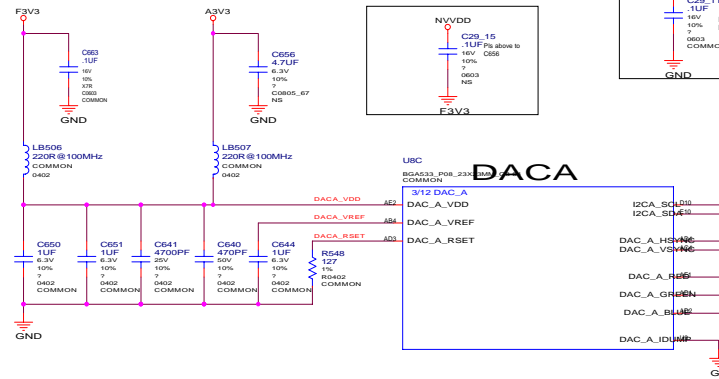
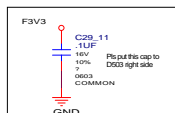
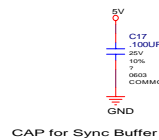


Net	Diffpair	NET_SPACING_RULE
IN	FBACLK1	25MMI
IN	FBACLK1	25MMI
IN	FBIOB3_30	10MMI
IN	FBIOB3_4	10MMI
IN	FBIOB5_7_4	10MMI
IN	FBIOB5_7_4	10MMI



## DAC-A, DB15 Connector

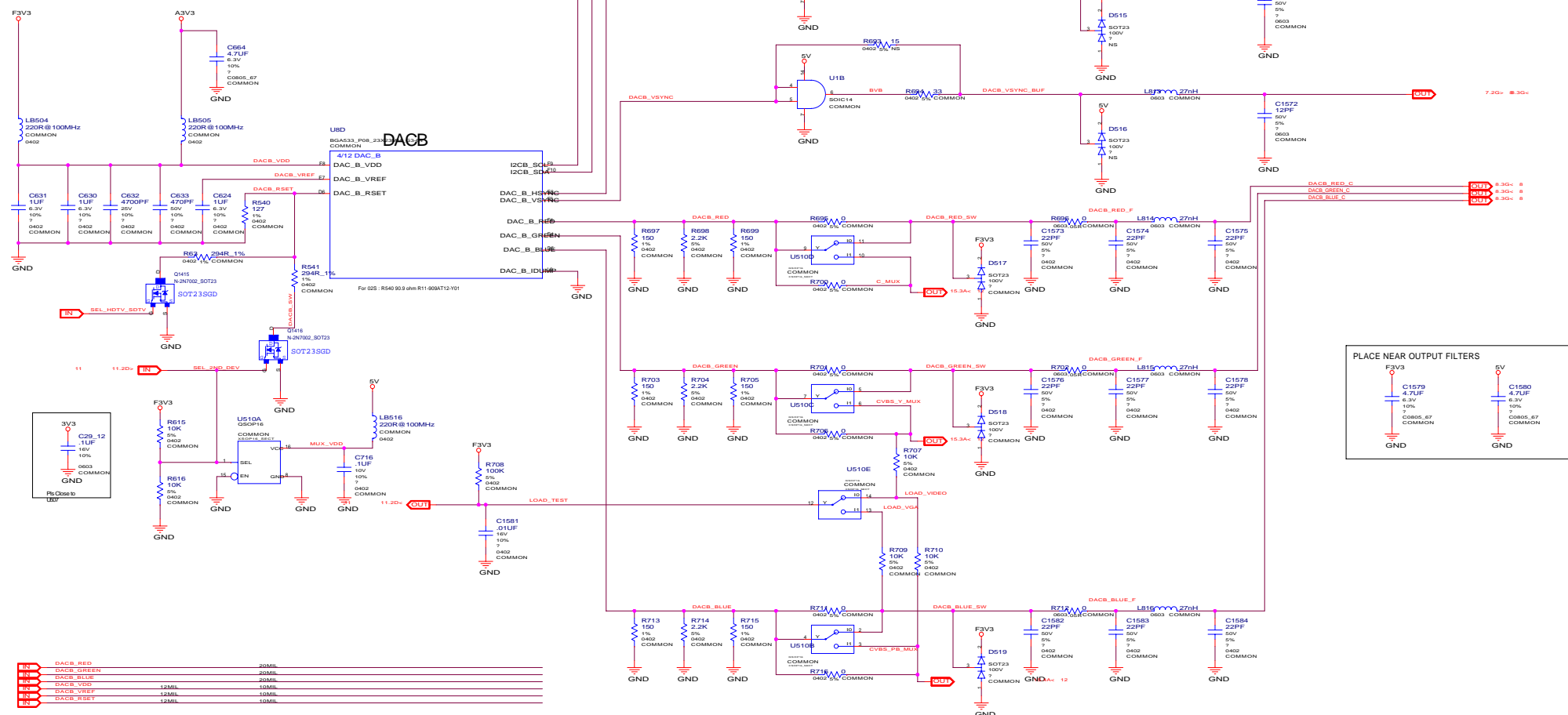
Net		MIN_LINE_WIDTH=NET_SPACING_TYPE
	<b>Name</b>	
R21	NET_1	10MIL
R22	DACA_18V2V6	10MIL
R23	DACA_18V2V6	10MIL
R24	DACA_VEST	10MIL
R25	DACA_GREEN5	20MIL
R26	DACA_BLUE	20MIL
R27	DACA_VDD	20MIL
R28	DACA_VDD	12MIL
R29	DACA_R_F	20MIL
R30	DACA_R_F	20MIL
R31	DACA_R_F	20MIL
R32	DACA_RED_6	20MIL
R33	DACA_GREEN_6	20MIL
R34	DACA_BLUE_6	20MIL



ASSEMBLY	BASE LEVEL GENERIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND
PAGE	BOM, NOT FINAL
	DACC, DACC, DACC, Sync

	DETAIL	Bulldoz
<p>ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OF INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE PROVIDED AS IS. THE MATERIALS MAY CONTAIN ERRORS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED OR IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS, AND EXPRESSLY DISCLAIMS ANY WARRANTY OF MERCHANTABILITY, NON-INFRINGEMENT, OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.</p>		

~~TRADE PRACTICE, OR INDUSTRY STANDARDS.~~

DAC-B, MUX,  
DB15

### PLACE NEAR OUTPUT FILTERS

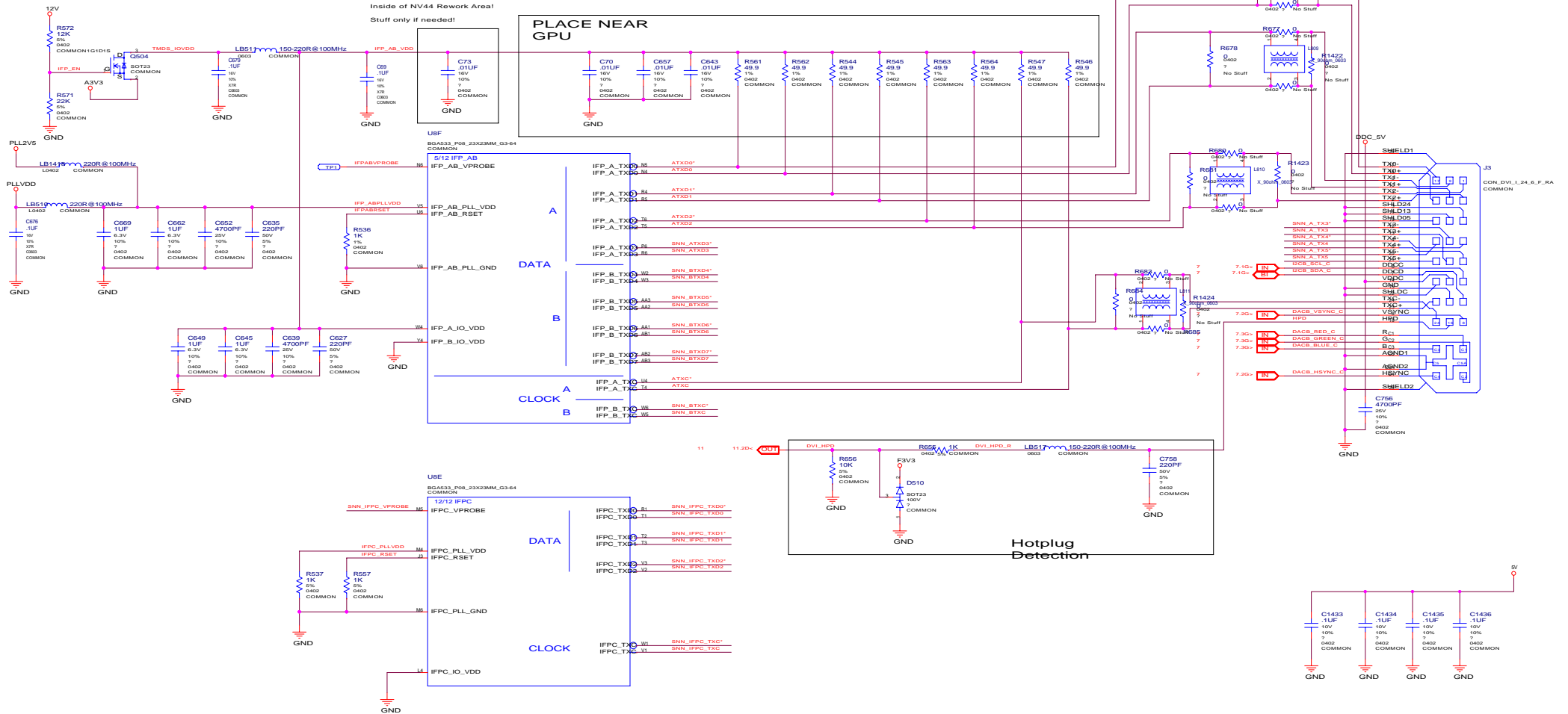
Diagram illustrating the placement of electrolytic capacitors near output filters for two voltage regulators:

- Left Circuit (3V3):** A 3V3 regulator is connected to a 6.3V 10% electrolytic capacitor (C1579, 4.7UF) which is connected to GND. The capacitor is marked with a 7 and COB05, 67 COMMON.
- Right Circuit (5V):** A 5V regulator is connected to a 6.3V 10% electrolytic capacitor (C1580, 4.7UF) which is connected to GND. The capacitor is marked with a 7 and COB05, 67 COMMON.

IN	DACS_RED	20MIL
IN	DACS_GREEN	20MIL
IN	DACS_BLUE	20MIL
IN	DACS_VDD	12MIL
IN	DACS_VREF	10MIL
IN	DACS_RSET	10MIL

## Internal TMDS, DVI-Connector

Net Name	Diffpair	NET_SPACING_RULE	Voltage
RP_2A01_VDD			3.3V
RP_AB_VDD			3.3V
ATX01	ATX0	250M	
ATX02	ATX0	250M	
ATX03	ATX00	250M	
ATX04	ATX00	250M	
ATX05	ATX01	250M	
ATX06	ATX01	250M	
ATX07	ATX02	250M	
ATX08	ATX02	250M	



ASSEMBLY PAGE	BASE LEVEL GENERIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL TMDs
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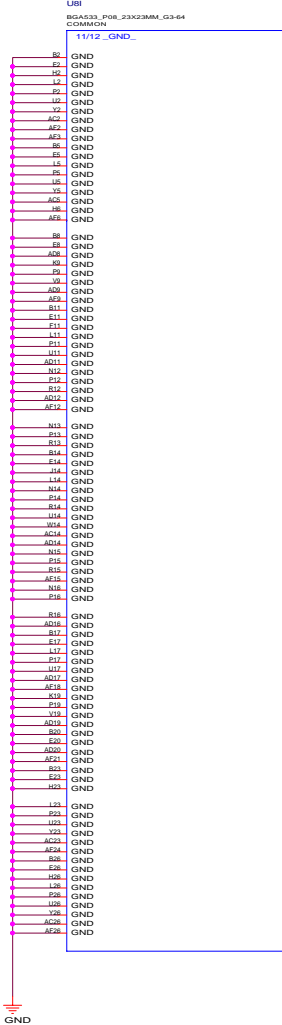
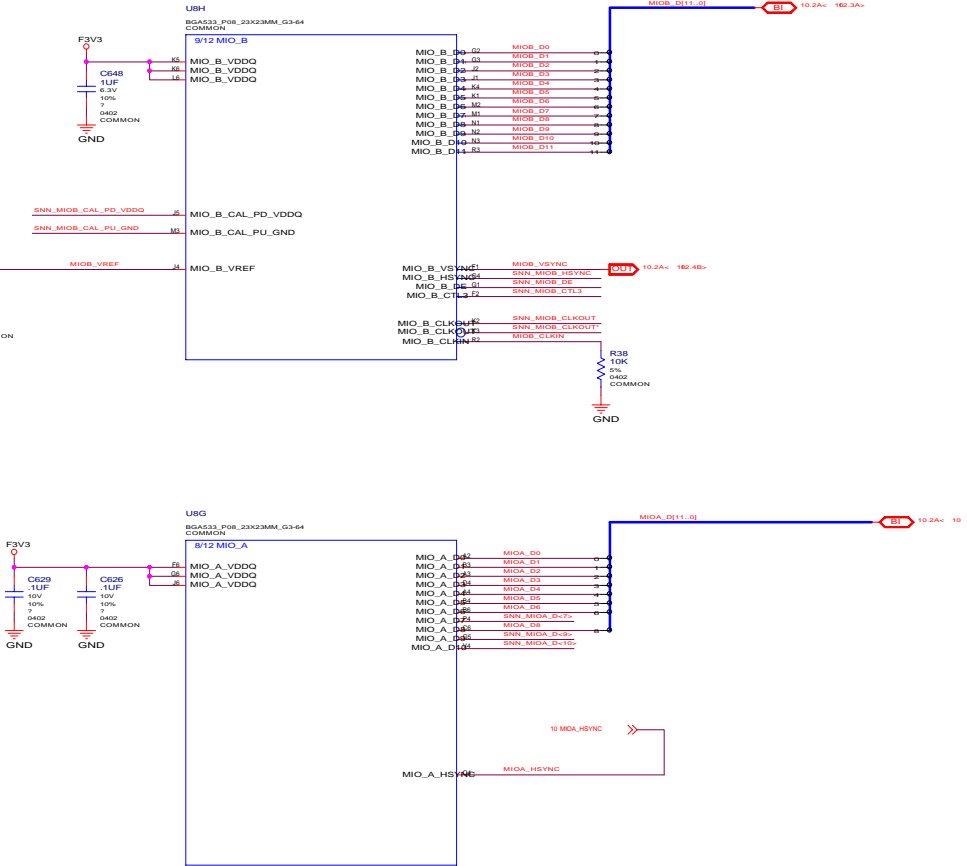
V034

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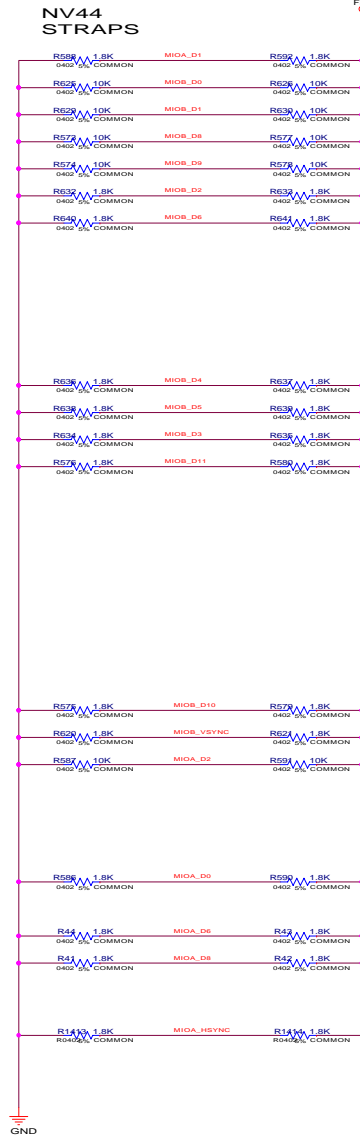
MIOA, MIOB  
Interface,  
LPC-ROM



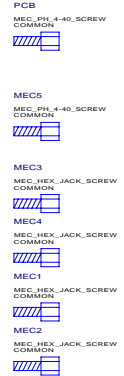
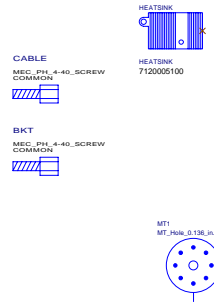
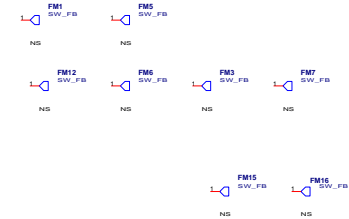
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ASSEMBLY	LPC_ROM
PAGE	MIOA, MIOB Interface.

# STRAPS, Mechanical Parts

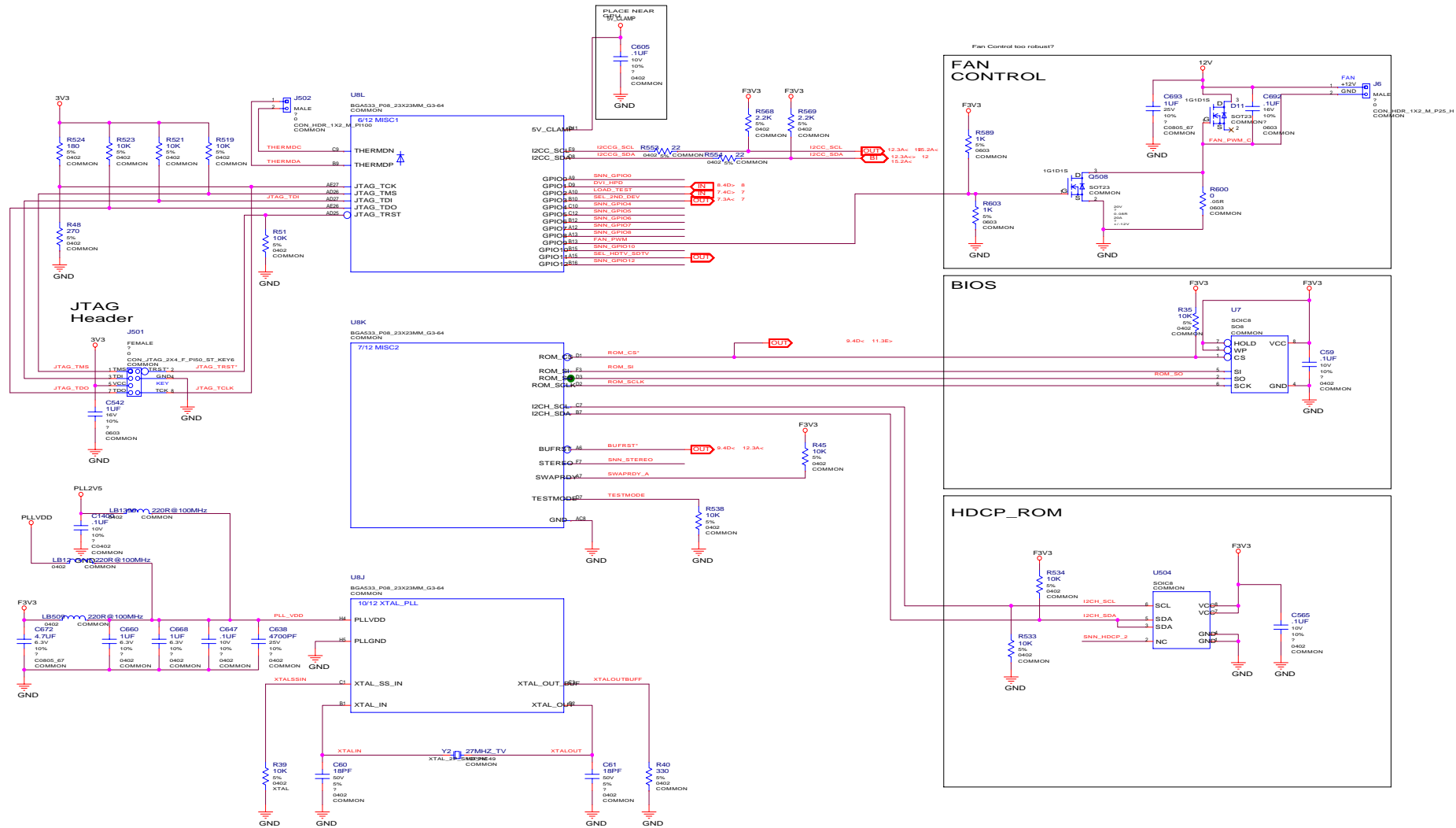


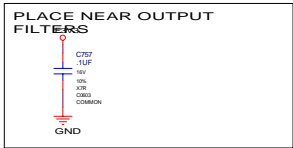
Bit	Signal	Values
00:	PCI_AD_SWAP	0 REVERSED 1 NORMAL
01:	SUBV_VENDOR	0 VENDOR FROM BIOS 1 VENDOR FROM ROM
02:	RAM_CFG_0	0000 1000 0001 8Mx16DDR 0010 16Mx16DDR 0011 8Mx16DDR 0100 16Mx16DDR 0101 8Mx16DDR 0110 16Mx16DDR 0111 8Mx16DDR 1000 16Mx16DDR 1001 16Mx16DDR 1010 16Mx16DDR 1011 16Mx16DDR 1100 16Mx16DDR 1101 16Mx16DDR 1110 16Mx16DDR 1111 16Mx16DDR
03:	RAM_CFG_1	0000 1000 0001 8Mx16DDR 0010 16Mx16DDR 0011 8Mx16DDR 0100 16Mx16DDR 0101 8Mx16DDR 0110 16Mx16DDR 0111 8Mx16DDR 1000 16Mx16DDR 1001 16Mx16DDR 1010 16Mx16DDR 1011 16Mx16DDR 1100 16Mx16DDR 1101 16Mx16DDR 1110 16Mx16DDR 1111 16Mx16DDR
04:	RAM_CFG_2	0000 1000 0001 8Mx16DDR 0010 16Mx16DDR 0011 8Mx16DDR 0100 16Mx16DDR 0101 8Mx16DDR 0110 16Mx16DDR 0111 8Mx16DDR 1000 16Mx16DDR 1001 16Mx16DDR 1010 16Mx16DDR 1011 16Mx16DDR 1100 16Mx16DDR 1101 16Mx16DDR 1110 16Mx16DDR 1111 16Mx16DDR
05:	RAM_CFG_3	0000 1000 0001 8Mx16DDR 0010 16Mx16DDR 0011 8Mx16DDR 0100 16Mx16DDR 0101 8Mx16DDR 0110 16Mx16DDR 0111 8Mx16DDR 1000 16Mx16DDR 1001 16Mx16DDR 1010 16Mx16DDR 1011 16Mx16DDR 1100 16Mx16DDR 1101 16Mx16DDR 1110 16Mx16DDR 1111 16Mx16DDR
06:	CRYSTAL_0	00 13.500 Mhz 01 14.31818 Mhz 10 19.200 Mhz 11 UNKNOWN
07:	TV_MODE_0	00 0FCAM 01 0FCAM 10 0FCAM 11 0FCAM
08:	TV_MODE_1	00 0FCAM 01 0FCAM 10 0FCAM 11 0FCAM
09:	AGP_30_8x	0 AGP8x 1 AGP8x 2 AGP8x 3 AGP8x 4 AGP8x 5 AGP8x 6 AGP8x 7 AGP8x 8 AGP8x 9 AGP8x 10 AGP8x 11 AGP8x
10:	AGP_SBA	0 SBA 1 SBA 2 SBA 3 SBA 4 SBA 5 SBA 6 SBA 7 SBA 8 SBA 9 SBA 10 SBA 11 SBA
11:	AGP_FASTWR	0 FW 1 FW 2 FW 3 FW 4 FW 5 FW 6 FW 7 FW 8 FW 9 FW 10 FW 11 FW
12:	PCI_DEVID_0	1100 (default 0x00FC)
13:	PCI_DEVID_1	
20:	PCI_DEVID_2	
21:	PCI_DEVID_3	
14:	BUS_TYPE	0 PCI 1 AGP
15:	FP_FACE	0 24BR 1 12BR (DEFAULT)
23:	FB_0	00 0PM 01 125PM 10 125PM 11 125PM
24:	FB_1	00 0PM 01 125PM 10 125PM 11 125PM
25:	BR	0 BRIDGE 1 BRIDGE 2 BRIDGE 3 BRIDGE 4 BRIDGE 5 BRIDGE 6 BRIDGE 7 BRIDGE 8 BRIDGE 9 BRIDGE 10 BRIDGE 11 BRIDGE
26:	BR_128M	0 BRIDGE IGNORED IF BRIDGE IS DISABLED 1 BRIDGE IGNORED IF BRIDGE IS DISABLED
27:	BR_AGP	0 BRIDGE IGNORED IF BRIDGE IS DISABLED 1 BRIDGE IGNORED IF BRIDGE IS DISABLED
28:	BR_IO	0 BRIDGE IGNORED IF BRIDGE IS DISABLED 1 BRIDGE IGNORED IF BRIDGE IS DISABLED
29:	ROM_TYPE_0	00 0PARALLEL 01 0PARALLEL 10 0PARALLEL 11 0PARALLEL
30:	ROM_TYPE_1	00 0PARALLEL 01 0PARALLEL 10 0PARALLEL 11 0PARALLEL
16:	USER_0	0000 (DEFAULT)
17:	USER_1	
18:	USER_2	
19:	USER_3	
PEX_PLL_EN_TERM100		
3GIO_PADCFG_LUT_ADDR[0]		
3GIO_PADCFG_LUT_ADDR[1]		



## XTAL, GPIO, BIOS, Fan Control, JTAG Headers

Net		NET_PHYSICAL_TYPENET_SPACING_RULE
RV	Name	
RV	PLLVDD	5MIL_TRACE 20MIL
RV	PLLVDD	5MIL_TRACE 20MIL
RV	PLLVDD	12MIL_TRACE 10MIL
RV	DHSP_PLLVDD	12MIL_TRACE 10MIL



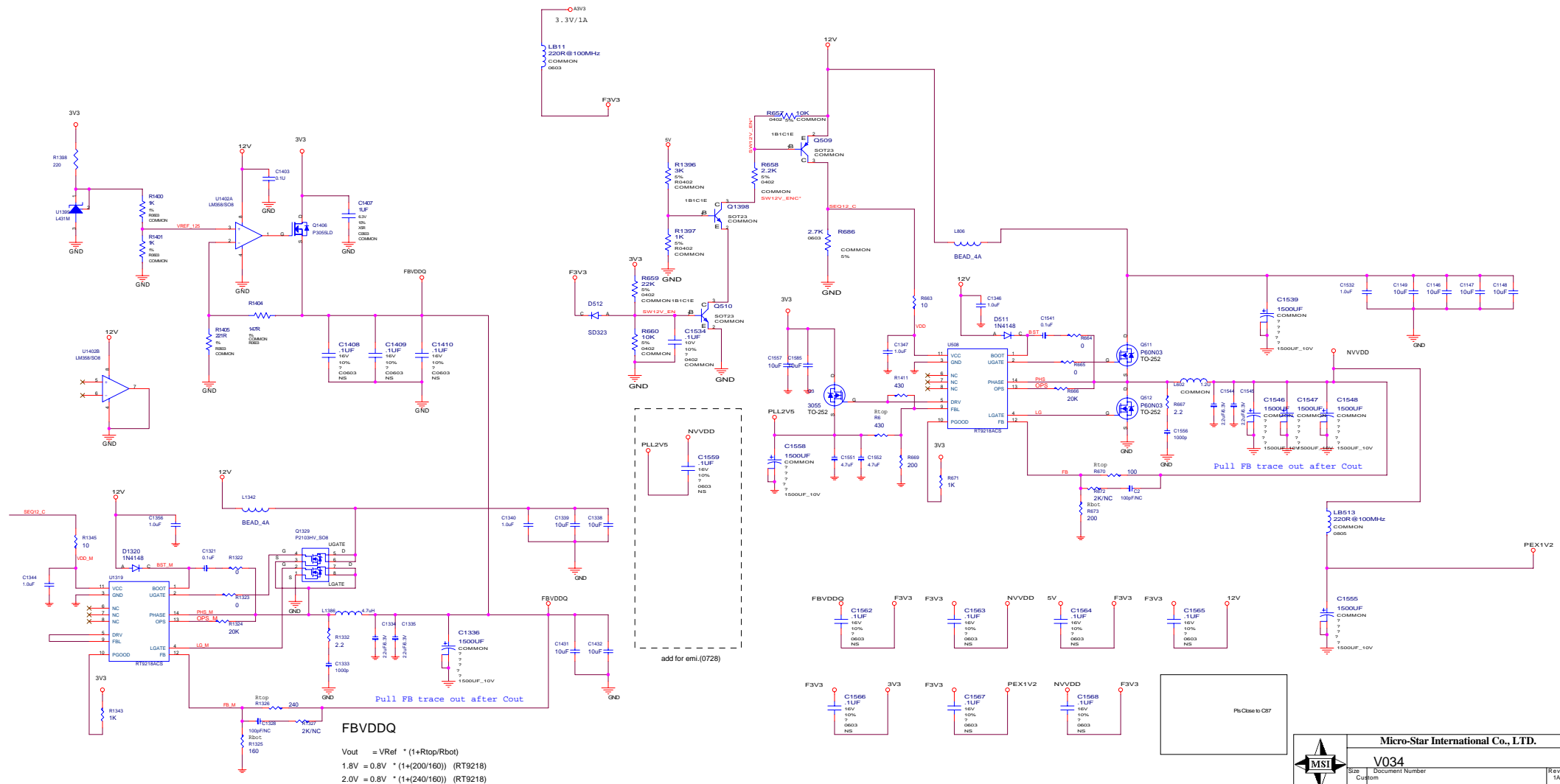


ASSEMBLY	BASE LEVEL GENERIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND
PAGE	FROM NOT FINAL
DETAIL	PRO CONNECTORS: MiniDIN, 2x6
	HCR

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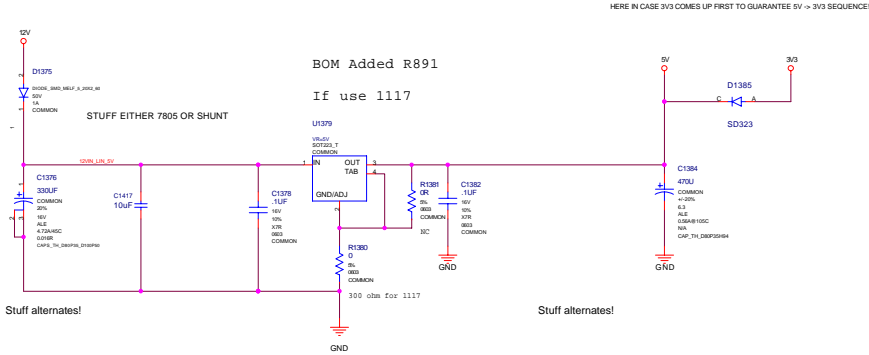
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## PowerSupplyII: 5V, DDC5V, A3V3, F3V3, TMDS\_PLLVDD



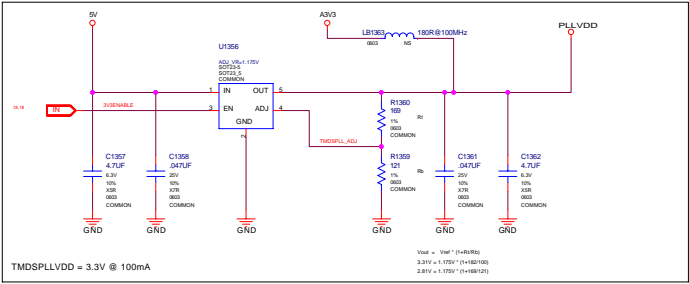
# 14 Others Power Supply (Linears)

## 5V,FBVDDQ,A3V3,3V3,TMDS\_PLLVDD,TMDS\_IOVDD,FBVTT

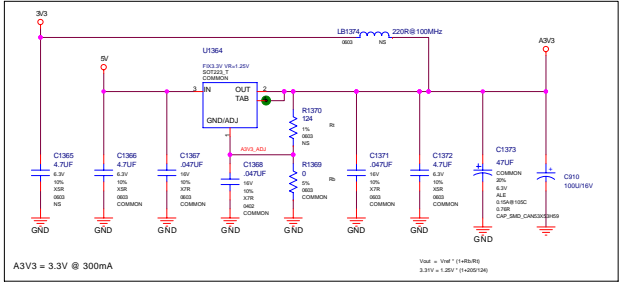


## 5V LOW COST REGULATOR

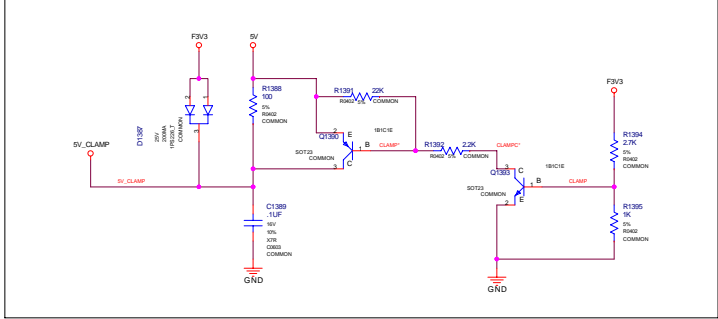
TMDS PLL Supply



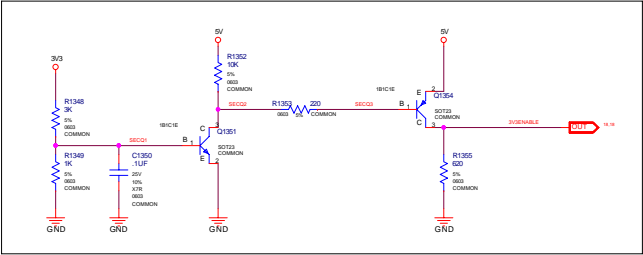
A3V3 Power Supply



5V\_CLAMP



Power Sequencing



5V and PEX3V3 UP . ALL OTHER LINEARS UP

