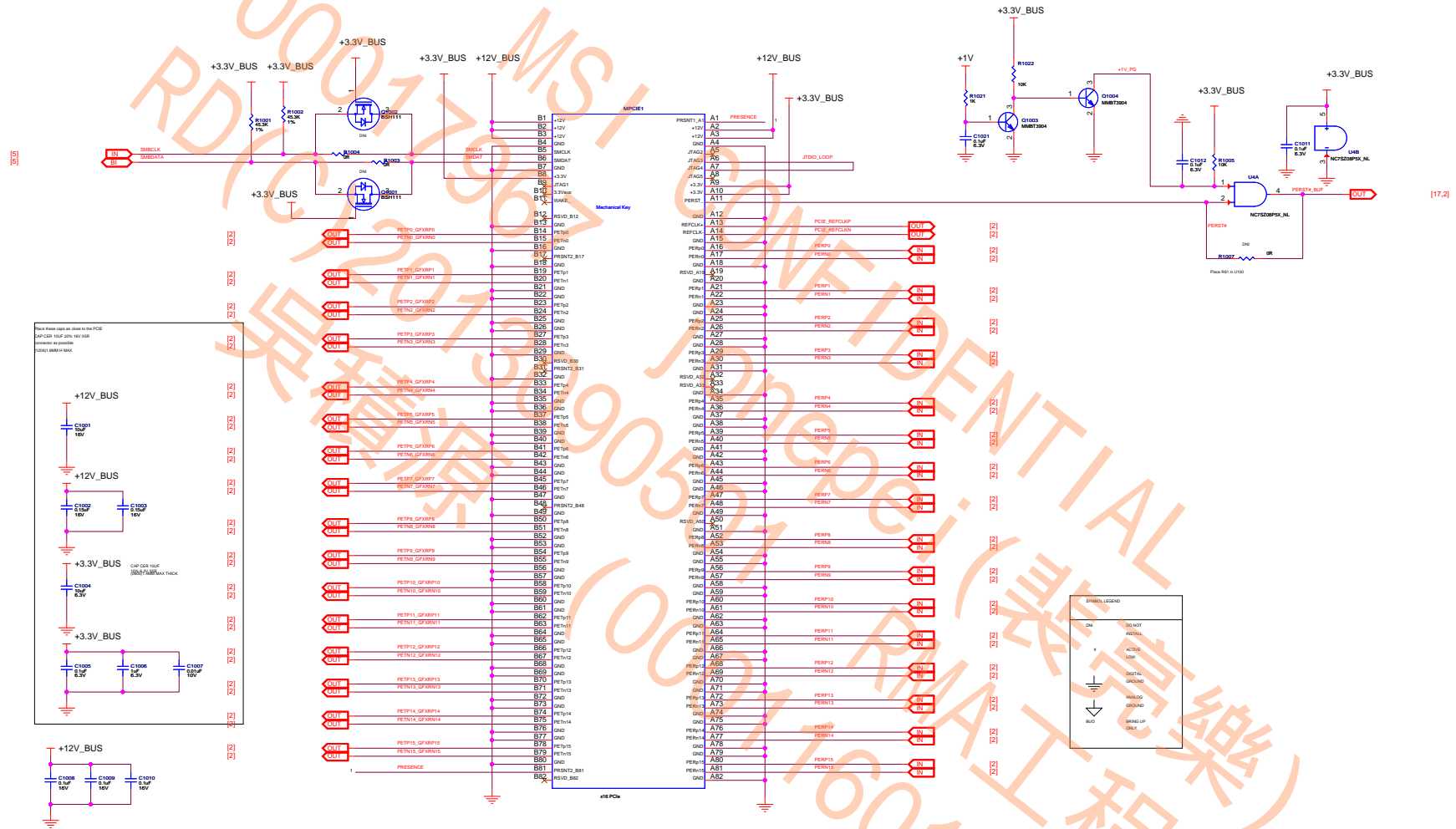
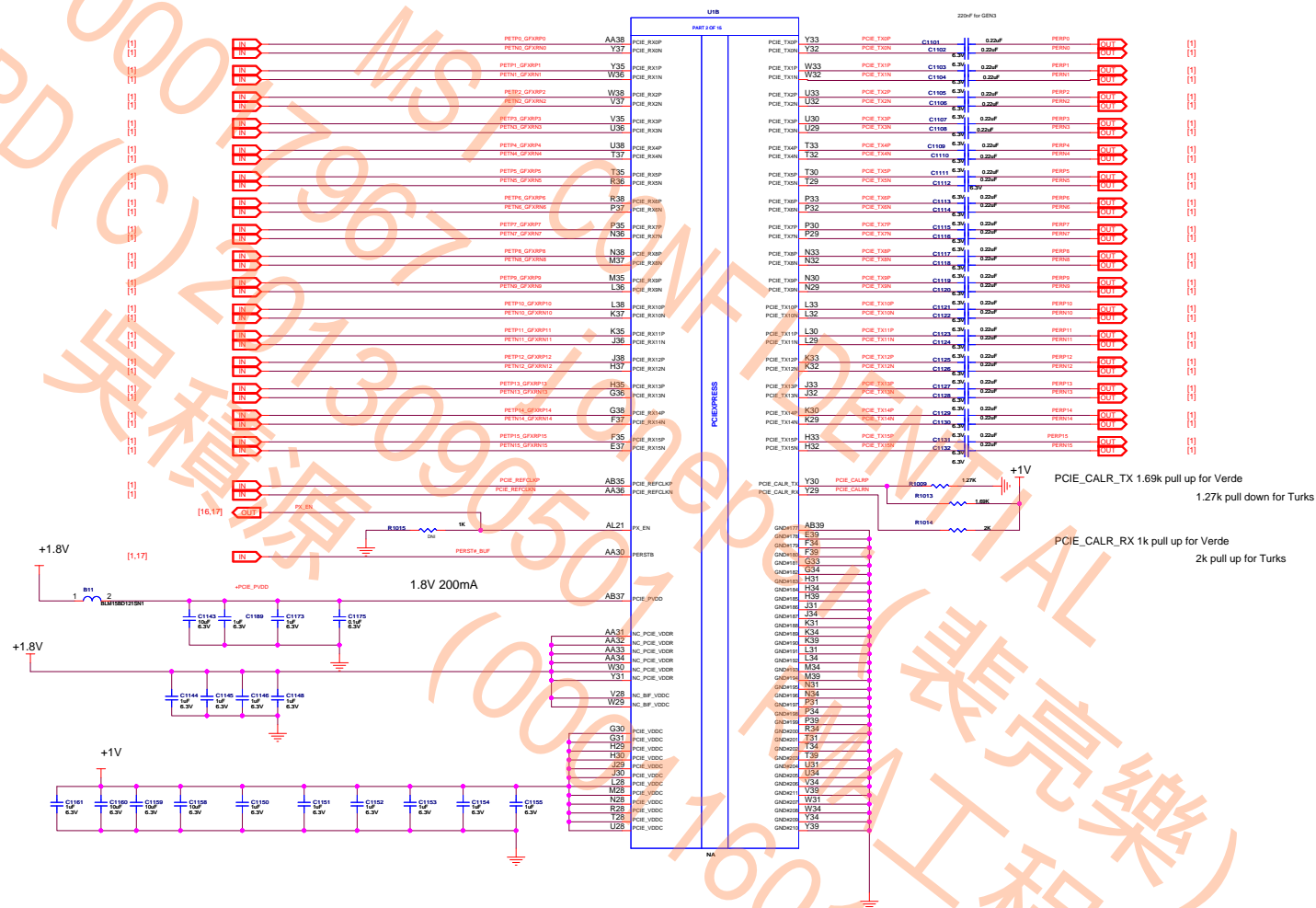
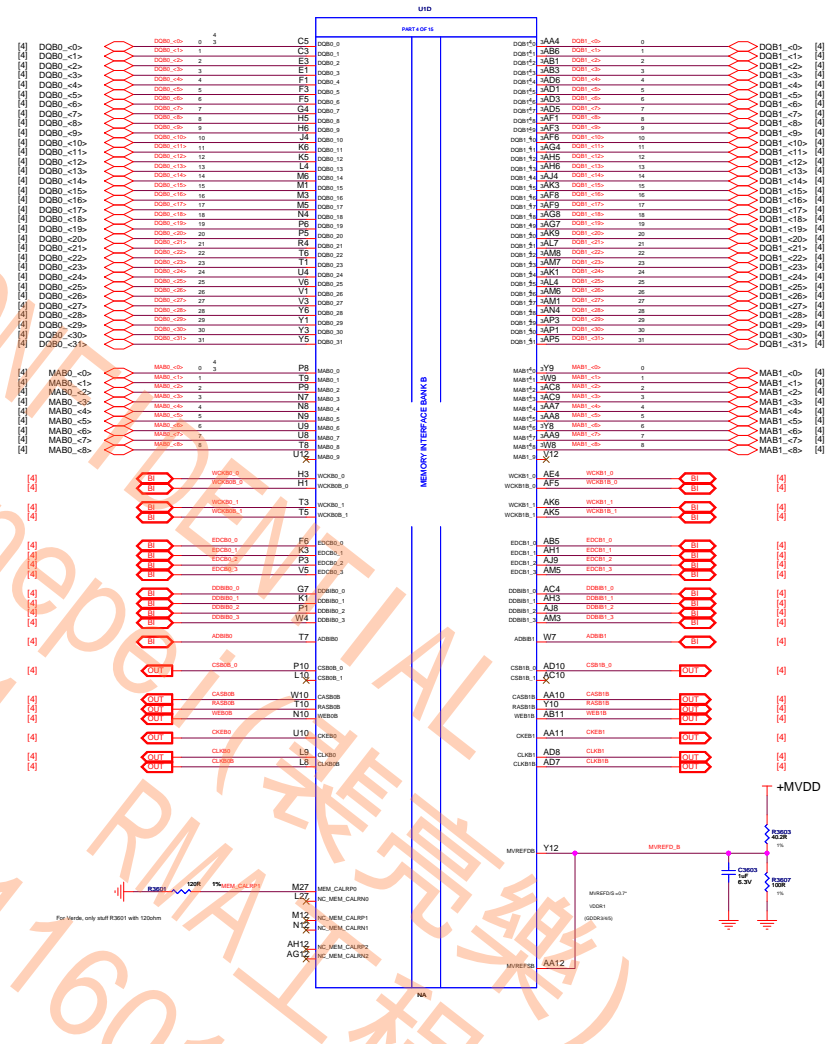
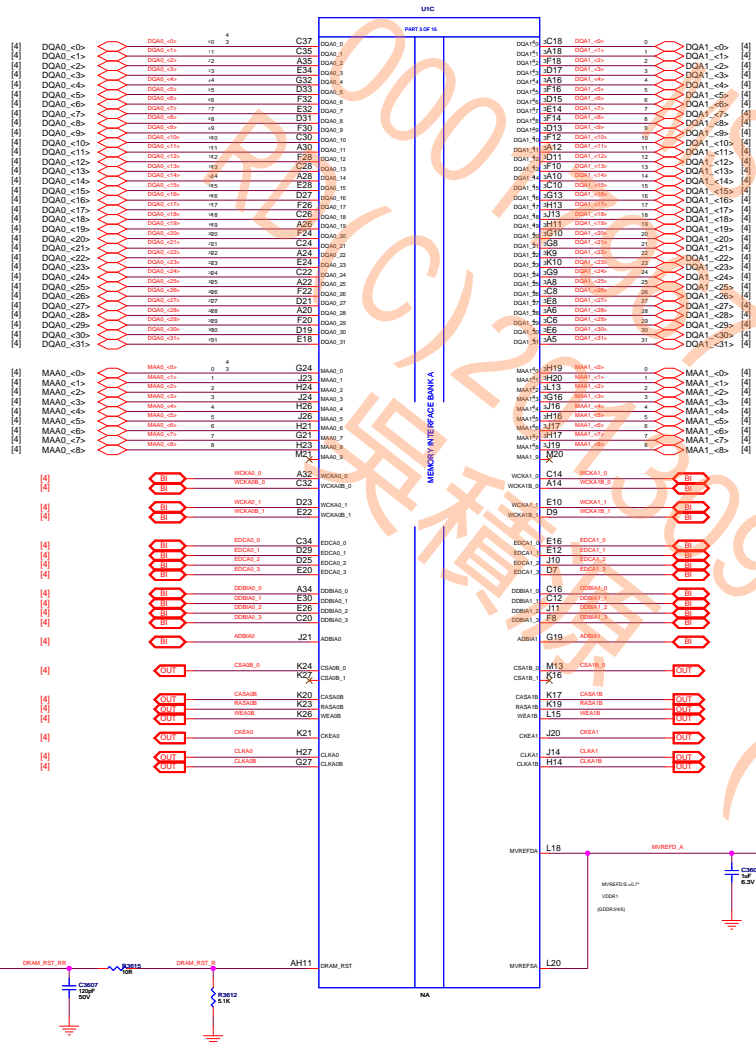


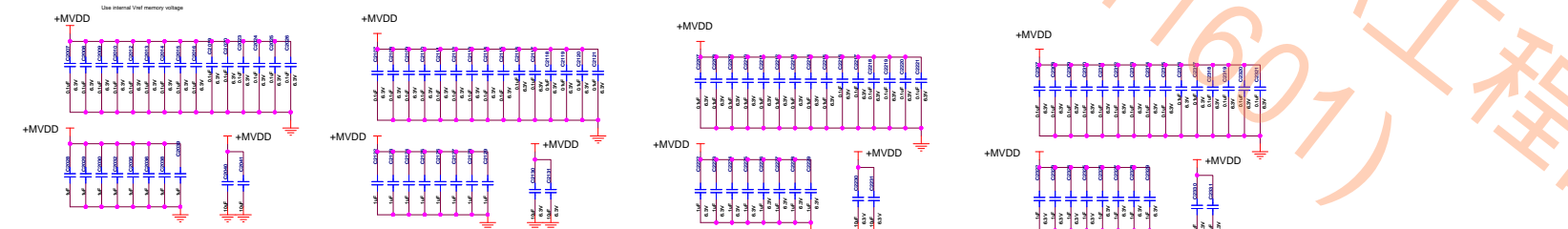
PCI-EXPRESS EDGE CONNECTOR



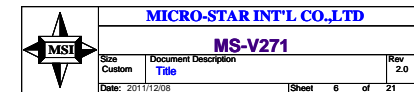
NOTE: Some of the PCIE testpoints will be available through vias on traces.

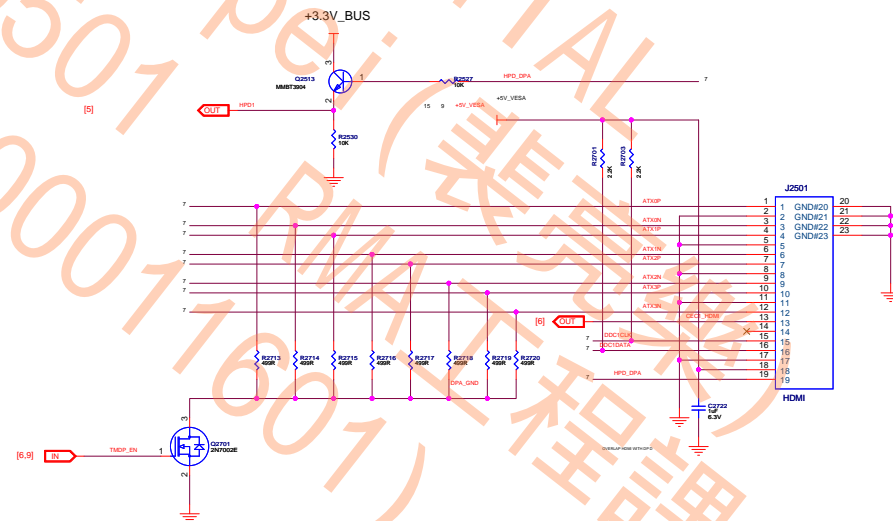
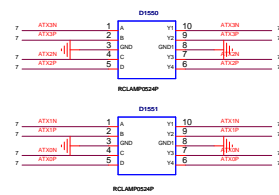


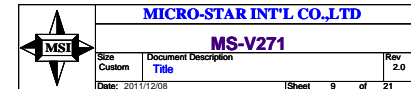
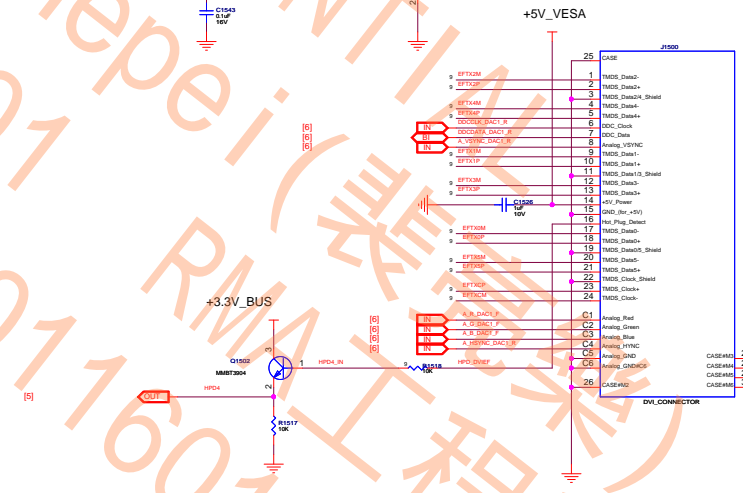
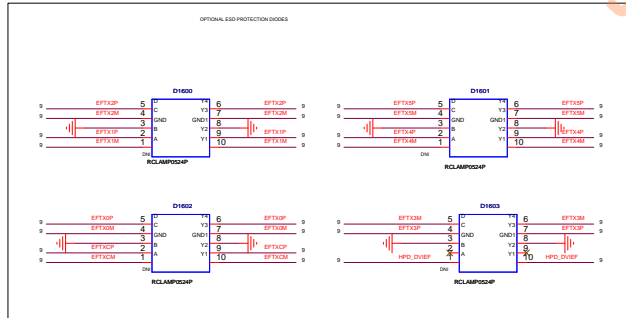




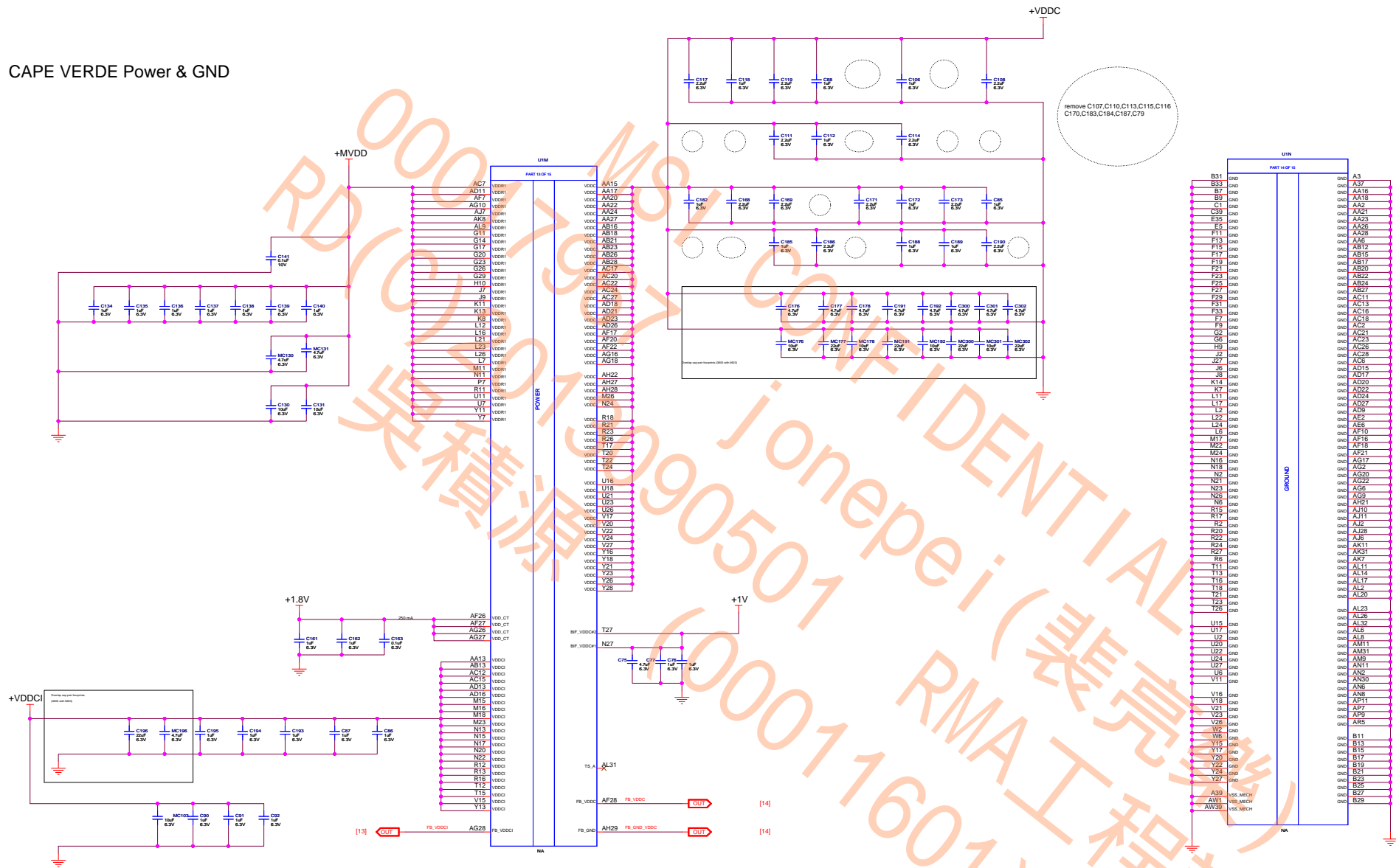


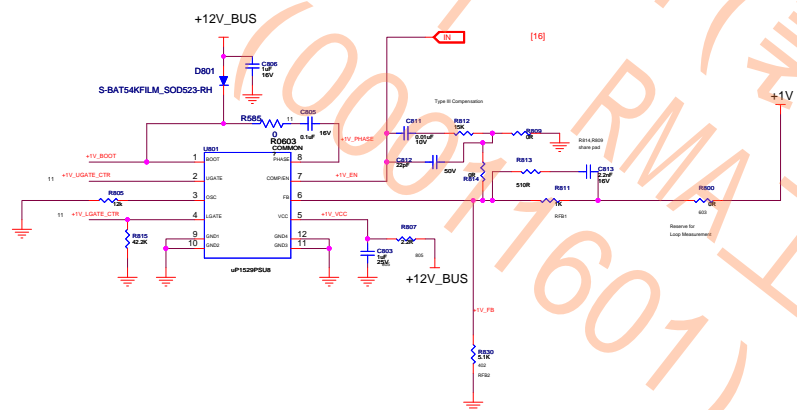


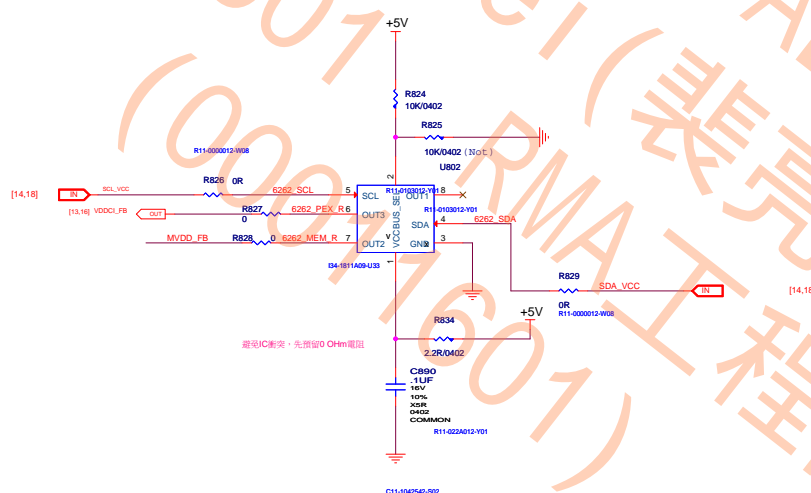
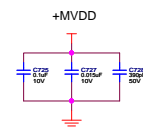
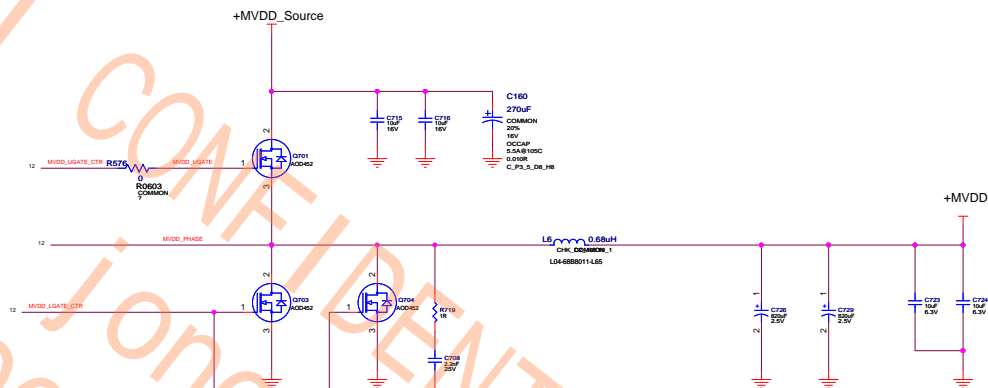
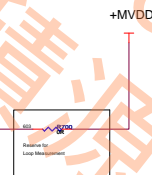
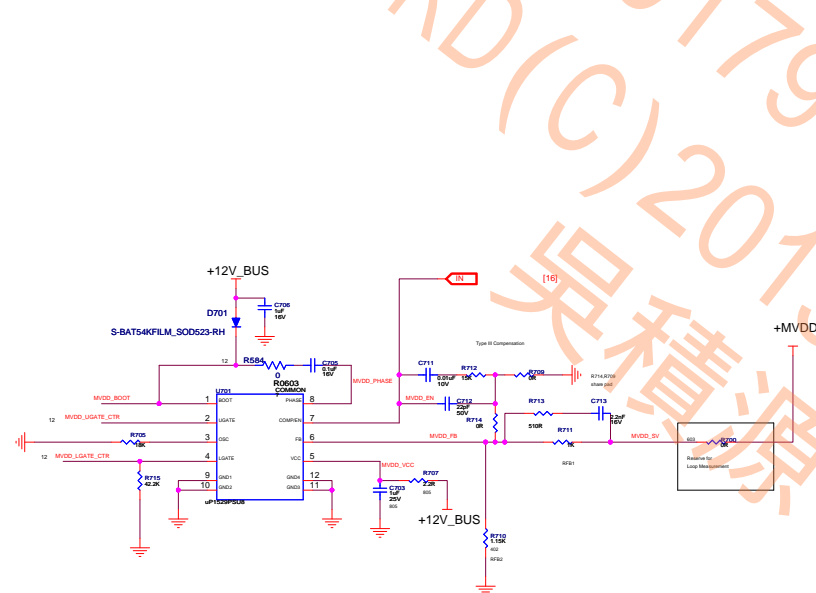




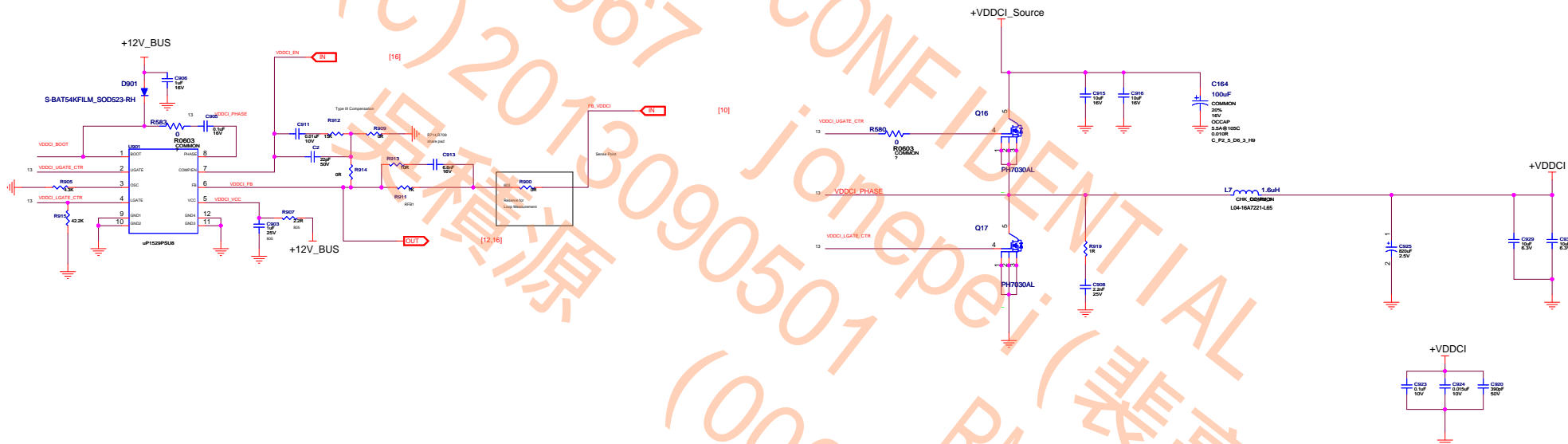
CAPE VERDE Power & GND





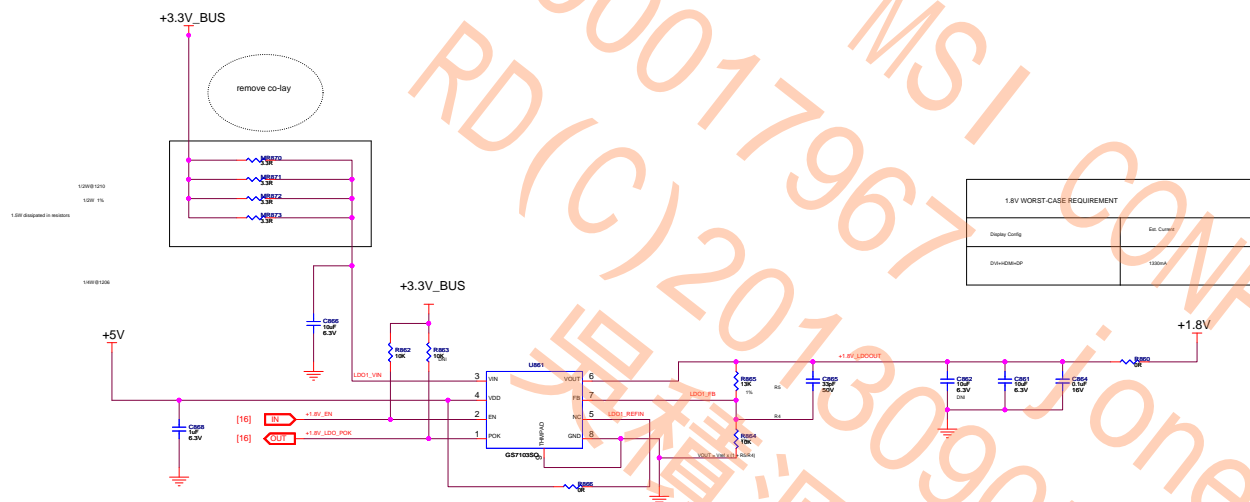


避免IC衝突，先預留0 OHm電阻



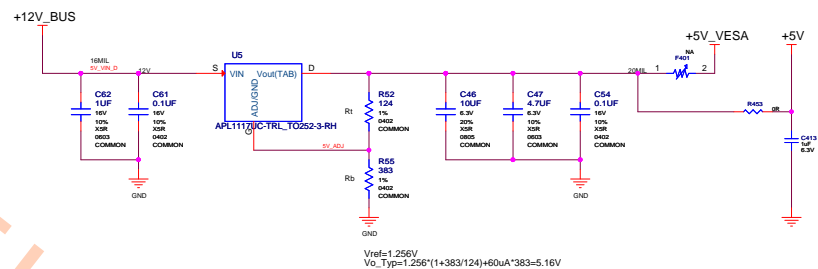
LDO #1: $V_{in} = 3.00V$ to $3.60V$ ($3.3V \pm 9\%$) $V_{out} = +1.8V \pm 2\%$ $I_{out} = 1.6A$ (TbV) RMS MAX

PCB: 50 to 70mm sq. copper area for cooling



1.5V WORST-CASE REQUIREMENT	
Display Config	Ex: Cuxmx
D16+1D16+2P	1300mA

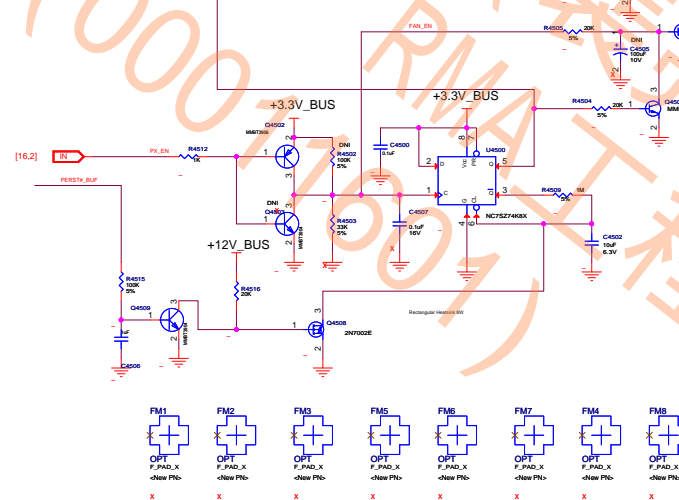
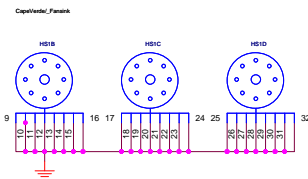
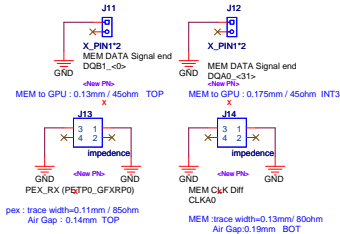
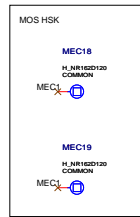
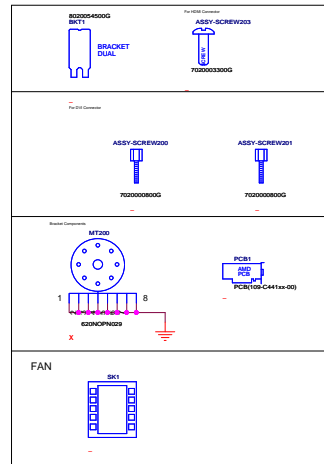
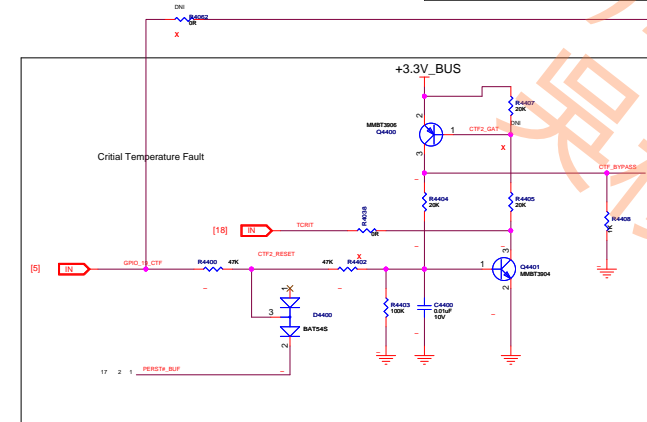
Regulators for +5V, +5V_VESA and +5V_VESA2





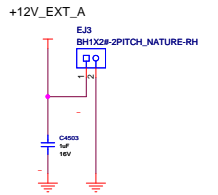
Socket	8020045000	
Bracket	8020045000	OS mDP mDP HEMA DV1
Fan	7120001000	For 100W board

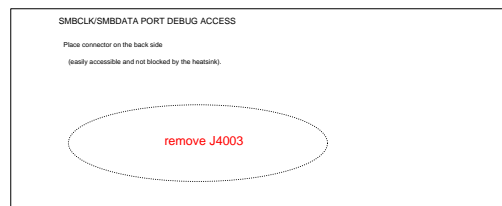
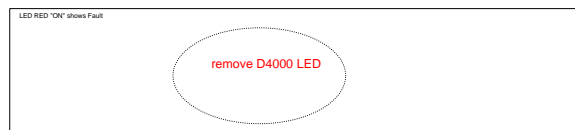
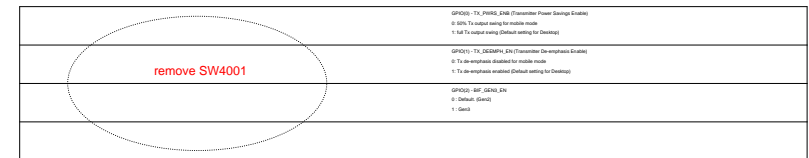
Critical Temperature Fault



Fan Control

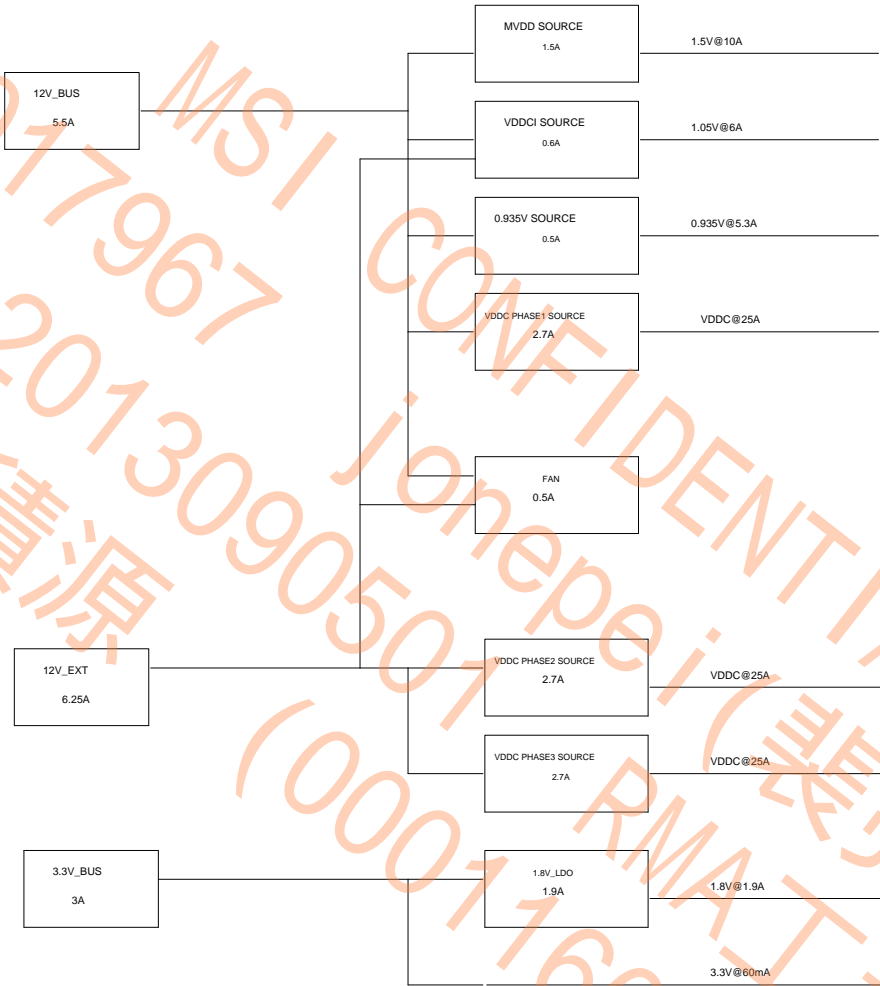
remove 2pin Fan control

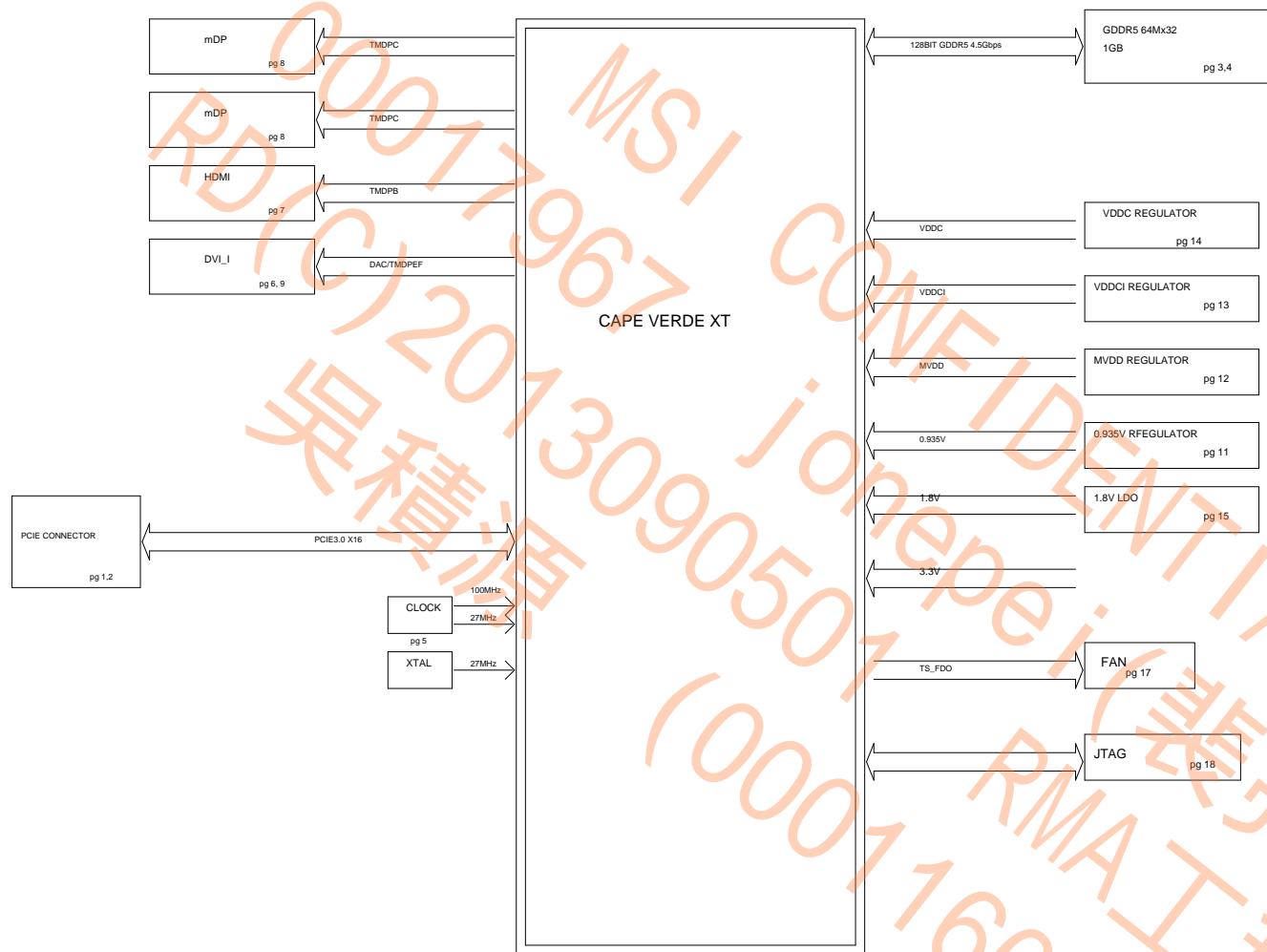




DM	HPD4	DDCAUX4
HDB	HPD1	DDCAUX1
HDRTOP	HPD2	DDCAUX2
HDP	HPD3	DDCAUX3

SPD15	VDDC_VBI	
SPD25	VDDC_VBI	
SPD7	VDDC_VBI	DEFAULT1
SPD5	VR_HOT	





			TITLE	SCHEMATIC NO.	DATE
			Verde XT 520RS 1GB dHD 1440MB+2x mDP	1GB-C441X3-00	
			REVISION HISTORY	NOTE:	REV
				THIS SCHEMATIC REPRESENTS THE P.O.B. IT DOES NOT REPRESENT ANY SPECIFIC BAY. FOR SCHEMATIC APPROVAL, PLEASE CONTACT THE PRODUCT DESIGNER. PLEASE CONTACT THE PRODUCT DESIGNER FOR ANY CHANGES TO THE SCHEMATIC. PLEASE CONTACT THE PRODUCT DESIGNER FOR ANY CHANGES TO THE SCHEMATIC.	PG3
SC1 REV	PCB REV	DATE	REVISION DESCRIPTION		
A1	001	2011/02/08	Initial Cape Verde XT schematic.		
A2	002	2011/02/08	1. Correct +5VDDC to +5VDDC 2. Add VDDC mode select circuit, which can be use to switch VDDC worked between 1phase, 3phase, 3phase 3. Correct HSVDNC, VSVDNC buffer power connection. 4. Correct U2200 address sequence, (switch MAB0_2 MAB0_3) 5. Add B501, B502 to support Cape Verde PRO without external power connector 6. Update spin fan circuit according to fan board wiring rule 7. Change R190R191R195 to 1.50k package (32200000000) 8. Delete MR54, ADD VR410A, 9. ADD TRIM0 TO PULL UP VR_HOT1.		
A3	003	2011/02/08	1. remove BP_VDDC 2. remove R190R191R195 3. add fan power up delay for BACD 4. COR1 instead of MC1CC1 and C20A C20B C20C		
A4	004	2011/02/08	1. remove BP_VDDC 2. Add VDDC1C1C2A B20 AS BACK UP		
		2011/12/07	P8.J1800由mini DP改為一般DP,J1801移除 P10.MC178/MC176/MC301/MC192 change to 0805 10u P11.U801 change to UP1529 ,L801 change to 1.6u P12.U701 change to UP1529 ,L6 change to 0.68u , add UP1811 voltage control IC P13.U901 change to UP1529 ,L7 change to 0.68u P14.U601 change to UP1608P ,EL27/EL28/EL29/EL30 change to 0.3u,3-phase change to 4-phase P15.change +5V regulator P16.All the input choke change to R47 and remove co-lay 0Ohm , remove "FOR ST" circuit. P17.Remove 2-pin Fan control circuit P18.Remove debug circuit		