

Page	Description
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Page	Description	Page	Description
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REV	VARIANT	VPVN	ASSEMBLY
0	BASE	600-11062-base-100	PI02 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND ROM NOT FINAL
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ASSEMBLY	P1062 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Table of Contents

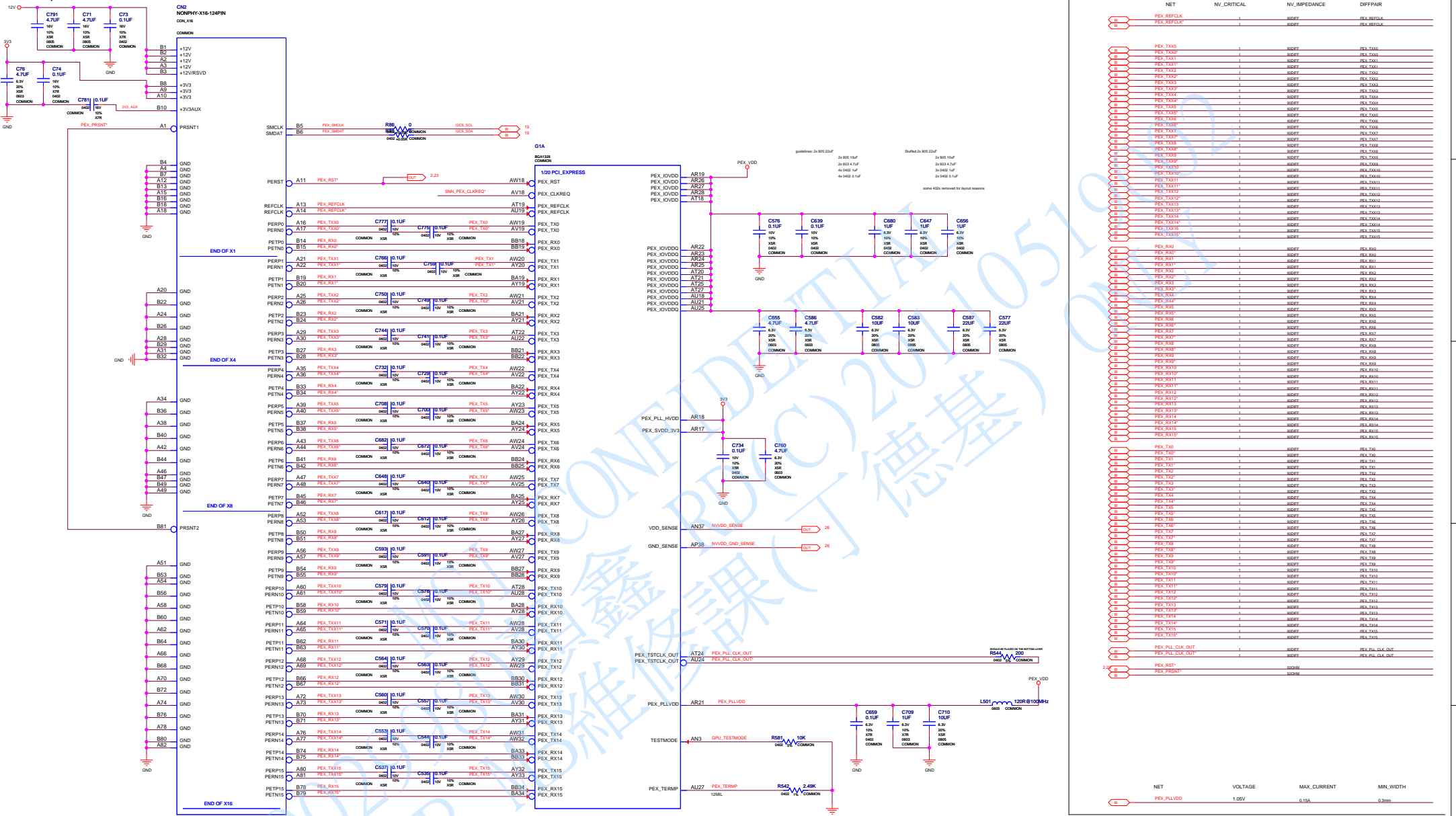
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NV_PN	600-11062-base-100		
PCB REV	P1062-B00	PAGE	
BOM REV	A	DATE	06-APR-2010


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PCI Express



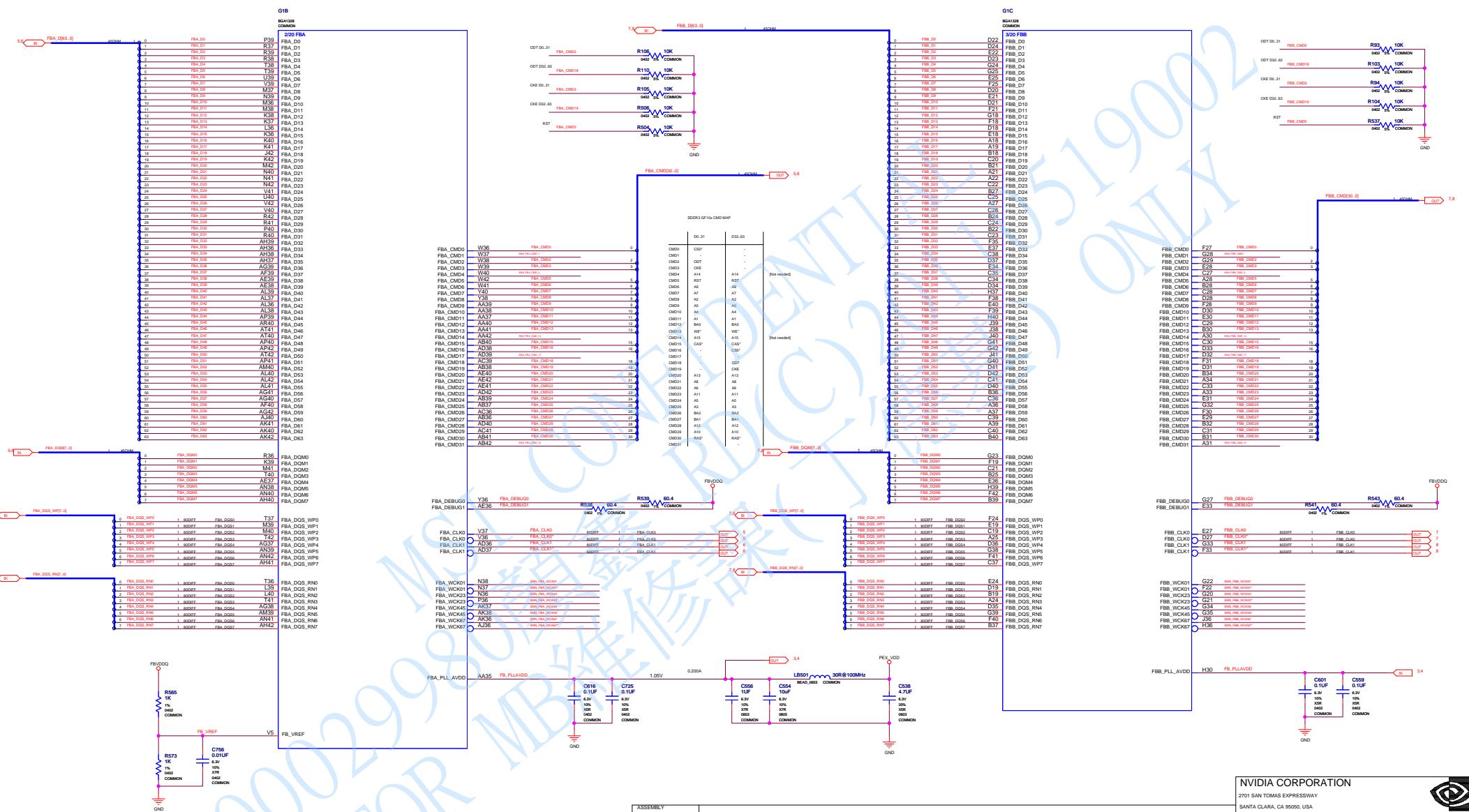
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PAGE DETAIL	PCI Express

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NV_PN	600-11062-base-100		
PCB REV	P1062-B00	PAGE	
BOM REV	A	DATE	06-APR-2010




MEMORY: GPU FB Partition A and B



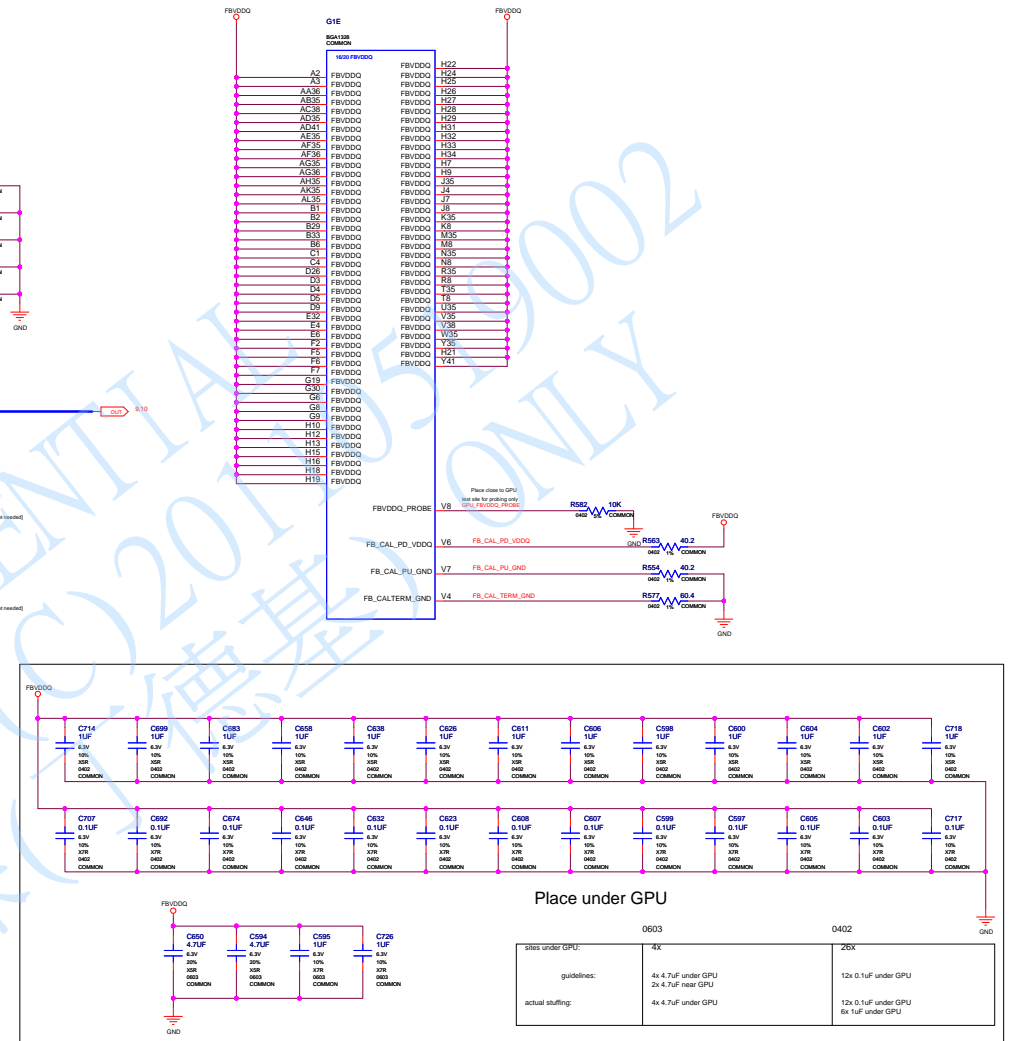
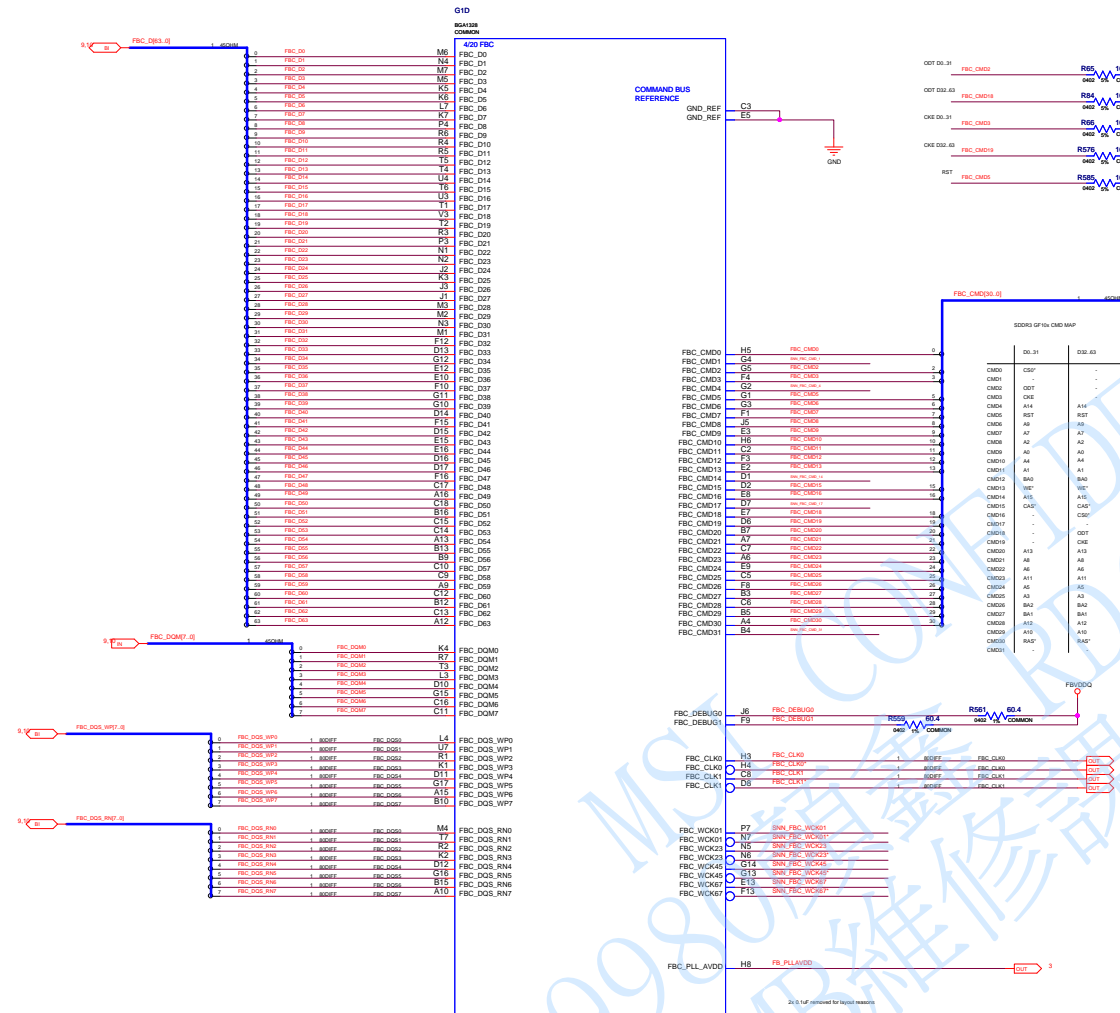
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ASSEMBLY	
PAGE DETAIL	GPU FB PARTITION A AND B

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NV_PN		600-11062-base-100	
PCB REV	P10B2-B00	PAGE	
BOM REV	A	DATE	06-APR-2010




MEMORY: GPU FB Partition C AND GPU FB Decap



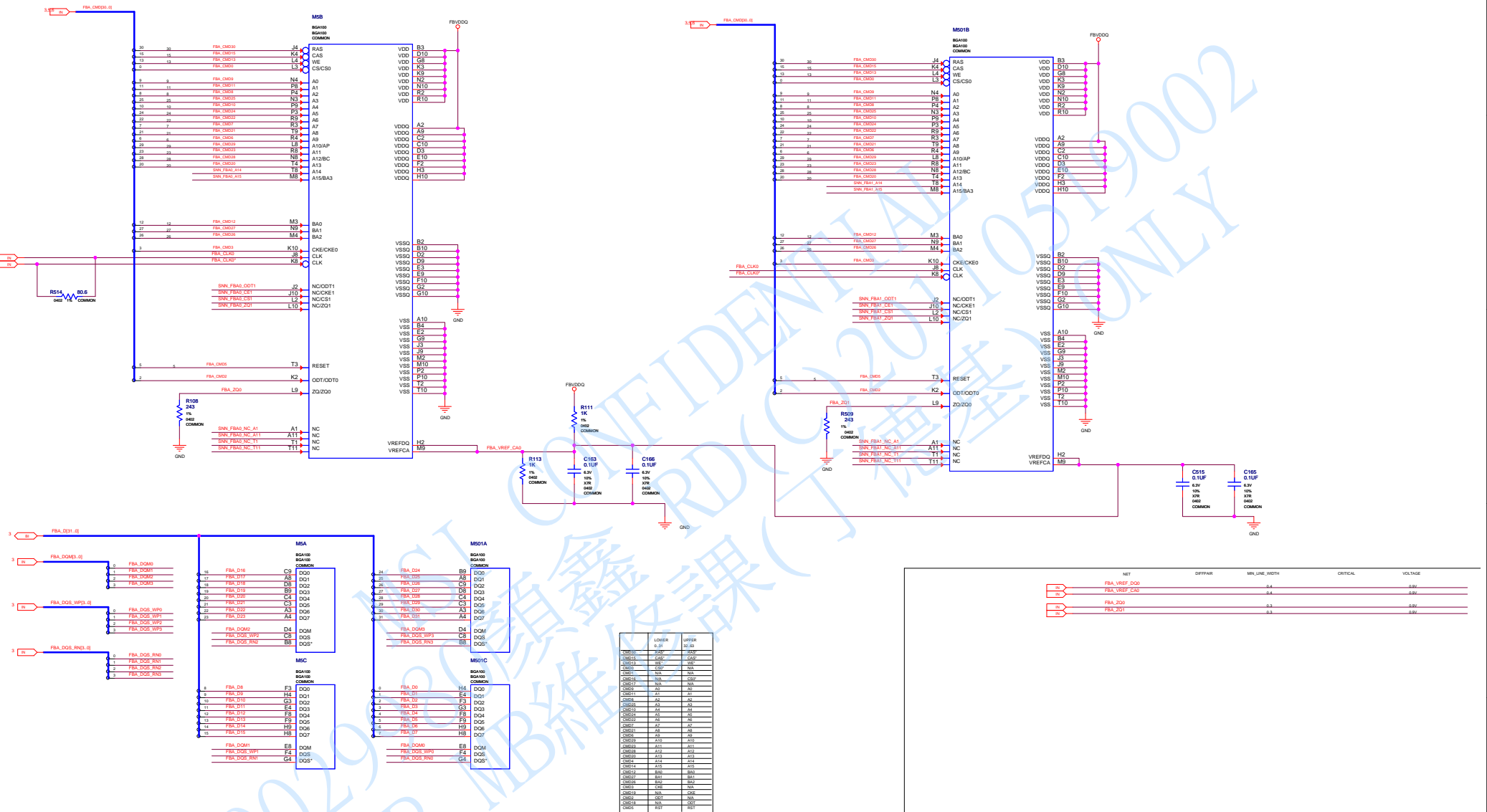
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PAGE DETAIL	GPU FB Partition C and GPU FB Decap

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NV_PN		P00-11062-base-100	
PCB REV	P1062-B00	PAGE	
BOM REV	A	DATE	06-APR-2010



Memory Lower Partition A



	NET	DIFFER	MIN_LINE_WIDTH	CRITICAL	VOLTAGE
IN	FBA_VREF_DQ0		0.4		0.0V
IN	FBA_VREF_CAD		0.4		0.0V
IN	FBA_ZQ0		0.3		0.0V
IN	FBA_ZQ1		0.3		0.0V

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NV_PN	600-11062-base-100
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PCB REV	P1062-B00	PAGE	
BOM REV	A	DATE	06-APR-2010

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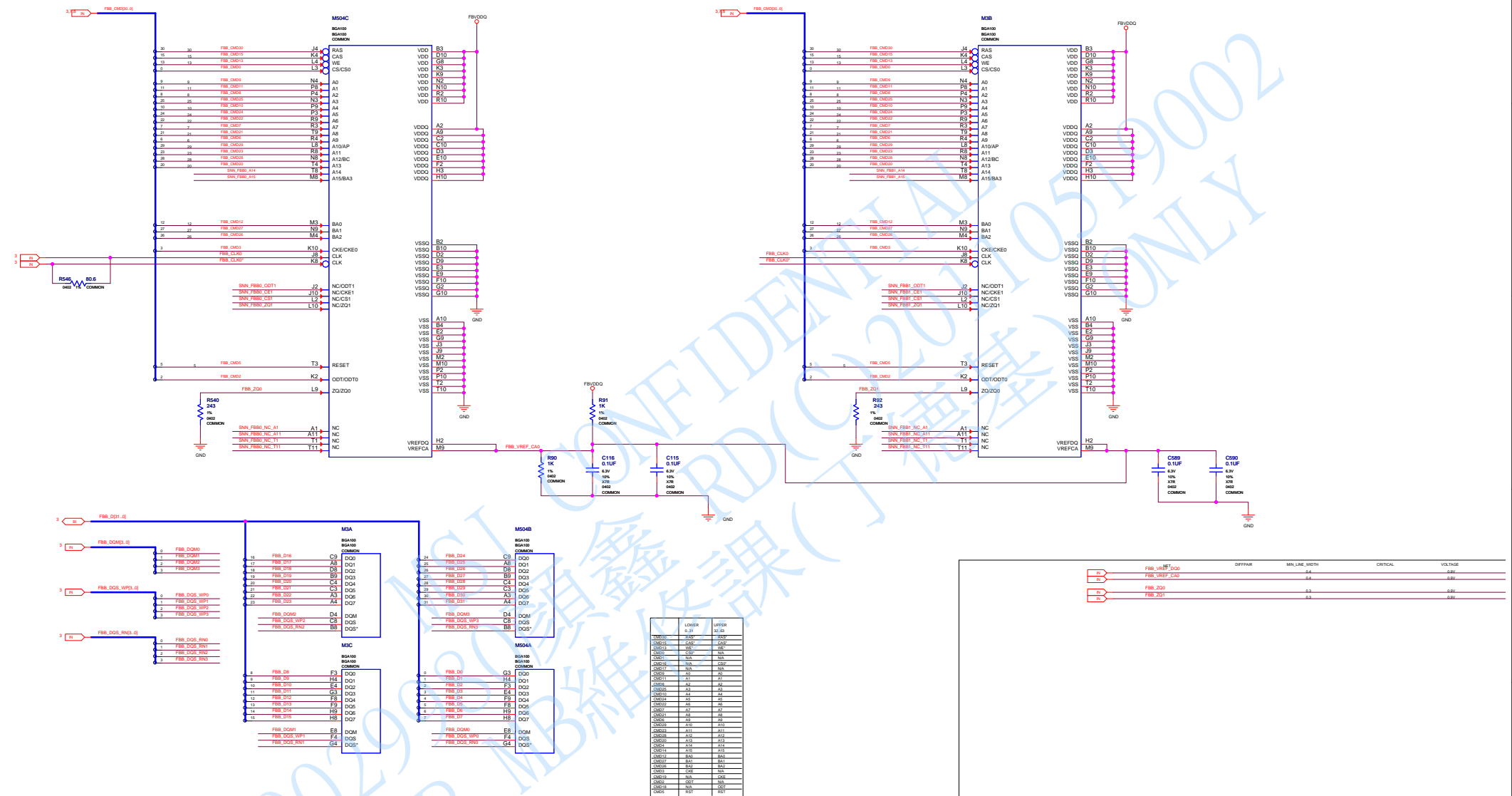
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ASSEMBLY	PT062 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Memory Lower Partion A

PAGE DETAIL	Memory Lower Partition A
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Memory Lower Partition B



	LOWER	UPPER
CMD0	0-2F	2F-3F
CMD15	CAS ²	CAS ²
CMD16	WR	WR
CMD17	COP	WR
CMD18	N/A	N/A
CMD19	N/A	N/A
CMD20	N/A	N/A
CMD21	A1	A1
CMD22	A2	A2
CMD23	A3	A3
CMD24	A4	A4
CMD25	A5	A5
CMD26	A6	A6
CMD27	A7	A7
CMD28	A8	A8
CMD29	A9	A9
CMD30	A10	A10
CMD31	A11	A11
CMD32	A12	A12
CMD33	A13	A13
CMD34	A14	A14
CMD35	A15	A15
CMD36	BA0	BA0
CMD37	BA1	BA1
CMD38	BA2	BA2
CMD39	CSE	CSE
CMD40	N/A	CMD
CMD41	CMD	CMD
CMD42	RST	RST

IN USE ON

ASSEMBLY	PT062 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Memory Lower Partition B

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PAGE DETAIL	Memory Lower Partition B

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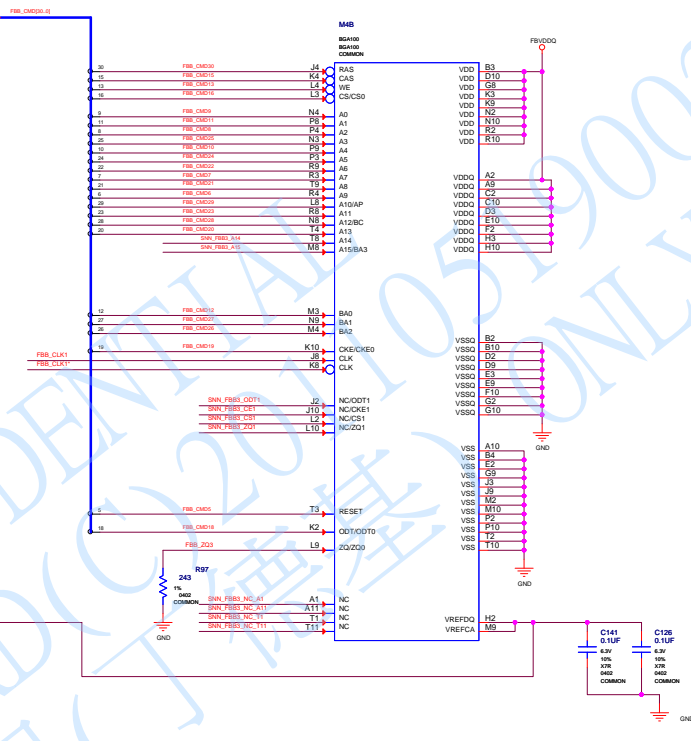
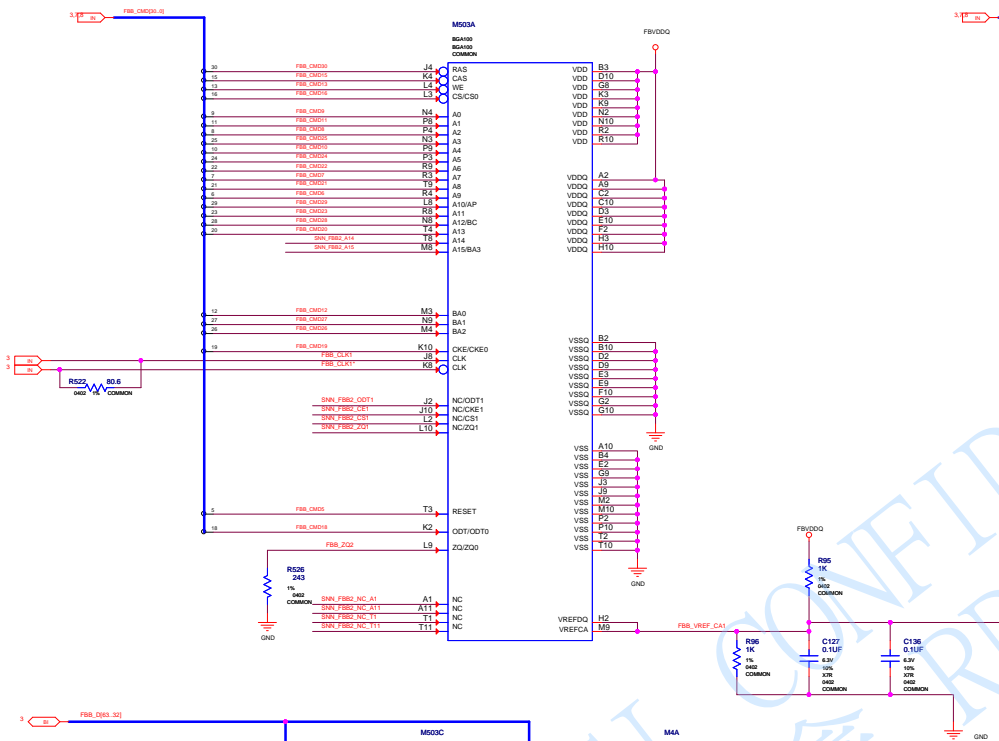
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NV_PN	600-11062-base-100
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PCB REV	P1062-B00	PAGE	
BOM REV	A	DATE	06-APR-2010

Memory Upper Partition B



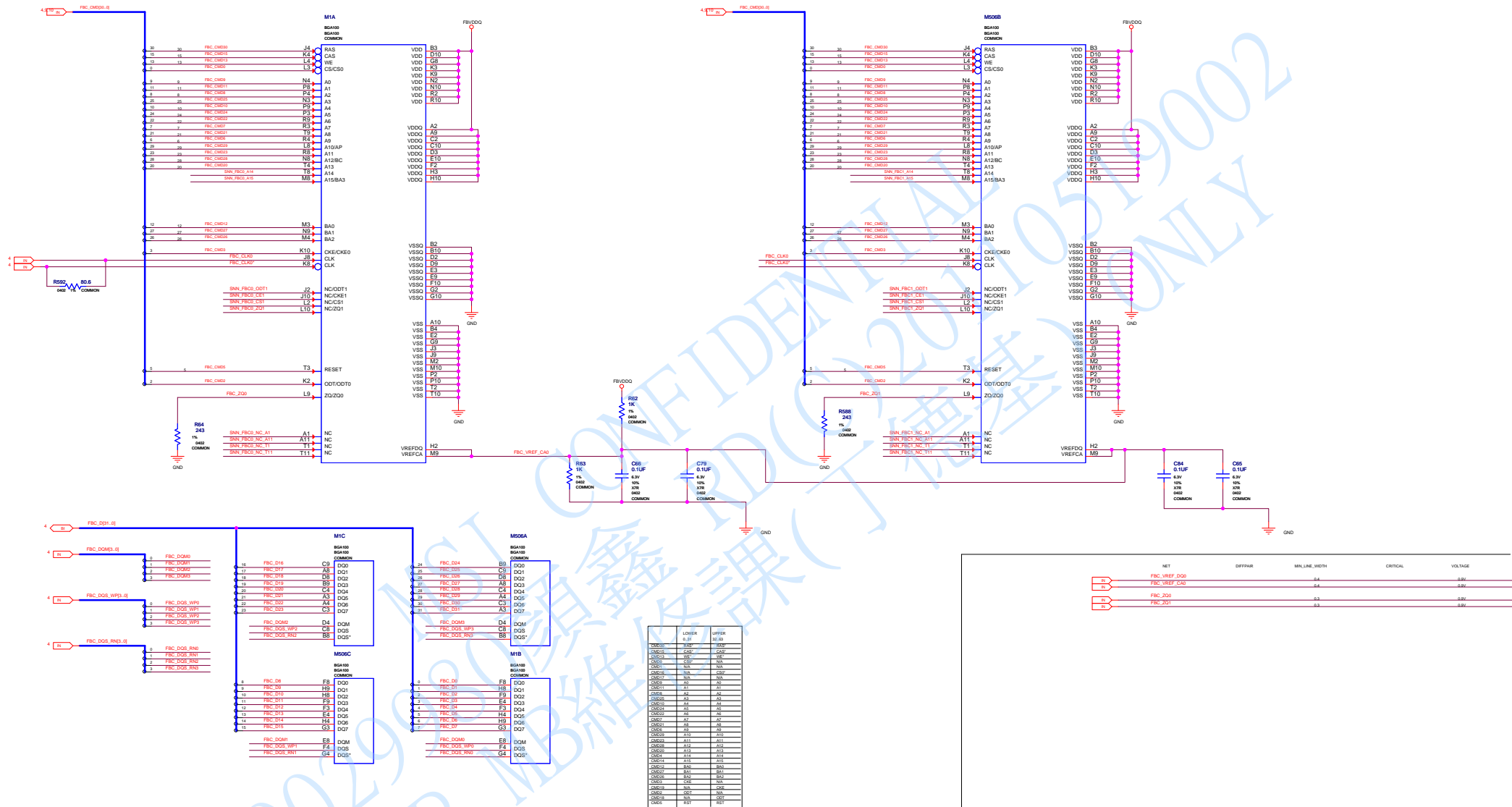
	LOWER	UPPER
	2-9	10-9
CMS05	CAS5	CAS5
CMS03	NE	NE
CMS9	CAS9	CAS9
CMS1	NA	NA
CMS15	CAS15	CAS15
CMS2	NA	NA
CMS11	A1	A1
CMS8	A2	A2
CMS5	A3	A3
CMS16	A4	A4
CMS24	A5	A5
CMS23	A6	A6
CMS7	A7	A7
CMS18	A8	A8
CMS21	A9	A9
CMS29	A10	A10
CMS20	A11	A11
CMS22	A12	A12
CMS20	A13	A13
CMS24	A14	A14
CMS12	A15	A15
CMS27	B1	B1
CMS12	B2	B2
CMS1	B4	B4
CMS36	B42	B42
CMS1	C18	C18
CMS18	NA	NA
CMS2	Q18	Q18
CMS2	Q17	NA
CMS18	Q17	Q17
CMS5	R17	R17

IN USE ON
THIS PAGE _____

ASSEMBLY	PT062 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Memory Upper Partition B

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Memory Lower Partition C



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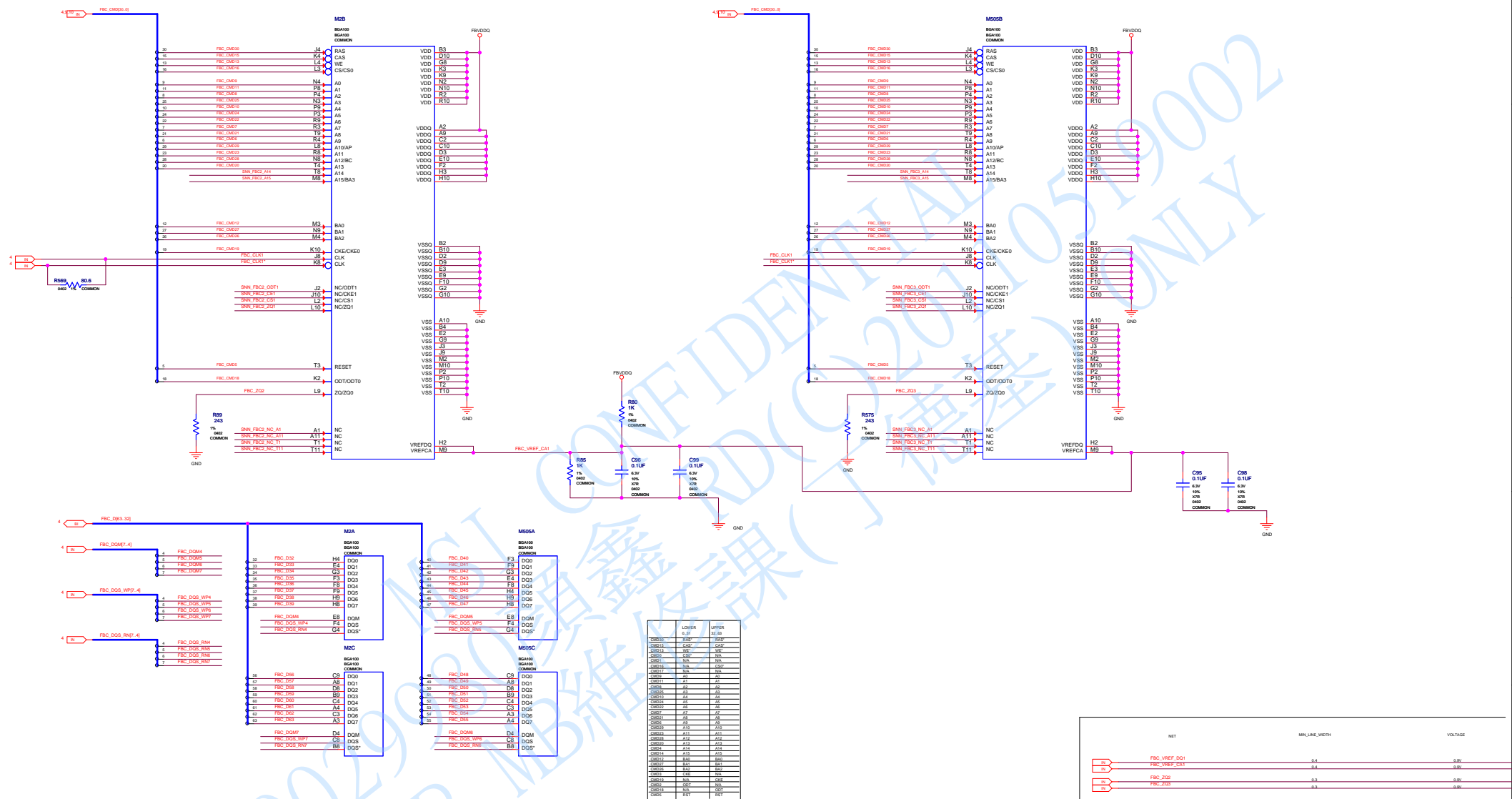
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PAGE DETAIL Memory Lower Partition C

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NV_PN	600-11062-base-100		
PCB REV	P1062-3000	PAGE	
BOM REV	A	DATE	06/APR/2010

Memory Upper Partition C



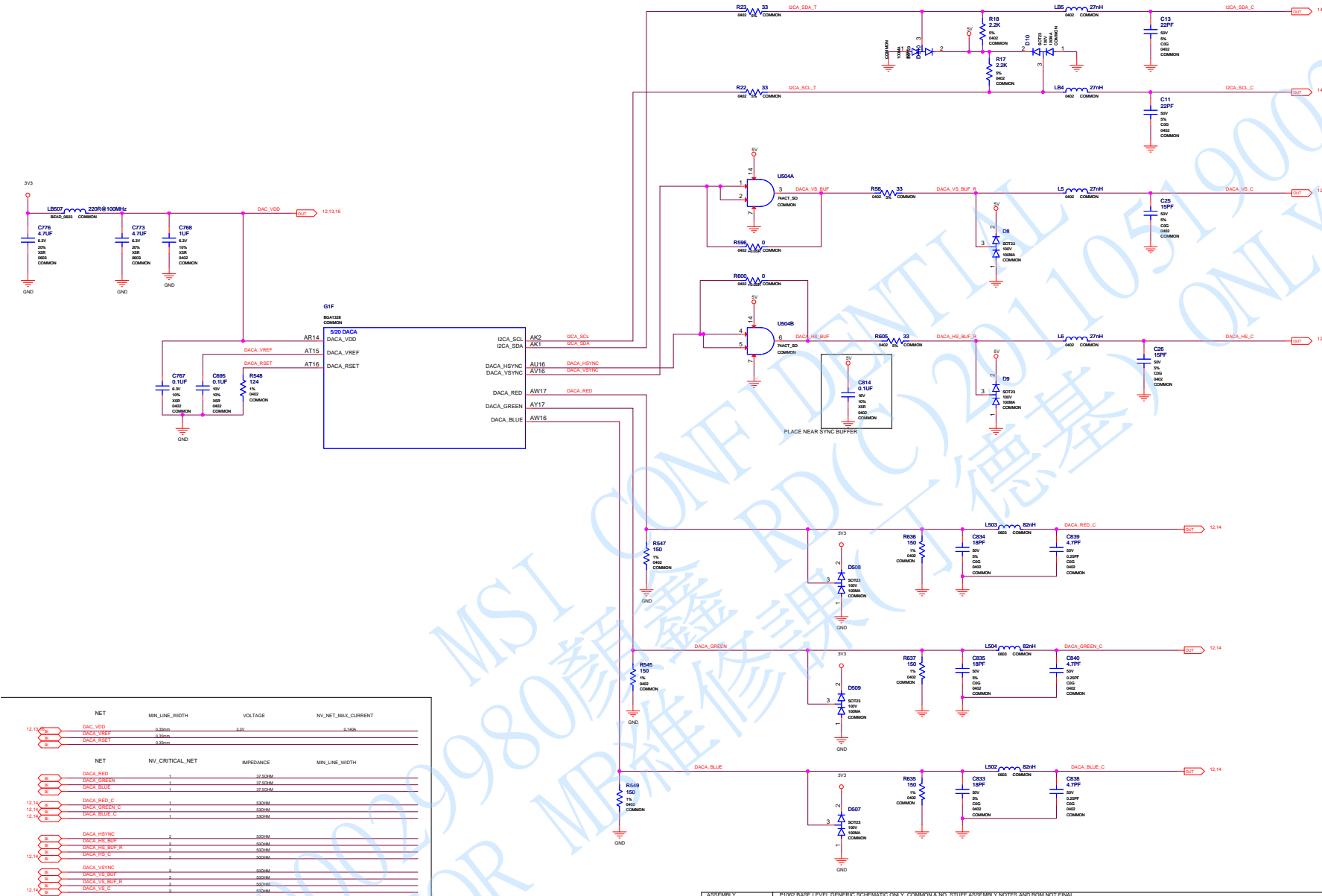
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PAGE DETAIL	Memory Upper Partition C

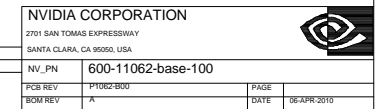
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NV_PN	600-11062-base-100		
PCB REV	P10B2-B00	PAGE	
BOM REV	A	DATE	06-APR-2010

DACA for South DVI Interface

DACA RGB-FILTER



DACB RGB-FILTER



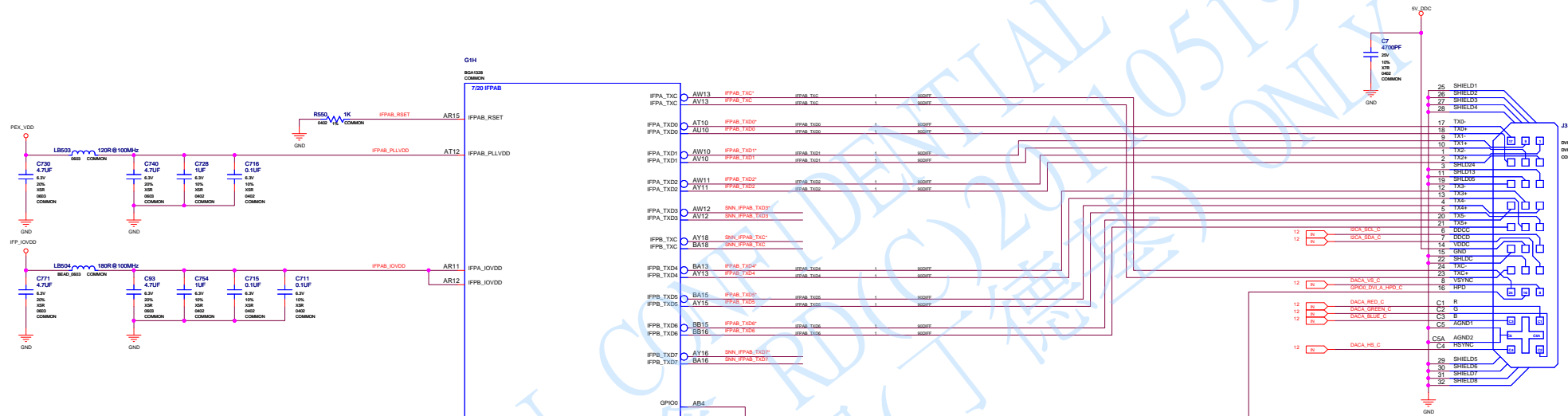
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PAGE DETAIL	DACB Interface for VGA

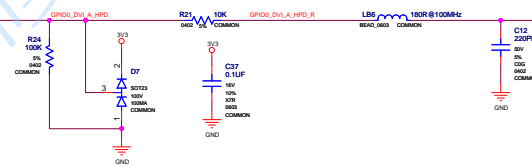
NV_PN	600-11062-base-100		
PCB REV	P1062-B00	PAGE	
BOM REV	A	DATE	06-APR-2010

IFP A/B Interface -- DVI Connector, South

NET	MIN_LINE_WIDTH	VOLTAGE	IV_NET_MAX_CURRENT
IPAD_PLLVDD	125ML	1.05V	0.130A
IPAD_KOVDD	125ML	3.3V	0.305A
IPAD_PSET	125ML		



DVIAB Hotplug Detection



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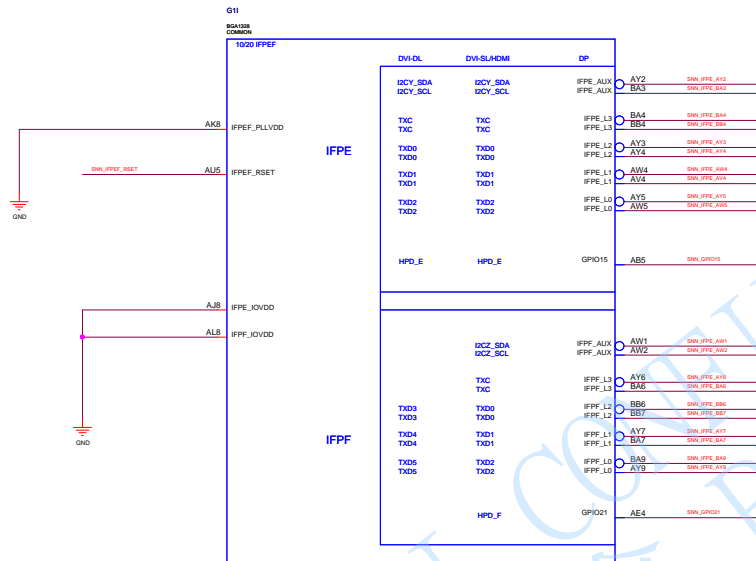
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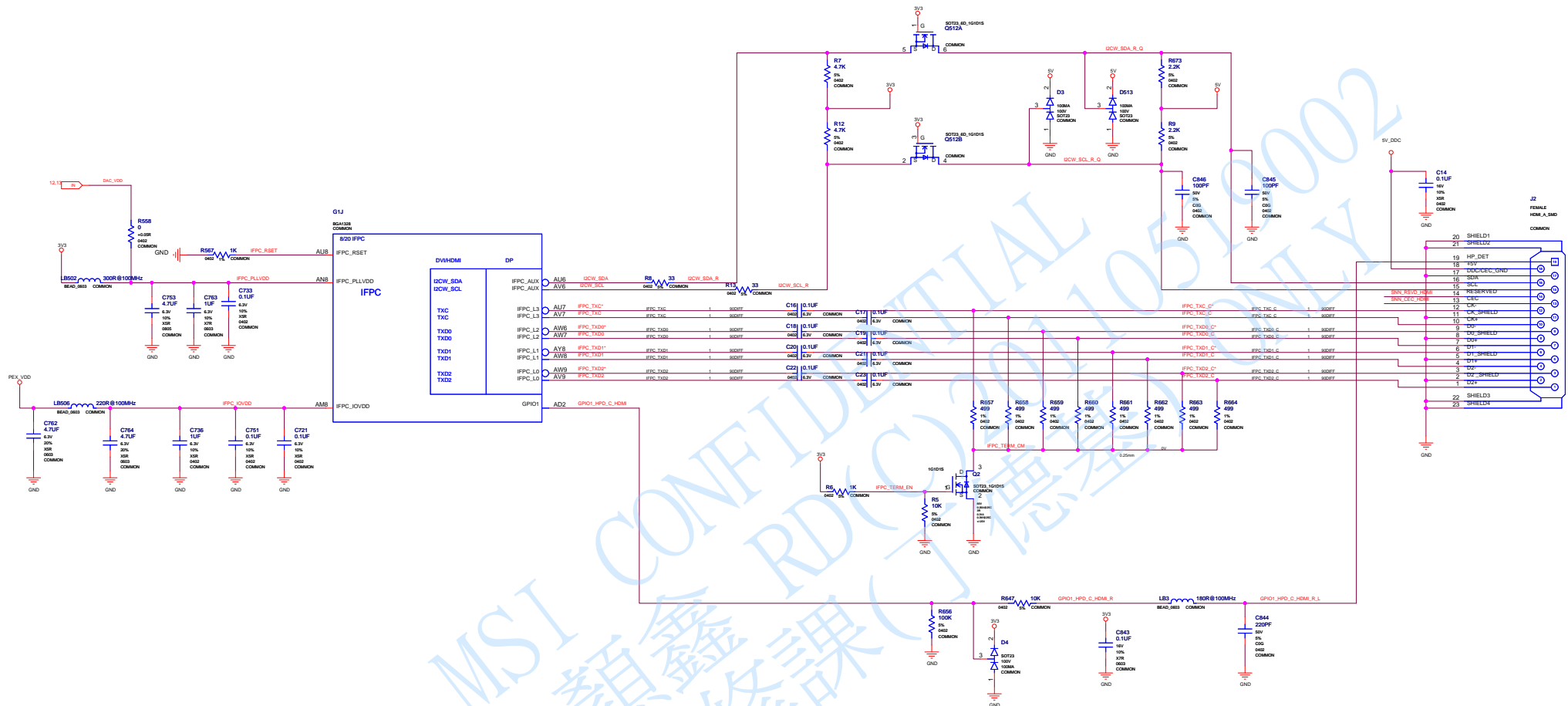
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PCB REV	P1062-B00	PAGE	
BOM REV	A	DATE	06-APR-2010

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ASSEMBLY	P1062 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	IFP A/B Interface -- DVI Connector, South



IFP C Interface -- HDMI, North



ASSEMBLY	PT062 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	IFPC Interface -- HDMI

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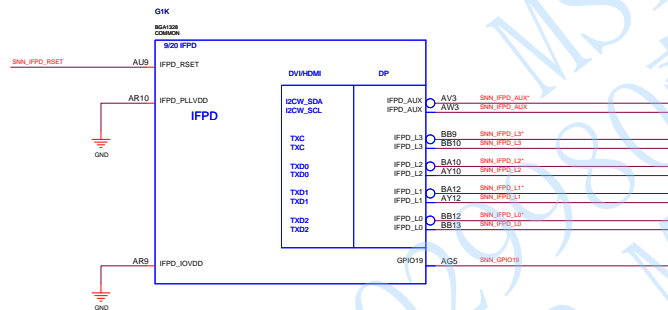


NV_PN	600-11062-base-100
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PCB REV	P1062-B00
BOM REV	A


PAGE	
DATE	06-APR-2010

IFP D INTERFACE (UNUSED)

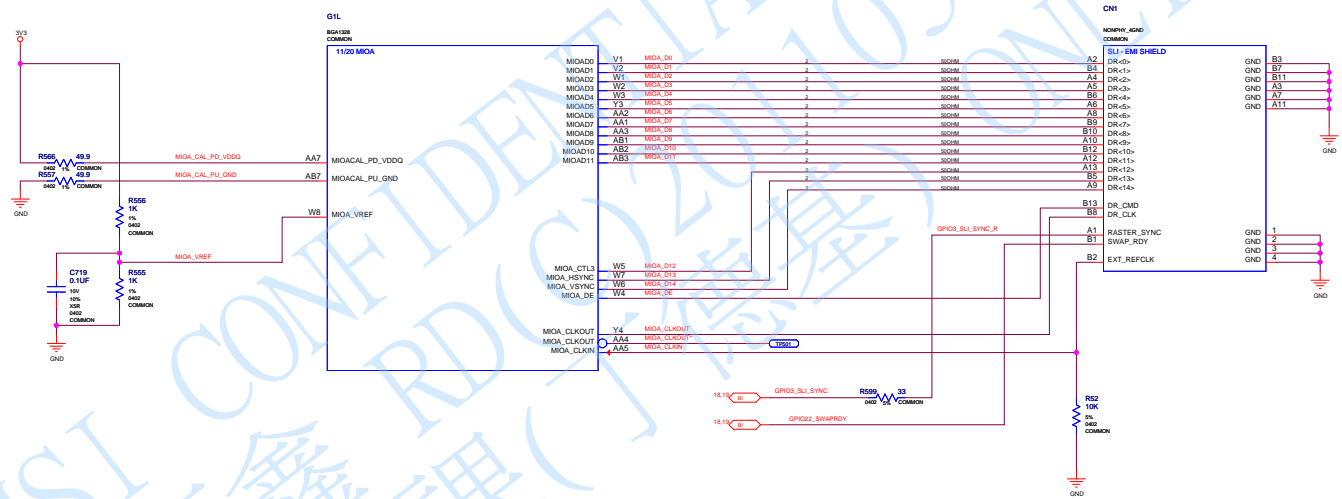


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PAGE DETAIL	IFPD (UNUSED)

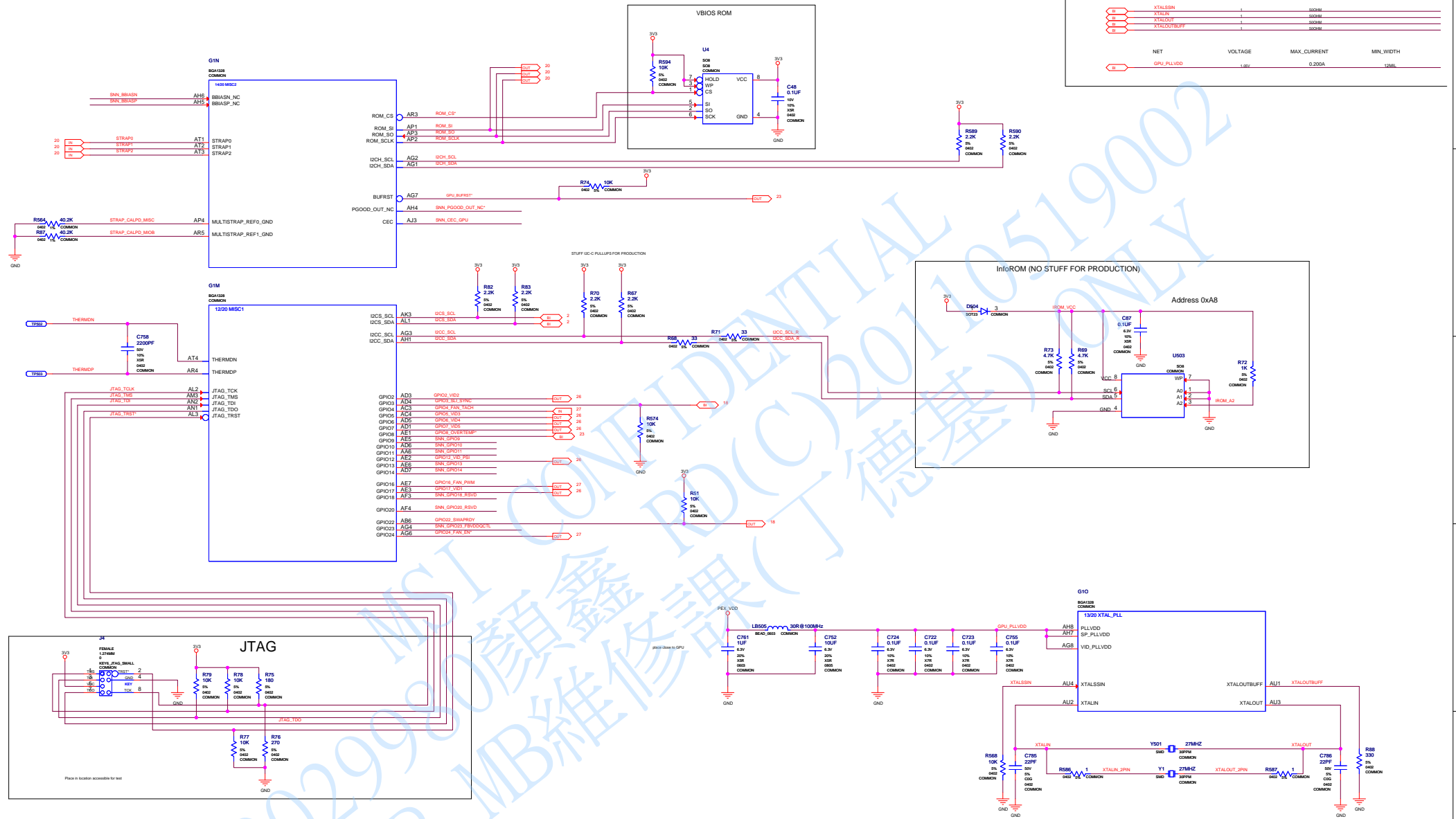
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NV_PN	600-11062-base-100		
PCB REV	P1062-B00	PAGE	
BOM REV	A	DATE	06-APR-2010


MIO A Interface, SLI



MISC: GPIO, I2C, ROM, HDCP, and XTAL



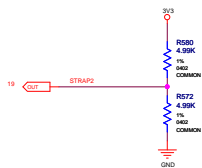
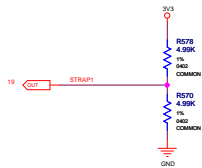
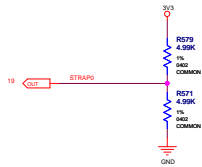
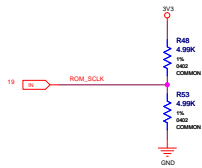
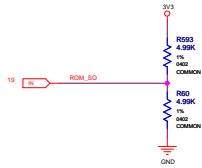
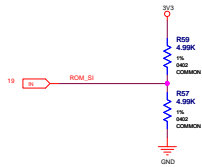
ASSEMBLY	P1062 BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	MISC: GPIO, I2C, ROM, and XTAL

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NV_PN		600-11062-base-100	
PCB REV	P1062-B000	PAGE	
BOM REV	A	DATE	06-APR-2010

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Strap Configuration

GF106-300: 0x0DC0



ROM_SO	VGA_DEVICE SMB_ALT_ADDR PU_0_B4H_SIZE VCLK_4T	LSb MSb	1: VGA_DEVICE 0: QWEP(POR) 0: QWEP(POR) 0: QWEP(POR) 0: QWEP(POR)	PU PD	15K
ROM_SI	RAM_CFG0		668A/16 100-bit Samsung 668A/16 100-bit Hynix 668A/16 100-bit Winbond 668A/16 100-bit Hynix	20K PD 10K PD 10K PD 5K PD	20K
ROM_SCLK	PEL_RL_EN_TERMINO SLOT_CLK_CONFIG SMB_TERMINO PCI_DEVICE_EXT	LSb MSb	0: DISABLED (POR) 1: GPU and MCH COMMON REFCLK 1: MCH COMMON REFCLK 0: PCI_DEVICE_EXT		35K
STRAP0	USER_BIT0		0:0 DEFAULT		5K
STRAP1	SGD_PADCFG_LUT_ADDR0		0:0 Default High Imped		5K
STRAP2	PCI_DEVICE0		0:0 PCI_DEVICE0DC0		5K

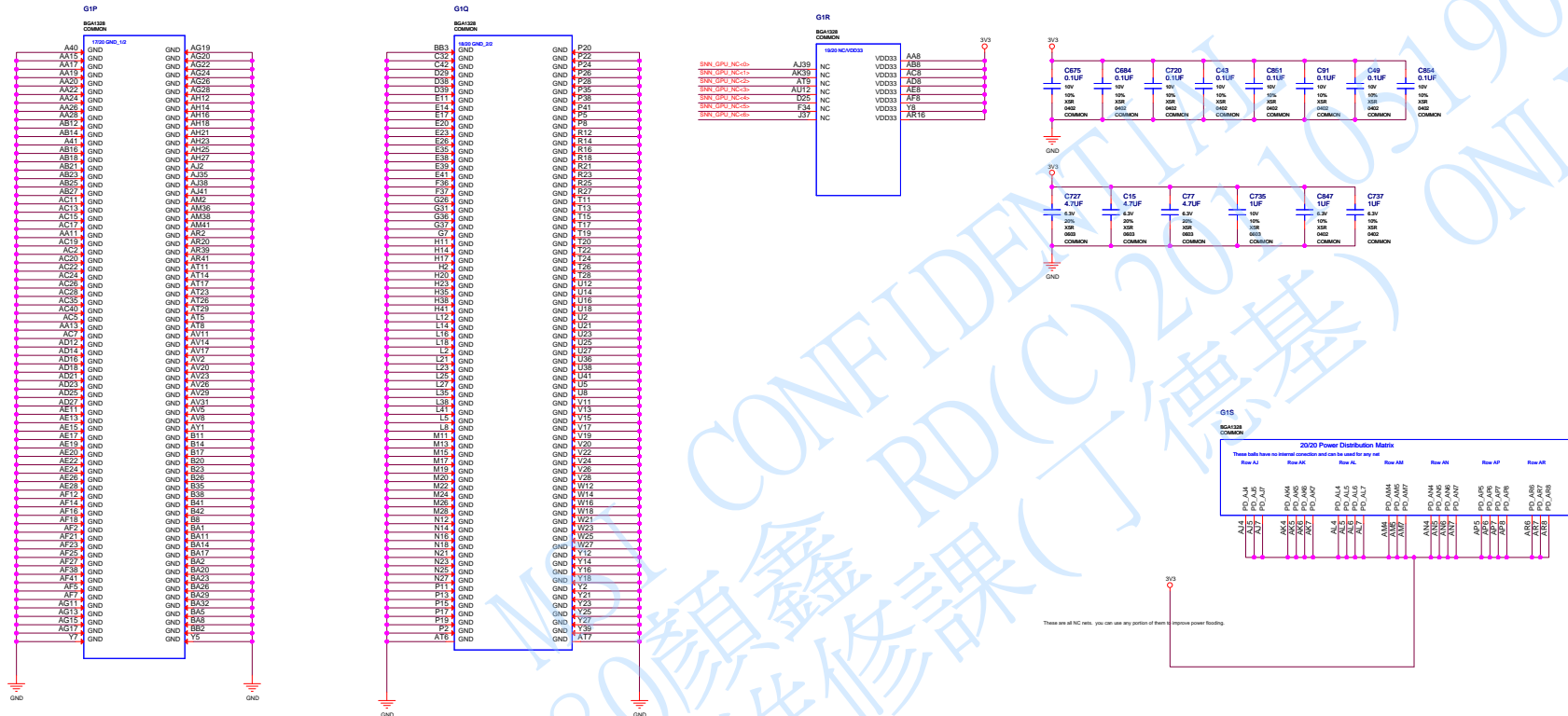
Multi-level Strap Resistor Values	
Strap Pull Up (kOhm)	
0	PD 5
1	PD 15
2	PD 15
3	PD 20
4	PD 20
5	PD 30
6	PD 30
7	PD 45
8	PU 5
9	PU 15
10	PU 15
11	PU 20
12	PU 20
13	PU 30
14	PU 30
15	PU 45

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PCB REV	P1062-300	PAGE	
BOM REV	A	DATE	06/04/2010


PWR and GND Signals



These are all NC nets. you can use any portion of them to improve power flood

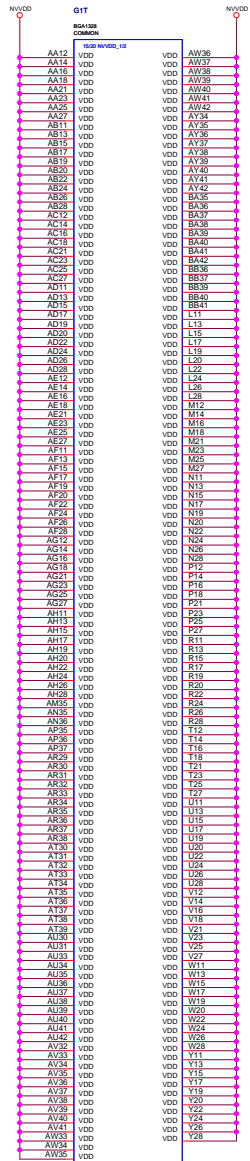
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PAGE DETAIL	PWR and GND Signals

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NVDD Decoupling



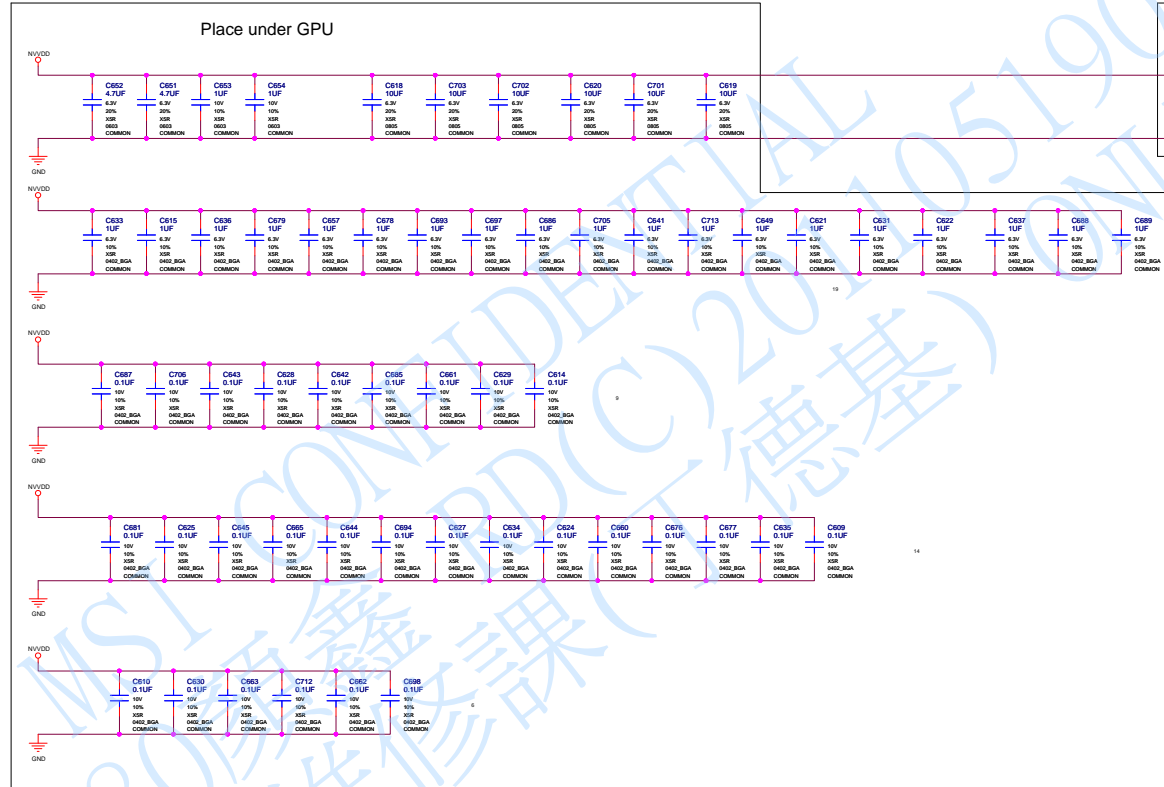
0805 0603 0402

Sites placed under GPU:

8x 4.7uF
8x 10uF
1x 22uF

4x 4.7uF
(2x unused)

48x 18uF
18x 0.1uF
(17x unused)



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PAGE 1

DATE 06/04/2010

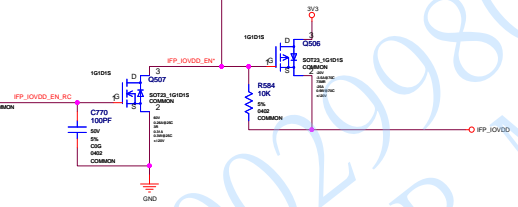
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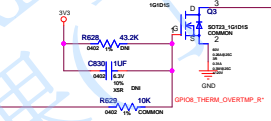
PAGE 1 OF 1

WVDD Decoupling

FP_IOVDD Backdrive Prevention



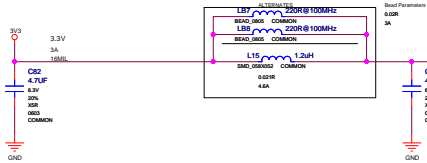
GPIO_THERM_OVERTMP



PS I: 3V3, 12V, and 12V_EXT Power Supply Filter, 5V, DDC VOLTAGE

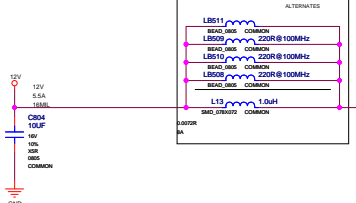
3V3 Power Supply Filter

3V3_F = 3.3V @ 2.4A

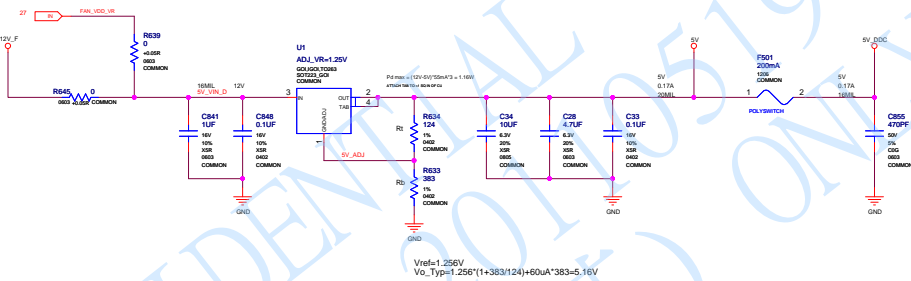


12V Power Supply Filter

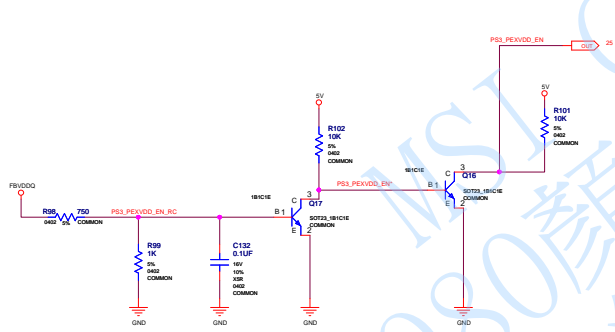
12V_F = 12V @ 5.5A



5V POWER SUPPLY, DDC POWER

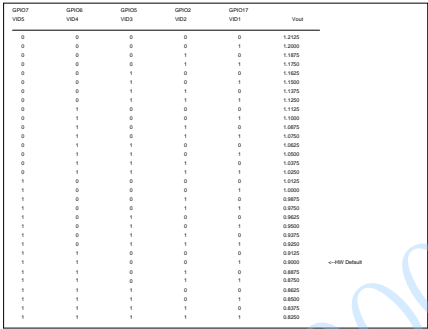


PEXVDD ENABLE



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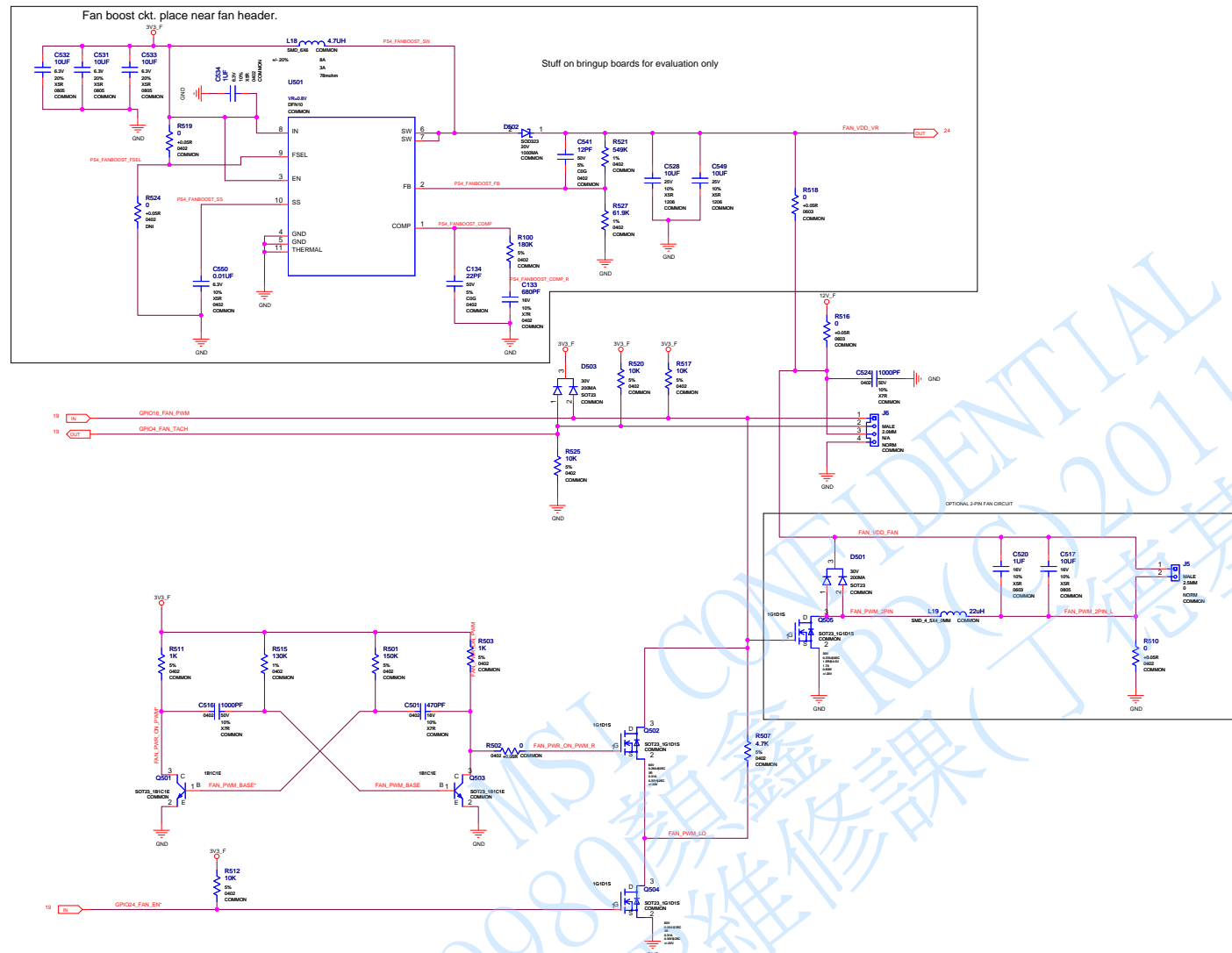
ASSEMBLY	
PAGE DETAIL	NVDD Power Supply

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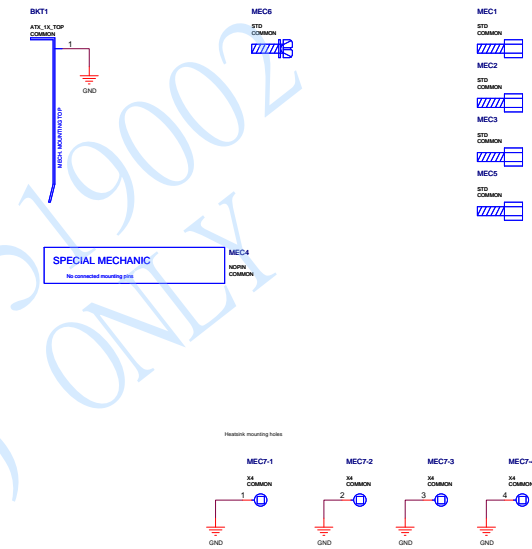
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BOM REV	A	DATE	06-APR-2010



Thermal, Mechanical, and Bracket




Bracket

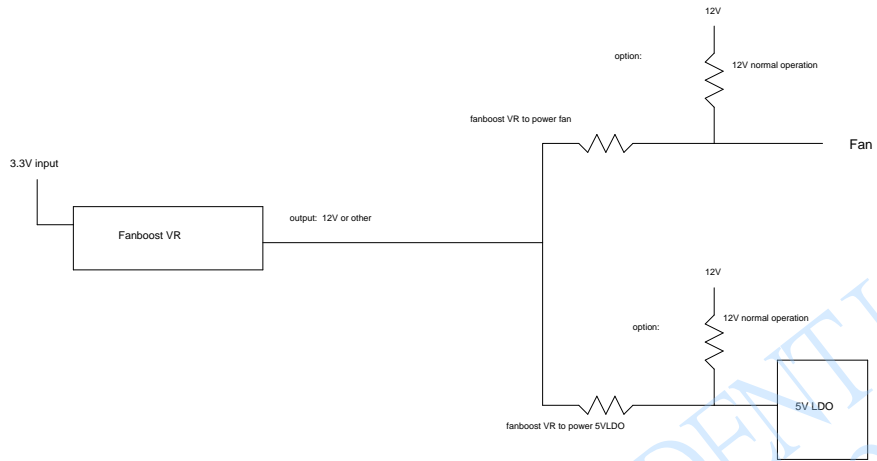


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PAGE DETAIL	Fanboost VR, multivibrator, Thermal, and Mechanical

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ASSEMBLY	"P1062 BASE LEVEL" GENERIC SCHEMATIC ONLY. COMMON & NO-STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Block diagram of 12V options for fan and 5V LDO

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