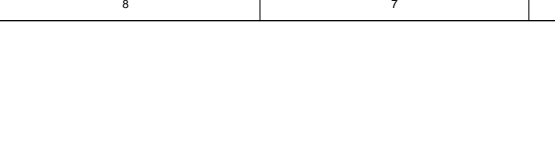
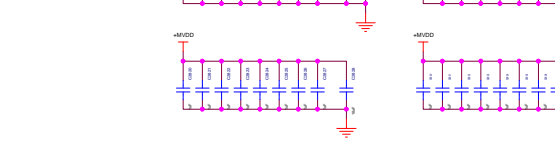
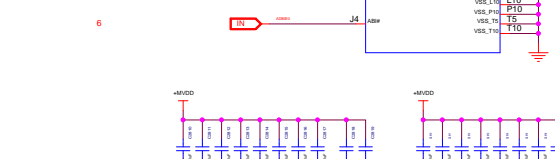
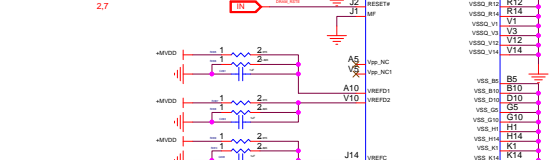
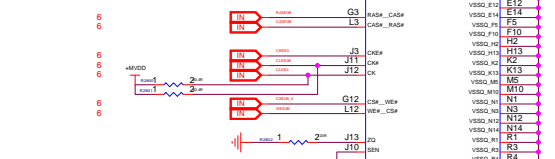
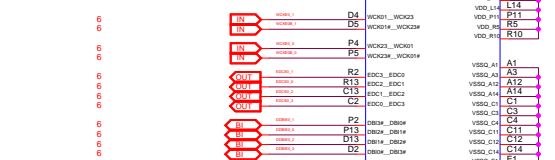
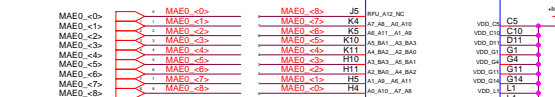
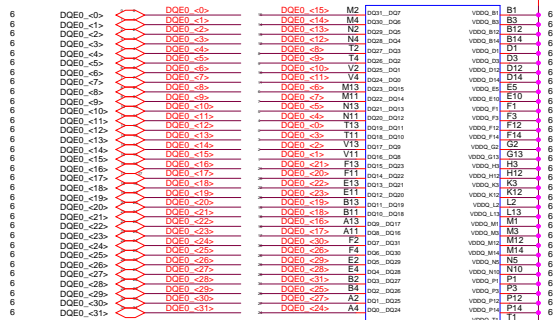
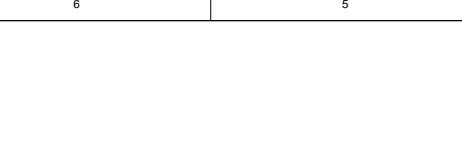
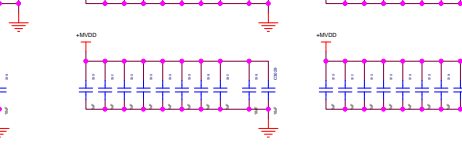
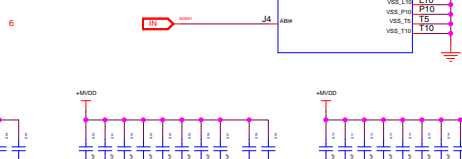
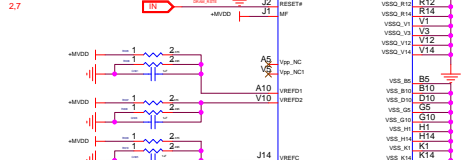
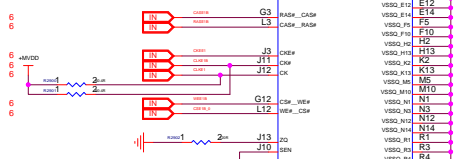
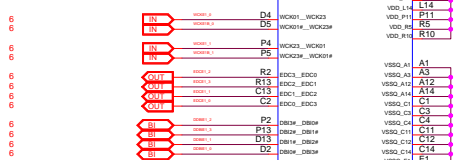
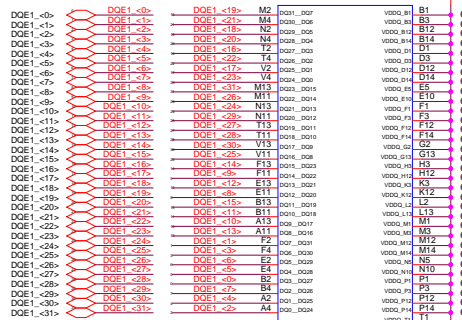


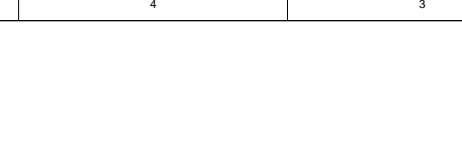
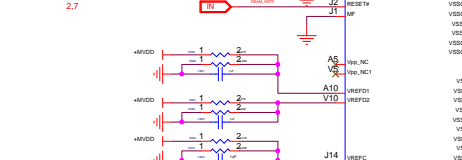
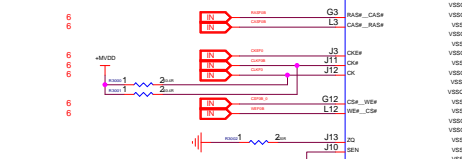
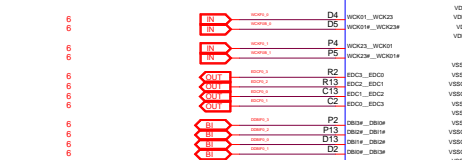
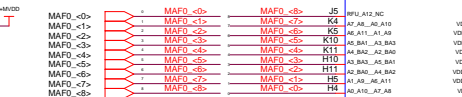
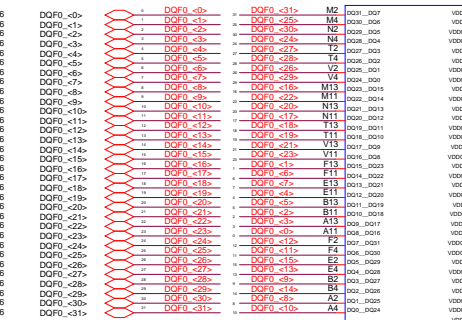
U2800



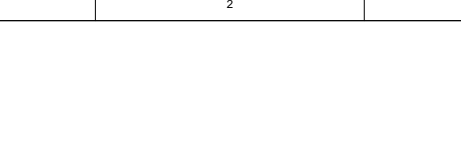
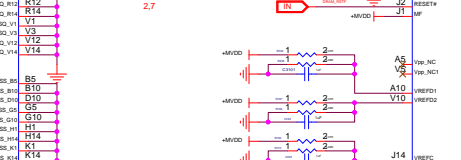
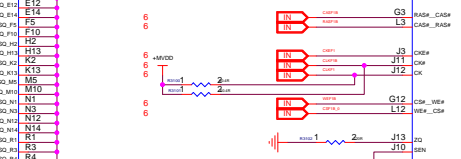
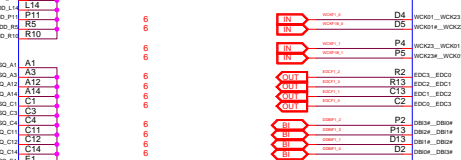
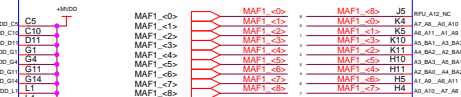
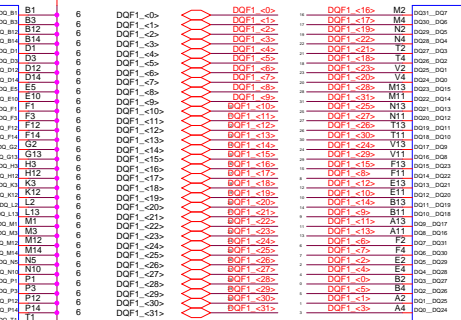
U2900



U3000



U3100



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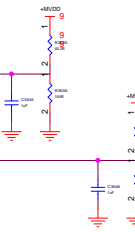
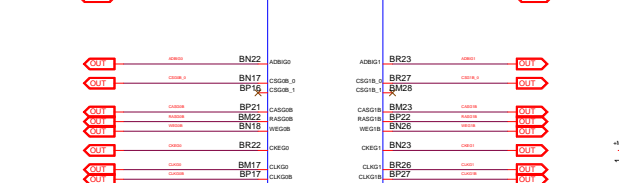
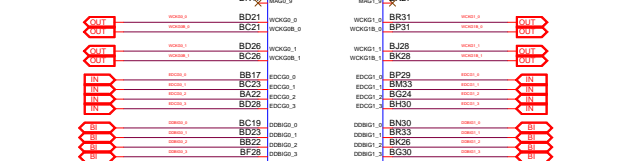
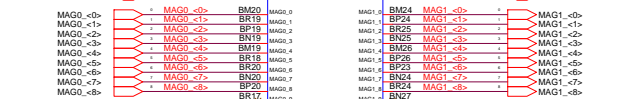
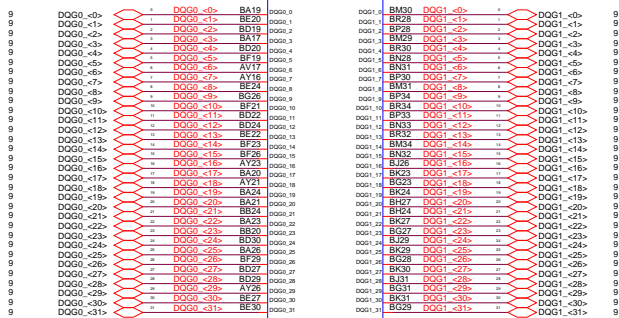
Date: Tuesday, October 29, 2013

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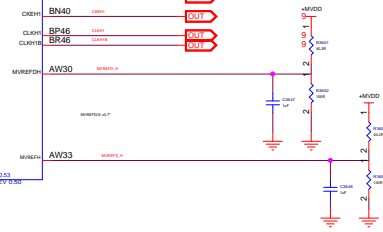
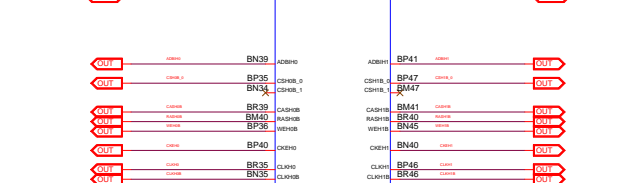
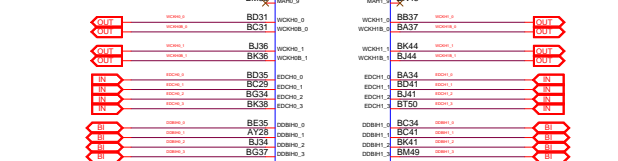
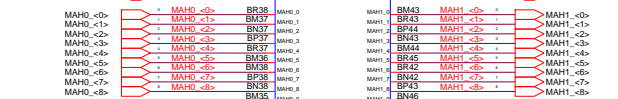
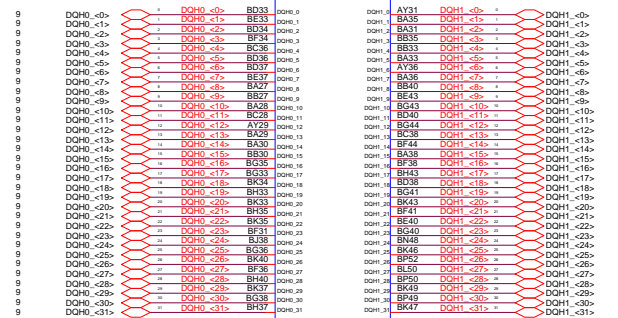
U1I

SYMBOL 9



U1J

SYMBOL 10



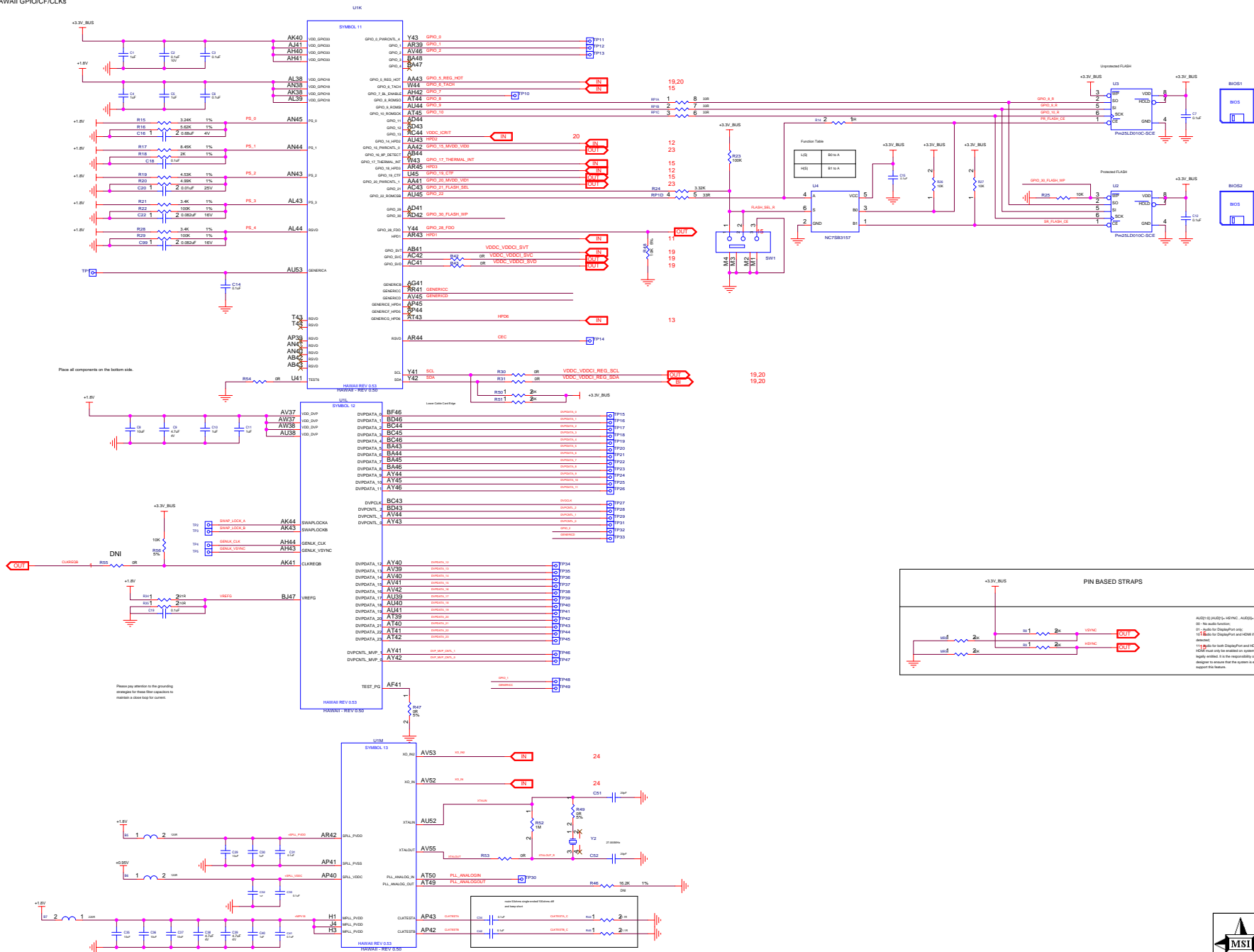
U3200

U3300

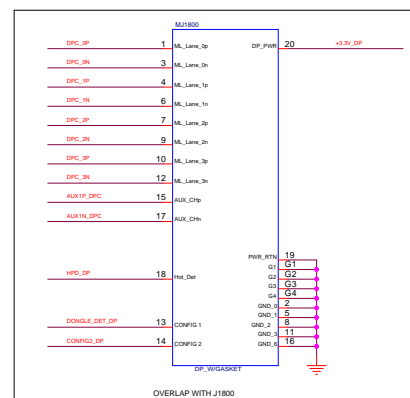
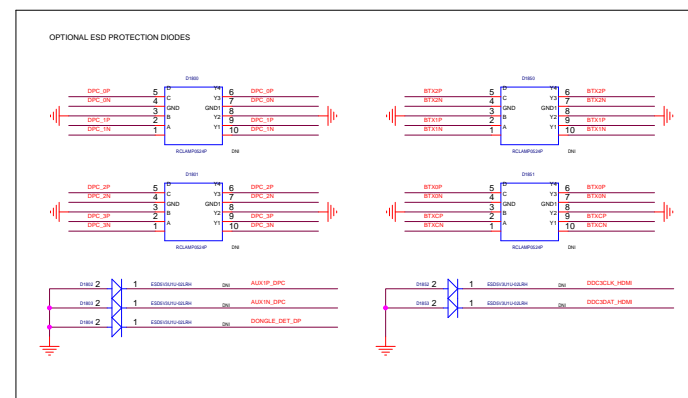
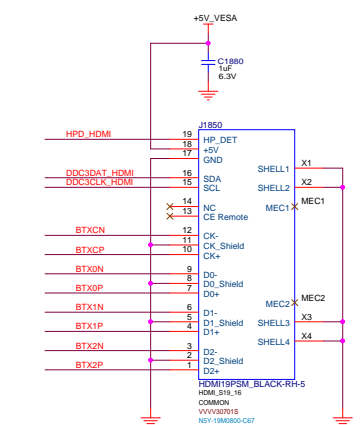
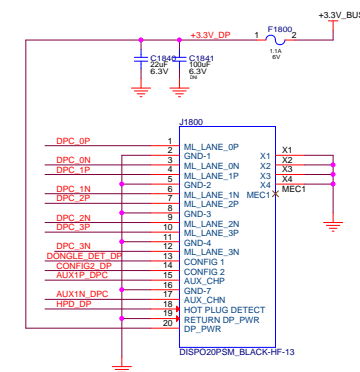
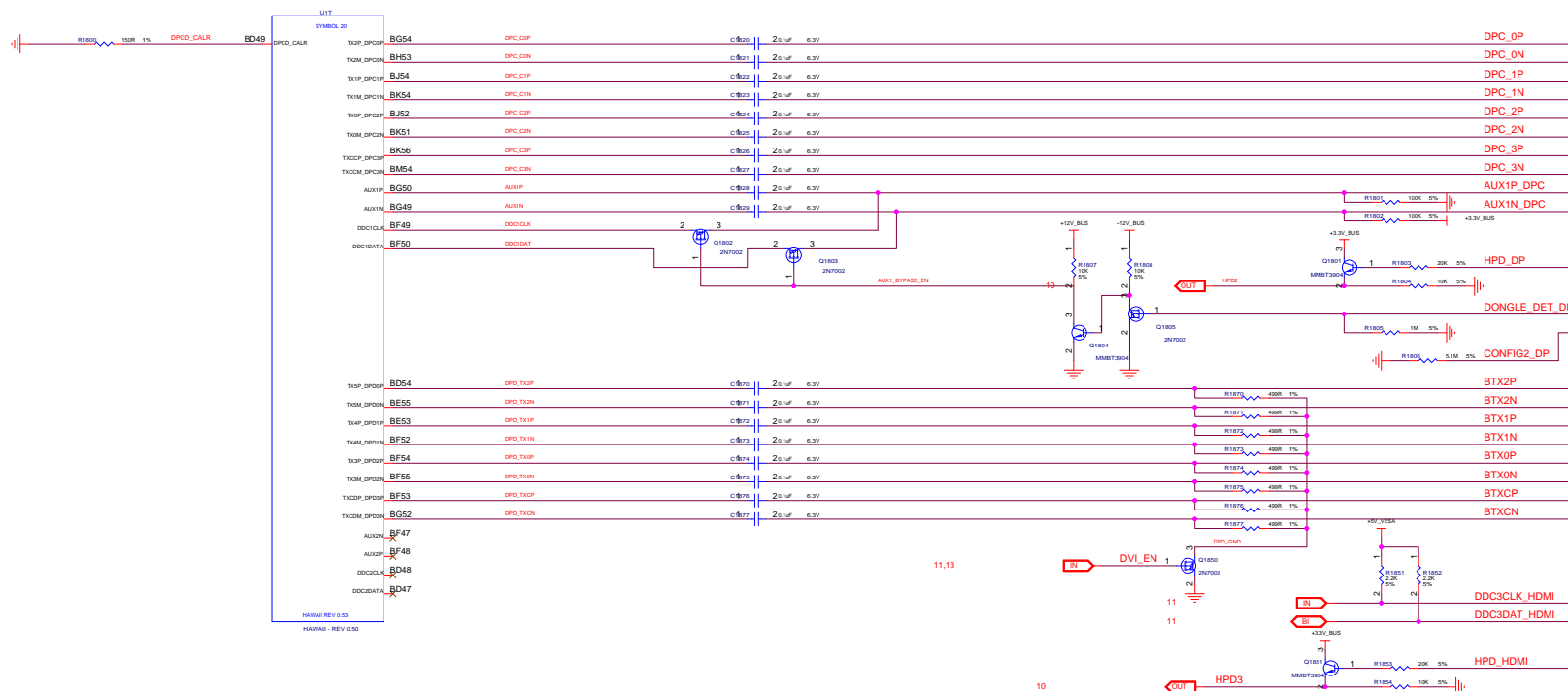
U3400

U3500

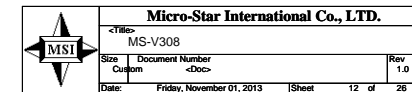




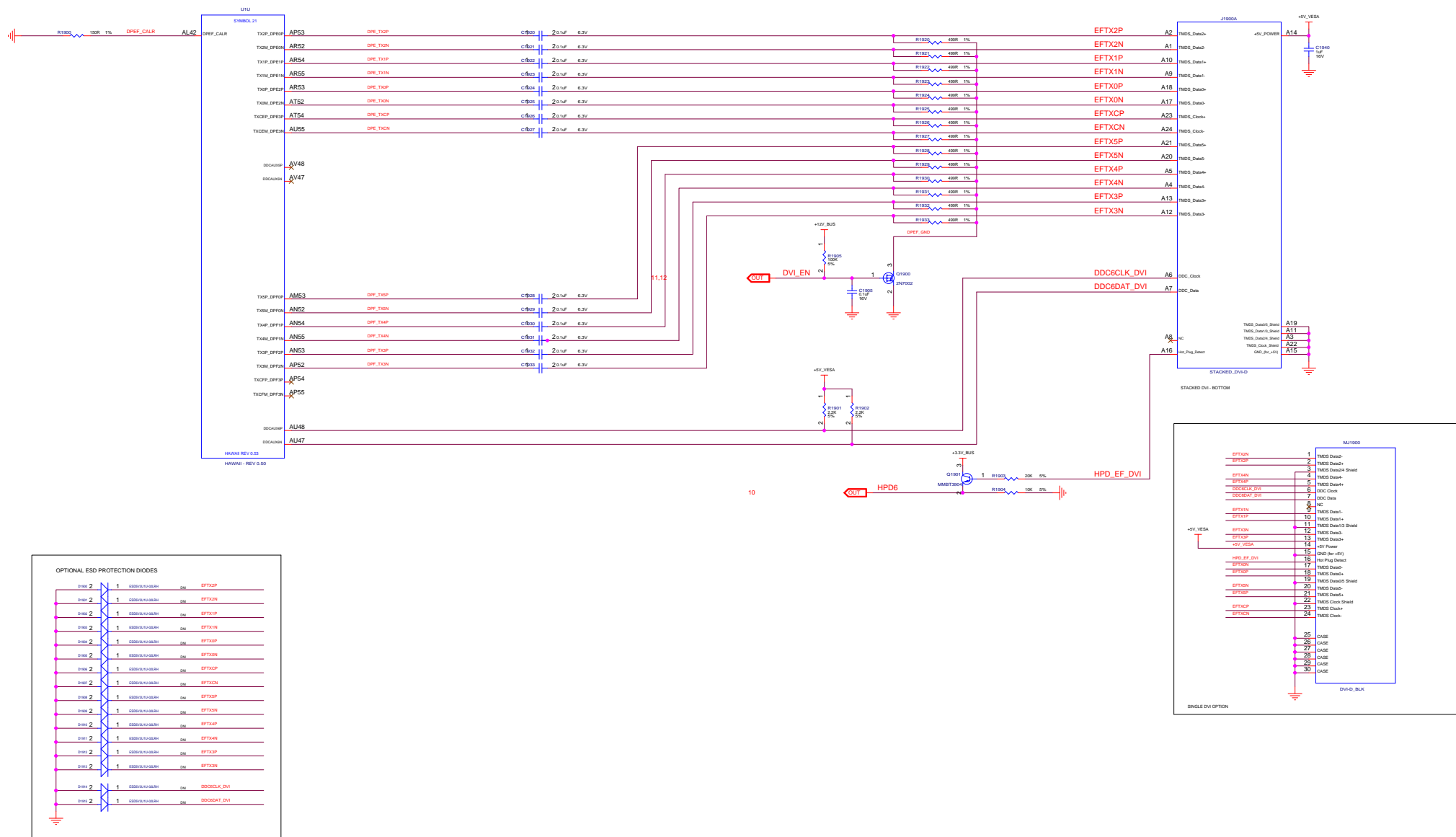


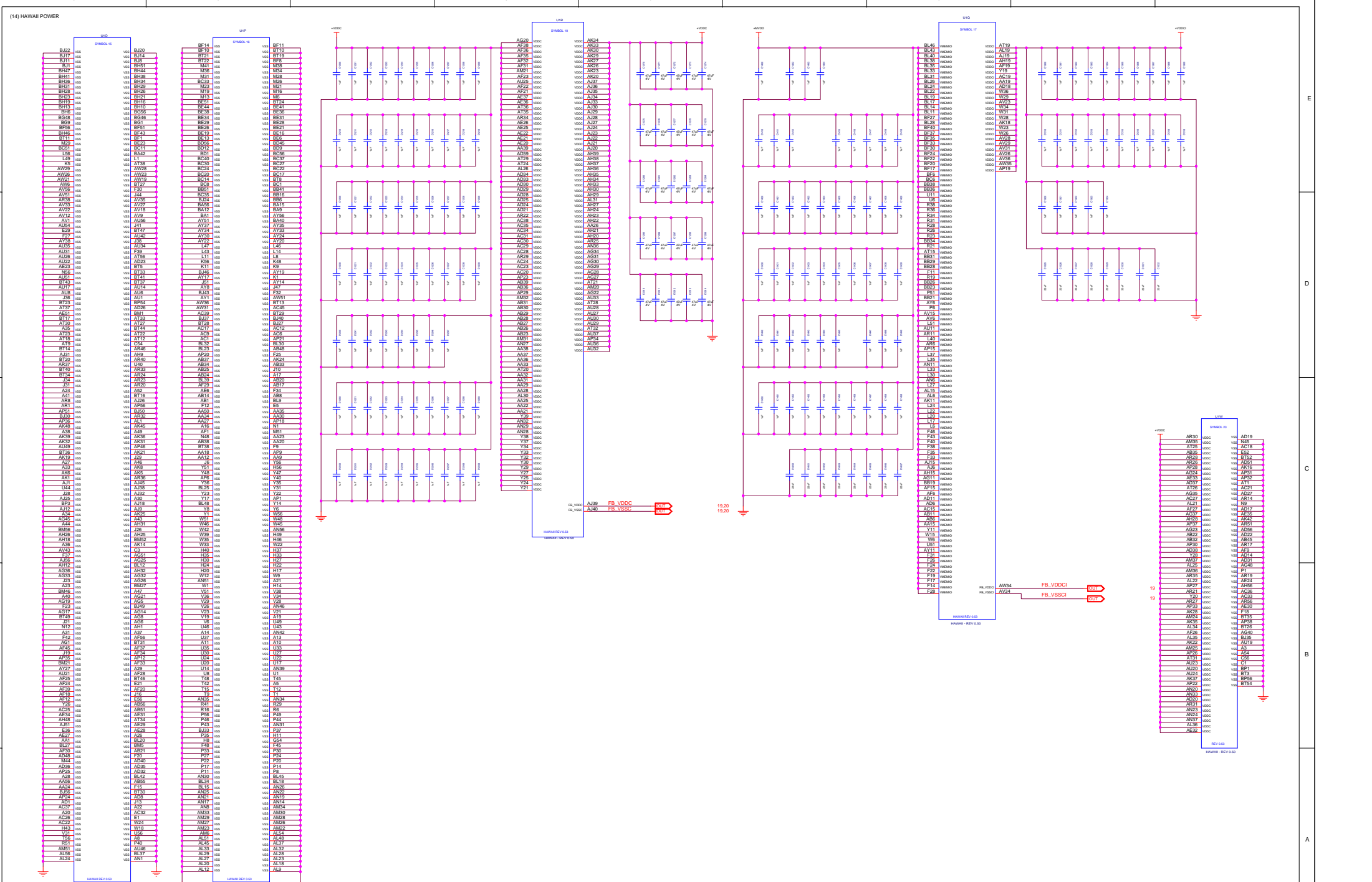


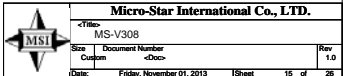
CHANGE HDMI/DP FOOTPRINT

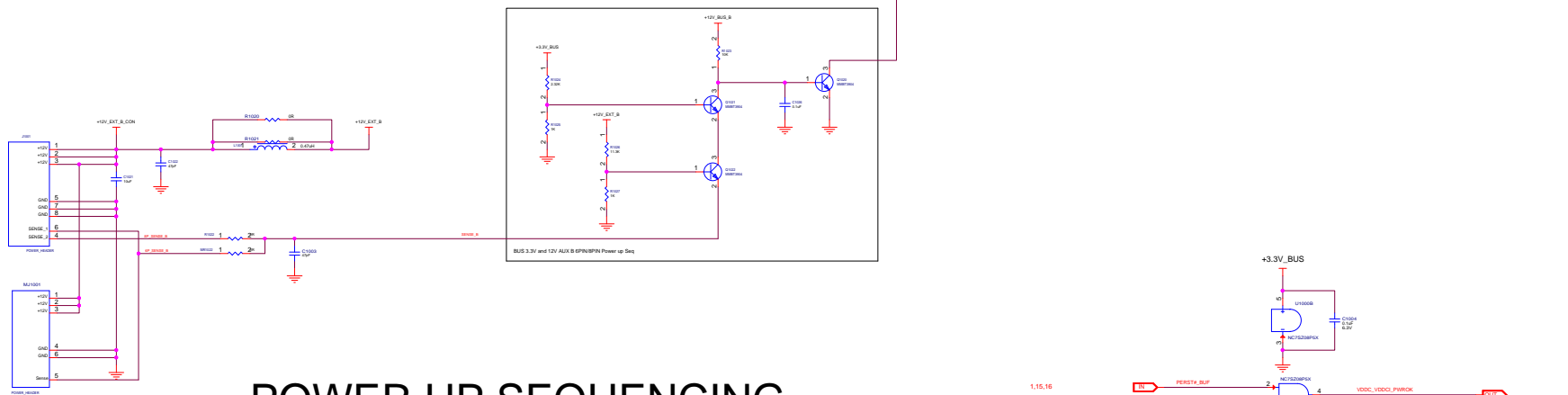
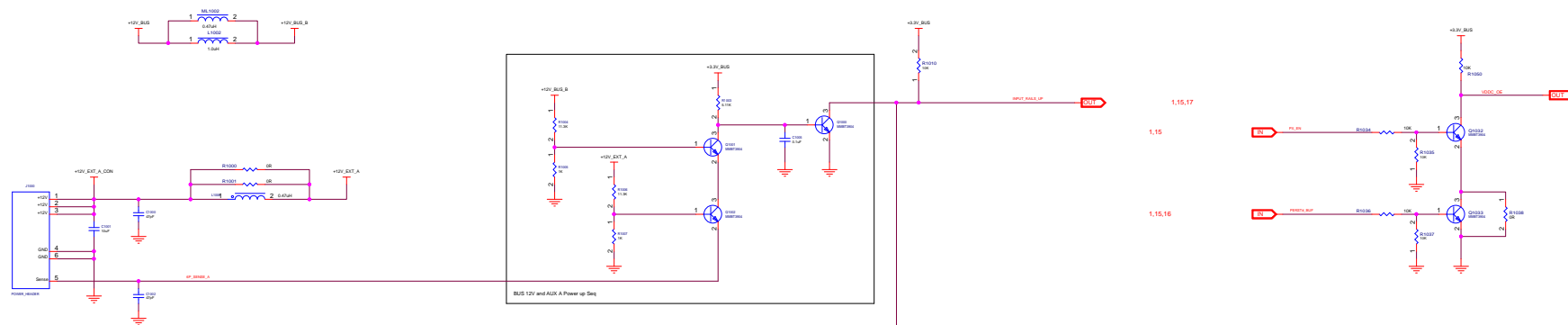


(13) HAWAII TMDPEF DVI

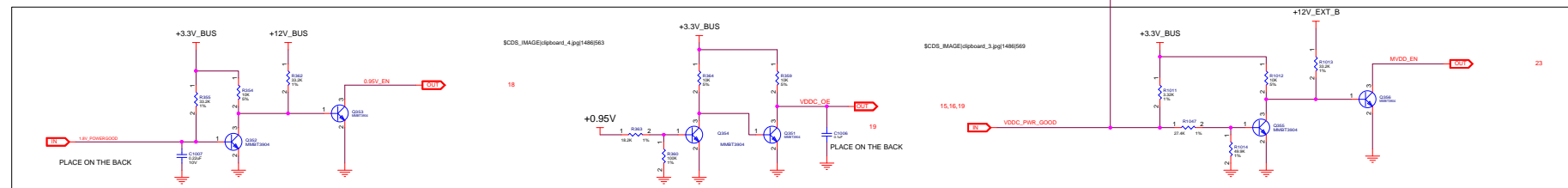




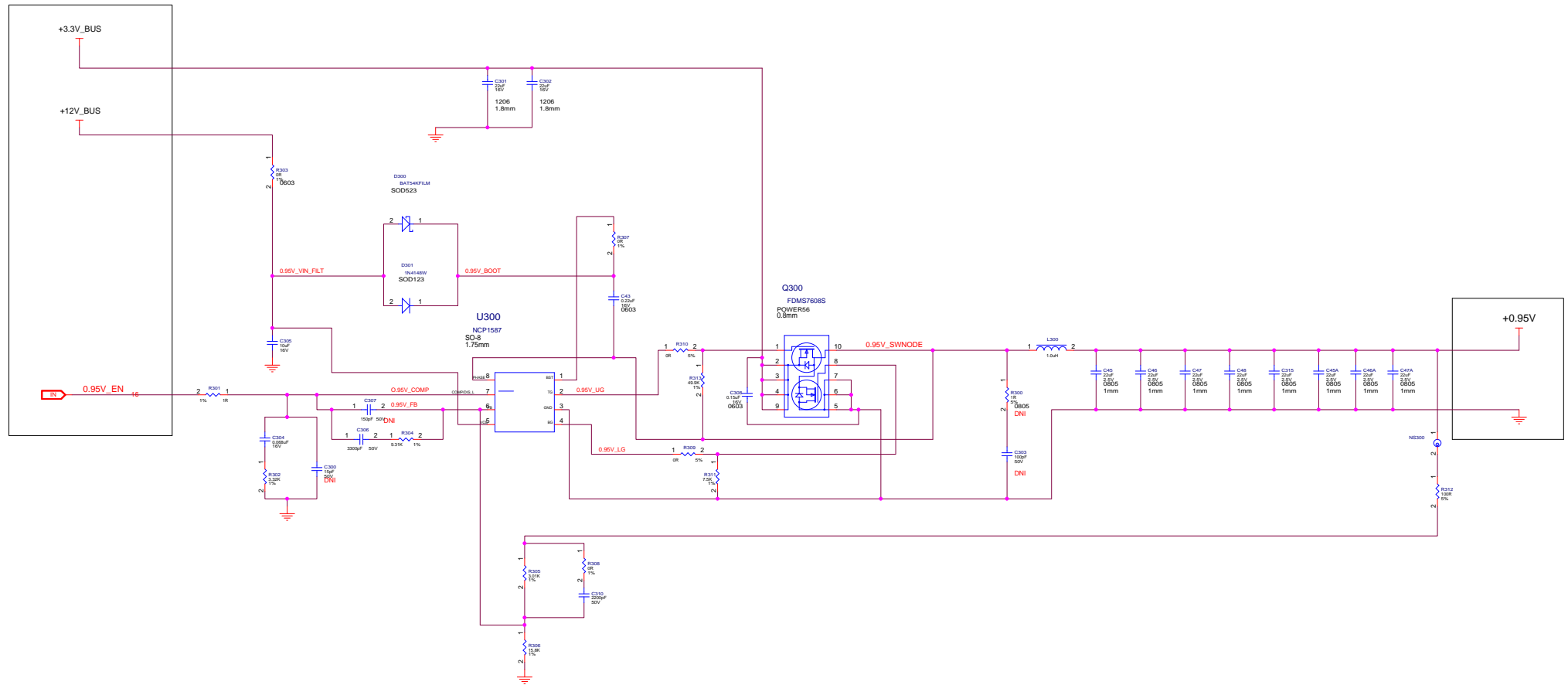


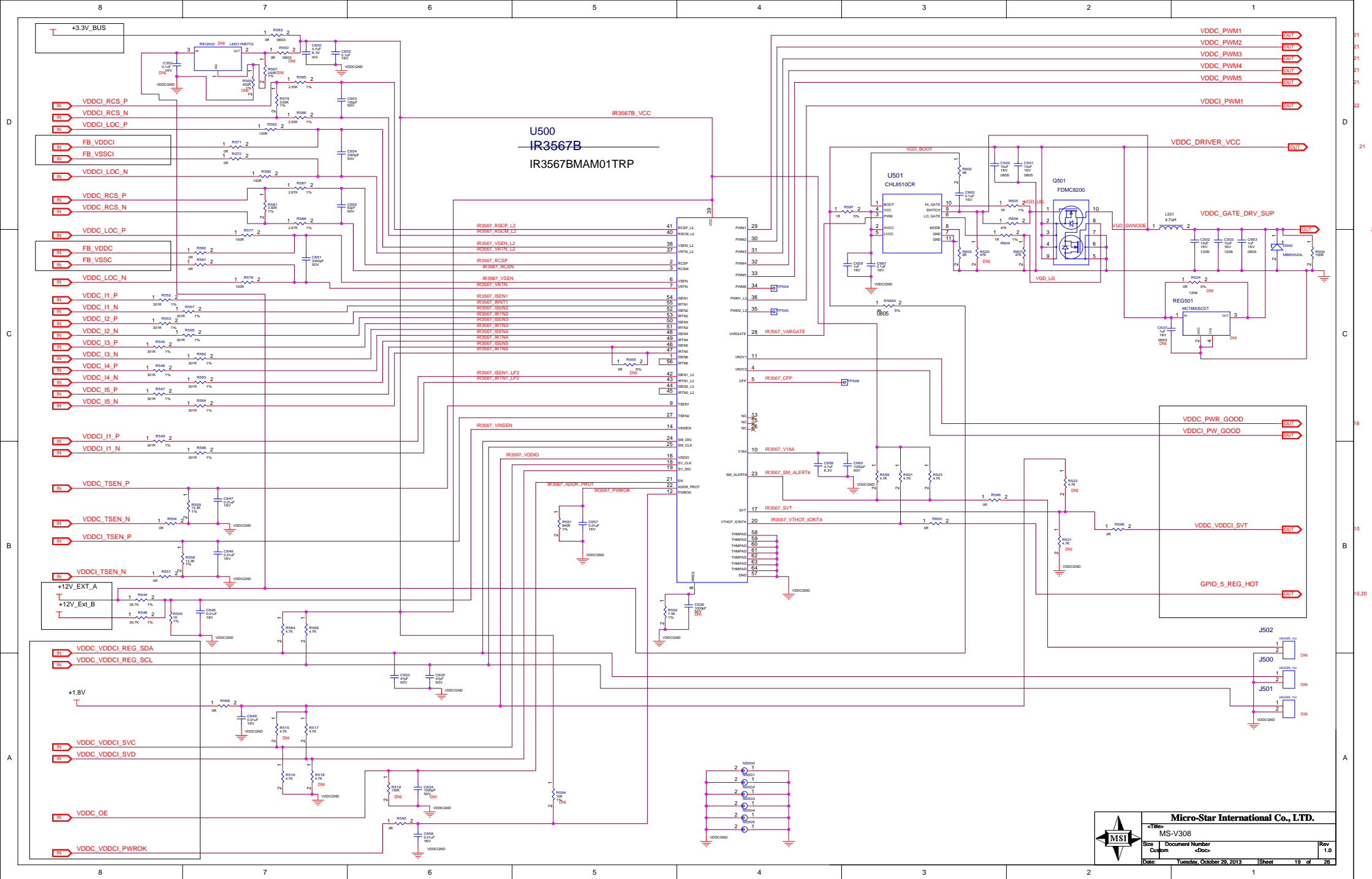


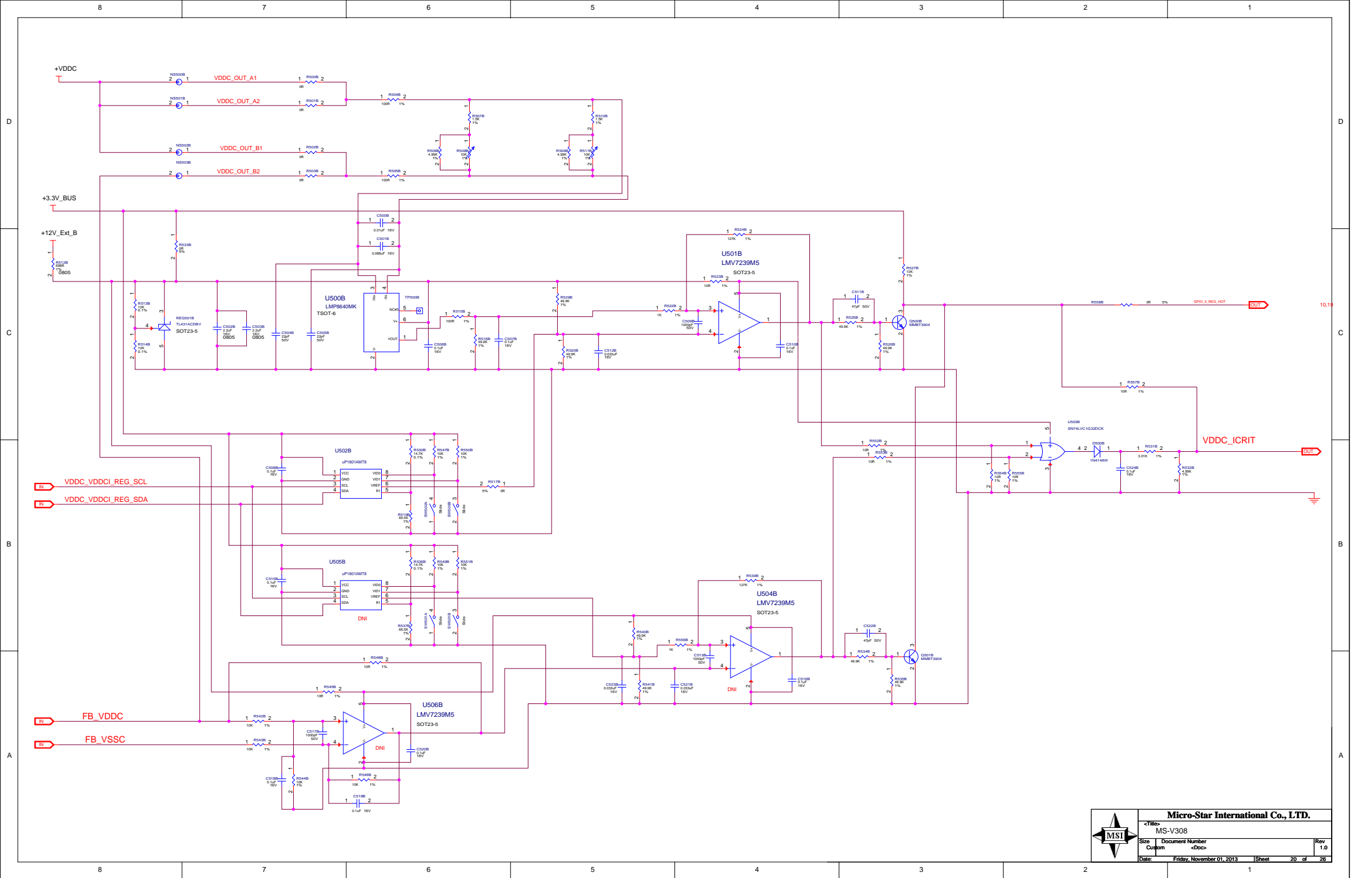
POWER UP SEQUENCING



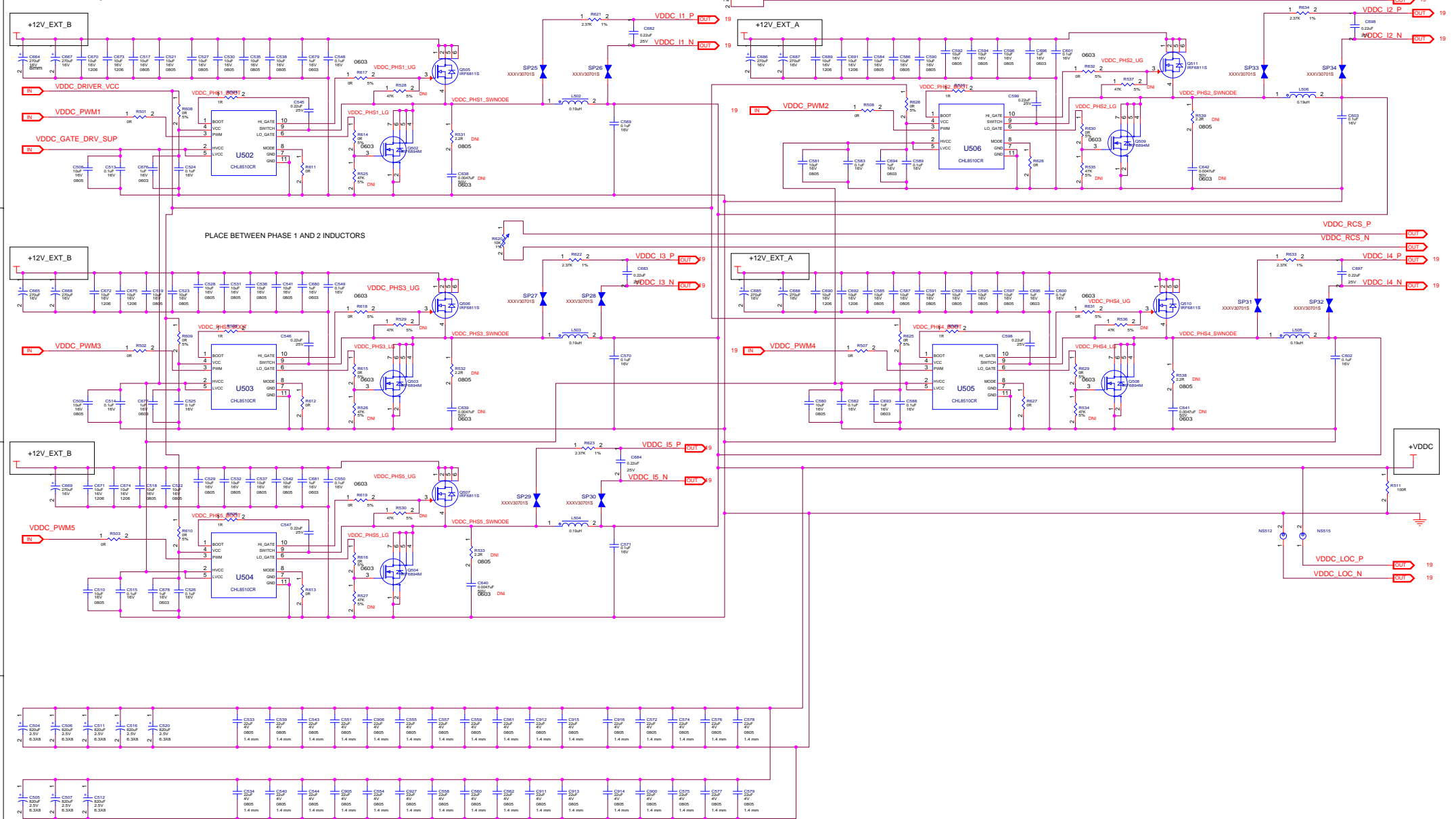
0.95V



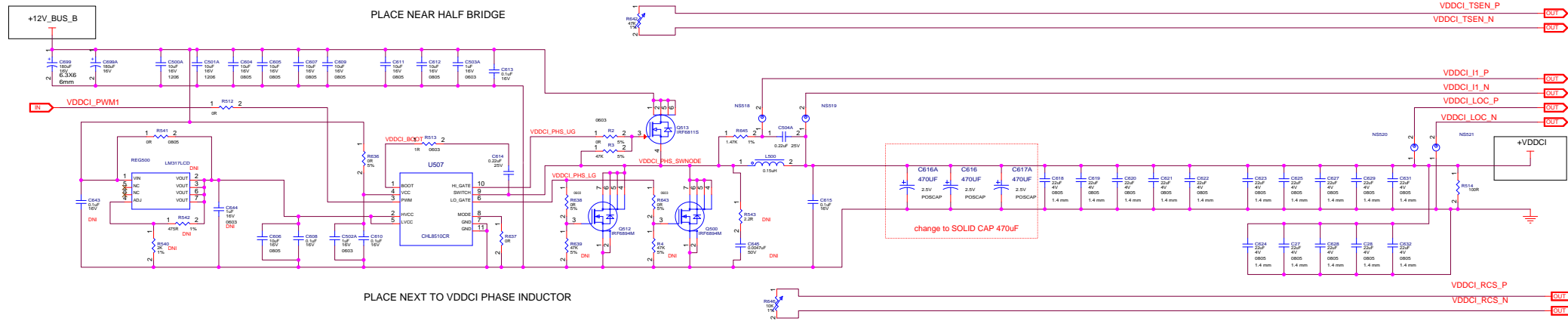




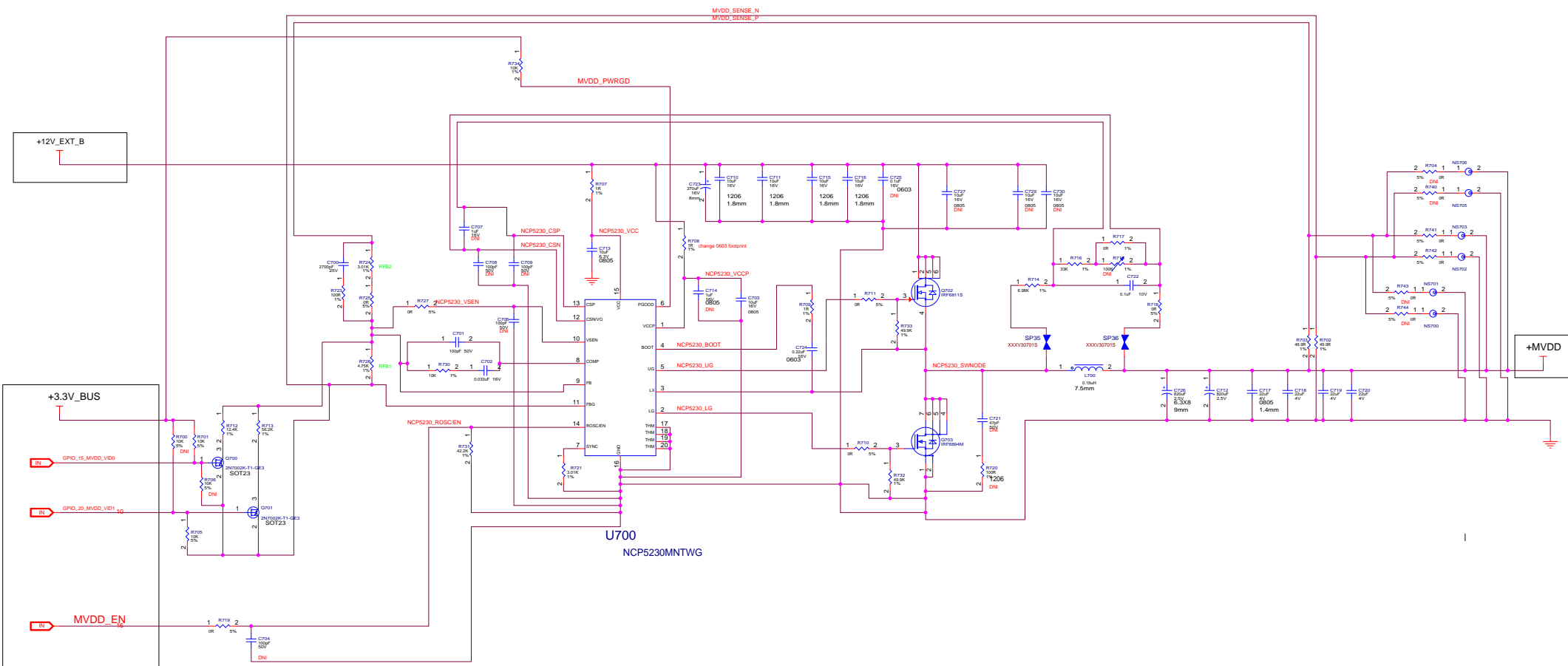
VDDC 1~5 phase



VDDCI phase



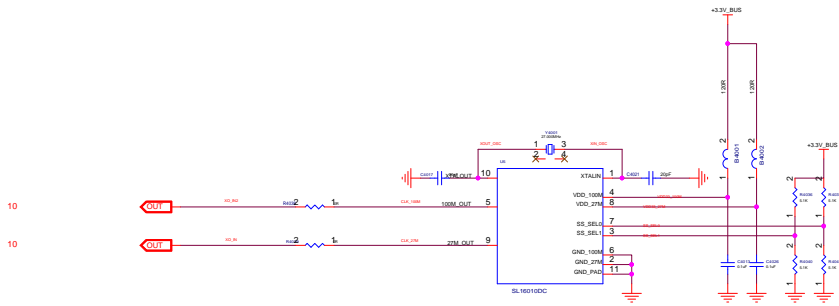
MVDD





remove JTAG circuit

LED GREEN "ON" shows Pk_EN
remove

LED RED "ON" shows Fault
remove



8			7			6			5			4			3			2			1		
AMD 			TITLE: Hawaii GD5 - DVI-DVI-HDMI-DP						DOCUMENT NUMBER: 105-C671XX-00			DATE: Wed Oct 16 20:41:25 2013						SHEET NUMBER: 26 OF 26			REV: 1.0		
REVISION HISTORY									ENGINEER: XXXX			NOTES: NOTE			<div>CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC.</div> <div><small>This AMD Board schematic and design is the exclusive property of AMD, and is provided only in written under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representation or warranty, of any kind regarding this schematic and design, including, not limited to, any implied warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information included herein.</small></div>						AMD - GRAPHICS 1 COMMERCE VALLEY MARKHAM, ONTARIO, L3T 7X6		
SCH Rev	PCB Rev	Date	REVISION DESCRIPTION																				
1																							
2	A	SEP 6 2013																					
	B	MAR 18 2013	REMOVED LOCAL ANALOG GROUND FOR U700 ADDED SELECTABLE REGULATION POINT AT THE LOAD CONNECTED VSEN (UVP AND OVP DETECT) THROUGH AN RC FILTER R727 C706 (DNI FOR THIS REVISION) MODIFIED PHASE LEAD ZERO POSITION BY CHANGING R723 FROM 590R TO 100R CHANGED C712 FROM DNI TO 820uF 2.5V , Remove C666 Added R363 , R364 , Q354 PCC CIRCUIT UPDATED WITH NON INVERTOR OPTION , RS31B , RS32B ADDED																				
	C		Power Management (16) , VDD (19) , PCC (20) , +1.8V regulator changed to LDO																				
	D		Fan supply changed from +12_BUS to +12_BUS_B , MC201 added , J201 removed																				
	E		PCC Circuit page 20 updated																				
	-00		CF Connector removed,Replaced C1270-C1294 (25 caps) with 47uF																				
	2010/10/25		P01. remove JTAG circuit P12. change DP and HDMI footprint P21. VDDC choke change to DIP package input cap change to 270uF 8X8 output cap change to 820uF 6.3X8 P22. VDDCI output CAP change to 470uF X3 P23. MVDD choke change to DIP package input cap change to 270uF 8X8 output cap change to 820uF 6.3X8 P24. remove debug circuit																				
8			7			6			5			4			3			2			1		
			<div>Micro-Star International Co., LTD.</div> <div><Title>MS-V308</div> <div>SizeCustomDocument Number<Doc>Rev1.0</div> <div>Date: Tuesday, October 29, 2013Sheet26 of 26</div>																				