

# PG160-B02

8GB GDDR6, 256b, X16  
Tall DVI-D + USB-C/DP + DP + HDMI/DP + DP

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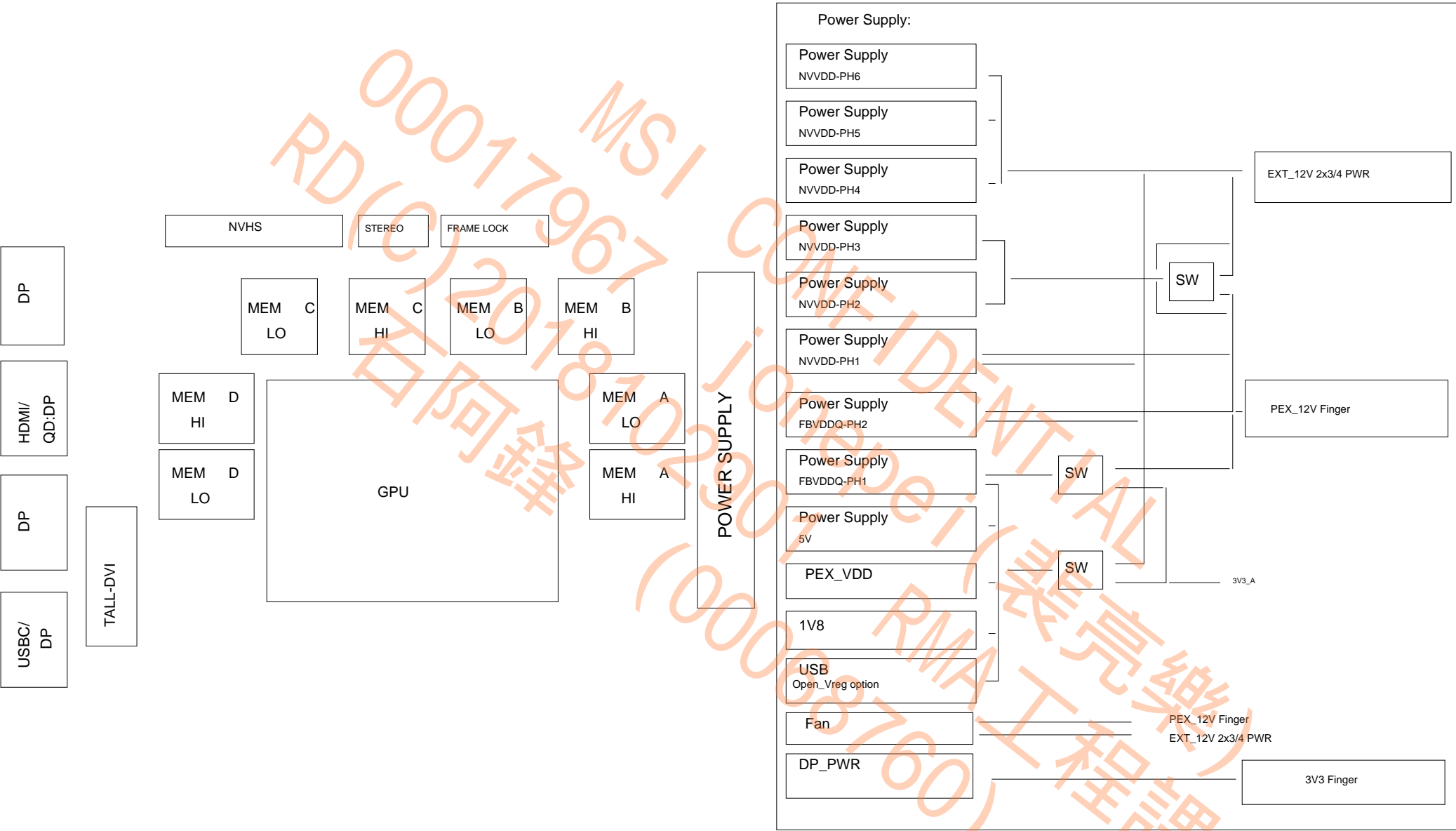
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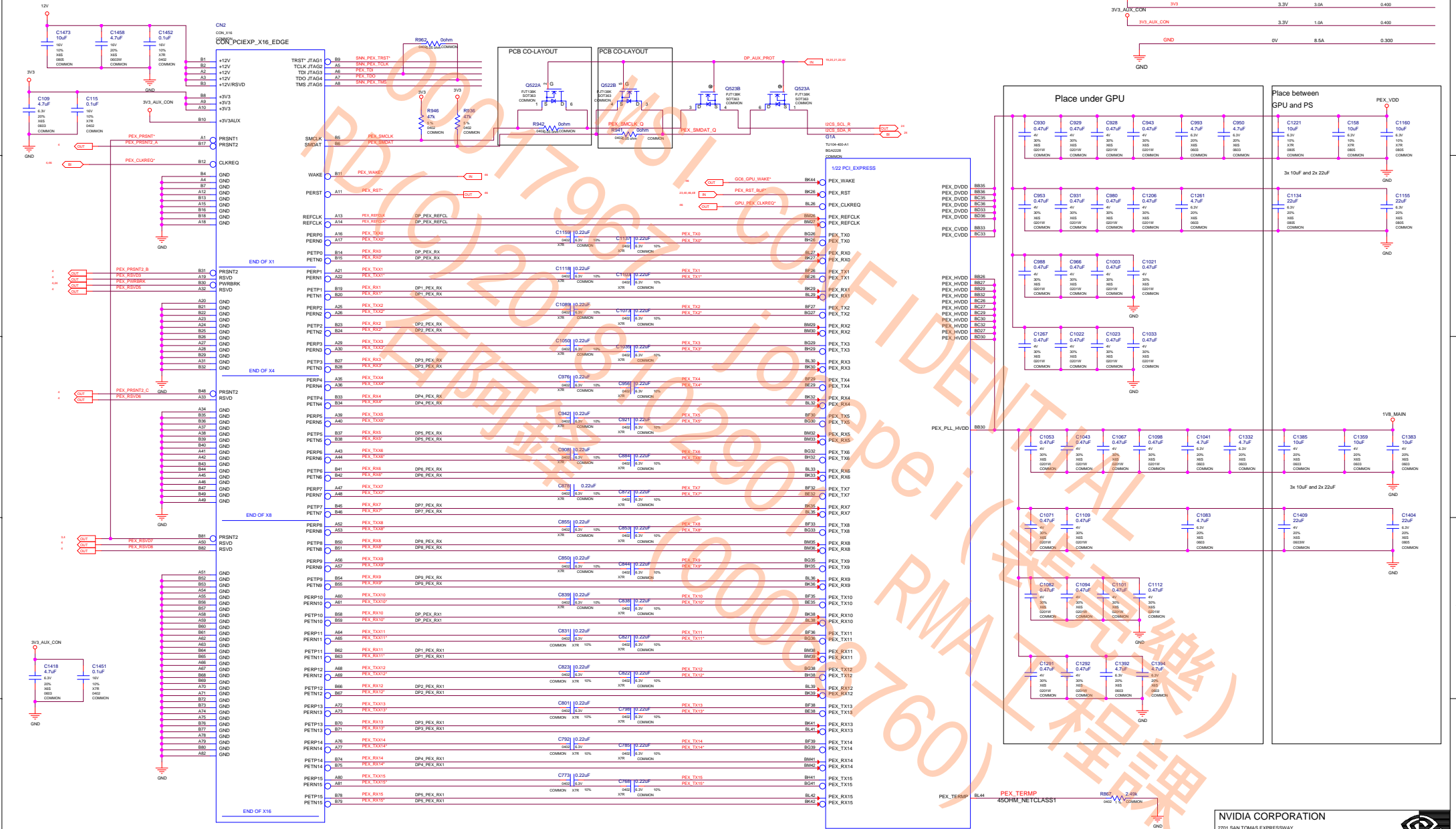
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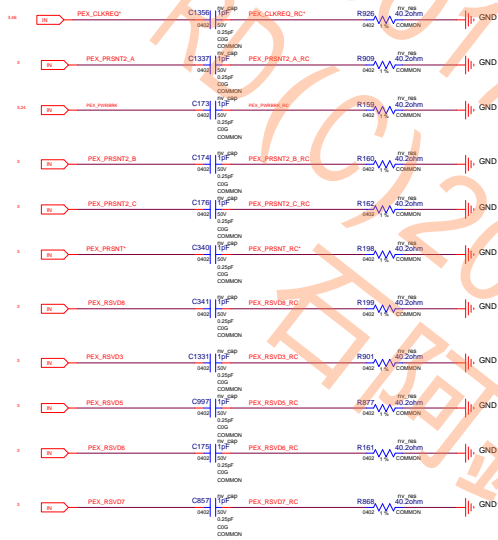
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PAGE DETAIL	Block Diagram

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PAGE DETAIL		PCI Express			
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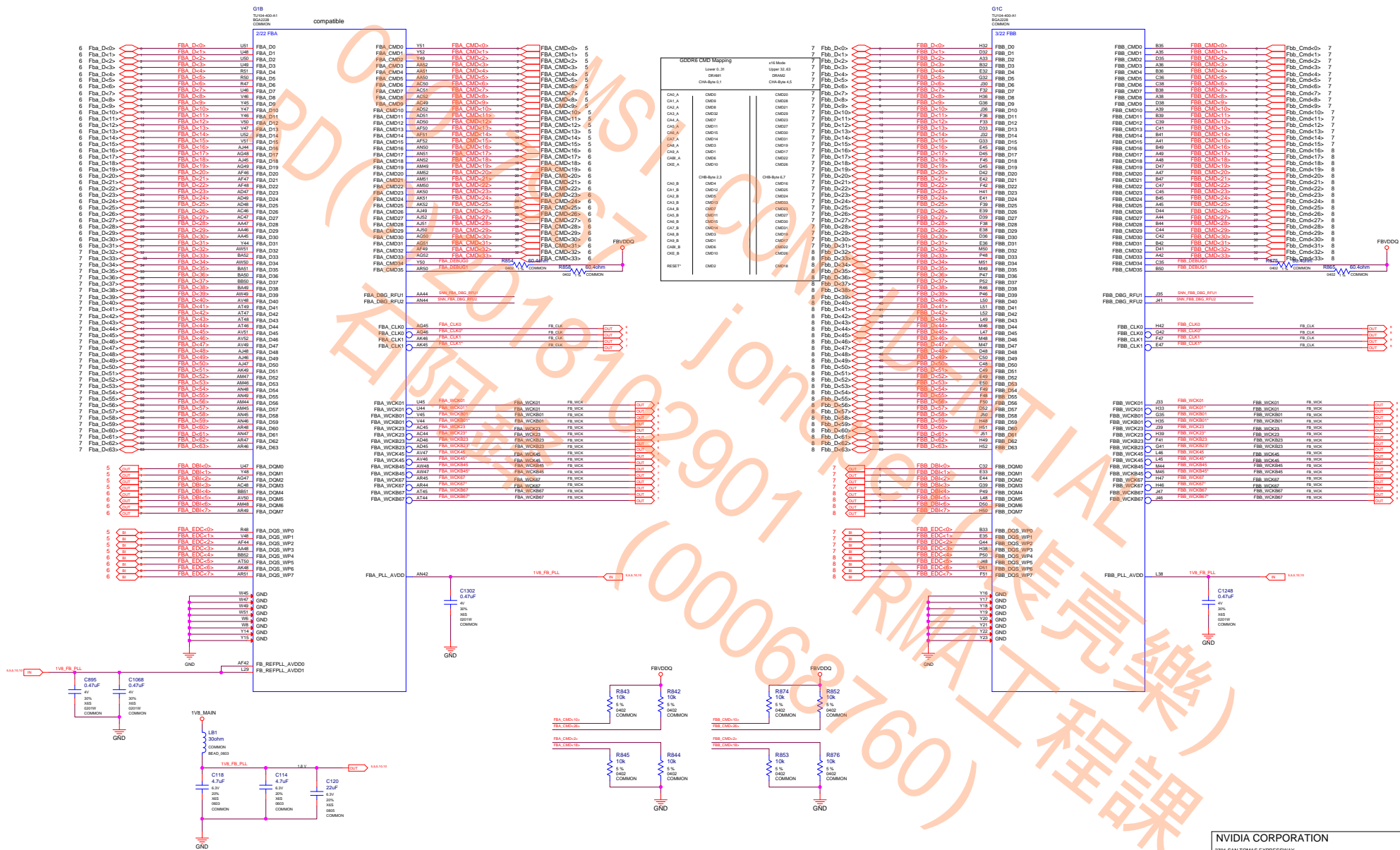
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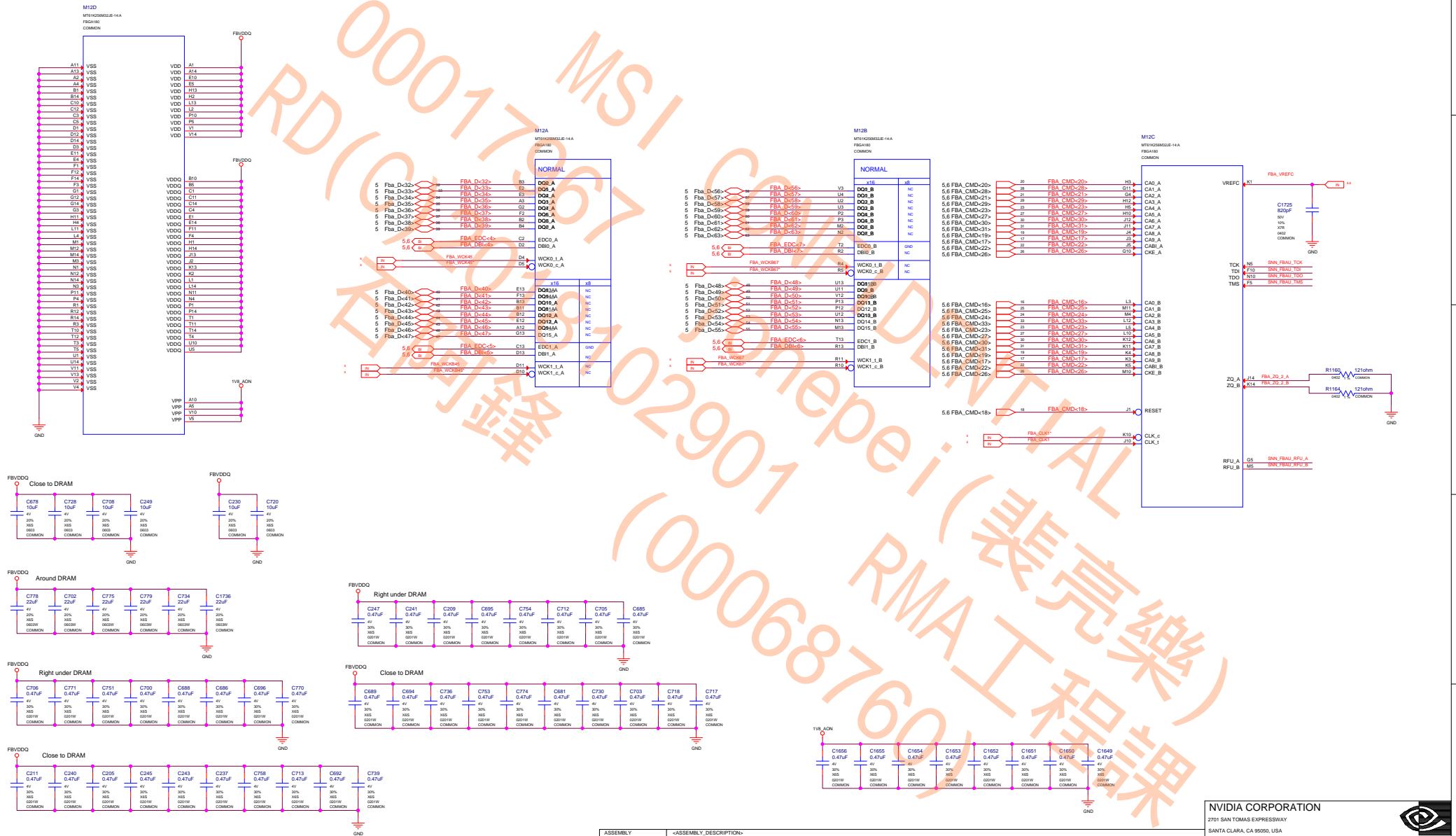
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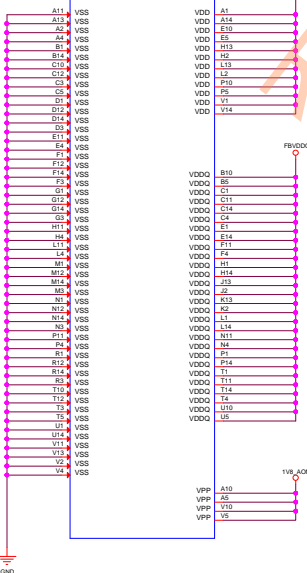




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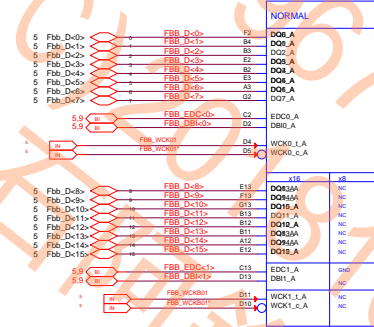


FBIVDDQ

FBIVDDQ

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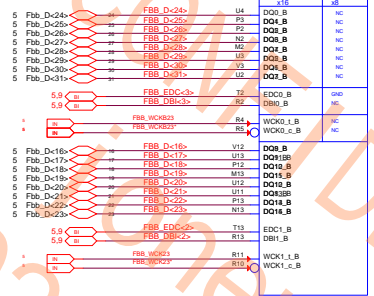
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FBIVDDQ

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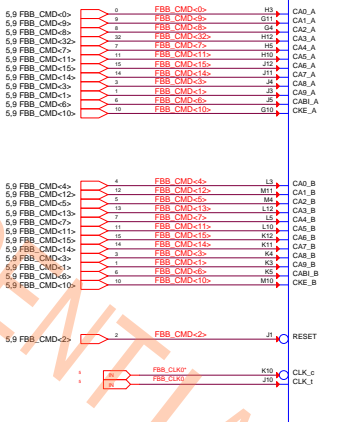
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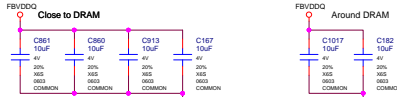
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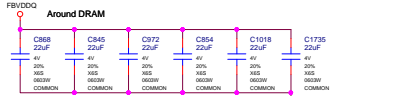
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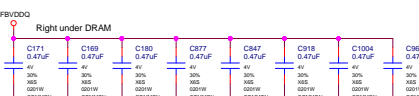


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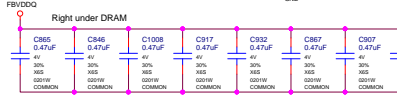
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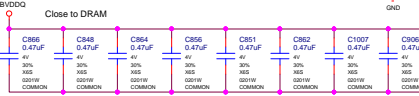
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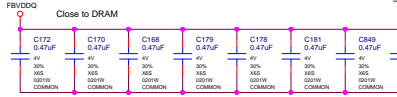
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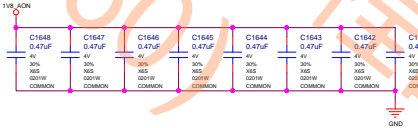
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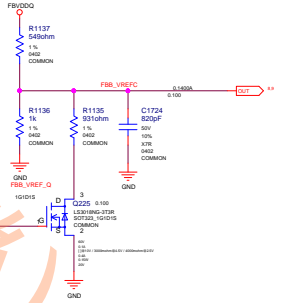


Close to DRAM



Close to DRAM

VREFC IS NOT USED IN  
x16 CONFIGURATION  
1K OHM PULL-DOWN IS  
IN PLACE OF THE 1.33K  
FOR R104



VREFC

FBIVDDQ

GND

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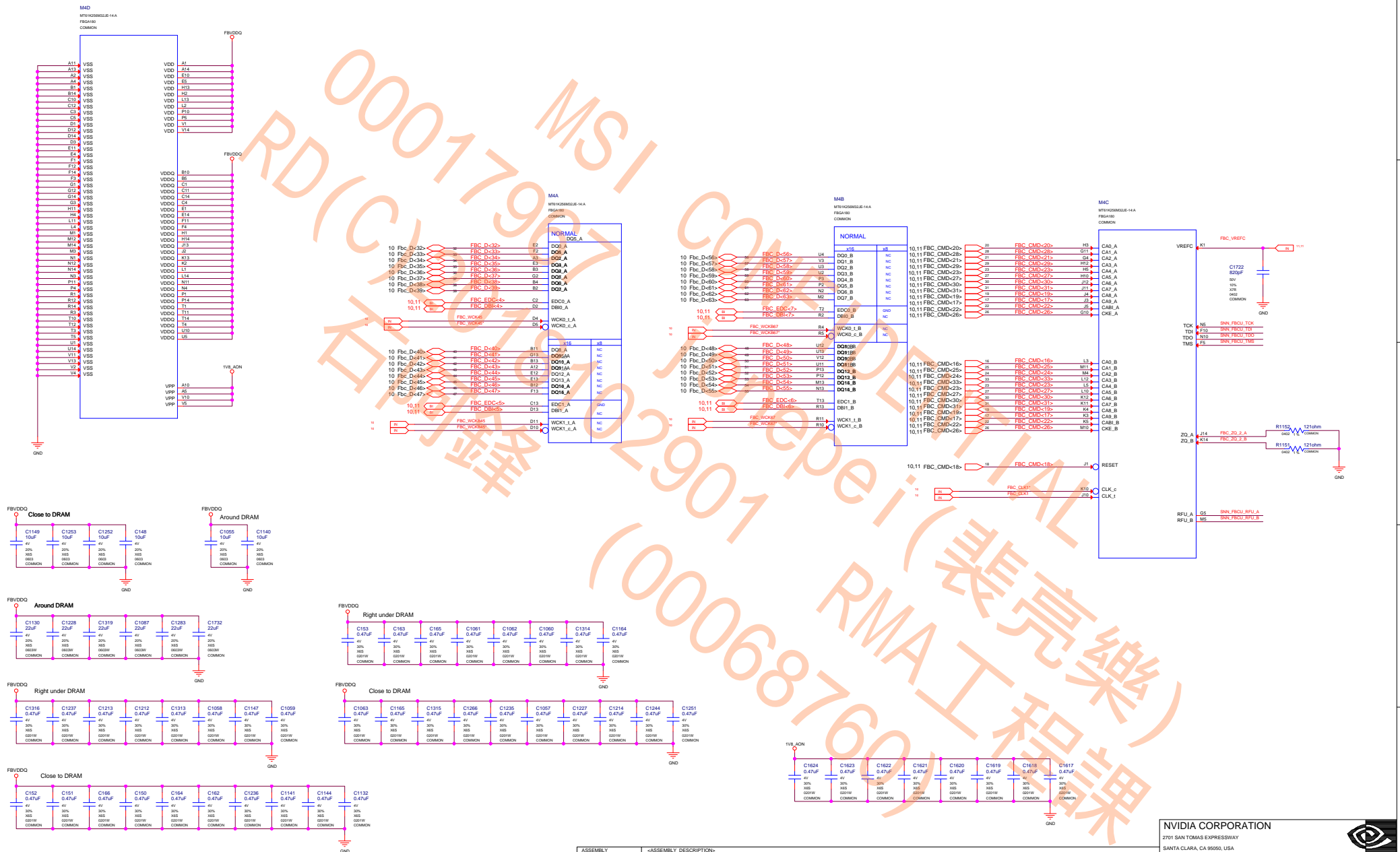
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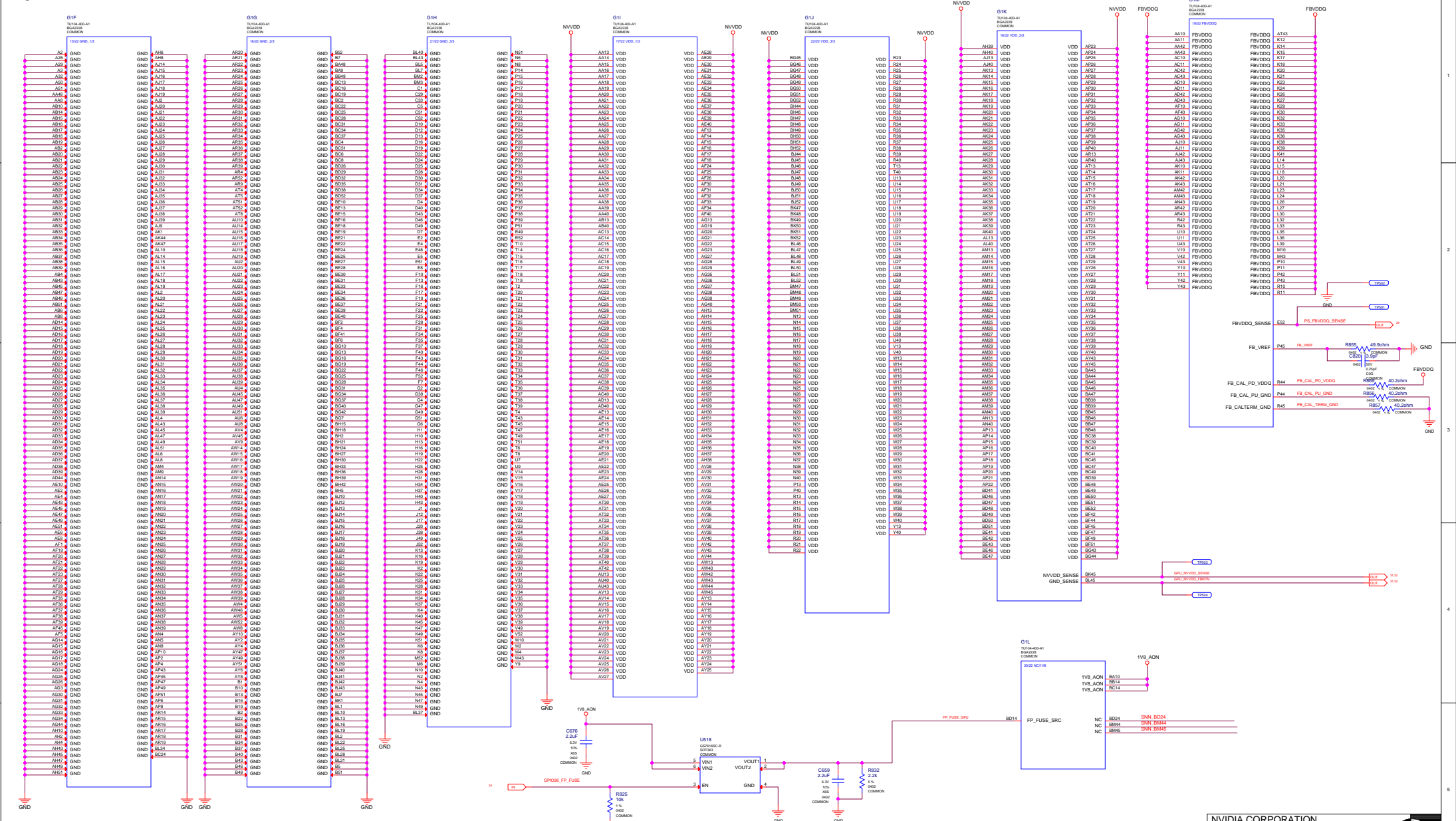


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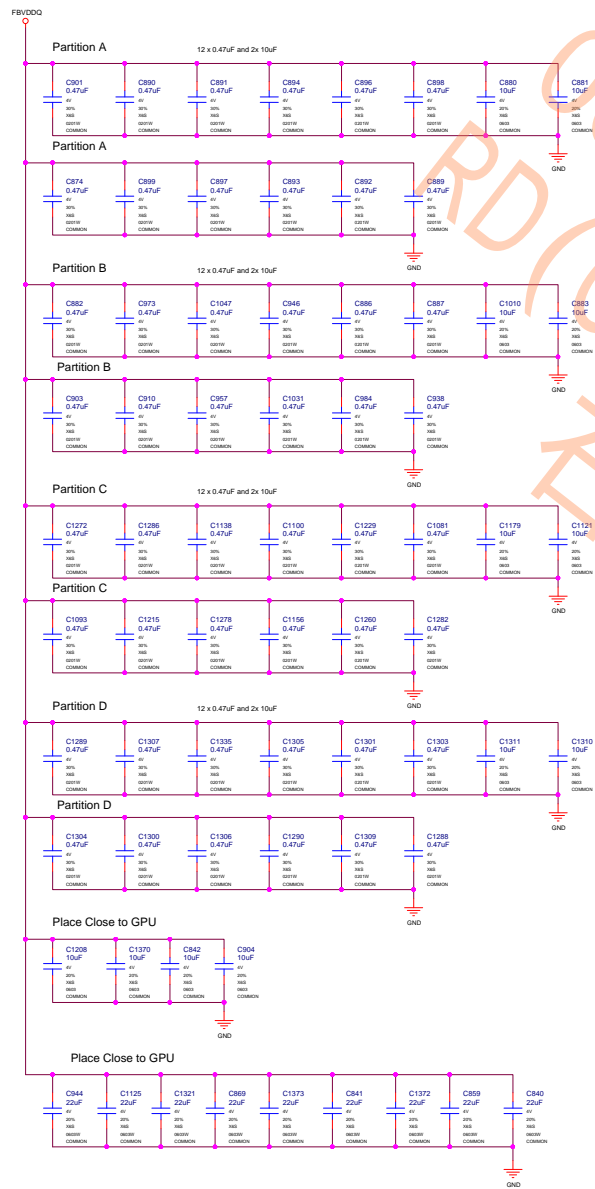




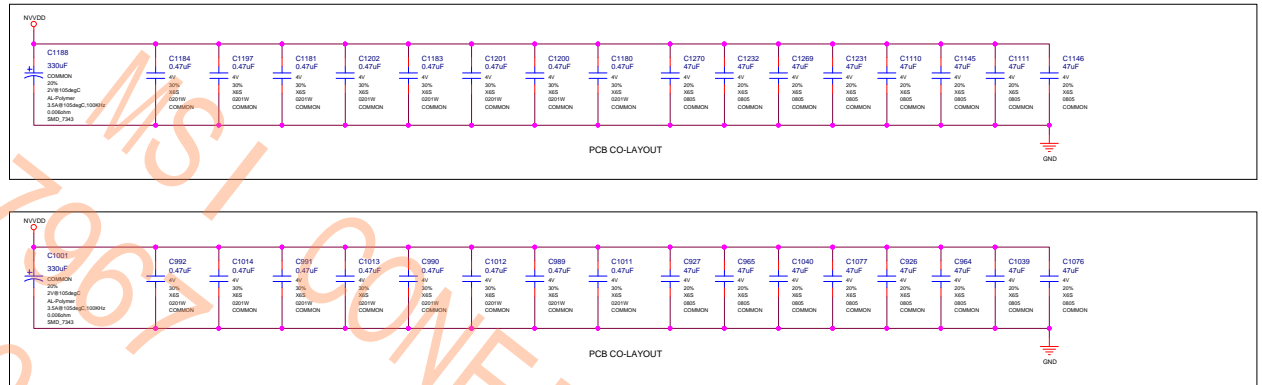




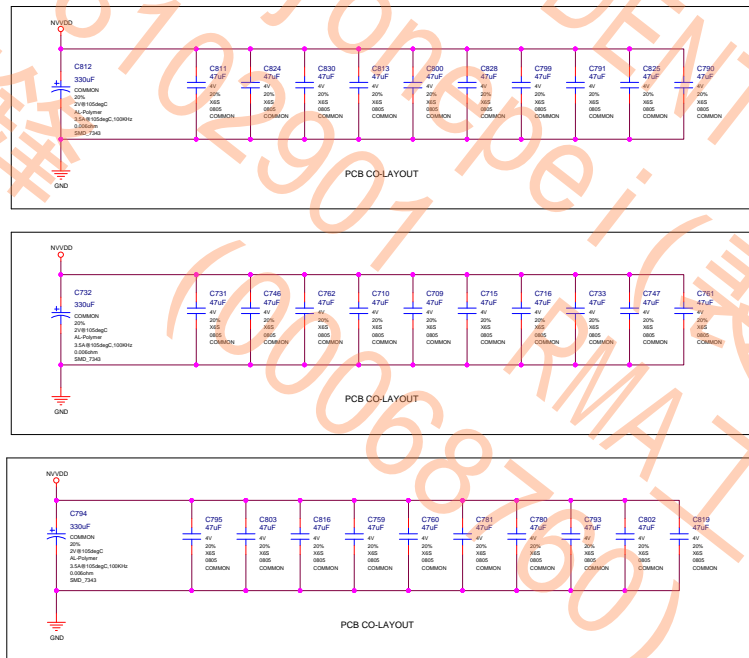
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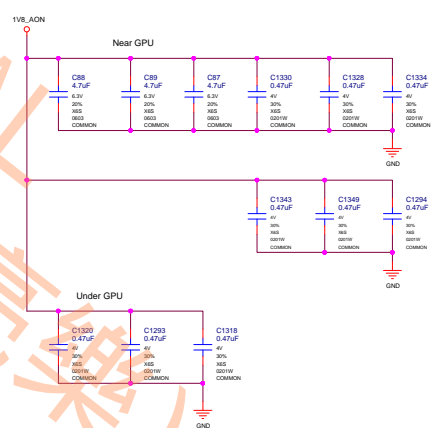
## Under GPU



## Near GPU



## 1V8\_AON



ASSEMBLY	ASSEMBLY DESCRIPTION
PAGE DETAIL	GPU Decoupling

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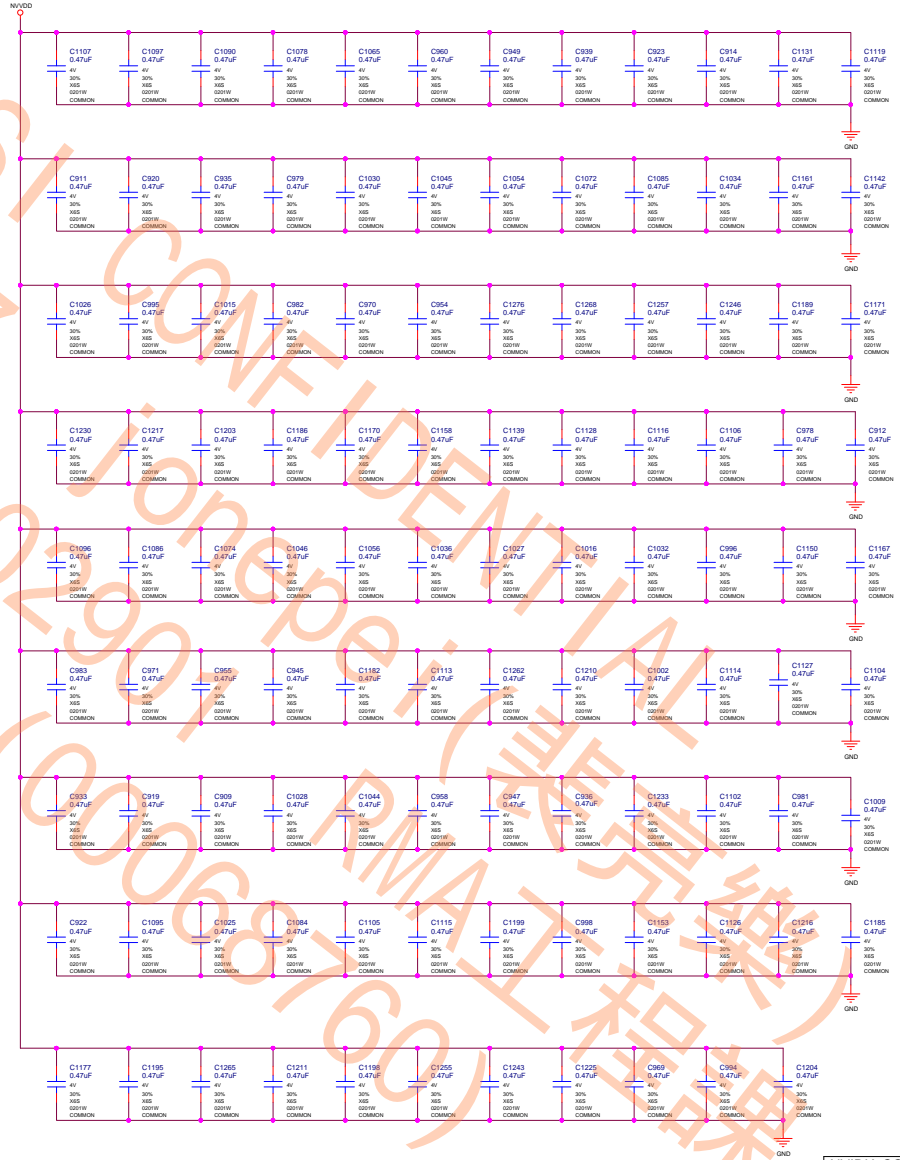
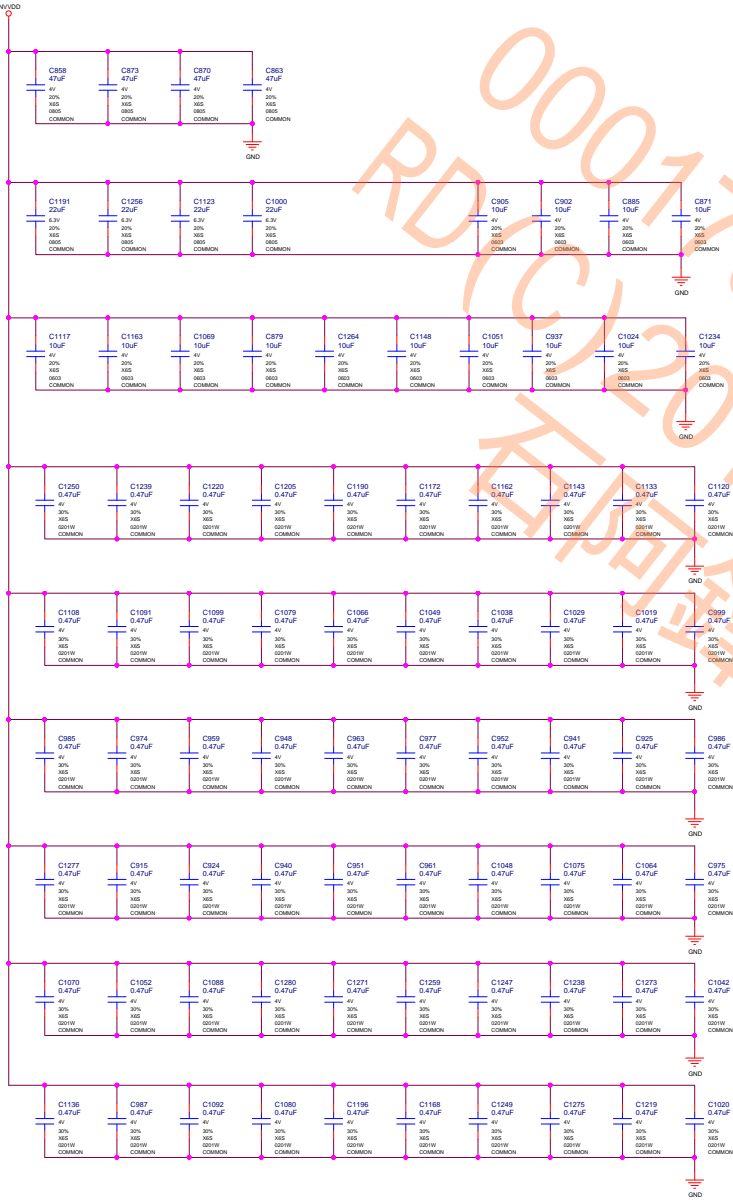
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A	B	C	D	E	F	G	H
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NVDD



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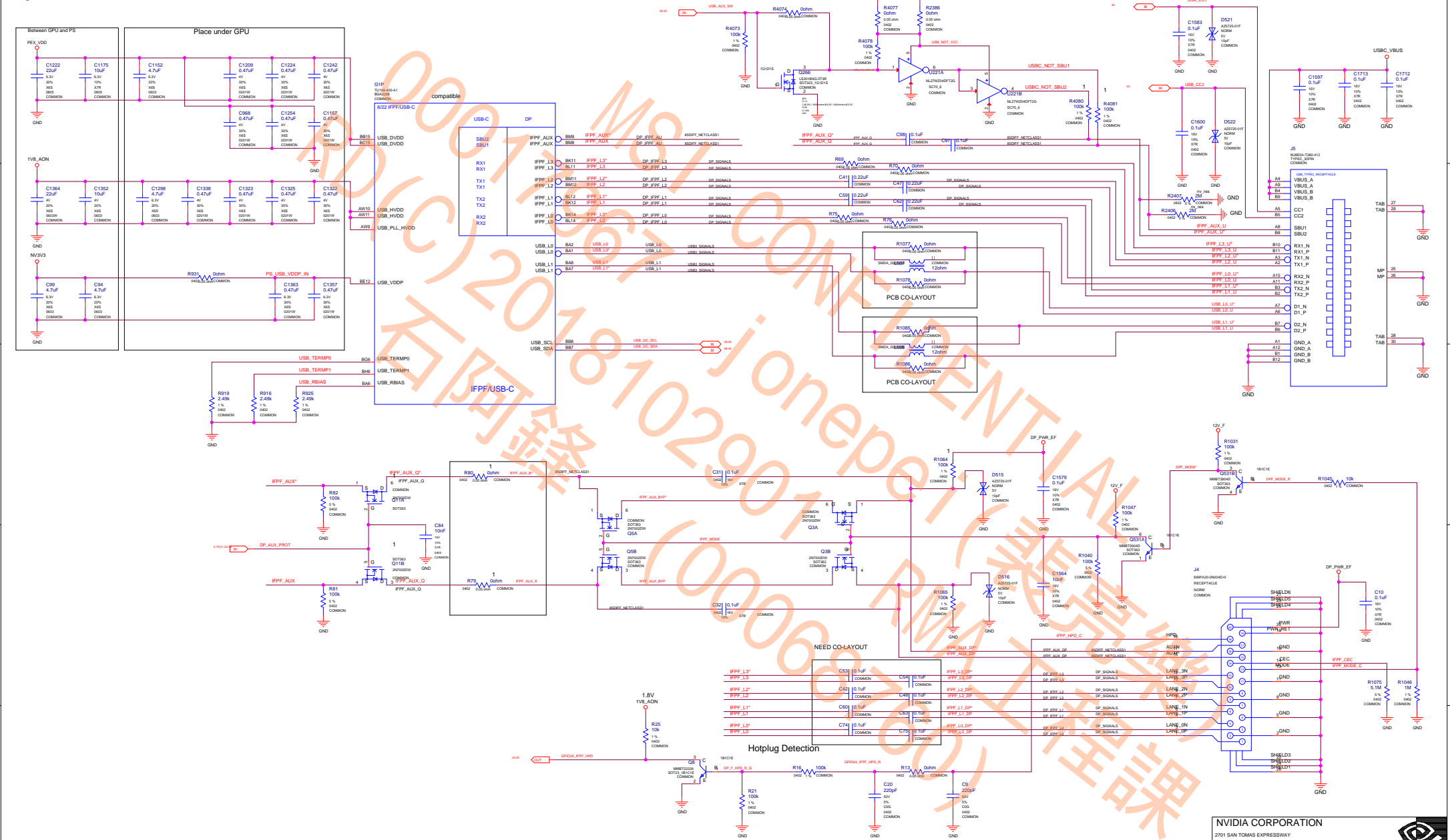
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PAGE DETAIL	GPU Decoupling2

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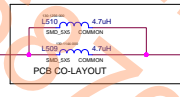
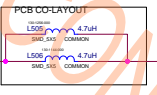
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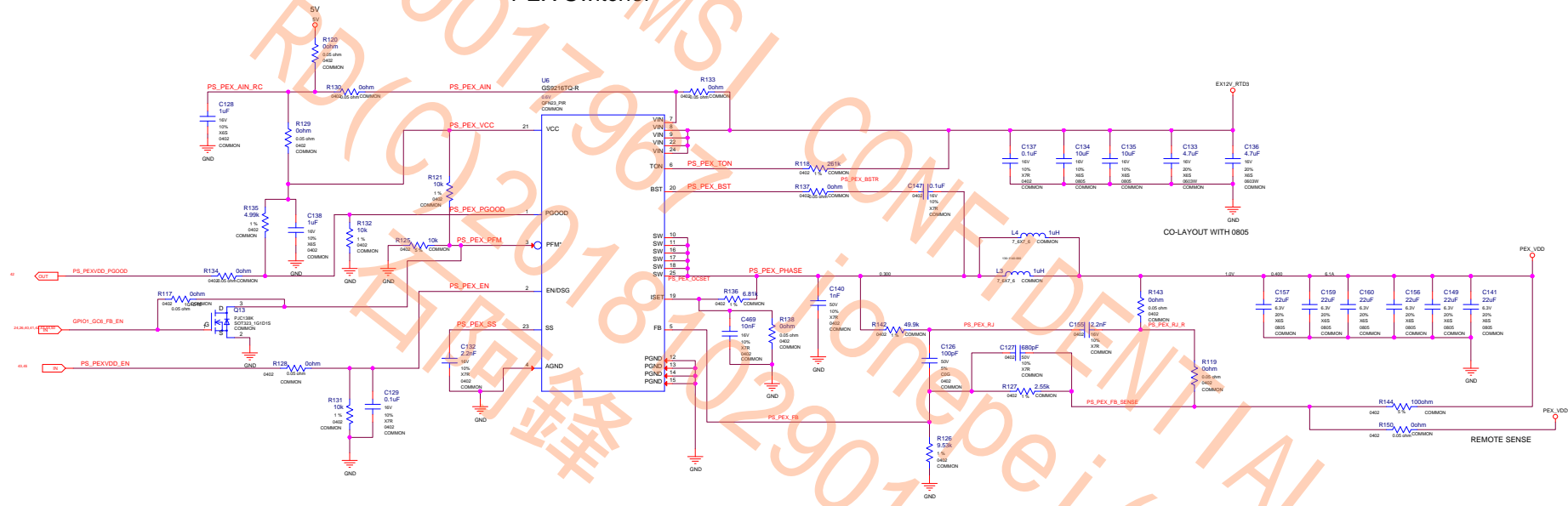





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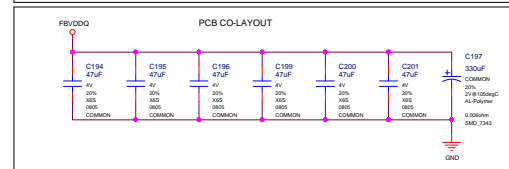
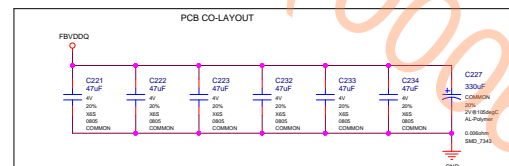
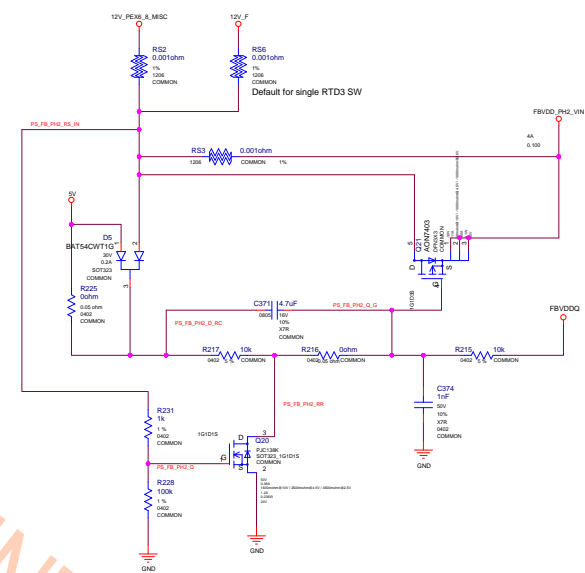
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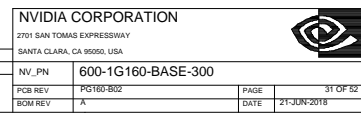




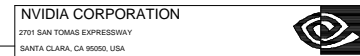
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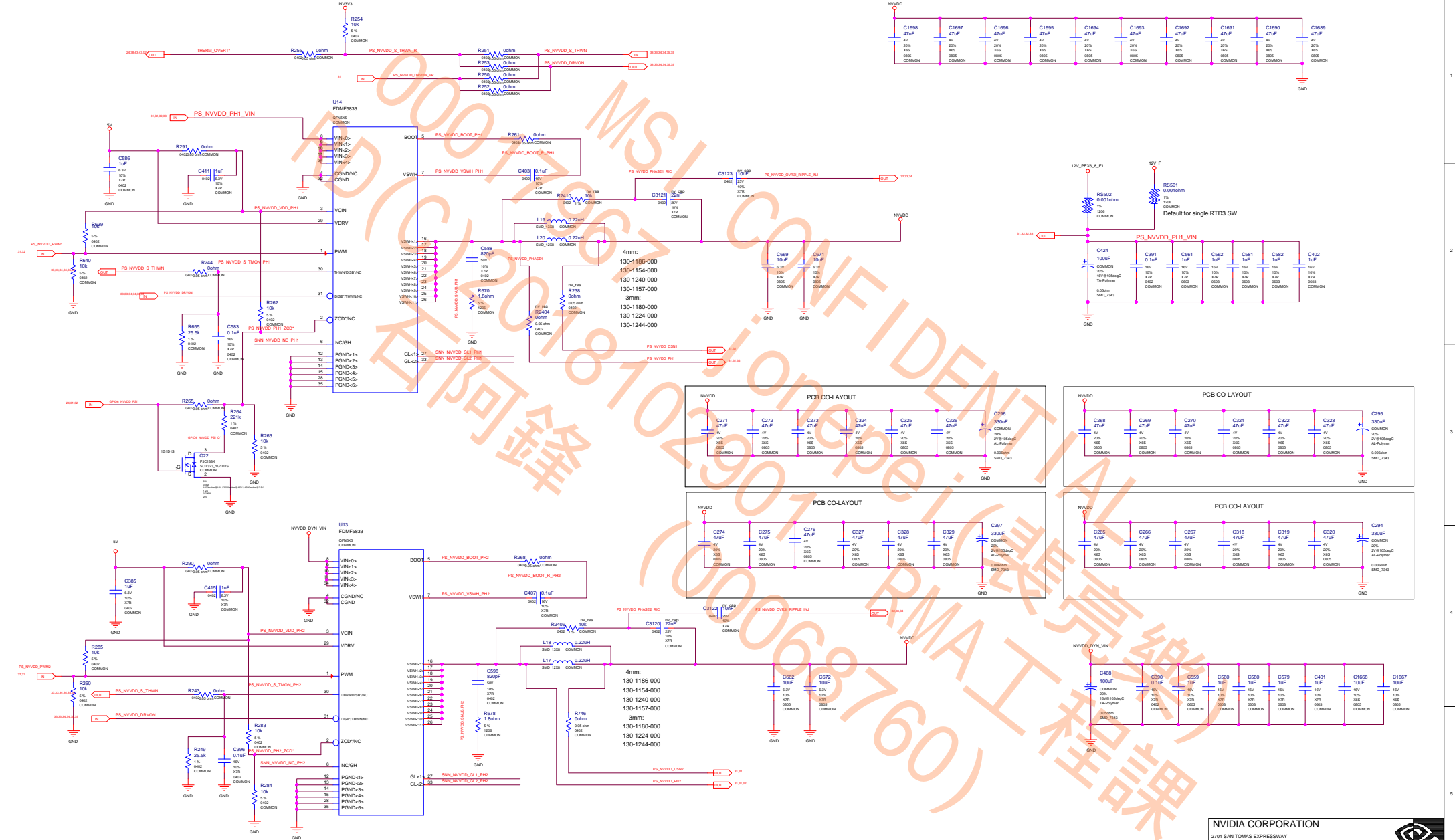


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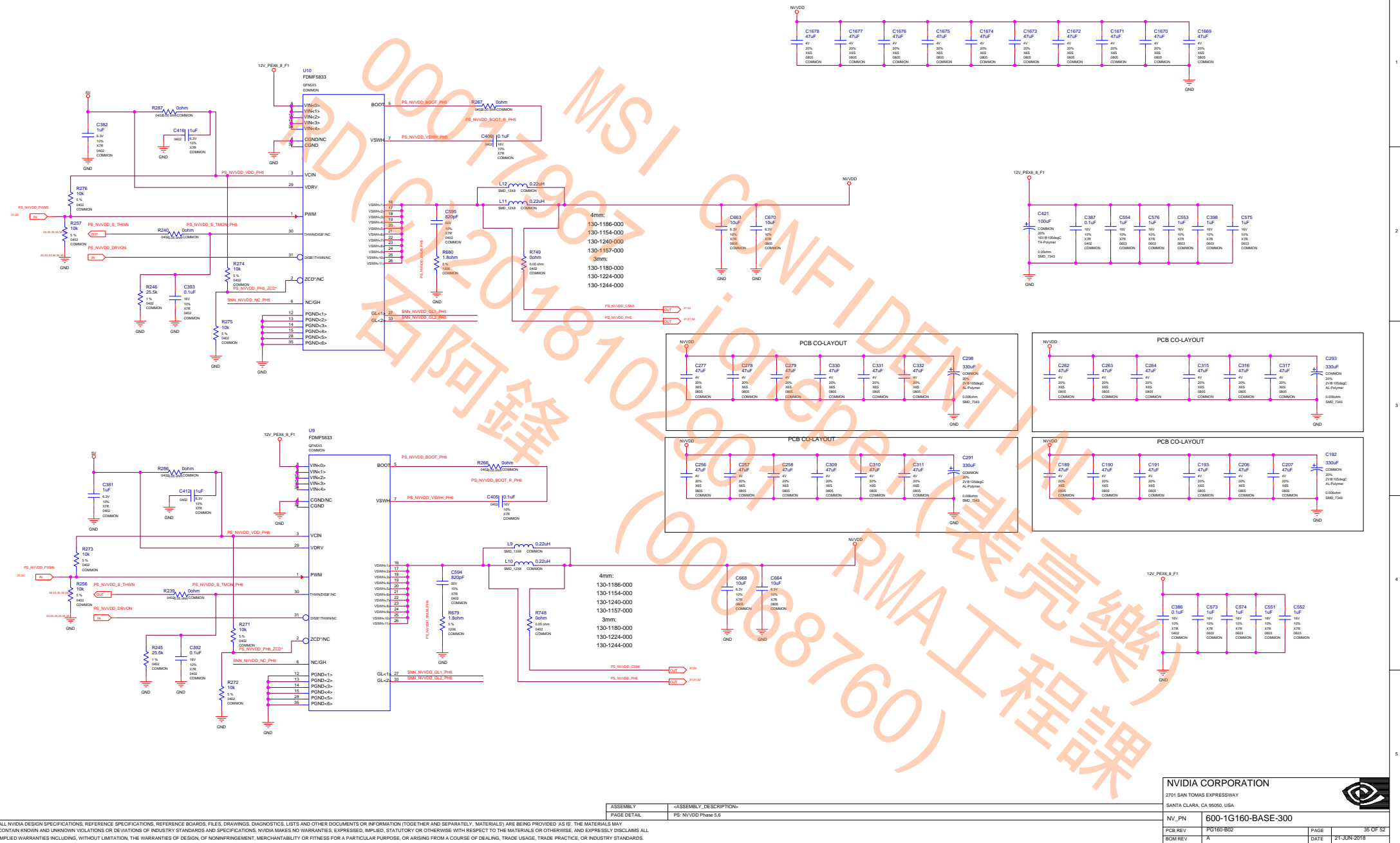


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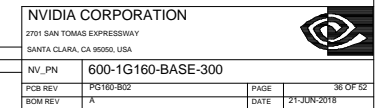
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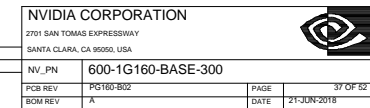
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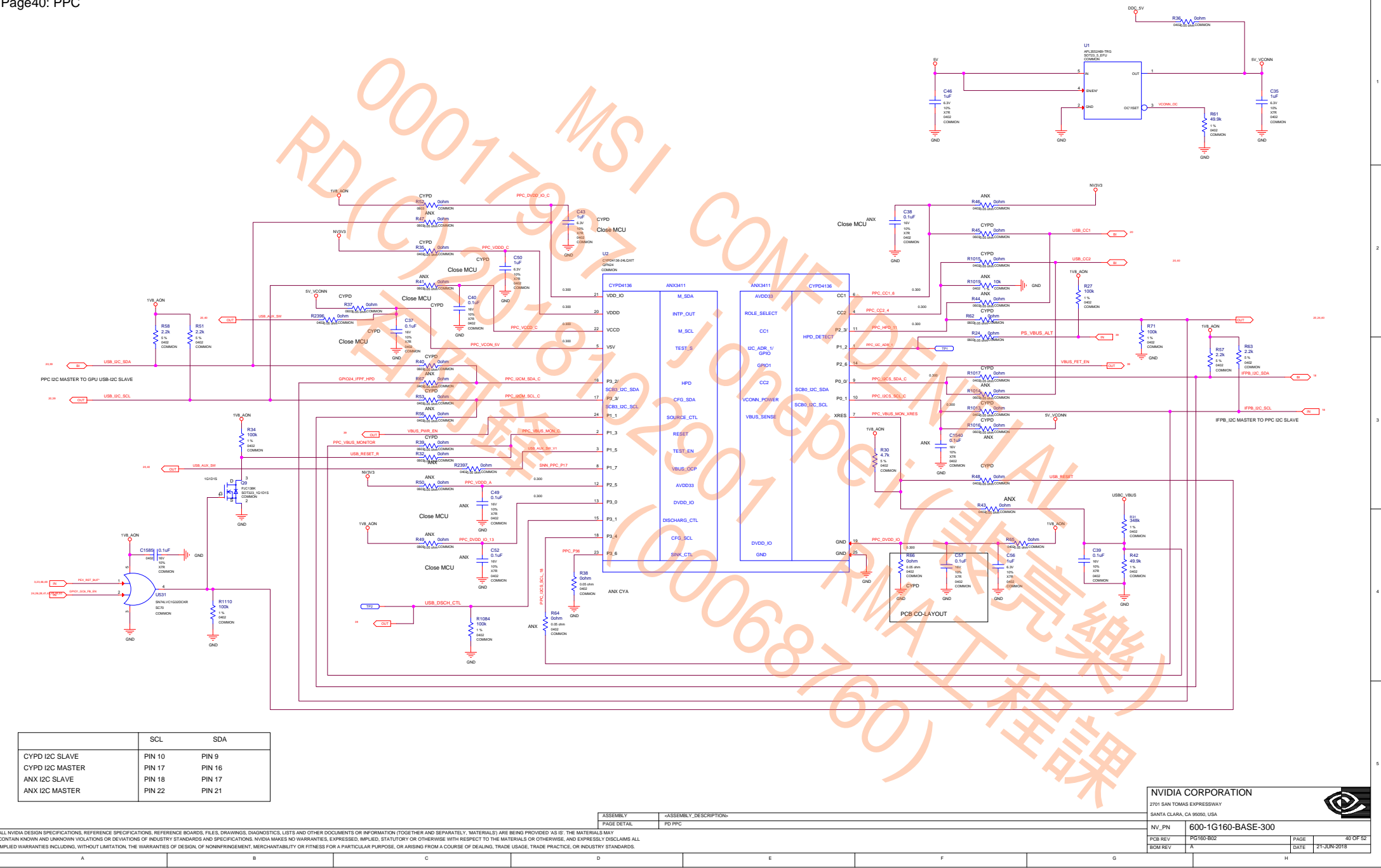




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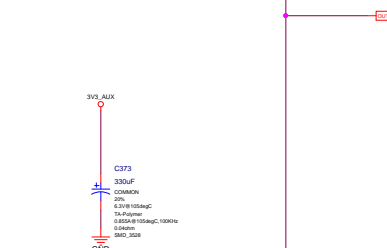
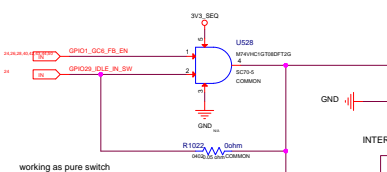




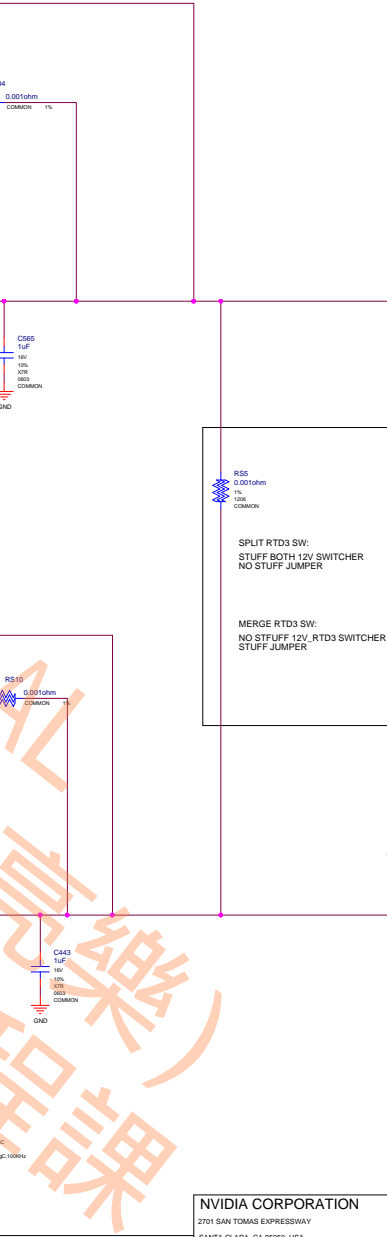
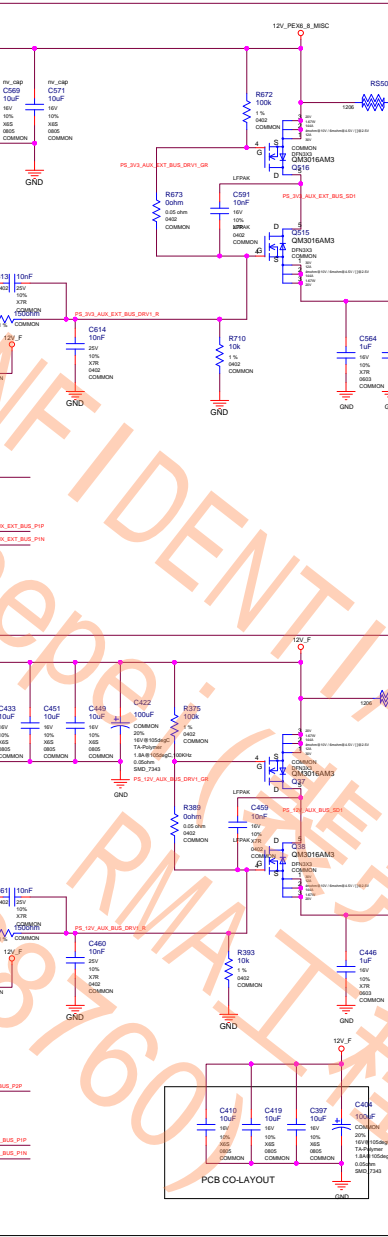
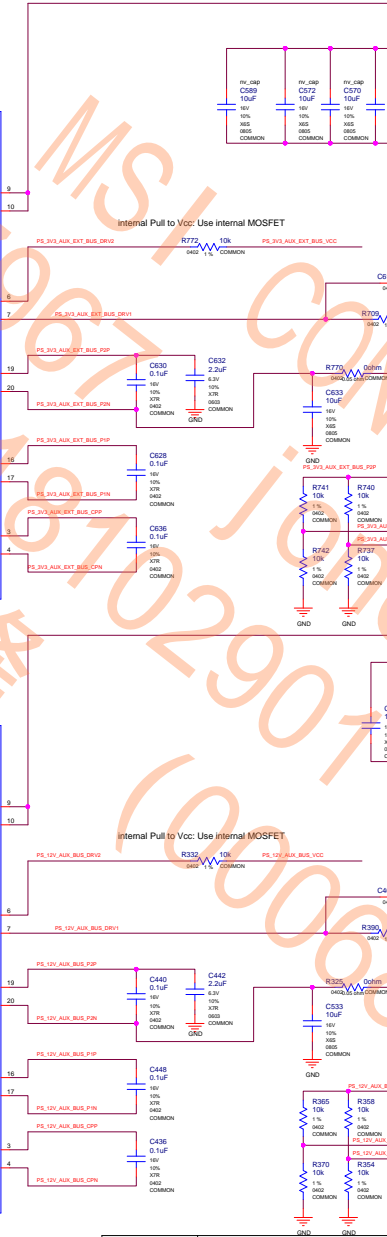
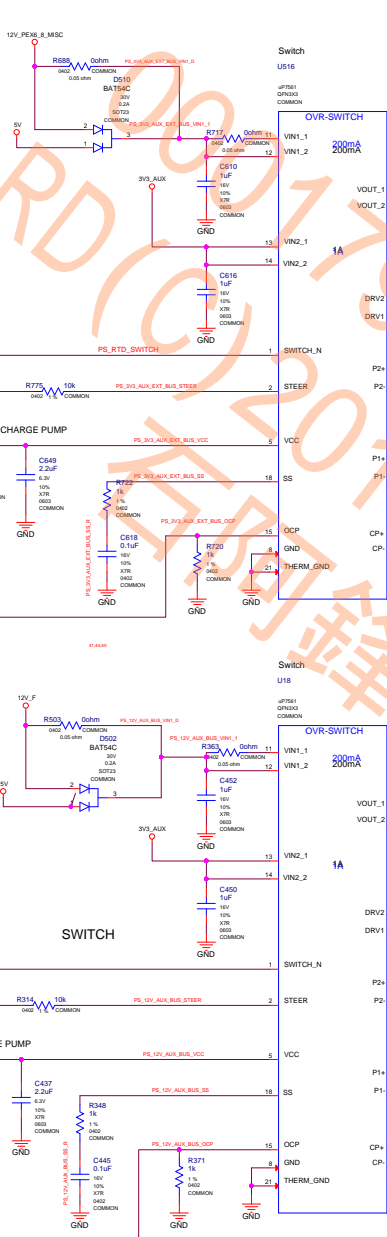
SWITCH		VOUT	
0		VOUT = VIN1	
1		VOUT = VIN2	

AND GATE LOGIC			
GPIO1	GPIO29	SWITCH	VOUT
0	0	0	12V
0	1	0	12V
1	0	0	12V
1	1	1	3V3A



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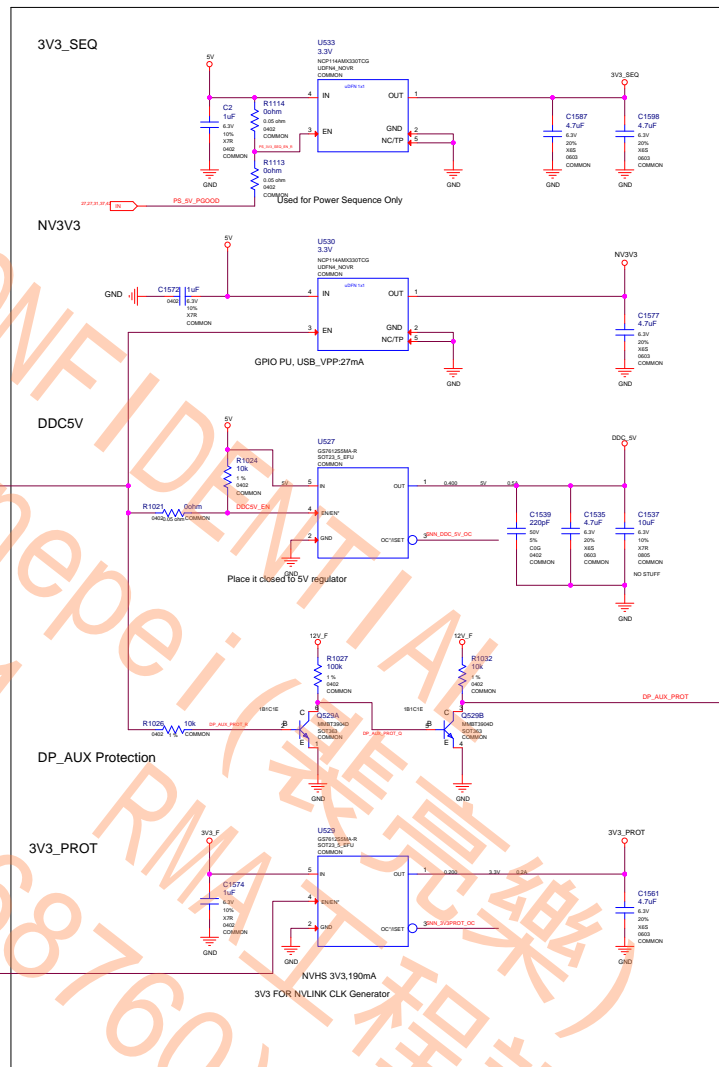


SPLIT RTD3 SW:  
NO STUFF BOTH 12V RTD3 SWITCHER  
NO STUFF JUMPER

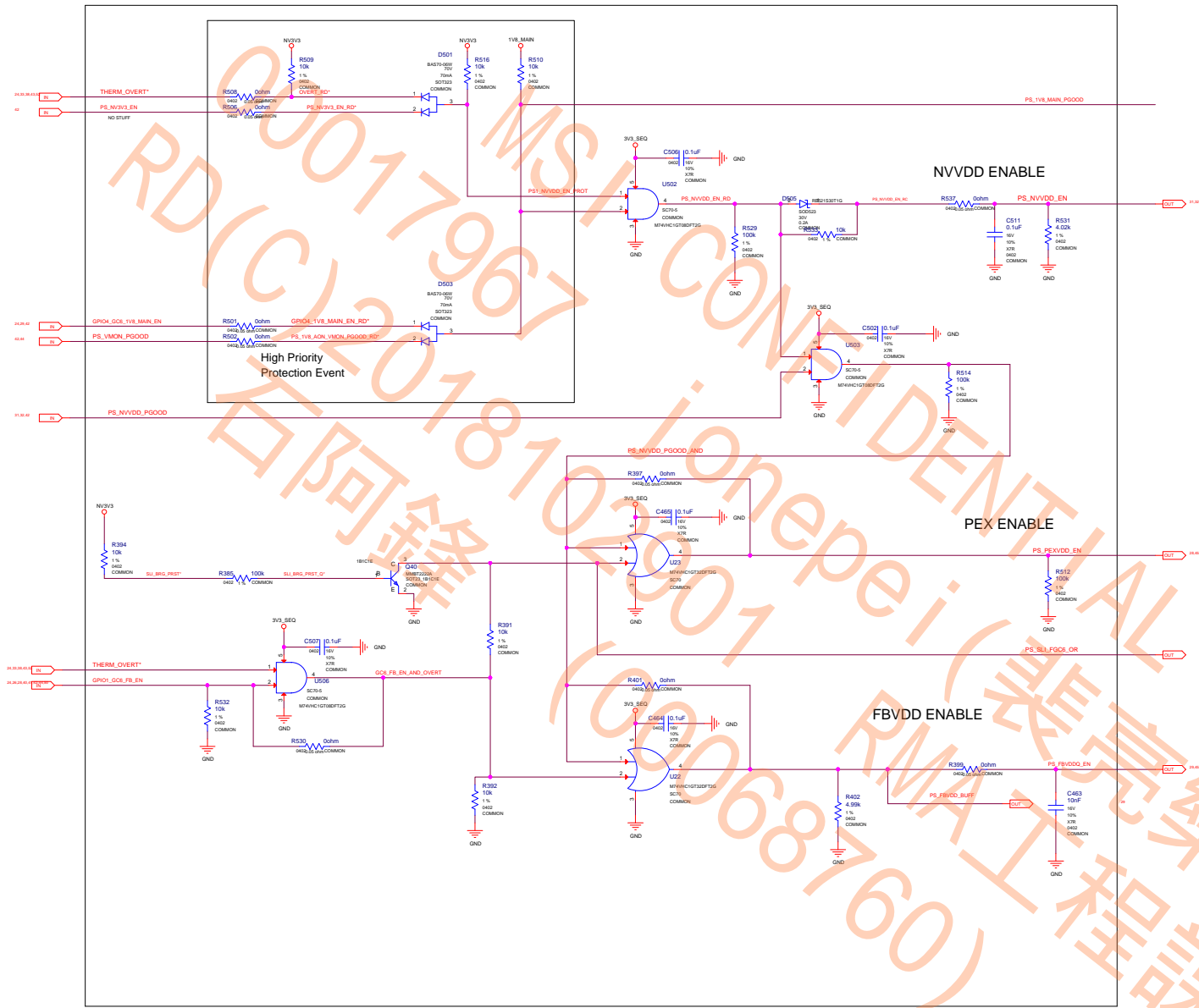
MERGE RTD3 SW:  
NO STUFF BOTH 12V RTD3 SWITCHER  
STUFF JUMPER

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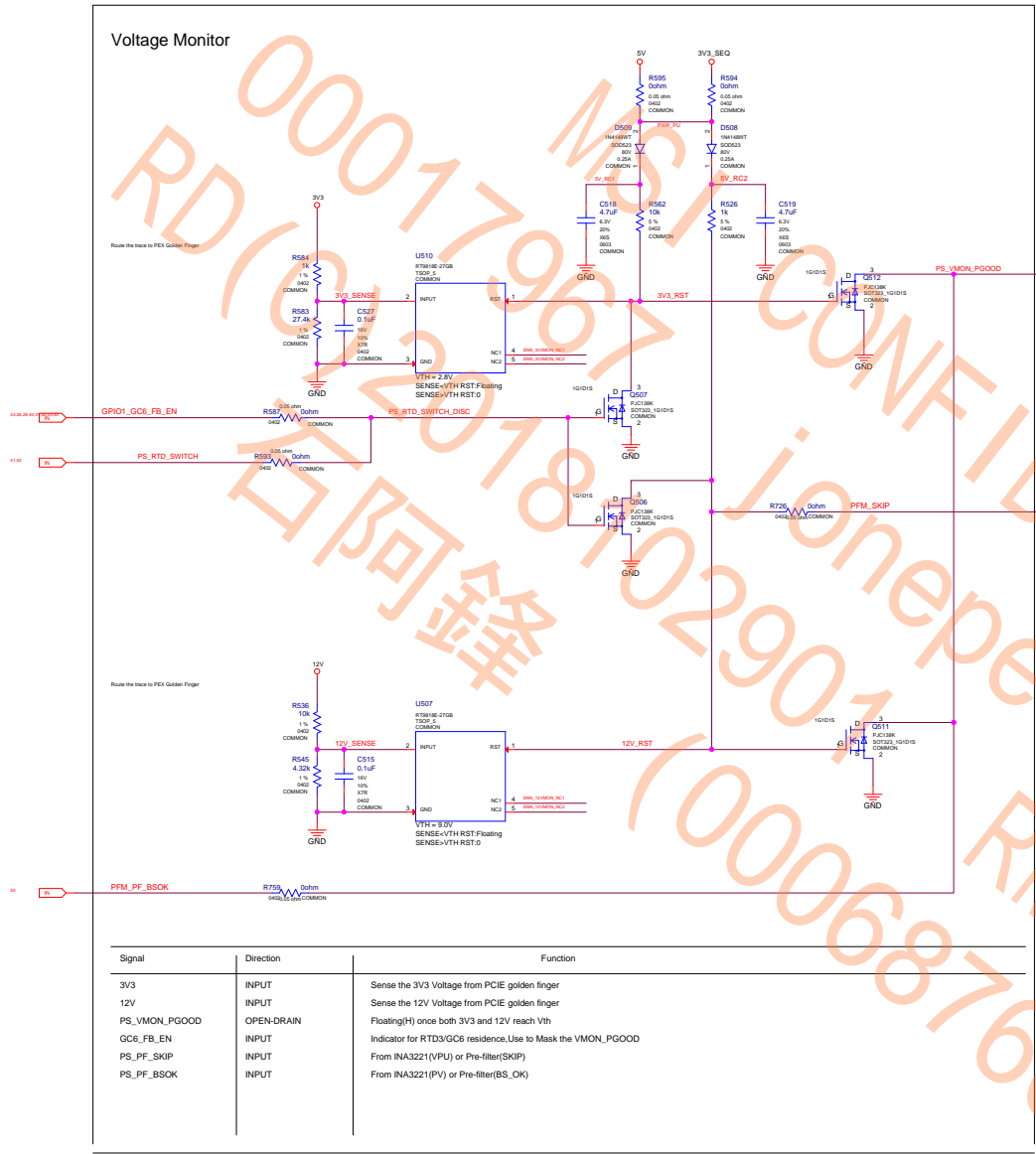
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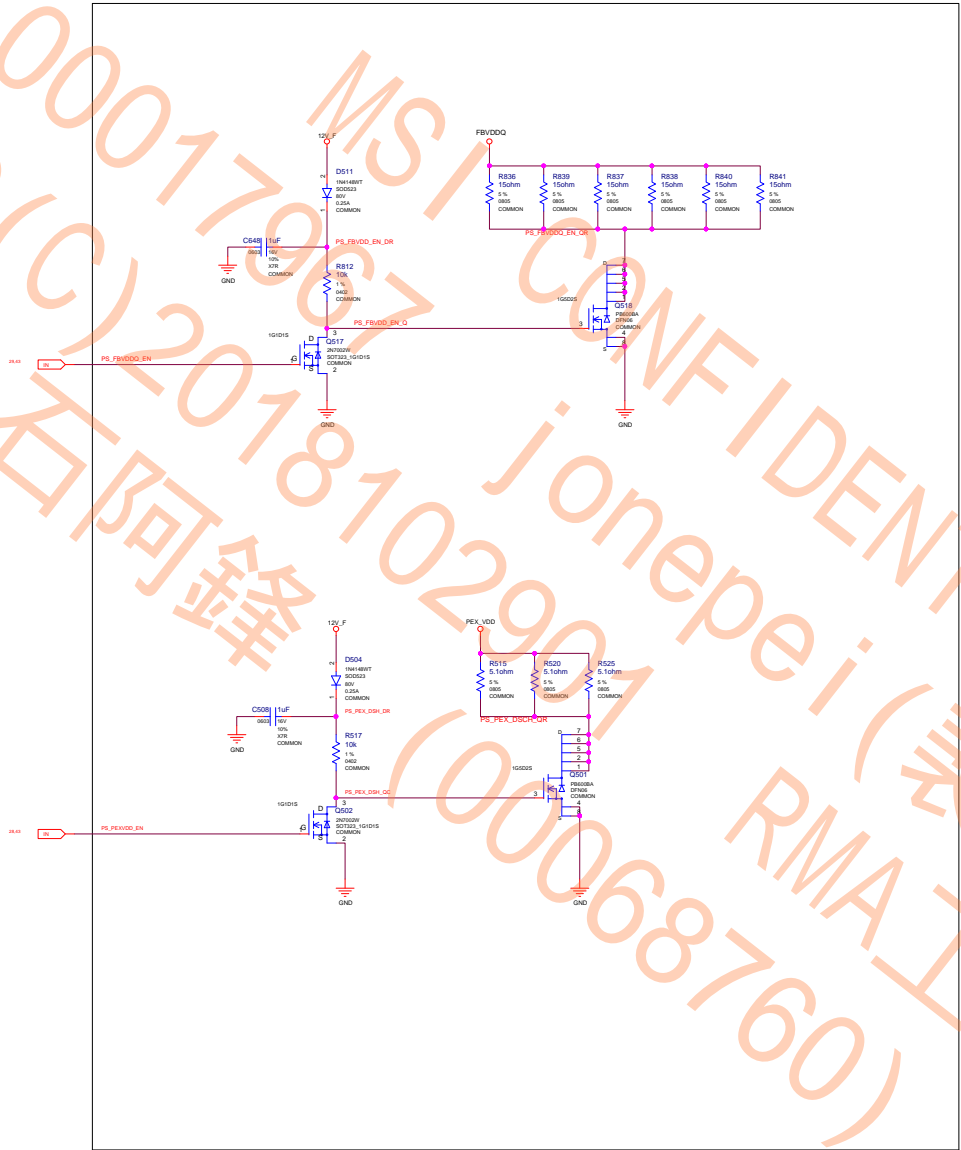


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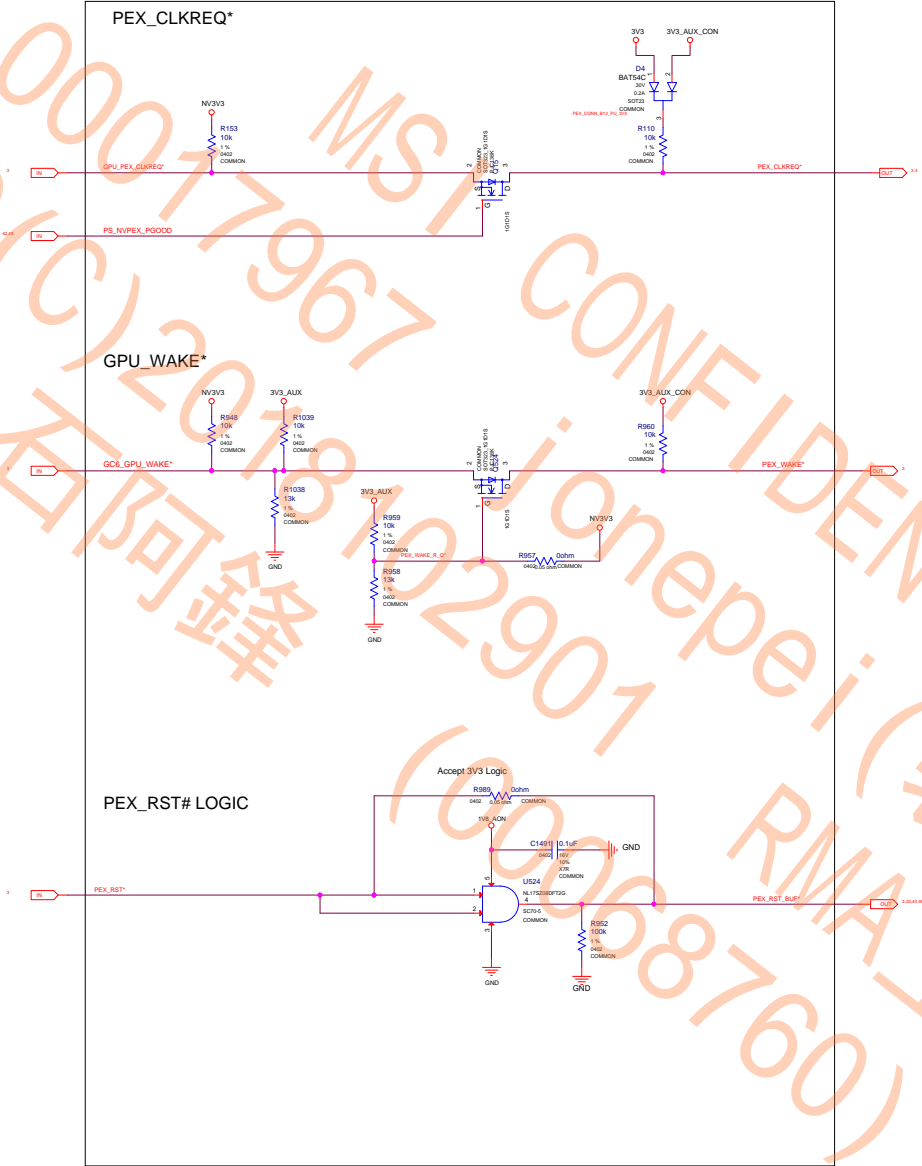
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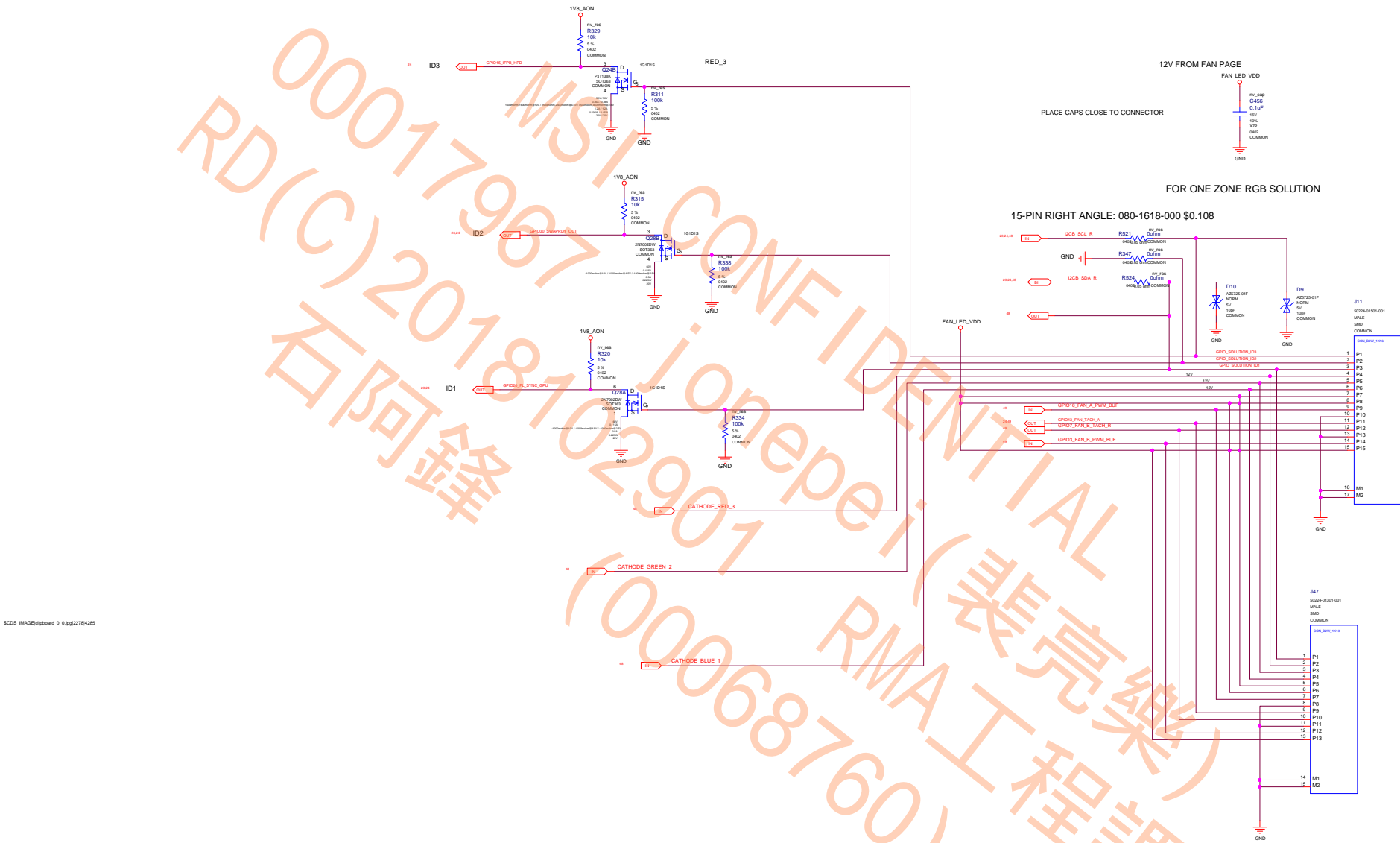
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SCDS\_IMAGE\upboard\_0.0.ppt2784285

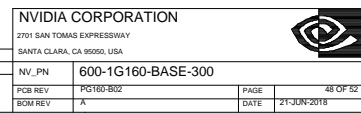
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ASSEMBLY	»ASSEMBLY DESCRIPTION«
PAGE DETAIL	LOGO LED

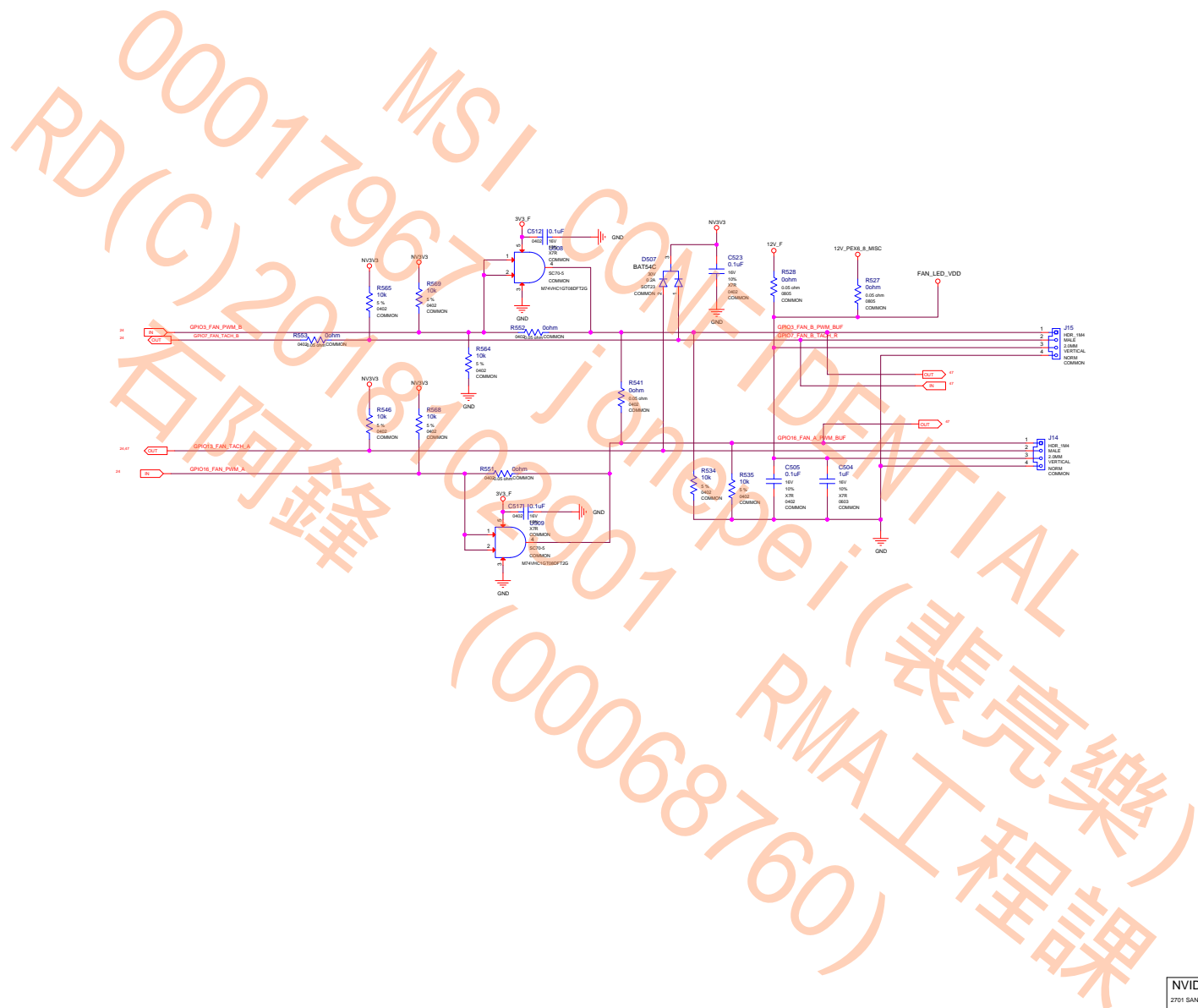
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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	LED DRIVER & BOOST



ASSEMBLY	«ASSEMBLY_DESCRIPTION»
PAGE DETAIL	DUAL FAN

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PCB REV	1
BOM REV	1

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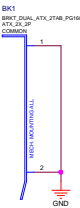
ENABLE PIN STATUS	MULTIPLEXER OPERATION
LOW	FULLY FUNCTIONAL
TRI-STATE(FLOATING)	LIMITED FUNCTION
HIGH	STANDBY

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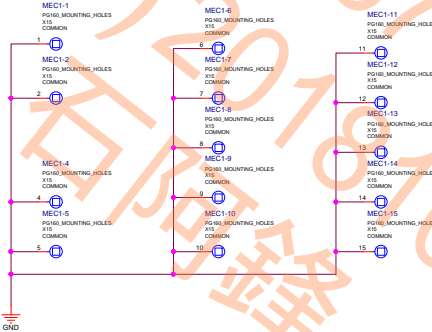
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Brackets:



MOUNTING HOLES FOR HS:



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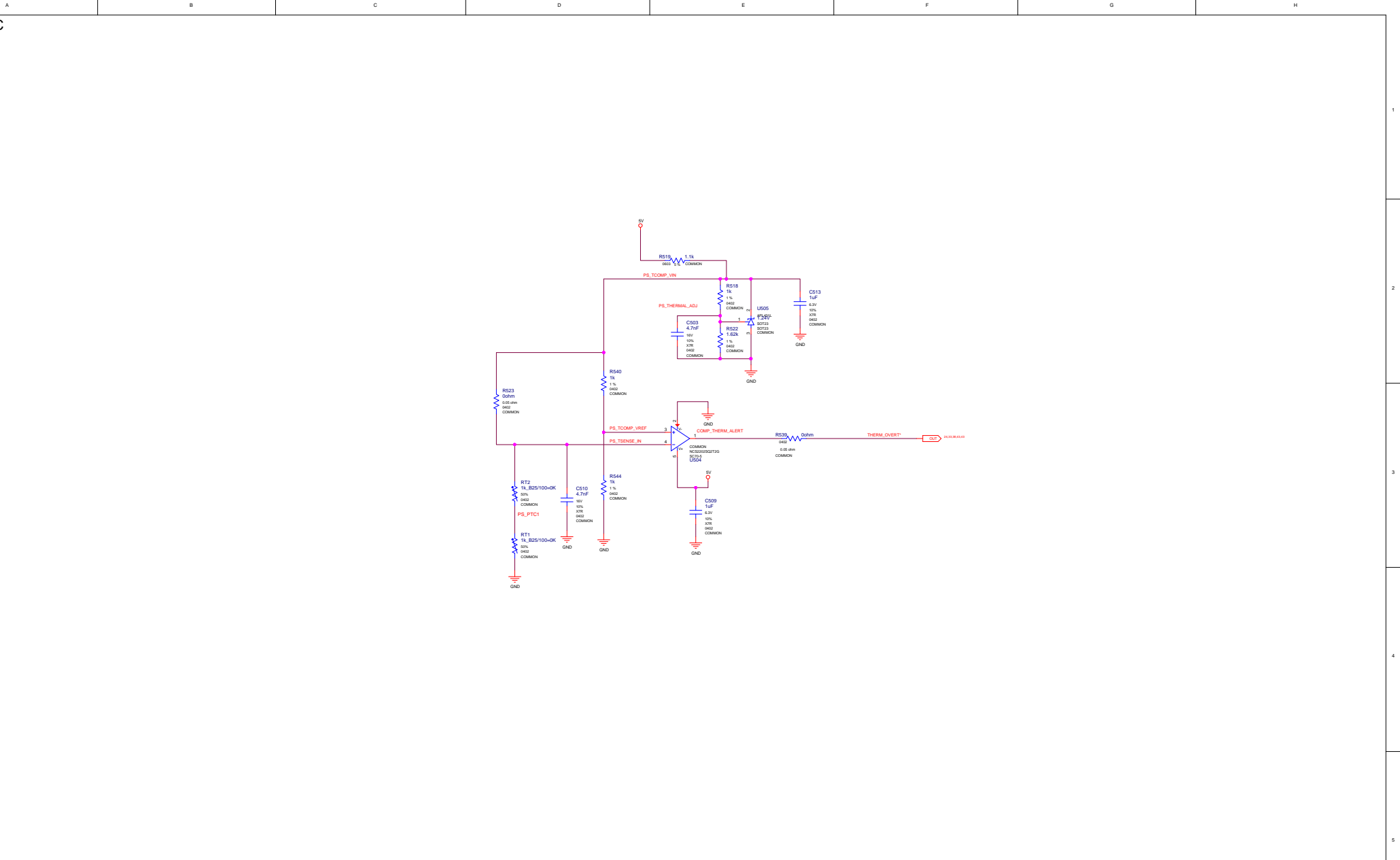
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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	MECH



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<p>                 NV_PN <b>600-1G160-BASE-300</b> </p>									
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A	B	C	D	E	F	G	H	I	J

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PCB REV		PG160-B02			PAGE		52 OF 52		
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