

| Patient Information | |
|----------------------------|--|
| First Name | |
| Last Name | |
| Address | |
| City | |
| State | |
| Zip | |
| Phone | |
| Age | |
| Gender | |
| Occupation | |
| Referral Source | |
| History of Present Illness | |
| Onset of symptoms | |
| Duration of symptoms | |
| Frequency of symptoms | |
| Severity of symptoms | |
| Associated symptoms | |
| Previous treatments | |
| Response to treatment | |
| Family History | |
| Social History | |
| Physical Examination | |
| Vital Signs | |
| General Appearance | |
| Head and Neck | |
| Chest | |
| Abdomen | |
| Extremities | |
| Neurological | |
| Psychiatric | |
| Laboratory Tests | |
| Imaging Studies | |
| Diagnosis | |
| Treatment Plan | |
| Follow-up | |

| Page | Description |
|------|-------------|
| | |

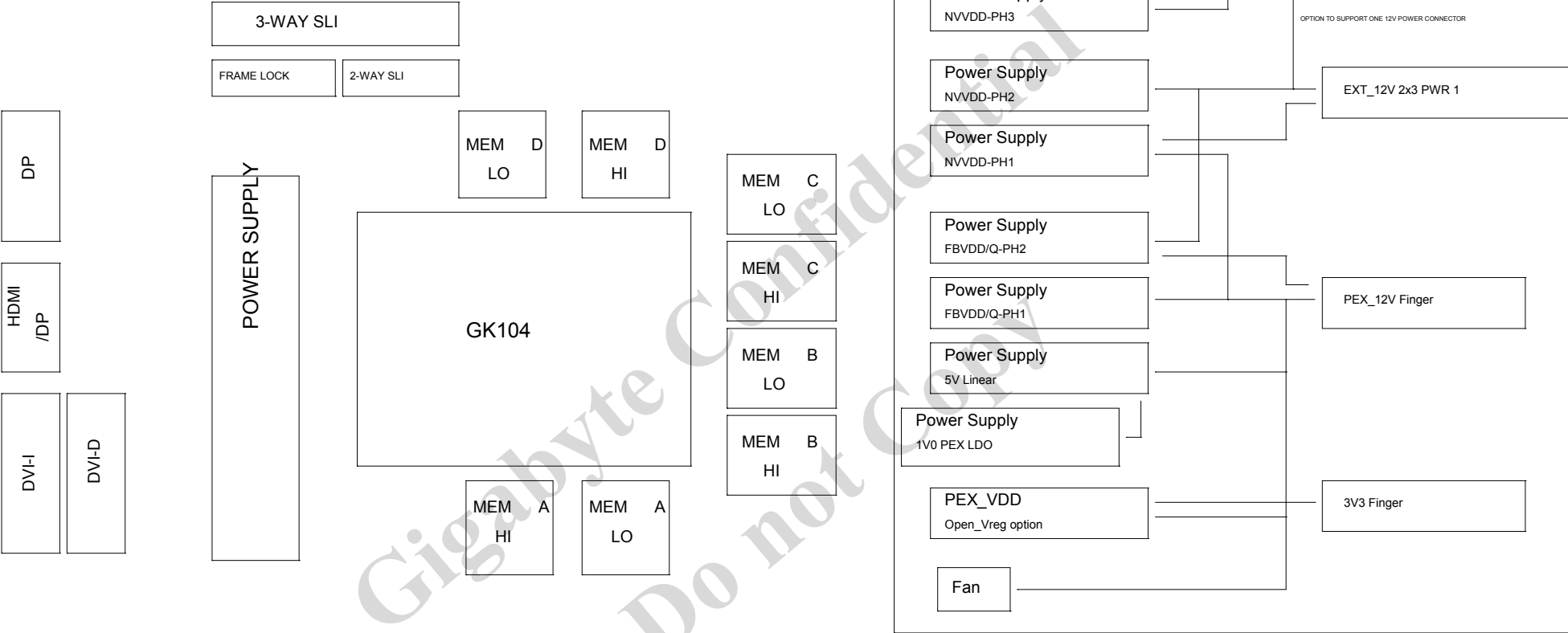
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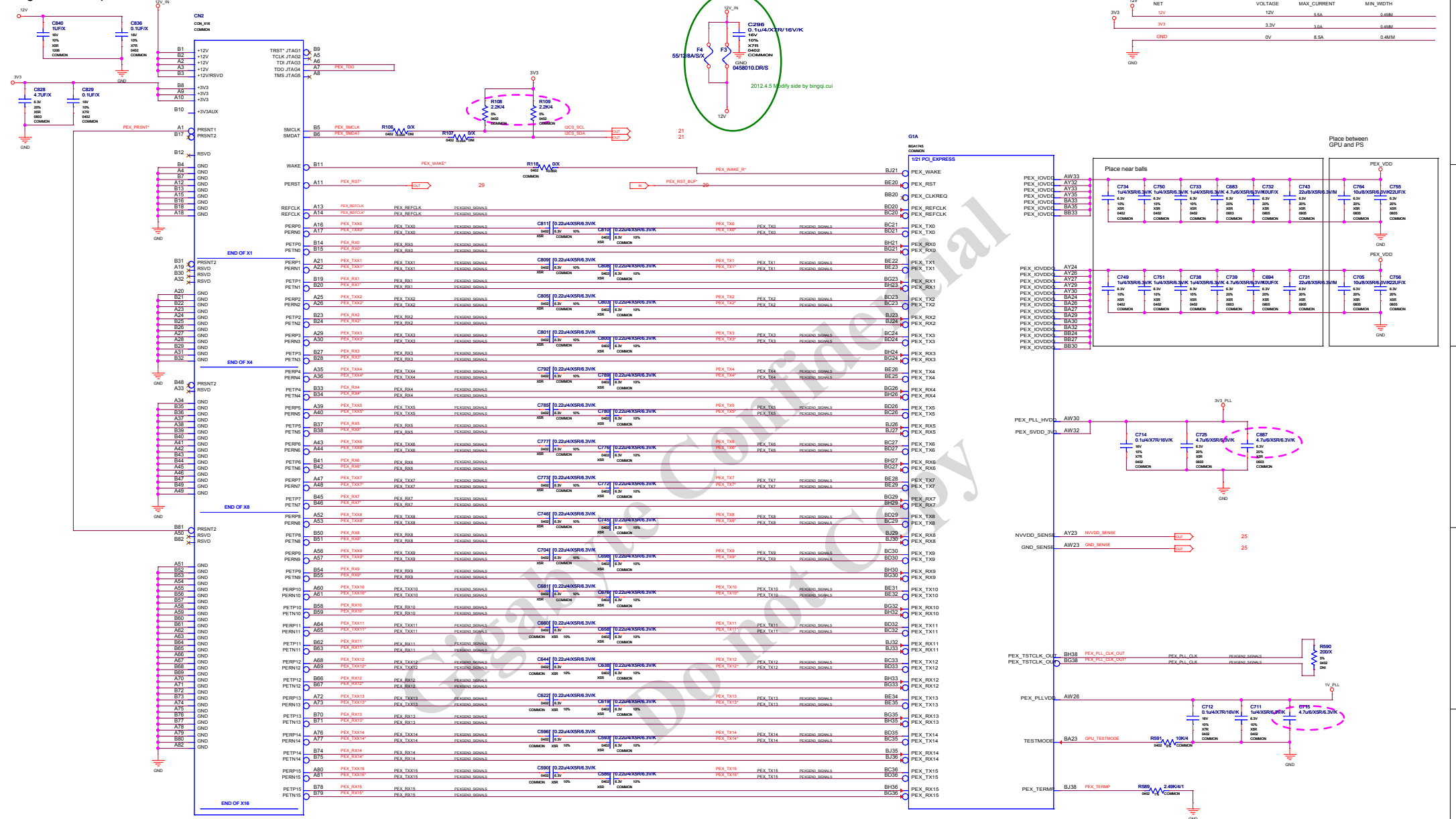


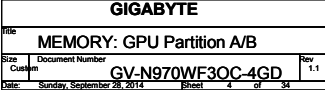
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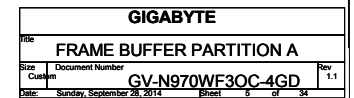
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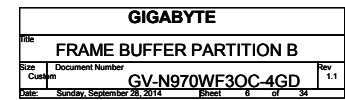
BASE LEVEL GENERIC SCHEMATIC ONLY
Block Diagram

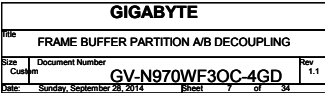
| GIGABYTE | | | |
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| Block Diagram | | | |
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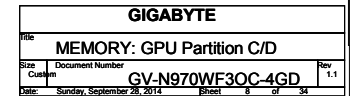


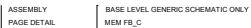


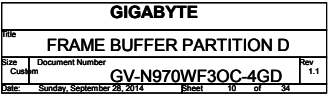


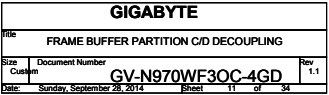


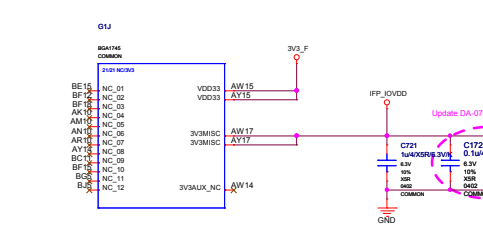




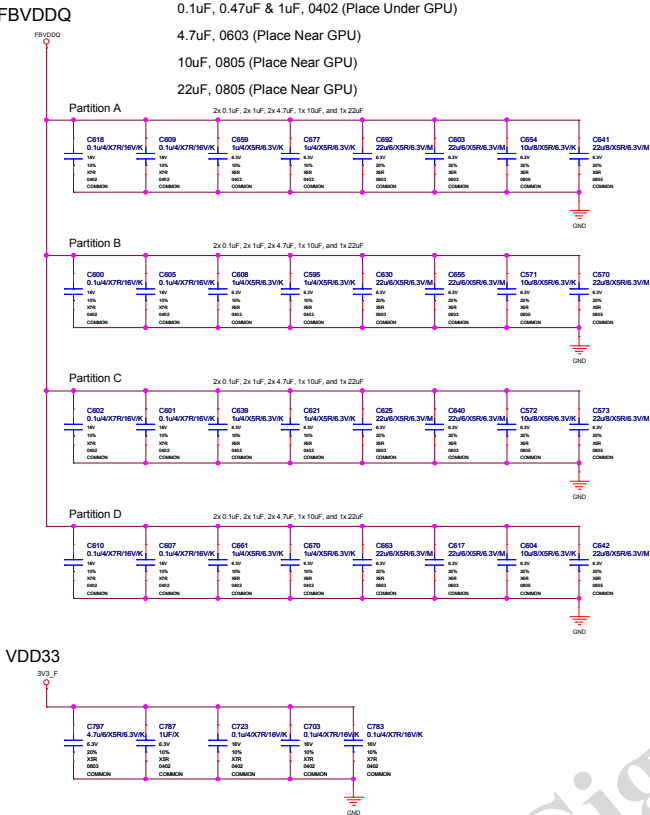




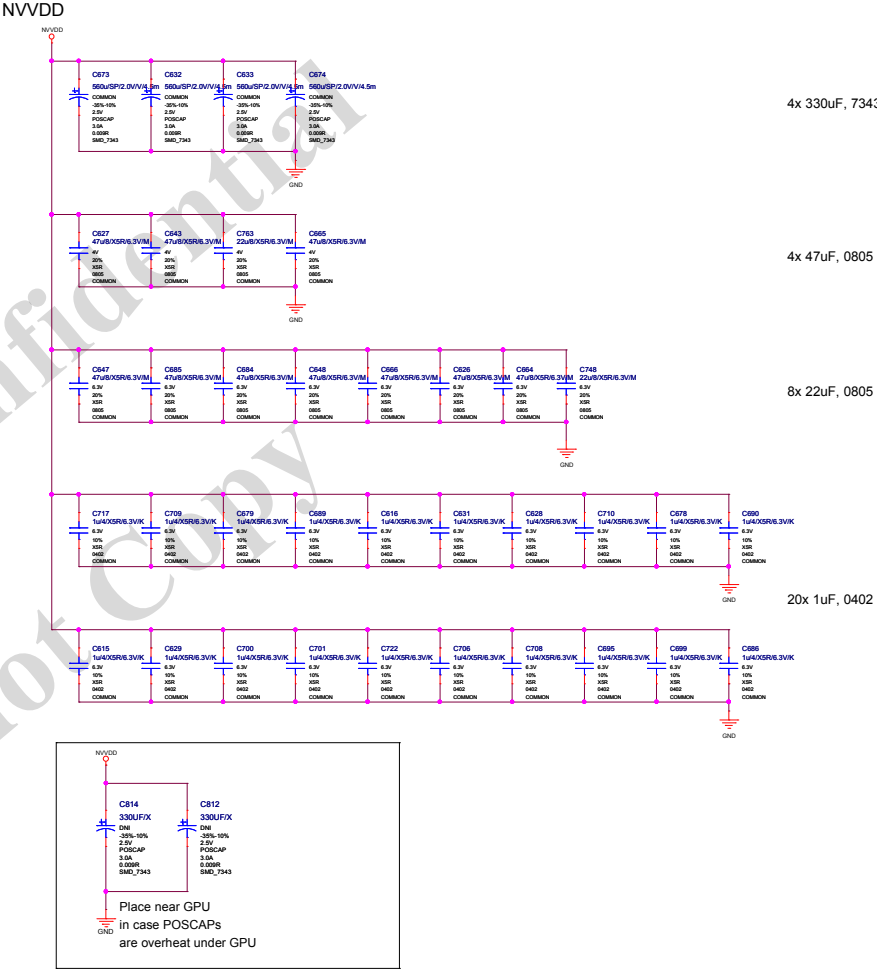




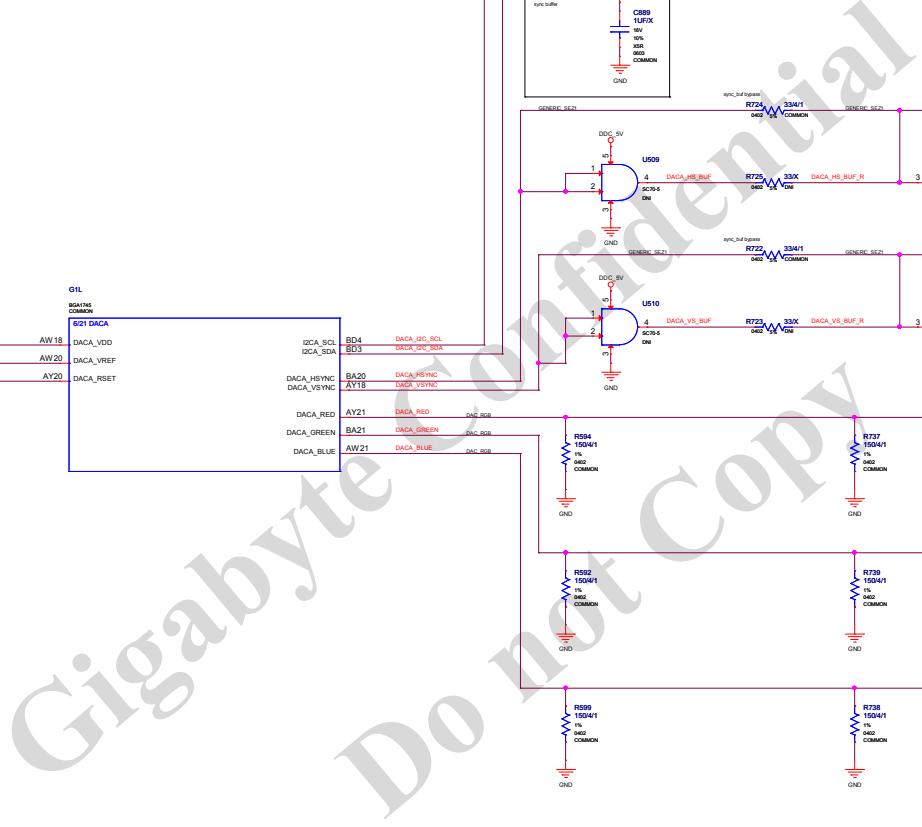
Based on GB2-X GDDR5 FBVDDQ Decap Guideline



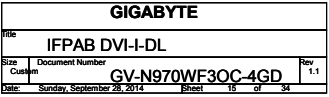
NVVDD Decoupling caps. Place under GPU.

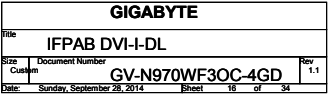


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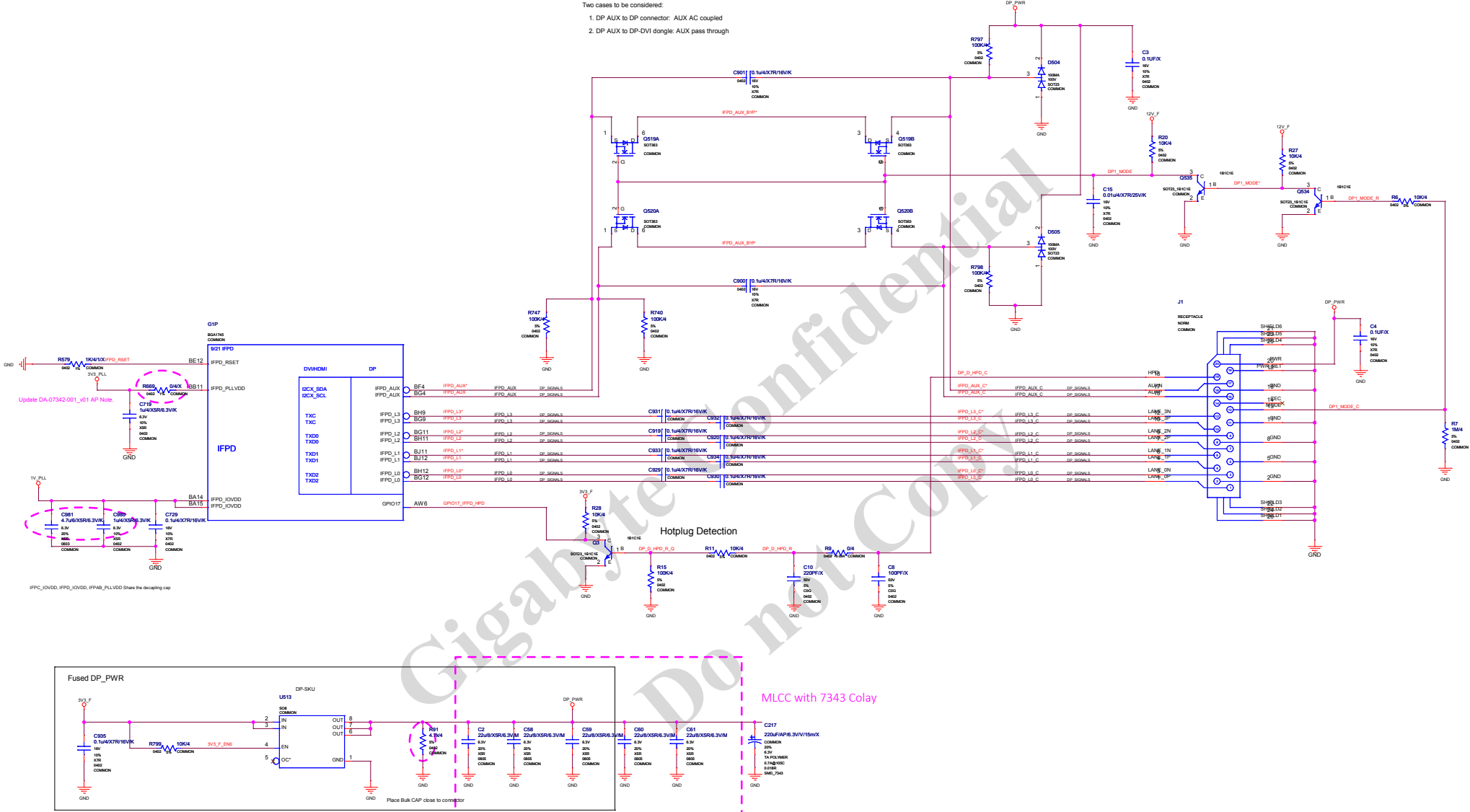
2012.04.05 Modify Size By Bingqi.cui







- Two cases to be considered:
- 1. DP AUX to DP connector: AUX AC coupled
 - 2. DP AUX to DP-DVI dongle: AUX pass through

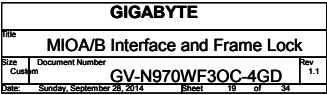


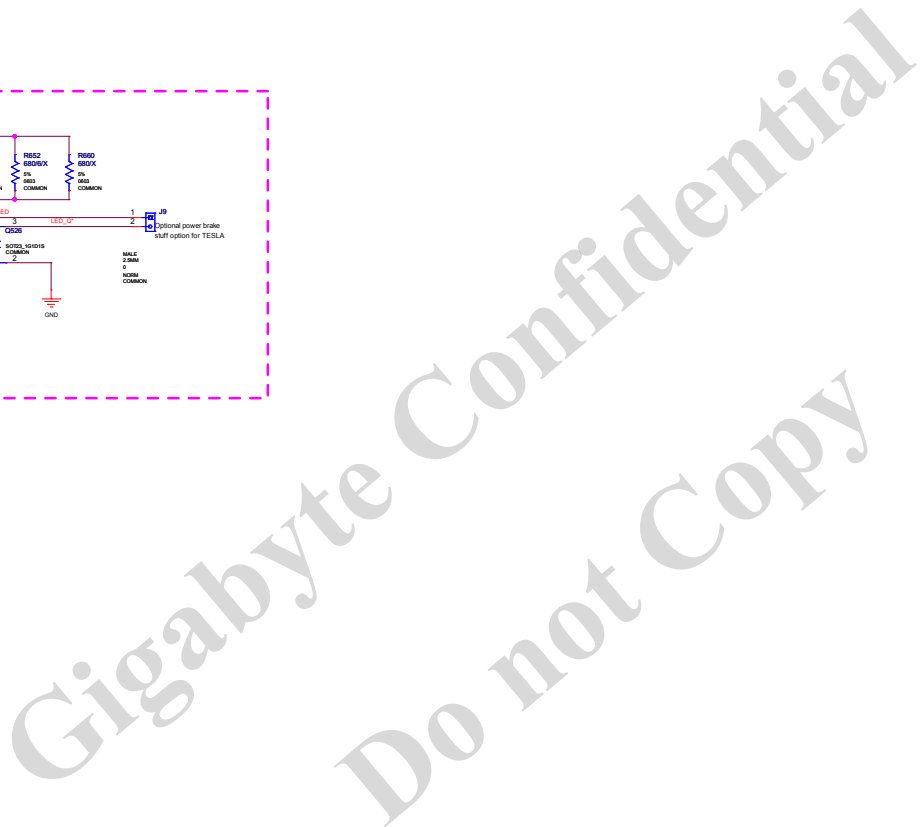
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IFPD DP

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| IFPD DP | | | |
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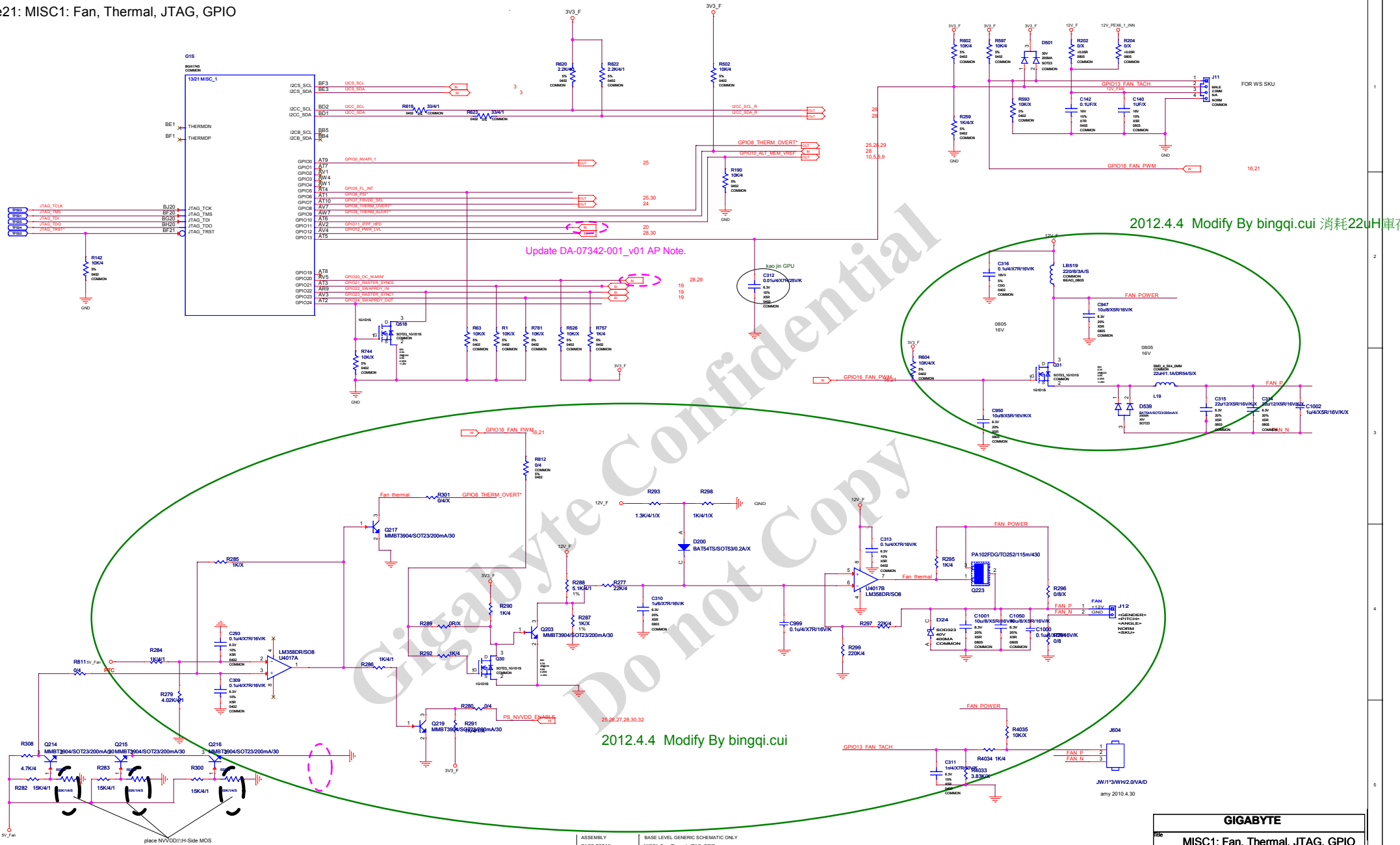


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| ASSEMBLY | BASE LEVEL GENERIC SCHEMATIC ONLY |
| PAGE DETAIL | Stereo Connector |

| | | | |
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| Title | | | |
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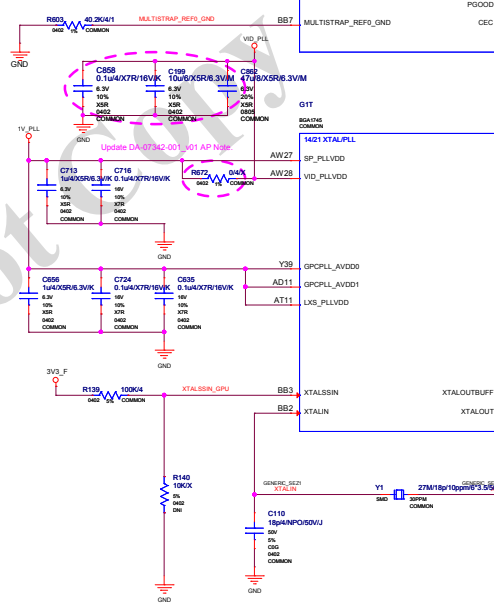
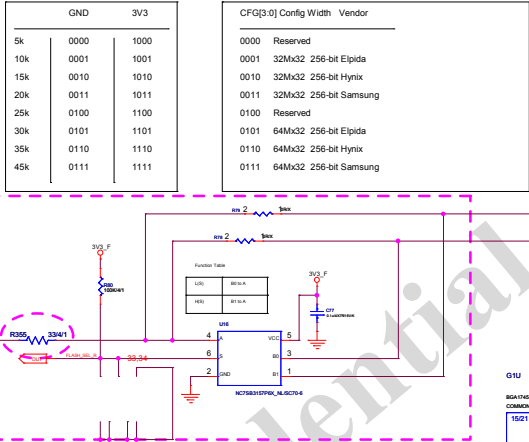
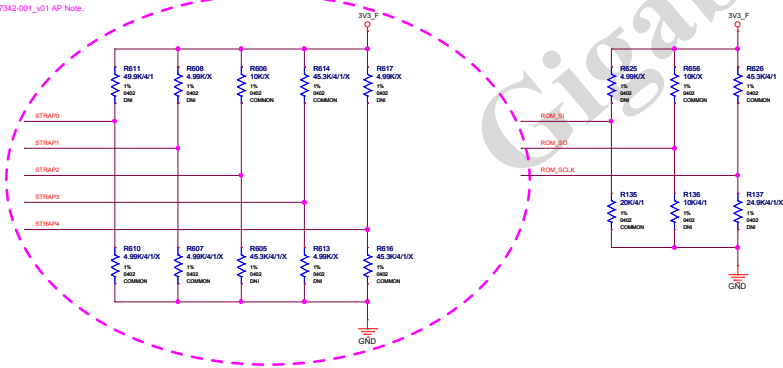


| GIGABYTE | | | |
|---------------------------------|----------------------------|-------|----------|
| MISC1: Fan, Thermal, JTAG, GPIO | | | |
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| | | | |
|----------|----------------------|--------------------|--|
| STRAP0 | USER_BIT [3:0]* | 0000* | 5K PD* |
| STRAP1 | 3GIO_PADCFG_LUT_ADR* | 0000* | 5K PD Desktop* |
| STRAP2 | PCI_DEVID [3:0]* | 0111 - (0x1187)* | 10K PU -325 GPU* |
| STRAP3 | SOR_EXPOSED [3:0]* | 1111* | 45K PU* |
| STRAP4 | DP_PLL_VDD_33V* | 1* FOR 3_3V* | |
| | PEX_MAX_SPEED* | 1* FOR GEN2/3* | 45K PD* |
| | PEX_SPD_CHANGE_GEN3* | 1* ENABLED* | |
| | * | | |
| ROM_SI | RAMCFG[0]* | 0* | |
| | RAMCFG[1]* | 1* | 64MB/256 bit byte for SKUDS primary memory |
| | RAMCFG[2]* | 1* | 35K PD* |
| | RAMCFG[3]* | 0* | |
| ROM_SO | VGA_DEVICE* | 1* | |
| | SMB_ALT_ADDR* | 0* | 30k PD* |
| | FB[0]_APERTURE_SIZE* | 1* For 128MB* | |
| | FB[1]_APERTURE_SIZE* | 0* For 128MB* | |
| ROM_SCLK | PEX_PLL_EN_TERM100* | 0* DISABLE* | |
| | PCI_DEVID_EXT[5]* | 0* For 0x1189* | 25K PD* |
| | SUB_VENDOR* | 1* Dedicated BIOS* | |
| | PCI_DEVID_EXT[4]* | 0* For 0x1189* | |

| | |
|-------------------|----------------------|
| | MULTI_STRAP_REF0_GND |
| BINARY PRODUCTION | NO |
| BINARY BINGUP | NO |
| MULTI-LEVEL | 49.24 1% TO GND |

Update DA-07342-001_v01 AP Note.

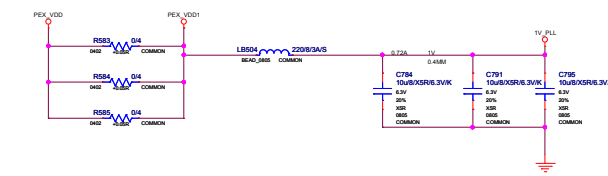
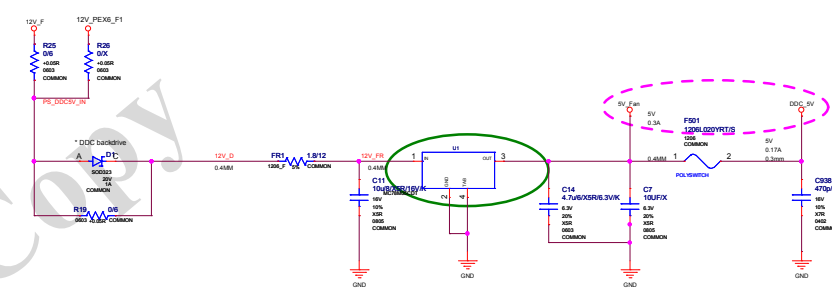
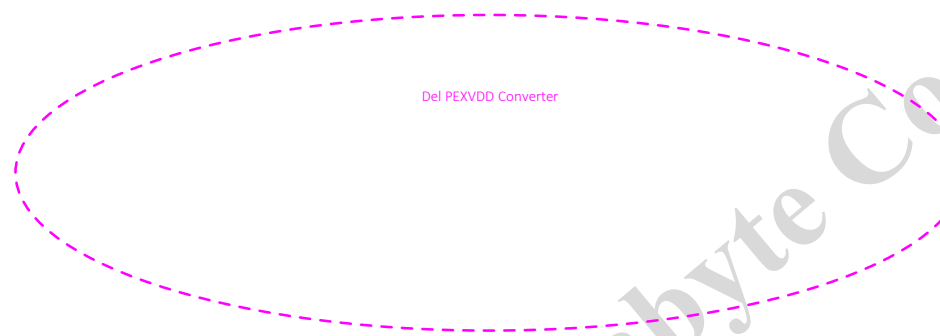
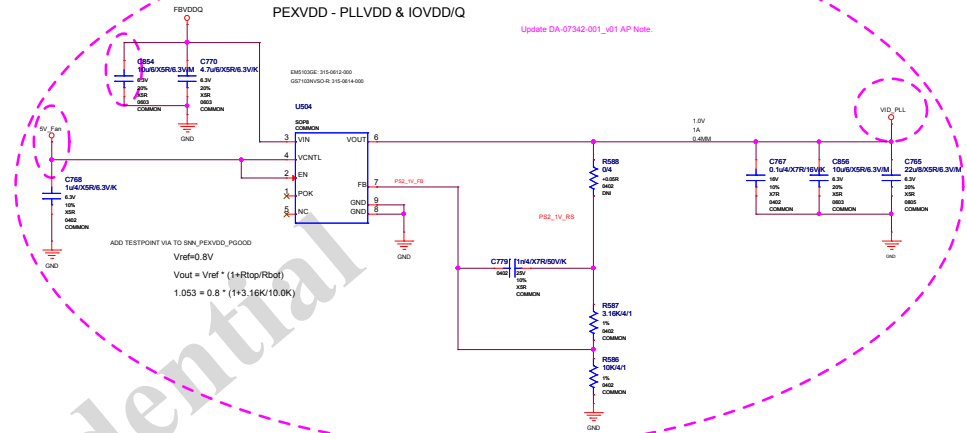
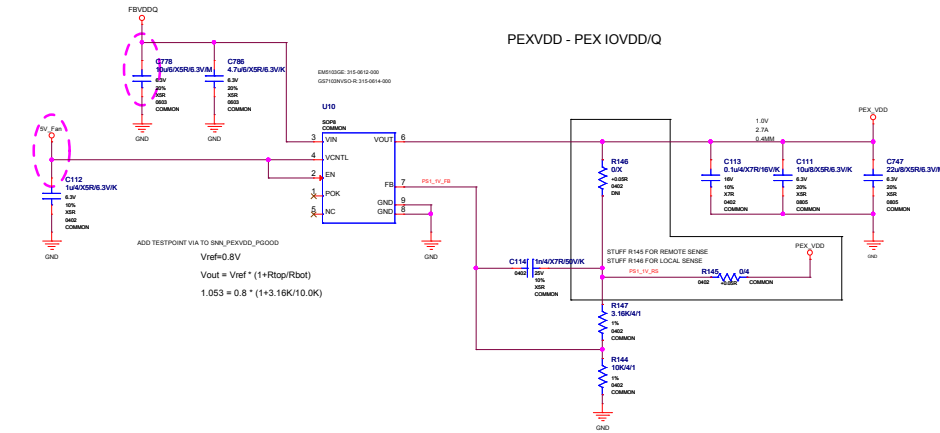


| XTALSSIN | XTALOUTBUFF | Inserted SmartFan PWRM % |
|----------|-------------|--------------------------|
| PU | PU | 66(33% High) |
| PU | PD | 50(50% High) |
| PD | PU | 33(66% High) |
| PD | PD | 0(100% High) |

Update DA-07342-001_v01 AP Note.

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|------------------|----------------------------|-------|----------|
| File | MISC2: ROM, XTAL, Straps | | |
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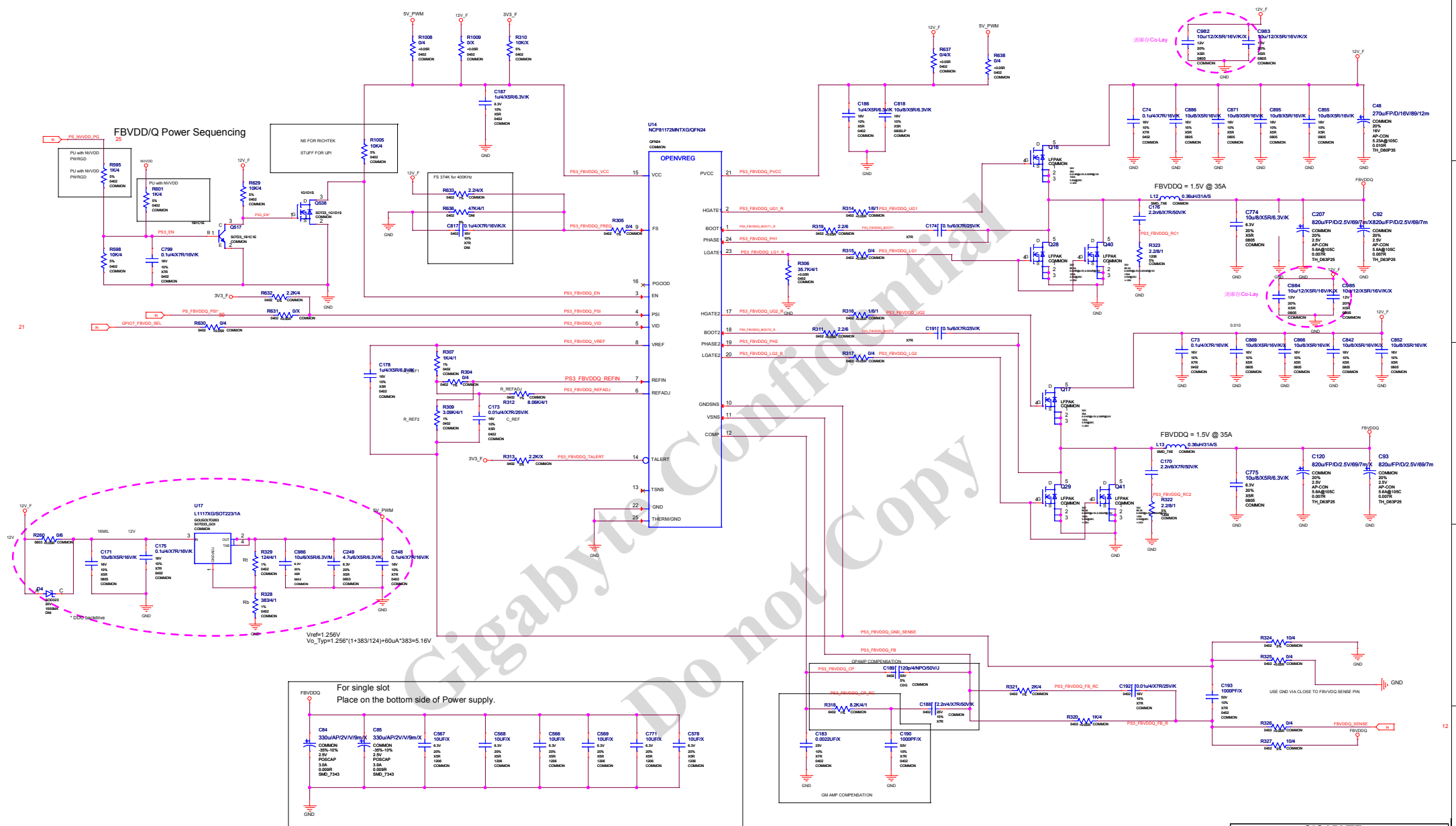


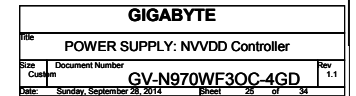
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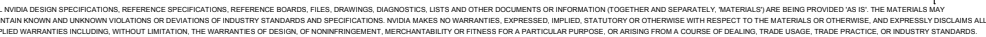
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BASE LEVEL GENERIC SCHEMATIC ONLY
PS: 5V, PEX_VDD

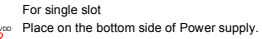
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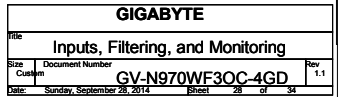
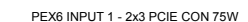
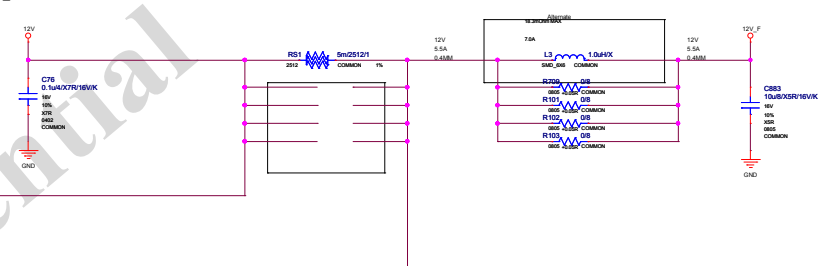
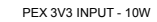




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IFP_IOVDD (backdrive prevention)

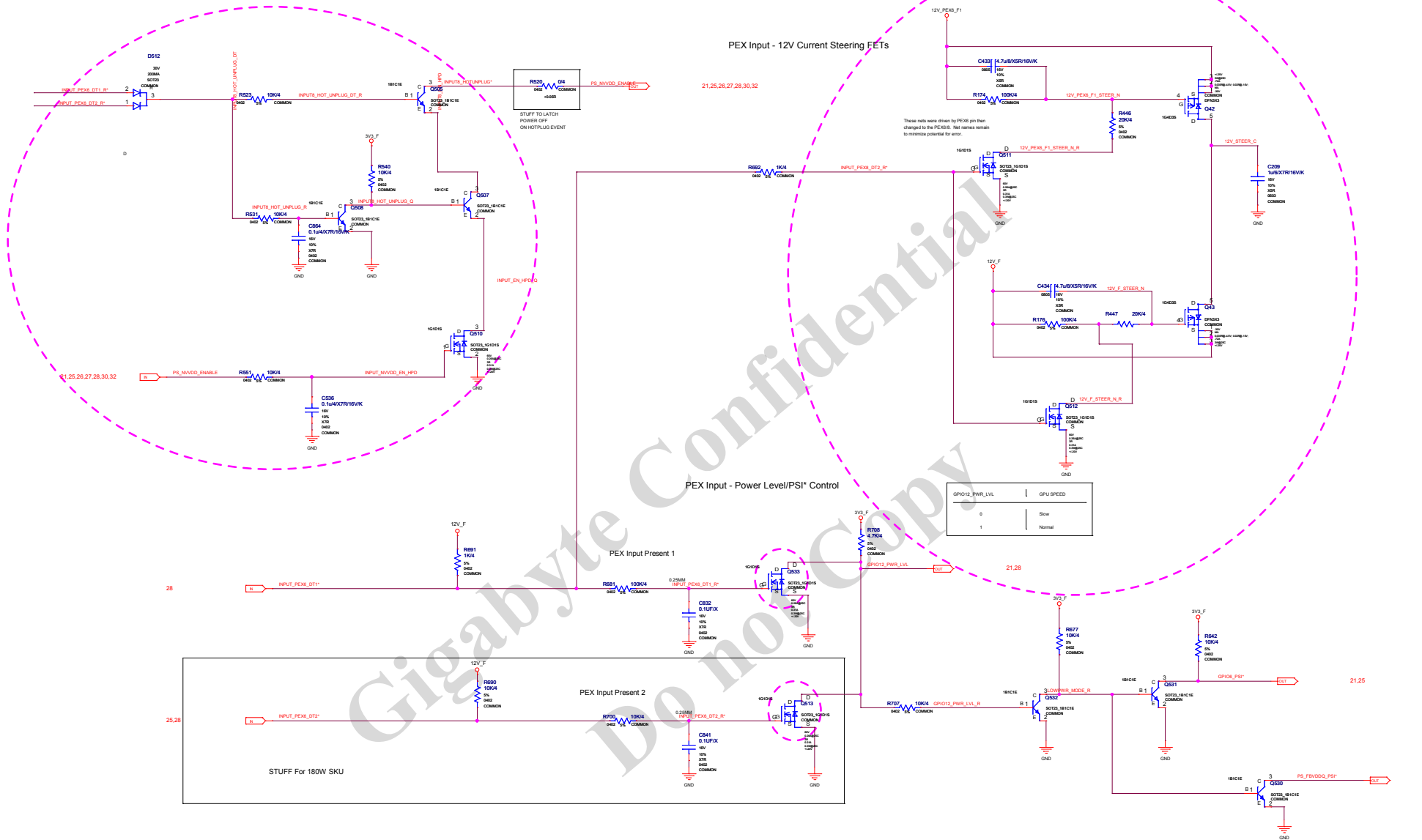
Prevention)



| | | | |
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| Title Thermal Shutdown& Light SLI DRIVER | | | |
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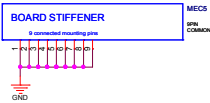
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BASE LEVEL GENERIC SCHEMATIC ONLY
PS: 12V Current Steering

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| 12V Current Steering | | | |
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Brackets:

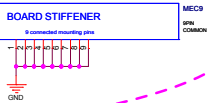
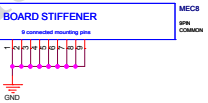
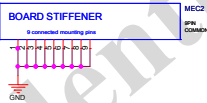
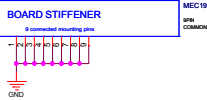
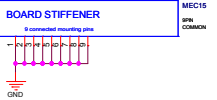
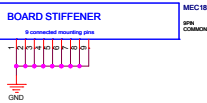
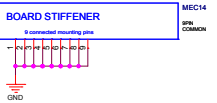
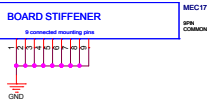
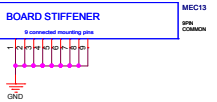
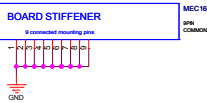
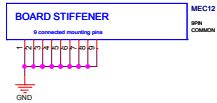
MH-C236D118-8



GPU Stiffener:

For GPU

MOUNTING HOLES FOR HS:

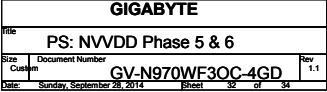


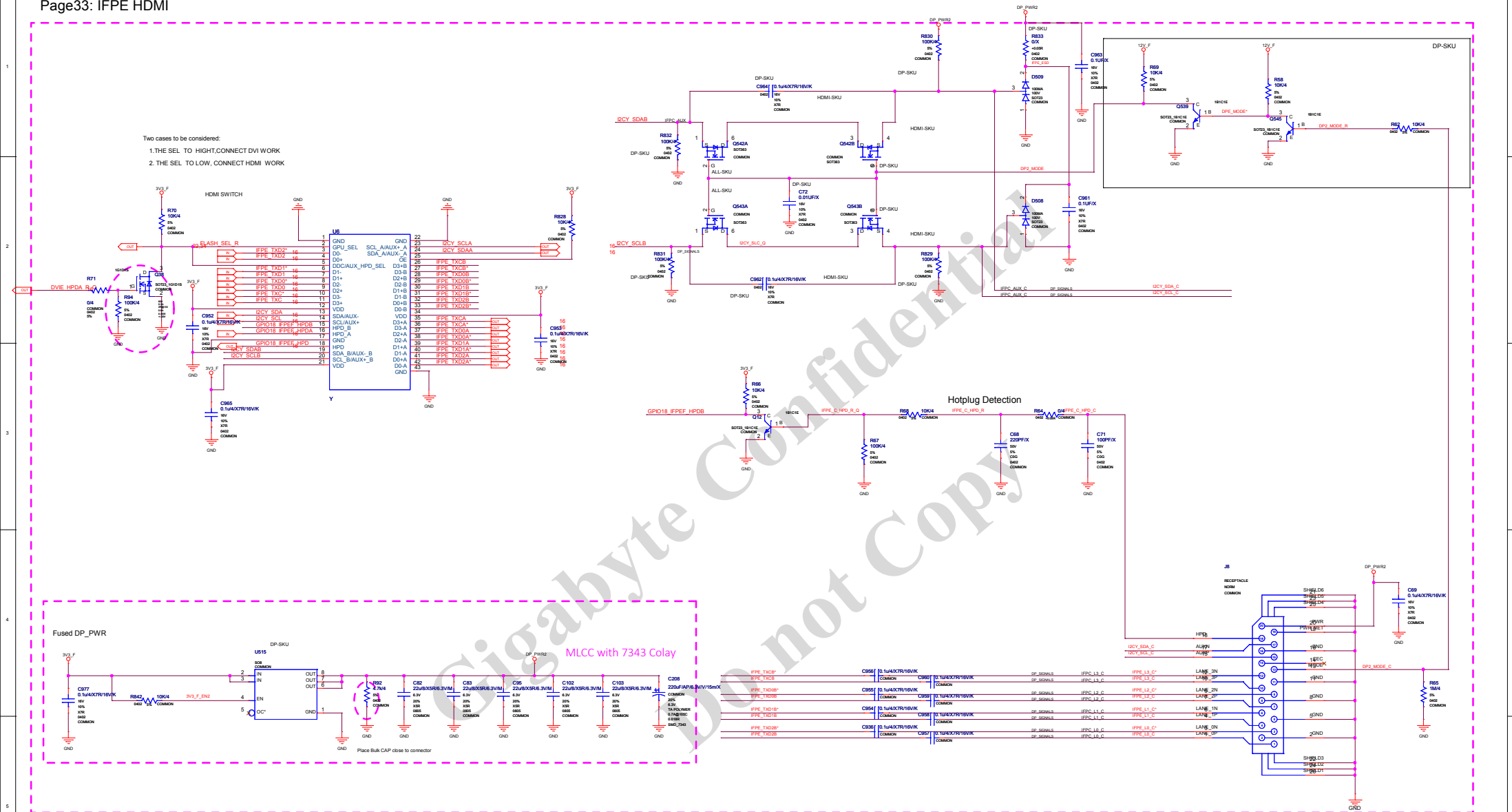
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ASSEMBLY
PAGE DETAIL
MECH

BASE LEVEL GENERIC SCHEMATIC ONLY
MECH

| GIGABYTE | | | |
|----------|----------------------------|-------|----------|
| Title | | | |
| MECH | | | |
| Size | Document Number | Rev | |
| Custom | GV-N970WF3OC-4GD | 1.1 | |
| Date | Sunday, September 28, 2014 | Sheet | 31 of 34 |





1. THE SEL TO HIGHT, CONNECT DVI WORK
2. THE SEL TO LOW, CONNECT HDMI WORK

