NV11, 1/2/4MX32 DDR, RGB, INTERNAL DVI-I, TV, AGP.

PCI DEVICE ID 0X0=0X112 FOR NV11.

STRAPS: AGP4X, SIDEBAND DIS, FAST WRITE ENA, ADP BIOS, NORMAL PCIAD, 14.318 MHZ STRAP_RAM_TYPE [5:2]=1011-1MX32 DDR SDRAM

01. List of Schematic sheets

02. HOST INTERFACE NEW

03. FB(MEMORY)/NVXX DECOUPLING

04. NVXX BOOT STRAPS

05. DAC and MEDIA PORT

06. LOWER SDRAM FBI

07. BT & CH TV ENCODER

08. DFP/DVI/CLK_DVR

HISTORY REV. :

1) Based on P39-A00

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2) Schematic changes; role rev to B00
-remove BT device
-strap resistor changes
-add/remove resistors and caps
-remove high dropout reg (uS14)
- R502 changed from 20K to 10K

- Swapped LVDS signals back to "Rev A connections".

- Hi Freq. cap arrays Decoupled to GND: 1) SMB_CLK 2) SMB_DNT 3) PID2_IZC_CLK (aka IZCSCL) 4) PID3_IZC_DNT (aka IZCSDA) 5) PID3_IZCDNT (aka PID3) 7) PID3_IZCCLK (aka PID2) 7) PID3_IZCCLK (aka PID2) 8) PID0

- Added 10K, NO STUFF, from AGP_BUSY# to +3.3VSUS.

4) D00:

5) E00:

- Implemented ECO change list "P41_D00_ECOs-X02.doc" in current working directory: \\nvcorp\applied\designs\working\nv11\XXX-P0041-0000-E00

- Connect the SO input of the IMISM530 to 3.3VRUN instead of CLK_VDD

- Added 1000pf cap (C535) in parallel to R535 for EMI supression.
- Changed mem termination from +3.3VRIN to +3.3VSIDS. Affects R183-R186, C416
68) E02-XD1 Change C279 to no stuff per Dell request.
7) E03: Added SM00603 foot print to the backside of board, C536 to DVI, DFP page.

MS8832,1/2/4MX32-DDR,TV Document Number

List of schematic sheets

Theories, April 30, 2002 | Sheet | 1

{Item}\t{Quantity}\t{Reference}\t{USR DFINE}\t{PN}\t{Assembly}\t{Value}{Assembly}{Source Package} {Value}{PN}{AVL1}{AVL2}{AVL3}{AVL4}{AVL4}

















