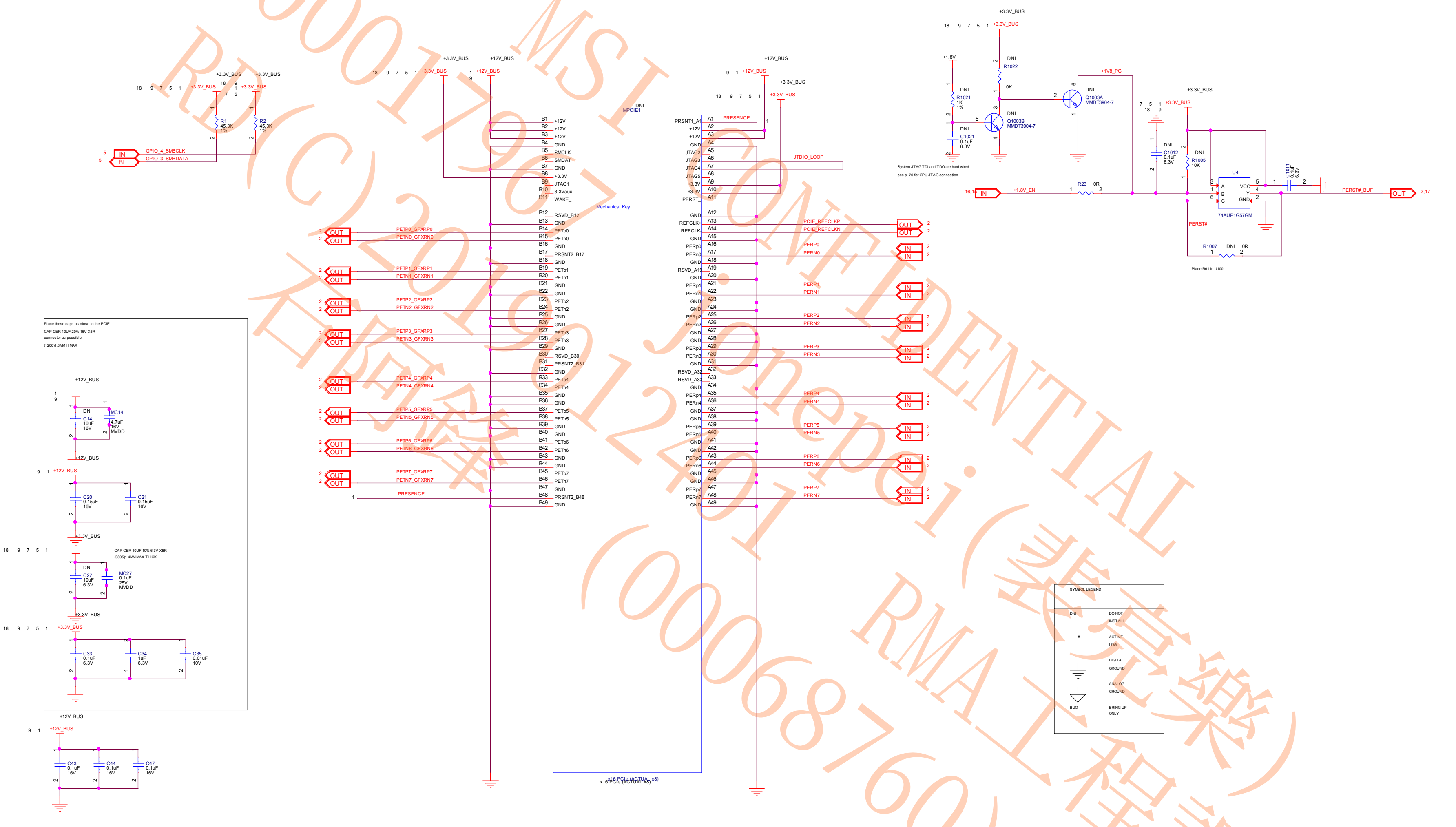
PCI-EXPRESS EDGE CONNECTOR



AMD - PLATFORM HARDWARE ENG
#46, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: PCI-E Edge Connector

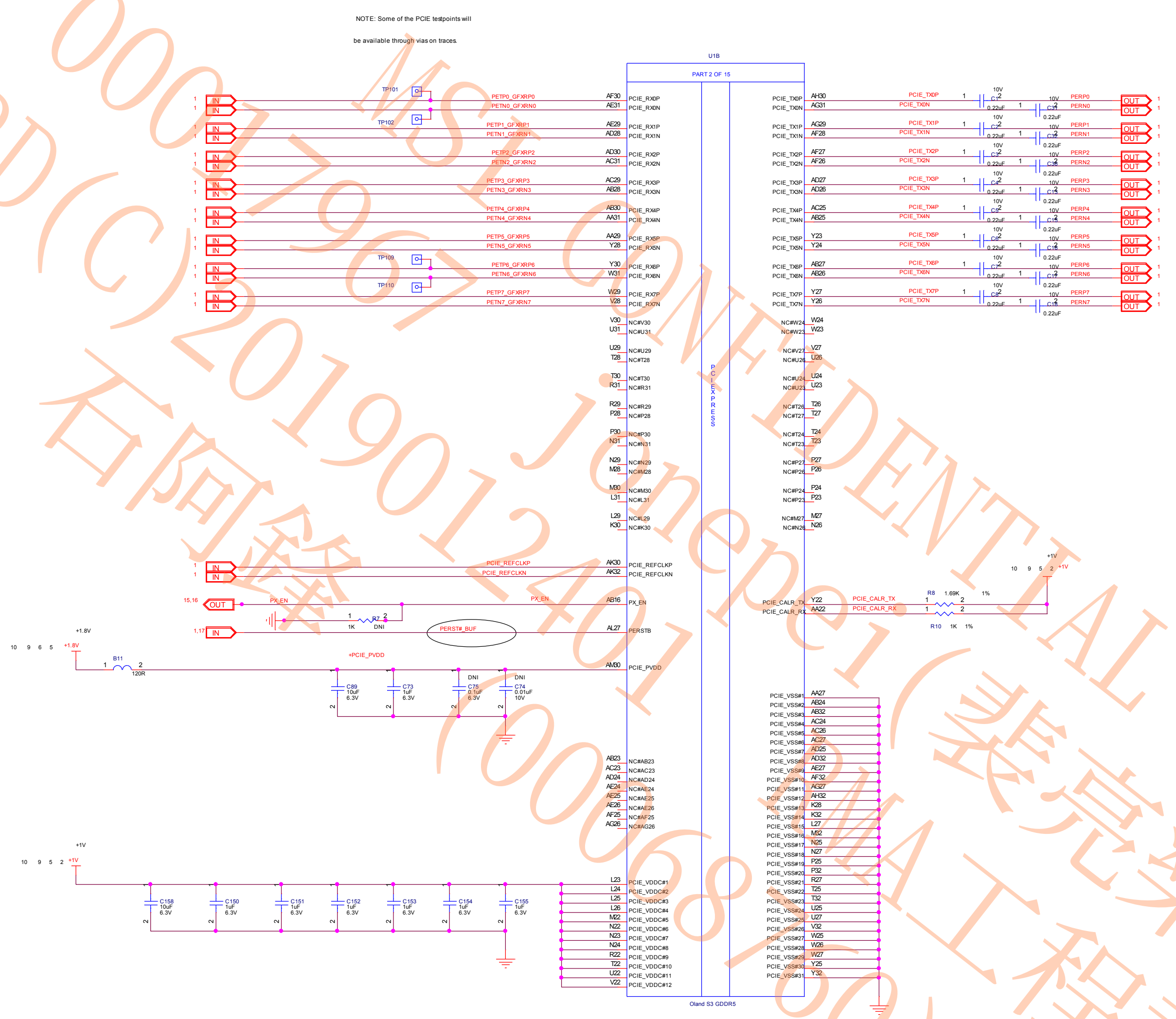
DATE: Wed Aug 29 03:27:33 2018 REV: P00

SHEET NUMBER: 1 OF 20

DOCUMENT NUMBER: 105_d12900_00A

TITLE: XXX

OLAND PCIe Interface



AMD - PLATFORM HARDWARE ENG
#46, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: Oland PCIEx8 Interface

DATE: Thu Jan 05 05:41:57 2017

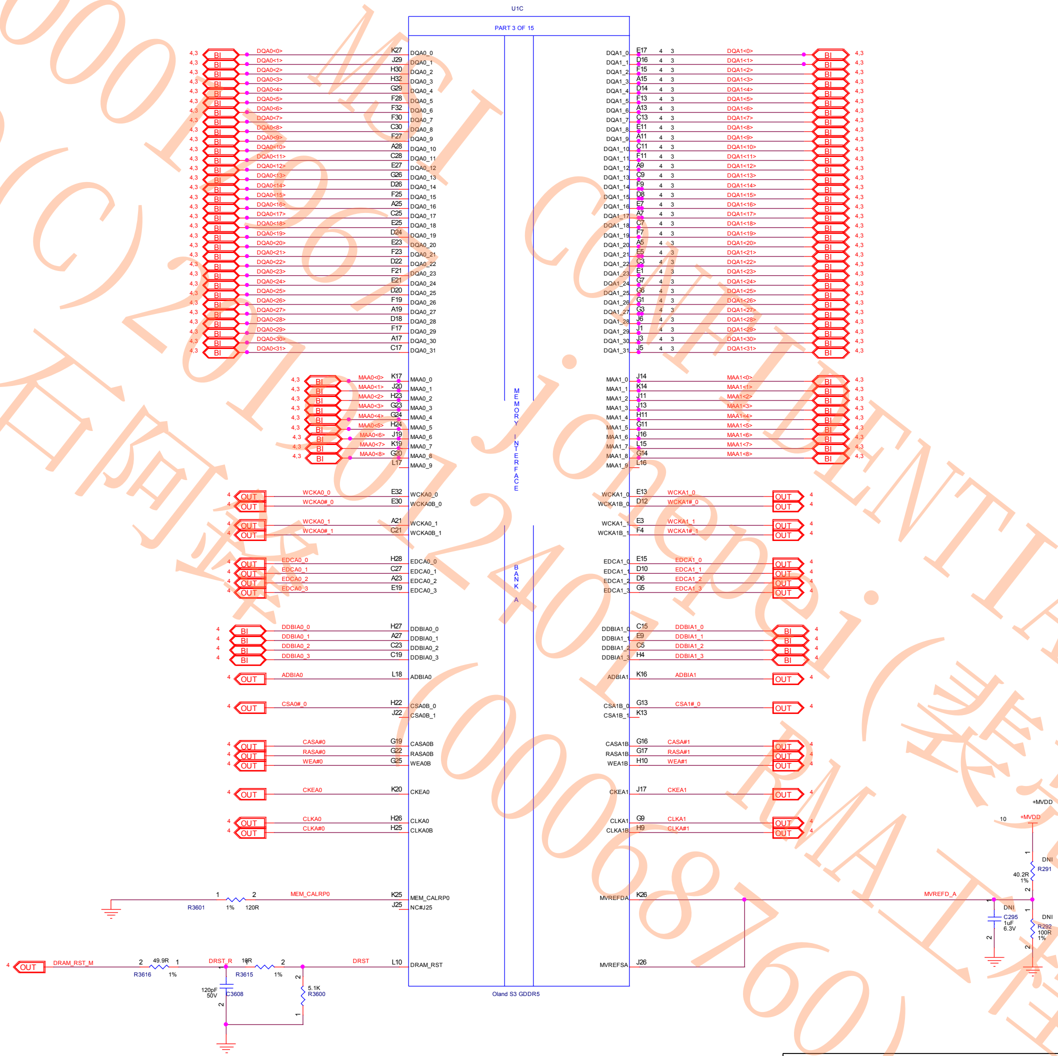
REV: P00

SHEET NUMBER: 2 OF 20

DOCUMENT NUMBER: 105_d12900_00A

TITLE: XXX

Oland S3 MEM Interface Ch A



AMD - PLATFORM HARDWARE ENG #46, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203			
SHEET:	VDDCI		
DATE:	Wed Aug 29 03:24:49 2018	REV:	P00
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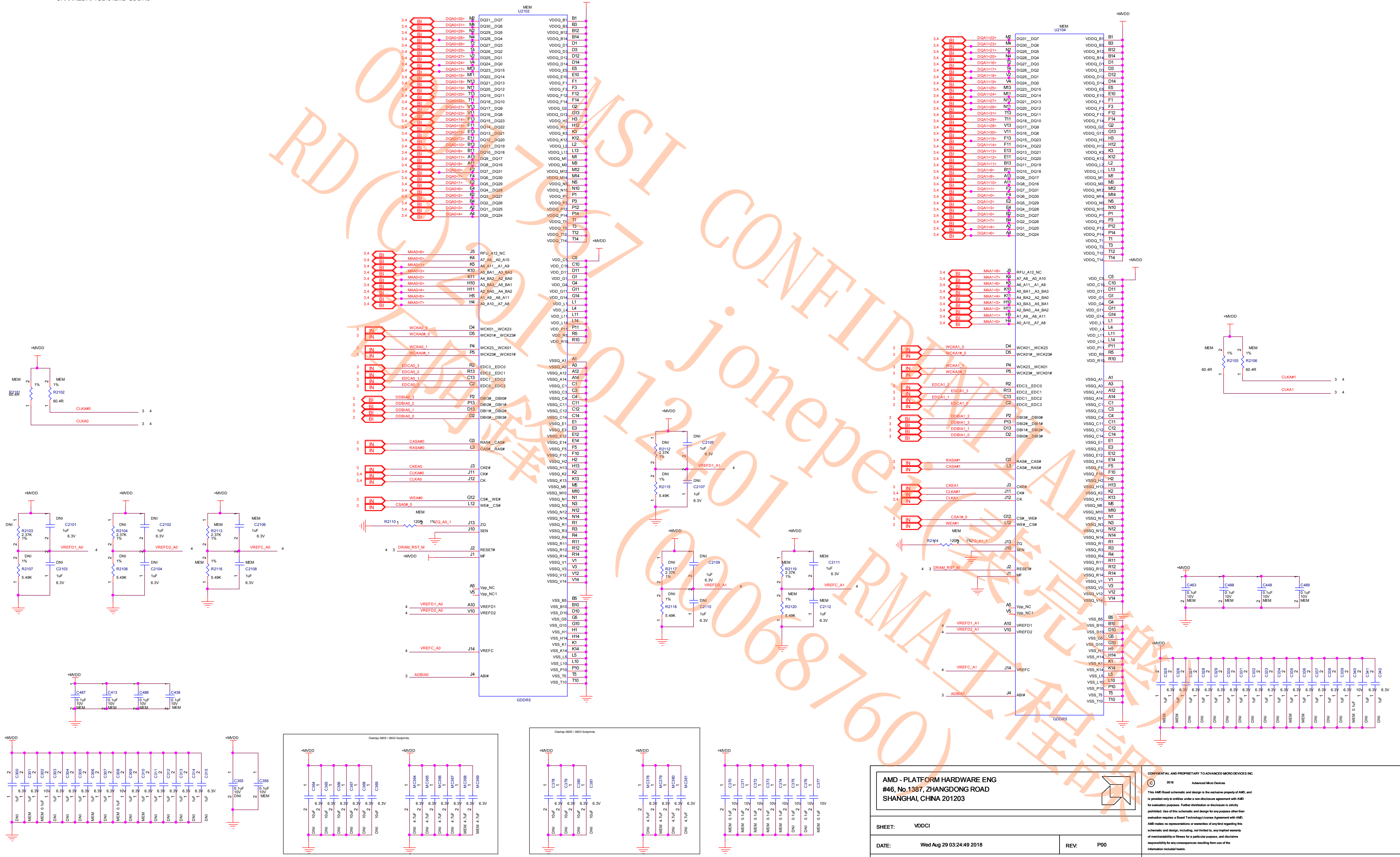
DOCUMENT NUMBER:	105_d12900_00A		
NOTES:	NOTE		

TITLE: XXX

CHANNEL A: 1GB/512MB GDDR5

(5) GDDR5 Memory Channel A&B



AMD - PLATFORM HARDWARE ENG
#46, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: VDDCI

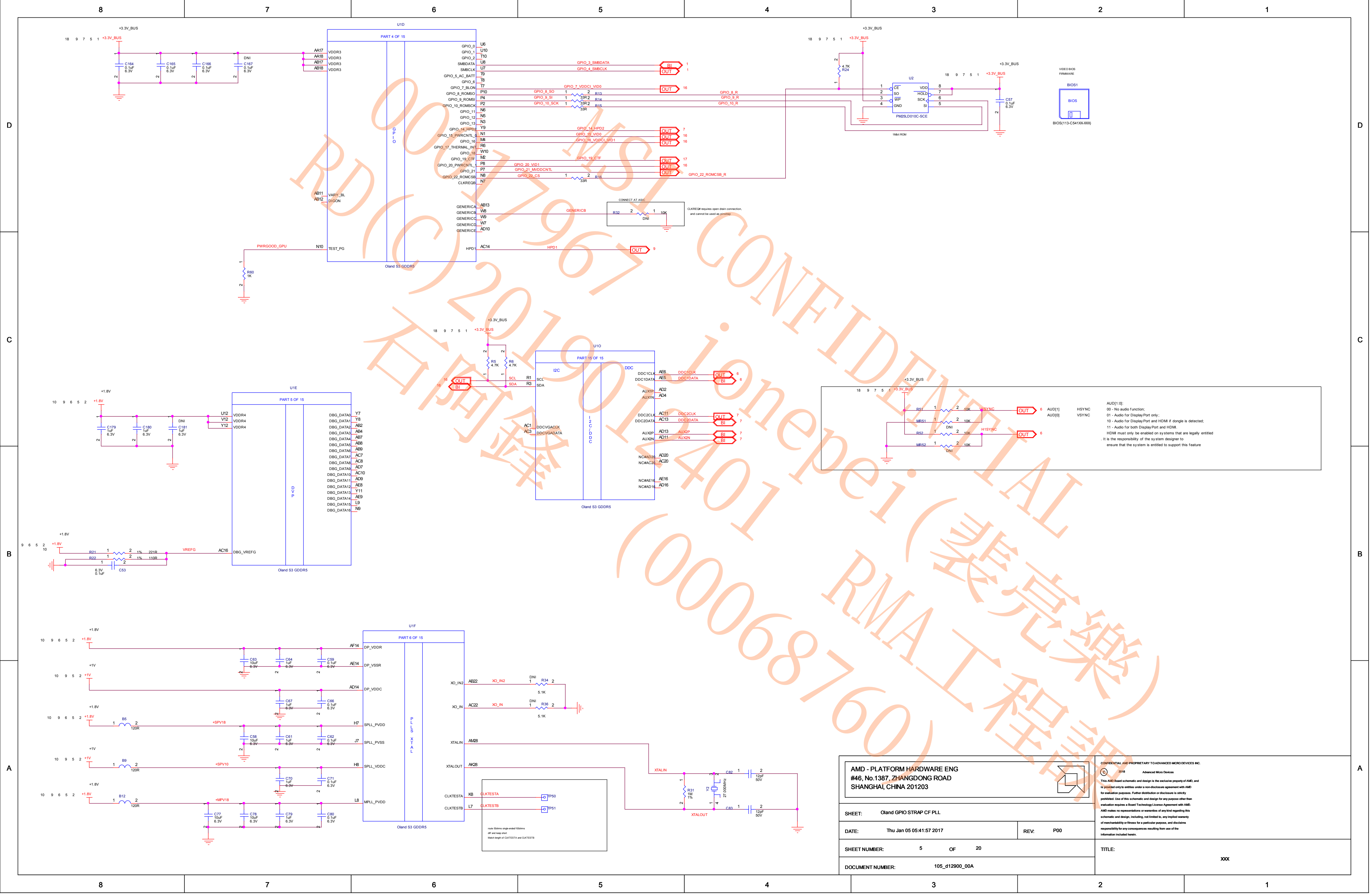
DATE: Wed Aug 29 03:24:49 2018 REV: P00

SHEET NUMBER: 4 OF 20

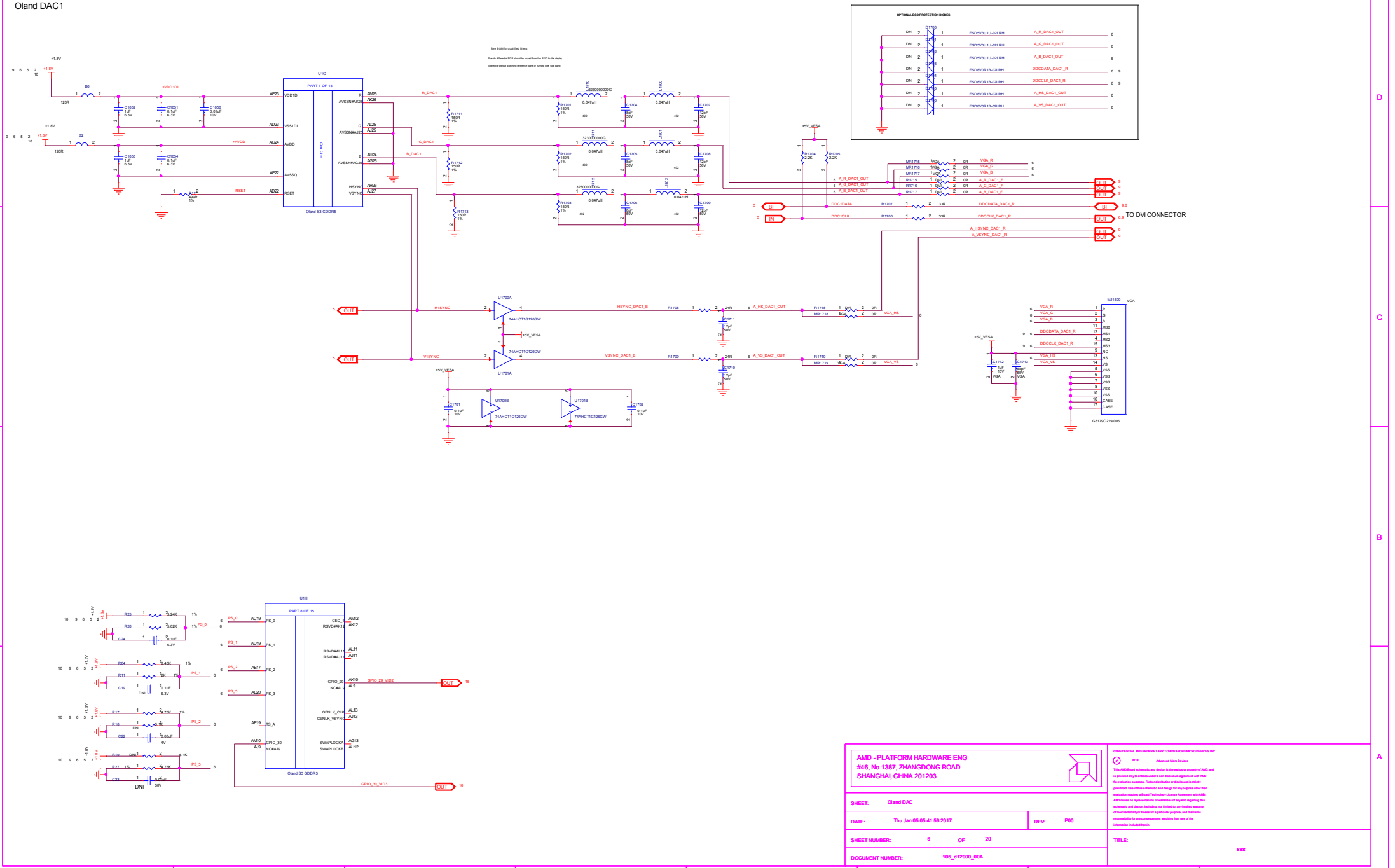
DOCUMENT NUMBER: 105_d12900_00A

NOTES: NOTE

TITLE: XXX

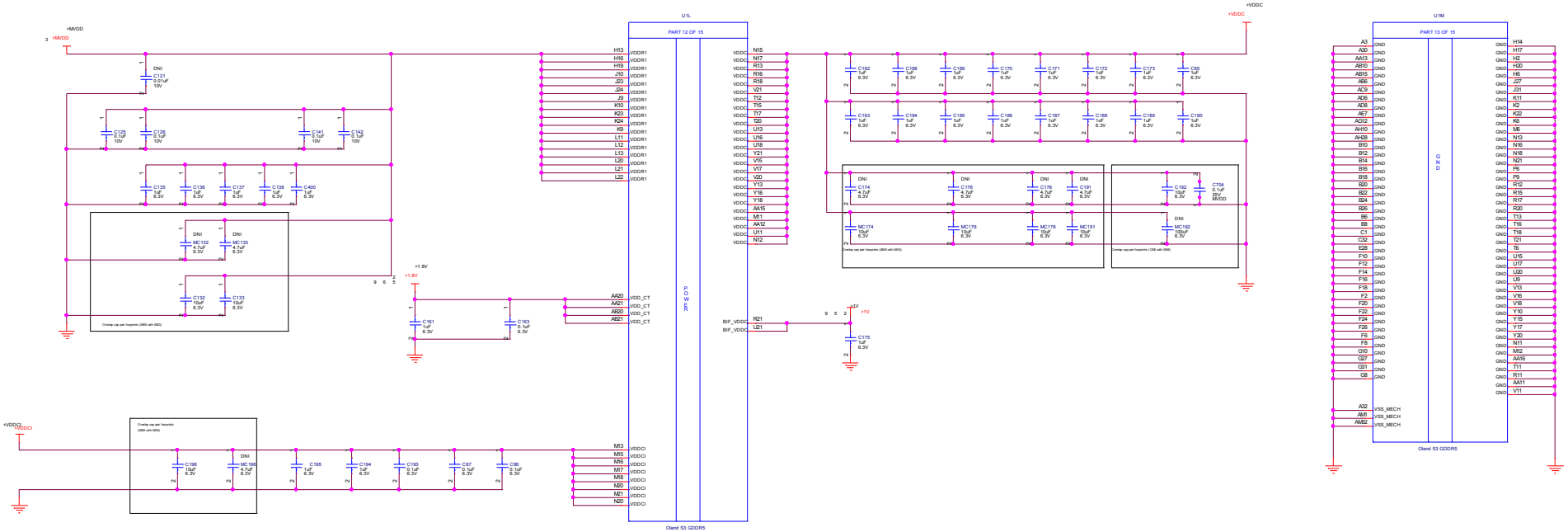


Oland DAC1



AMD - PLATFORM HARDWARE ENG #46, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	
SHEET: Oland DAC	
DATE: Thu Jan 05 05:41:56 2017	REV: P00
SHEET NUMBER: 6 OF 20	
DOCUMENT NUMBER: 105_d12900_00A	TITLE: XXX

OLAND S3 Power & GND



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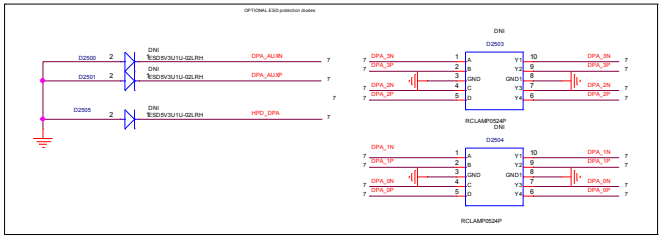
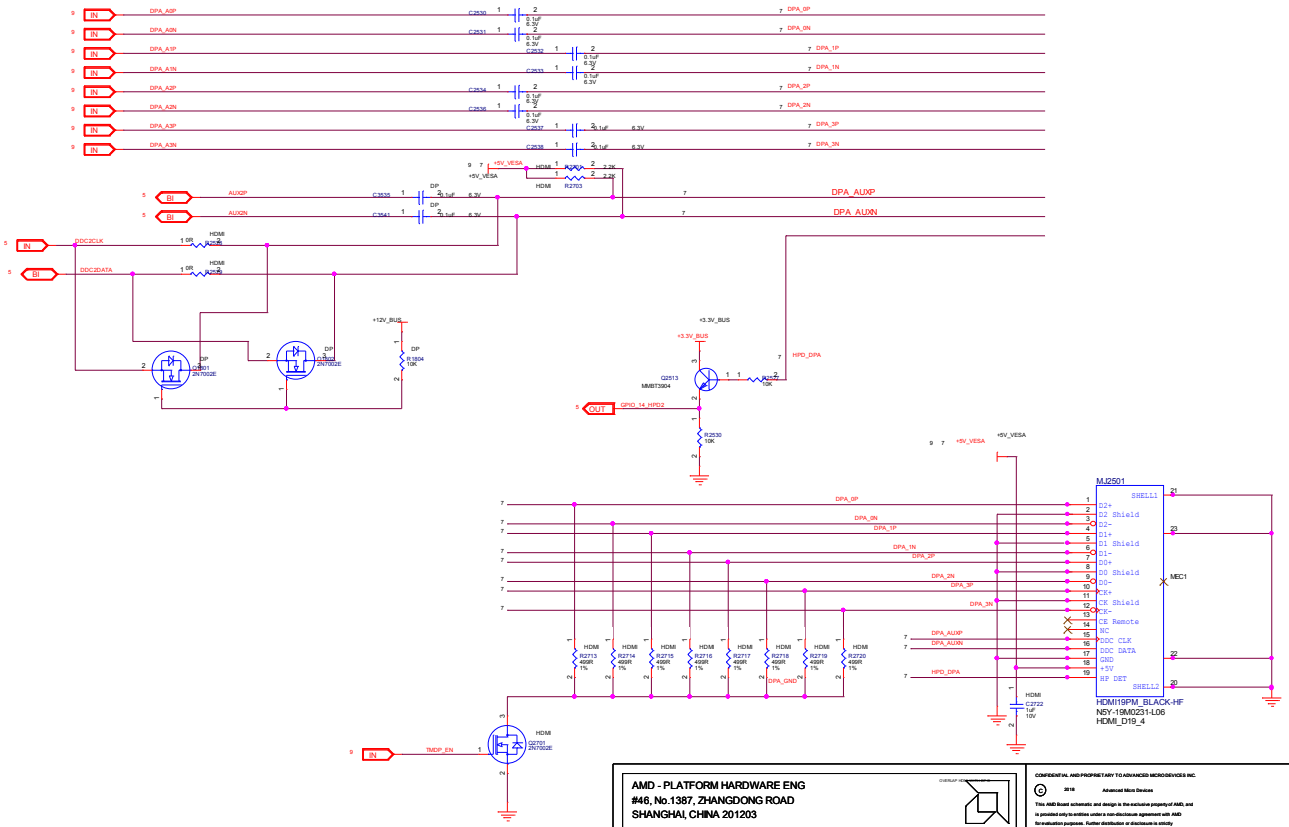
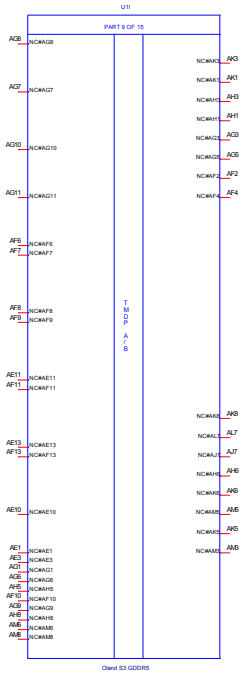
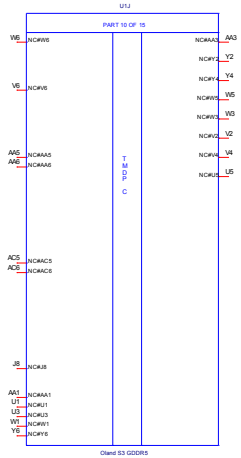
SHEET: VDDCI	
DATE: Wed Aug 29 03:24:49 2018 REV: P00	
SHEET NUMBER: 10 OF 20	TITLE: XXX
DOCUMENT NUMBER: 105_d12900_00A	
NOTES: NOTE	





AMD - PLATFORM HARDWARE ENG #46, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	
SHEET: Oland HDMI/DP	
DATE: Thu Jan 05 05:41:56 2017	REV: P00
SHEET NUMBER: 7 OF 20	
DOCUMENT NUMBER: 105_d12900_00A	TITLE: XXX

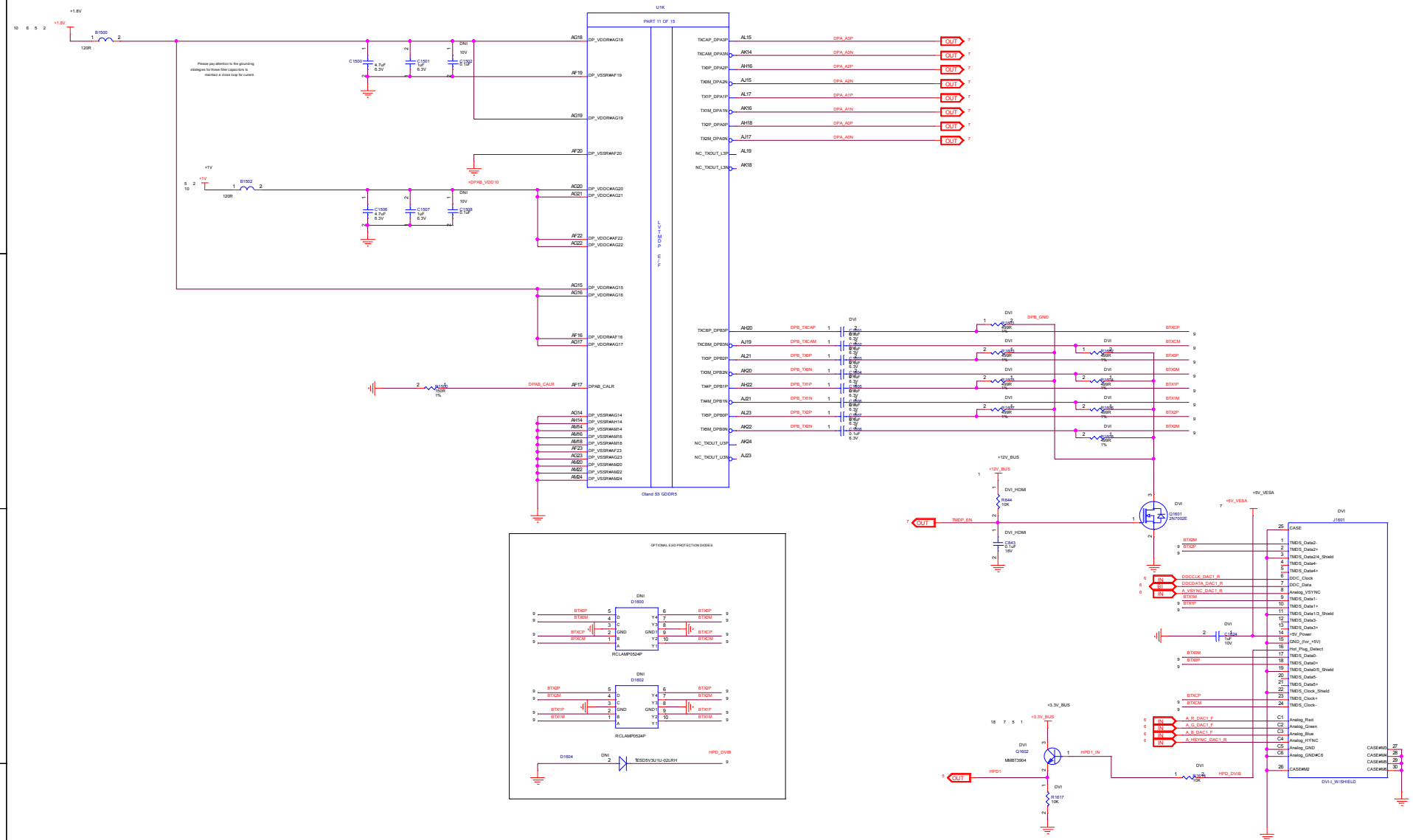




AMD - PLATFORM HARDWARE ENG
#46, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET:	Oland Unused Modules
DATE:	Wed Aug 29 03:24:48 2018
REV:	P00
SHEET NUMBER:	8 OF 20
DOCUMENT NUMBER:	105_d12900_00A
NOTES:	NOTE

TITLE: XXX


U1K
PART 11 OF 15
Oland S3 GDDR5
OPTIONAL ESD PROTECTION DIODES
RCLAMP0524P
RCLAMP0524P
TESD5V3U1U-02LRH
DVI-I_W/SHIELD
AMD - PLATFORM HARDWARE ENG
#46, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203
SHEET: Oland sDVI-I CONN
DATE: Wed Aug 29 03:24:48 2018
REV: P00
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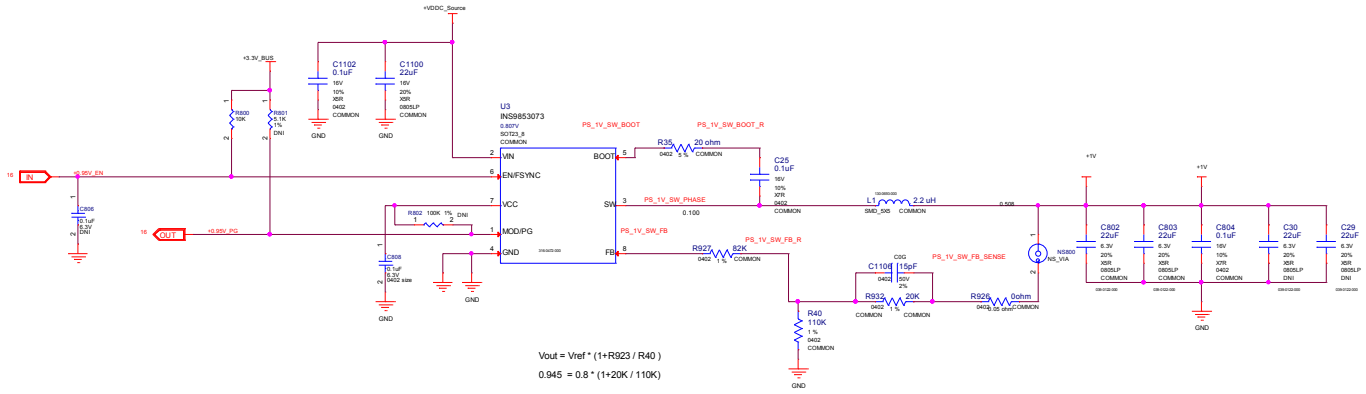
DOCUMENT NUMBER: 105_d12900_00A
NOTES: NOTE
TITLE: XXX

LDO #1: Vin = +1.5V +/-2% Vout = +0.95V +/- 2%; Iout = 1.44A (TBV) RMS MAX

PCB: 50 to 70mm sq. copper area for cooling



Vout = Vref * (1+R923 / R40)

0.945 = 0.8 * (1+20K / 110K)

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SHEET: VDDCI	
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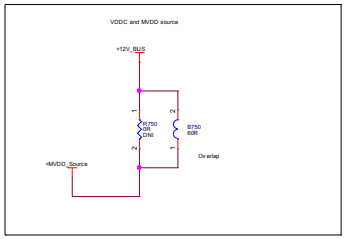
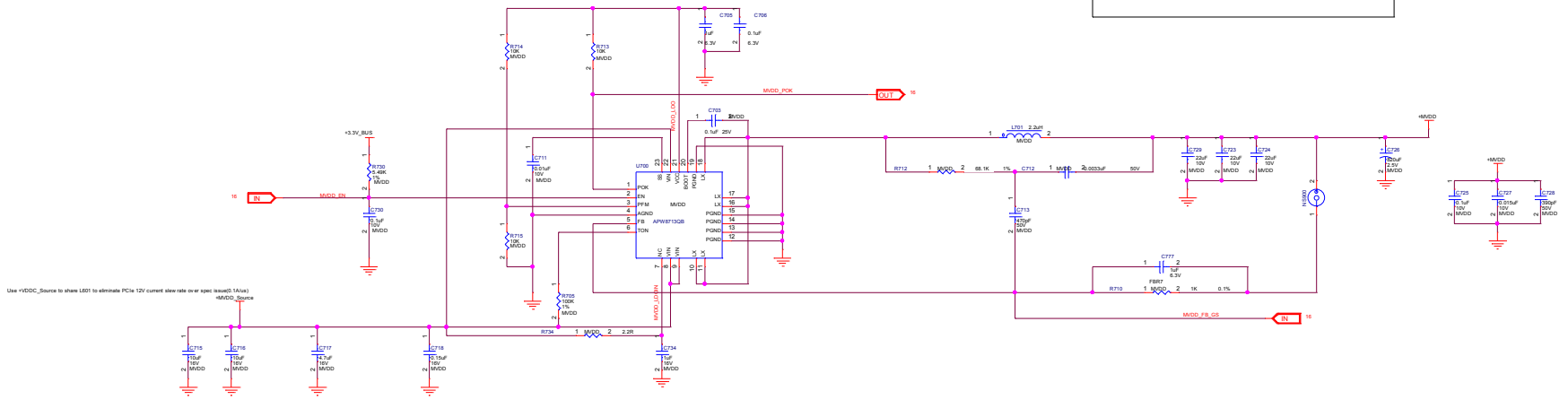
SHEET NUMBER: 11 OF 20	TITLE: XXX
DOCUMENT NUMBER: 105_d12900_00A	
NOTES: NOTE	

Regulator for MVDD





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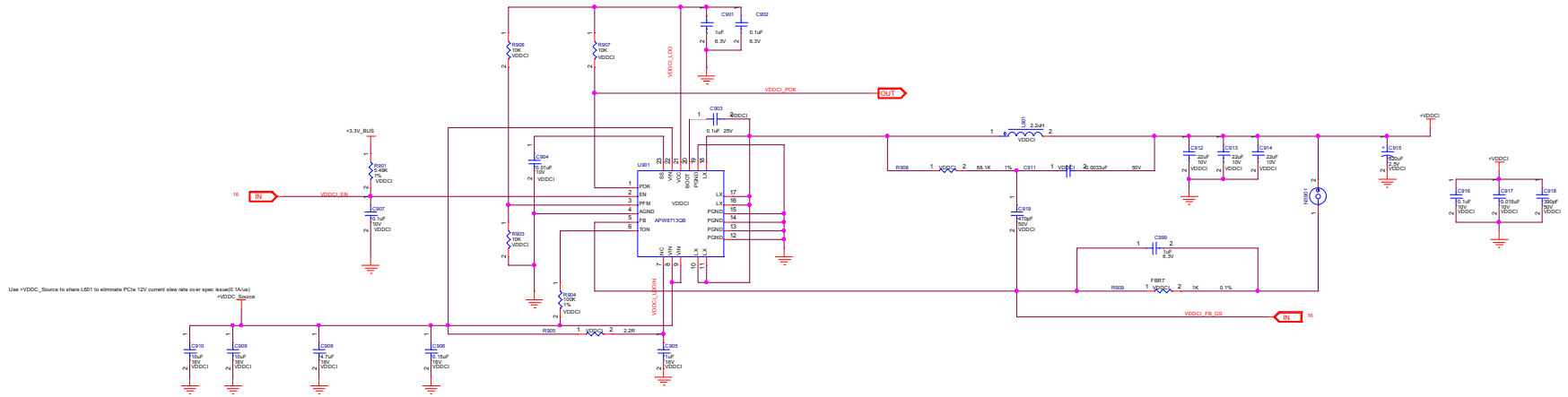
SHEET: MVDD	
DATE: Wed Aug 29 03:24:48 2018	REV: P00
SHEET NUMBER: 12 OF 20	
DOCUMENT NUMBER: 105_d12900_00A	TITLE: XXX

Regulator for MVDD



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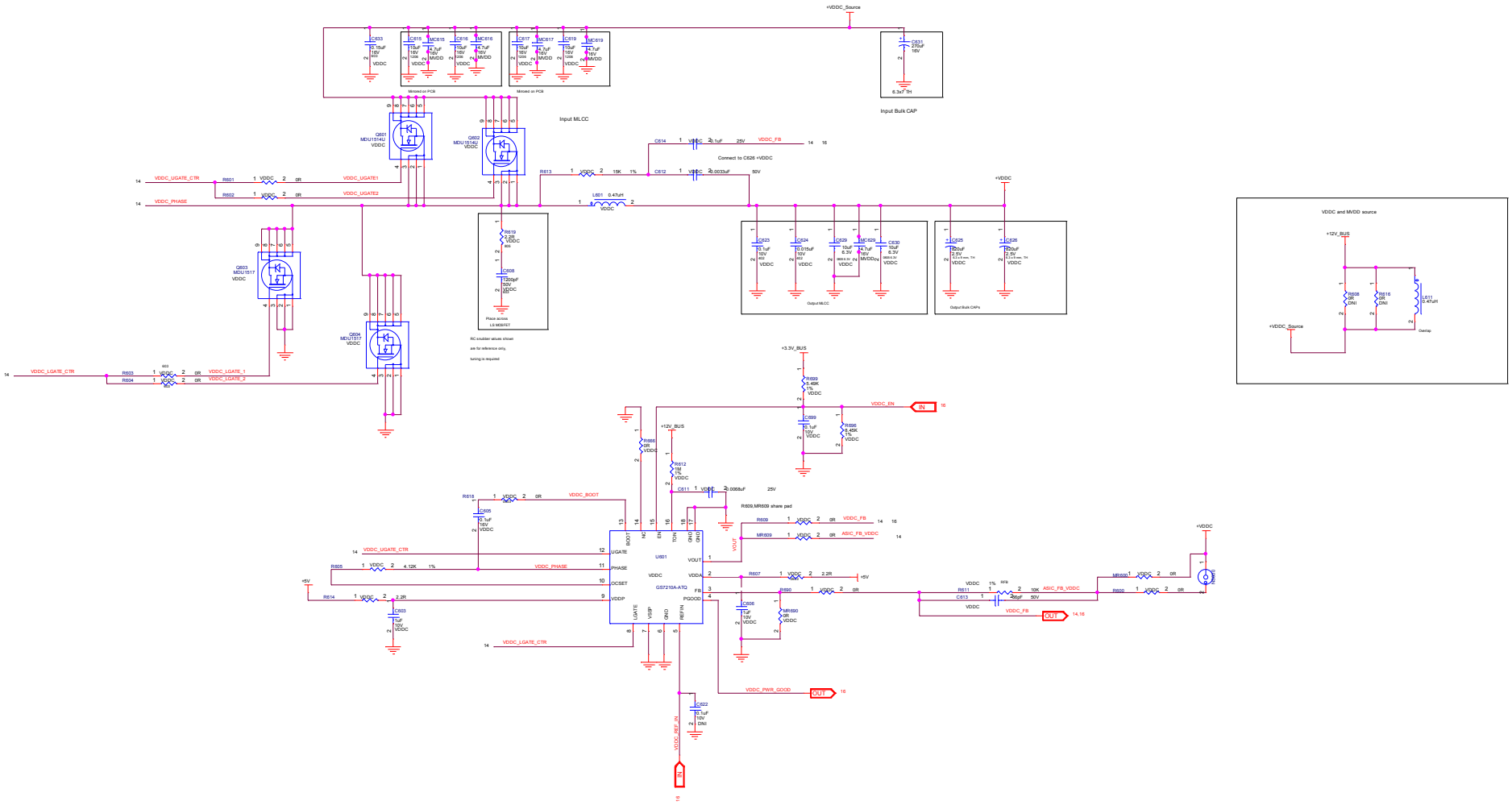
SHEET: VDDCI	
DATE: Wed Aug 29 03:24:49 2018 REV: P00	
SHEET NUMBER: 13 OF 20	TITLE: XXX
DOCUMENT NUMBER: 105_d12900_00A	
NOTES: NOTE	



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SHEET: VDDC	
DATE: Thu Jan 05 05:41:56 2017	REV: P00
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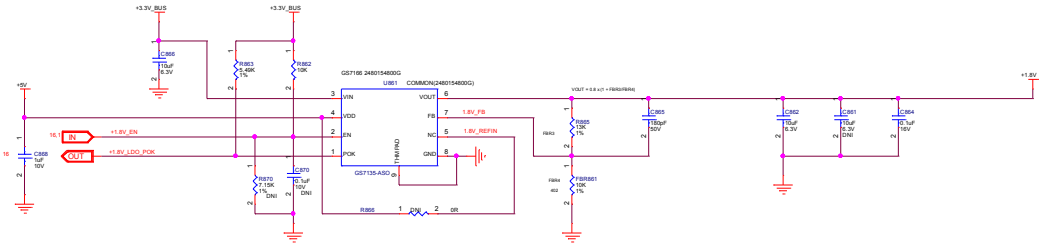
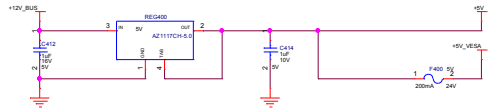
DOCUMENT NUMBER: 105_d12900_00A	TITLE: XXX

Linear Regulators

LDO #2: Vin = 3.00V to 3.60V (3.3V +/- 9%) Vout = +1.8V +/- 2%; Iout = 1.6A (TBV) RMS MAX

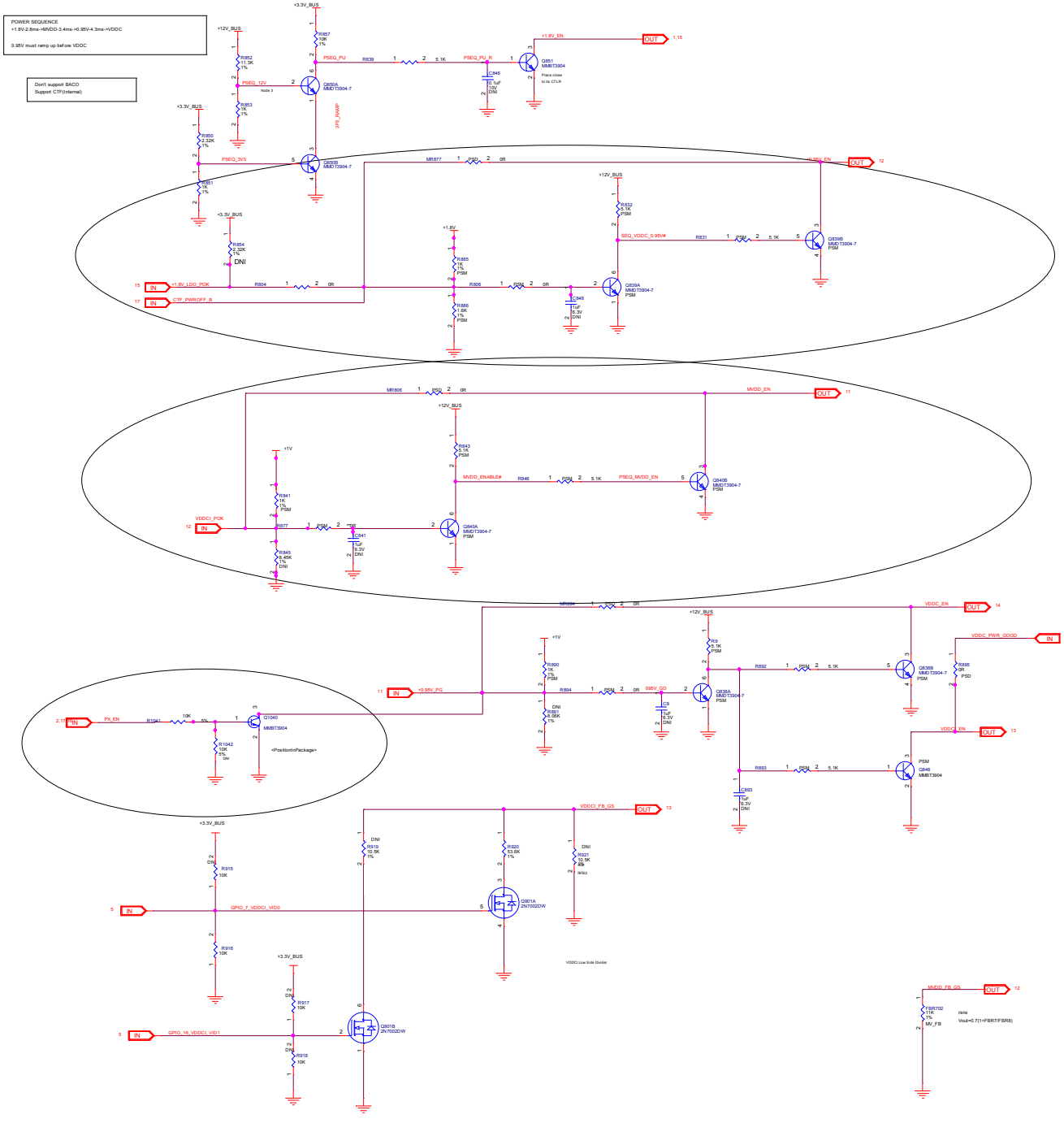
PCB: 50 to 70mm sq. copper area for cooling



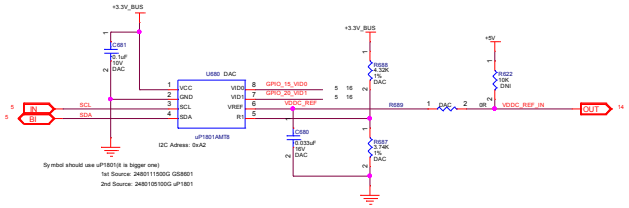


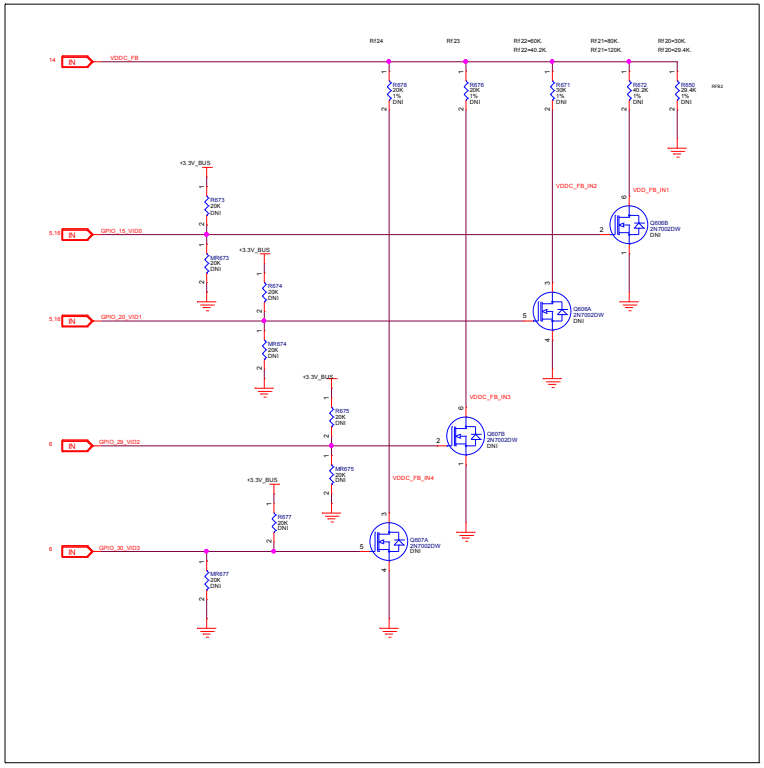
AMD - PLATFORM HARDWARE ENG #46, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	
SHEET: Linear Regulators	
DATE: Thu Jan 05 05:41:58 2017	REV: P00
SHEET NUMBER: 15 OF 20	
DOCUMENT NUMBER: 105_d12900_00A	TITLE: XXX

Power Management - Power Gating and Dynamic Voltage Control



VDDC Low Side Divider





AMD - PLATFORM HARDWARE ENG #46, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	
SHEET: VDDCI	
DATE: Wed Aug 29 03:24:49 2018	REV: P00
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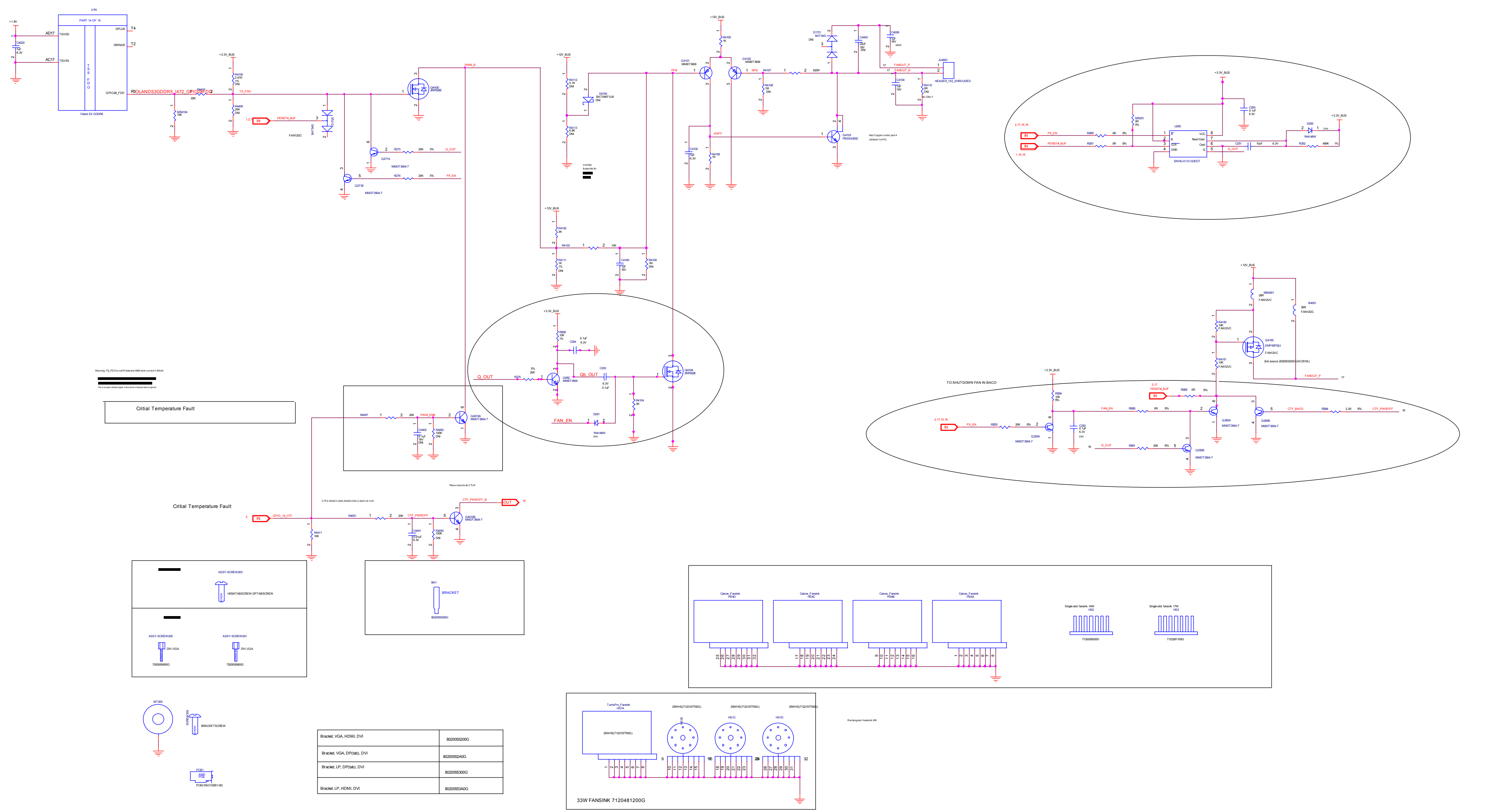
NOTES: NOTE NOTE	TITLE: XXX

Install R4520	GPIO6 is 0: Fan stop GPIO6 is PWM: Fan running
Install R4516 Q4511,R4519	GPIO6 is 1: Fan stop GPIO6 is PWM: Fan running

Mechanical and Thermal Management



Bracket, VGA, HDMI, DVI	8020055200G
Bracket, VGA, DP(tab), DVI	80200552A0G
Bracket, LP, DP(tab), DVI	8020055300G
Bracket, LP, HDMI, DVI	80200553A0G

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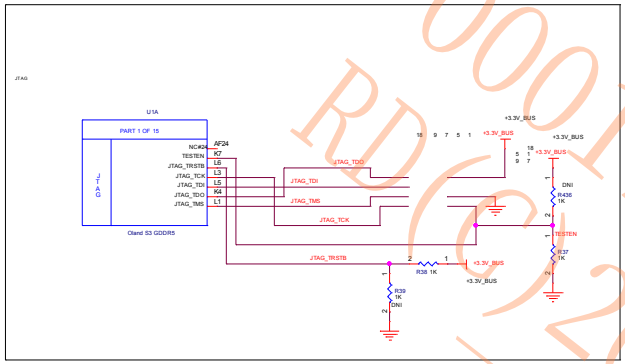
#46, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET:	VDDCI
DATE:	Wed Aug 29 03:24:49 2018
REV:	P00
SHEET NUMBER:	17 OF 20
DOCUMENT NUMBER:	105_d12900_00A
NOTES:	NOTE

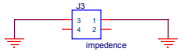
TITLE: XXX XXX

(19) Debug Circuits

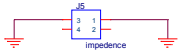


TOP
Single end
Memory data
45 ohm +/- 5 ohm
5.12 mils

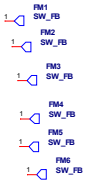
Bottom
Single end
Address trunk
45 ohm +/- 5 ohm
5.12 mils



TOP
Different
TMDS
85 ohm +/- 10 %
4.33 mils / 5.51 mils



Bottom
Different
PEX_PCIE
85 ohm +/- 10 %
4.33 mils / 5.51 mils



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#46, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET:	Debug
DATE:	Thu Jan 05 05:41:58 2017
REV:	P00
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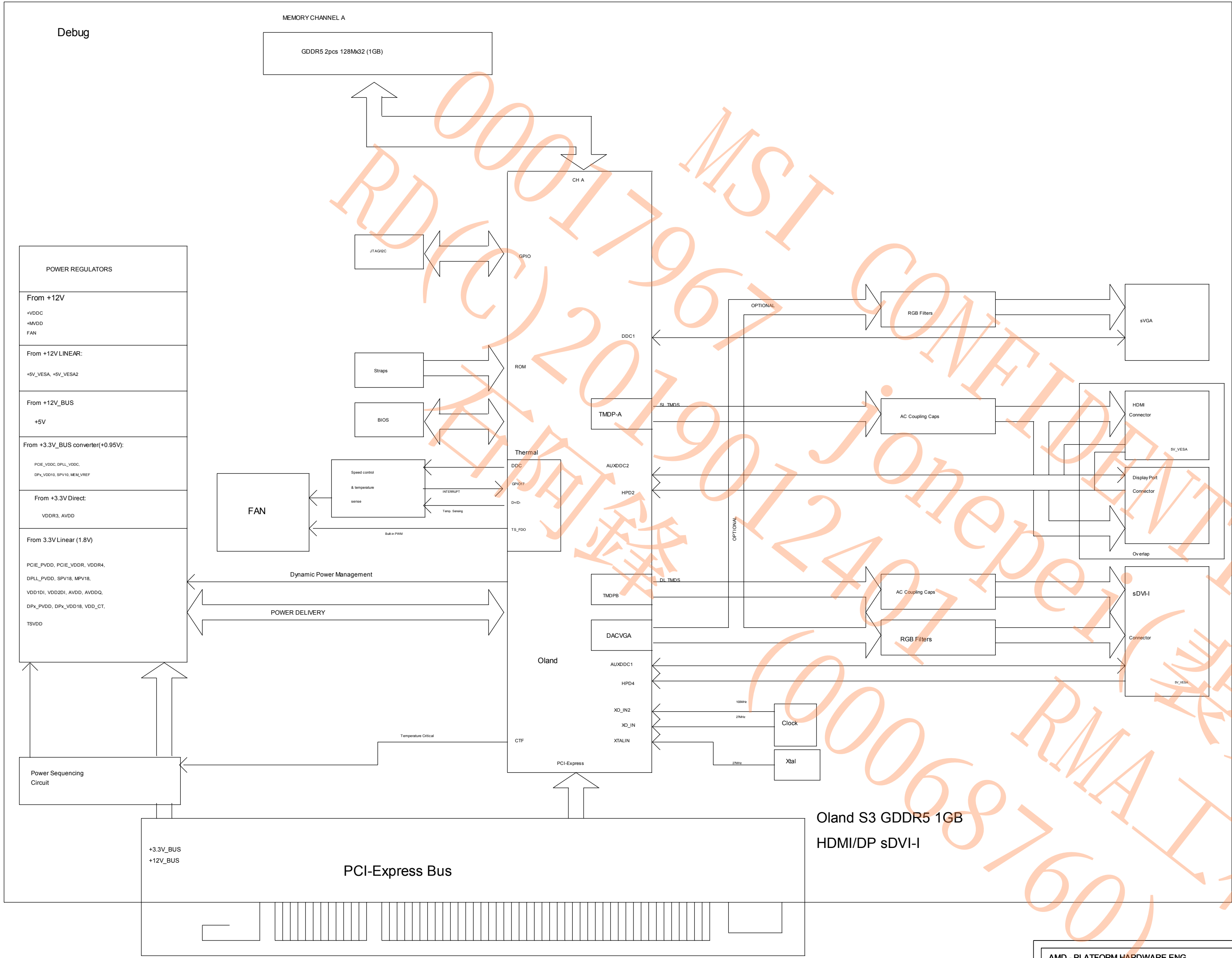
DOCUMENT NUMBER:	105_d12900_00A
TITLE:	XXX



Oland S3 GDDR5 1GB
HDMI/DP sDVI-I

AMD - PLATFORM HARDWARE ENG
#46, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: Diagram

DATE: Thu Jan 05 05:41:15 2017

REV: P00

SHEET NUMBER: 19 OF 20

DOCUMENT NUMBER: 105_d12900_00A

TITLE: XXX

	TITLE Oland 53 GDDR5 1GB HDMI/DP+DVI-I/VGA	SCHEMATIC NO. 105-D12Xxx-00A	DATE
	REVISION HISTORY	NOTE: THIS SCHEMATIC REPRESENTS THE PCB. IT DOES NOT REPRESENT ANY SPECIFIC SKU. FOR STUFFING OPTIONS, COMPONENT VALUES, ETC. PLEASE CONSULT THE PRODUCT SPECIFIC BOM. PLEASE CONTACT AMD REPRESENTATIVE TO OBTAIN LATEST BOM. SUBJECT TO THE APPLICATION DESIRED.	REV P00

SCH REV	PCB REV	DATE	REVISION DESCRIPTION
00	00A	2016.11.28	Initial Oland D129 schematic, based on CI41XX-00A

AMD - PLATFORM HARDWARE ENG
#46, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET:	History
DATE:	Thu Jan 05 05:41:18 2017
REV:	P00
SHEET NUMBER:	20 OF 20
DOCUMENT NUMBER:	105_d12900_00A

TITLE:
XXX