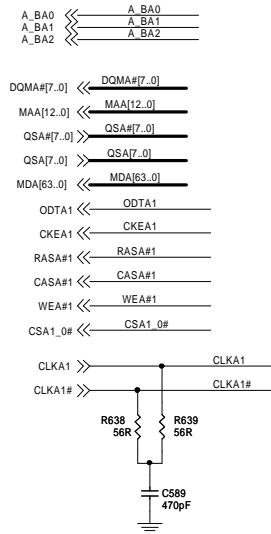
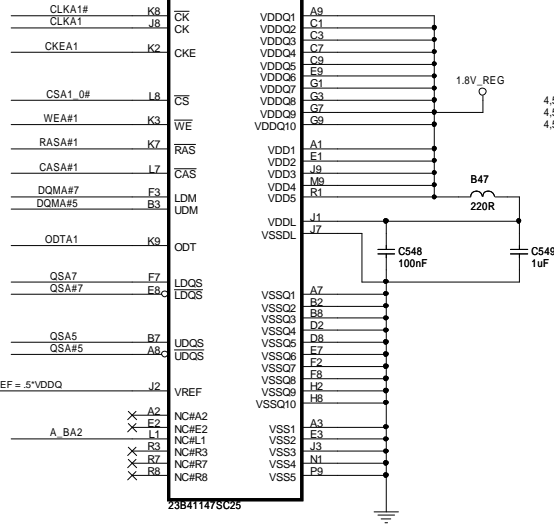
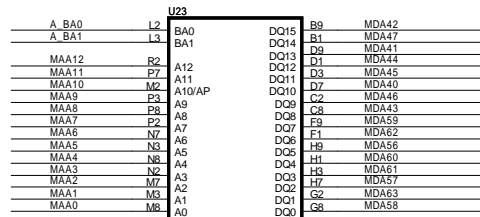
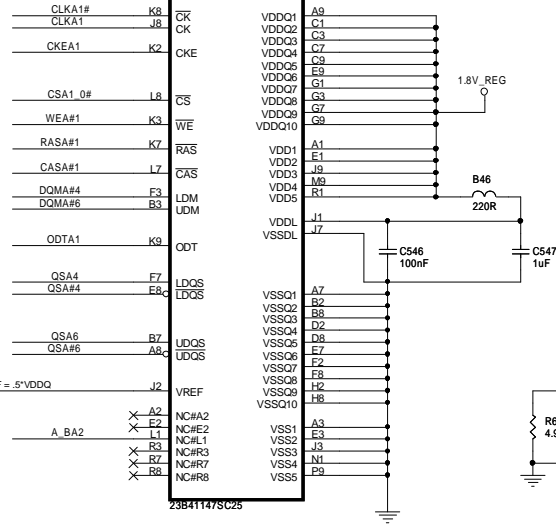
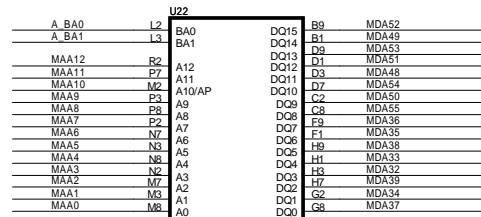


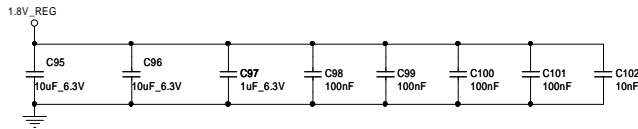


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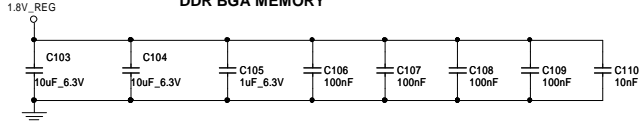
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DDR2 BGA MEMORY



DDR BGA MEMORY

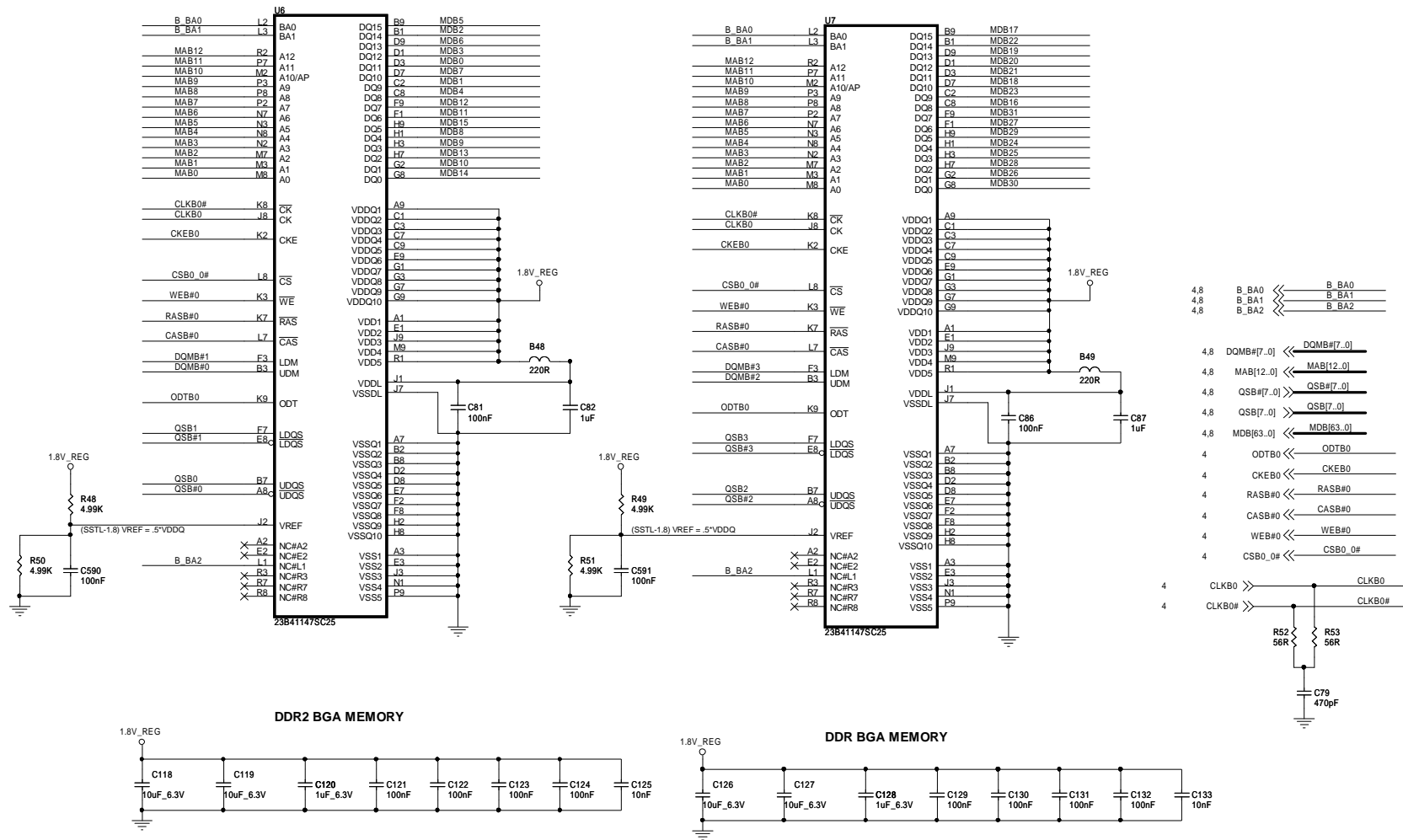


CHAN B RANK 0 DDR2 BGA MEMORY



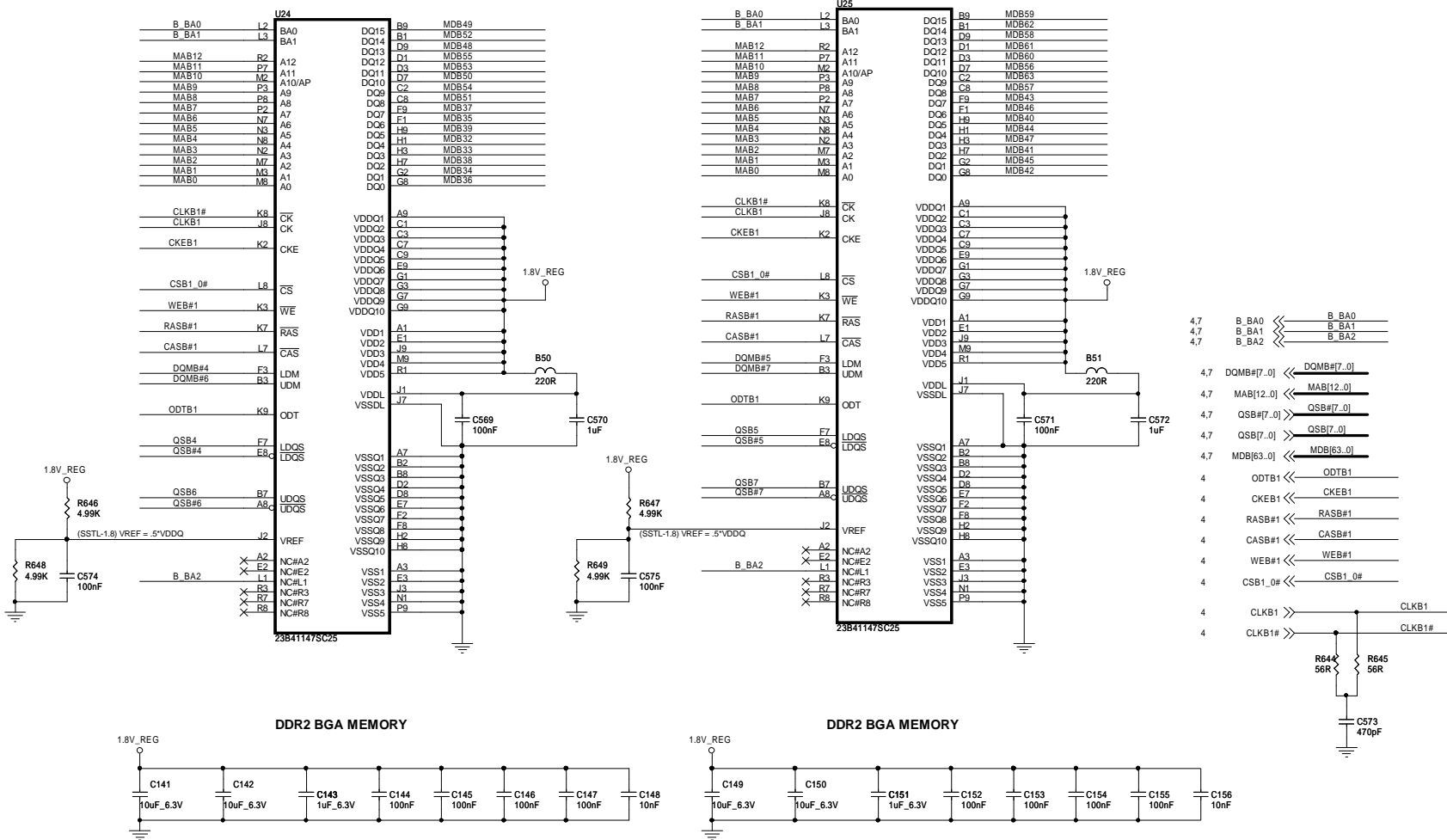
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DDR2 BGA MEMORY

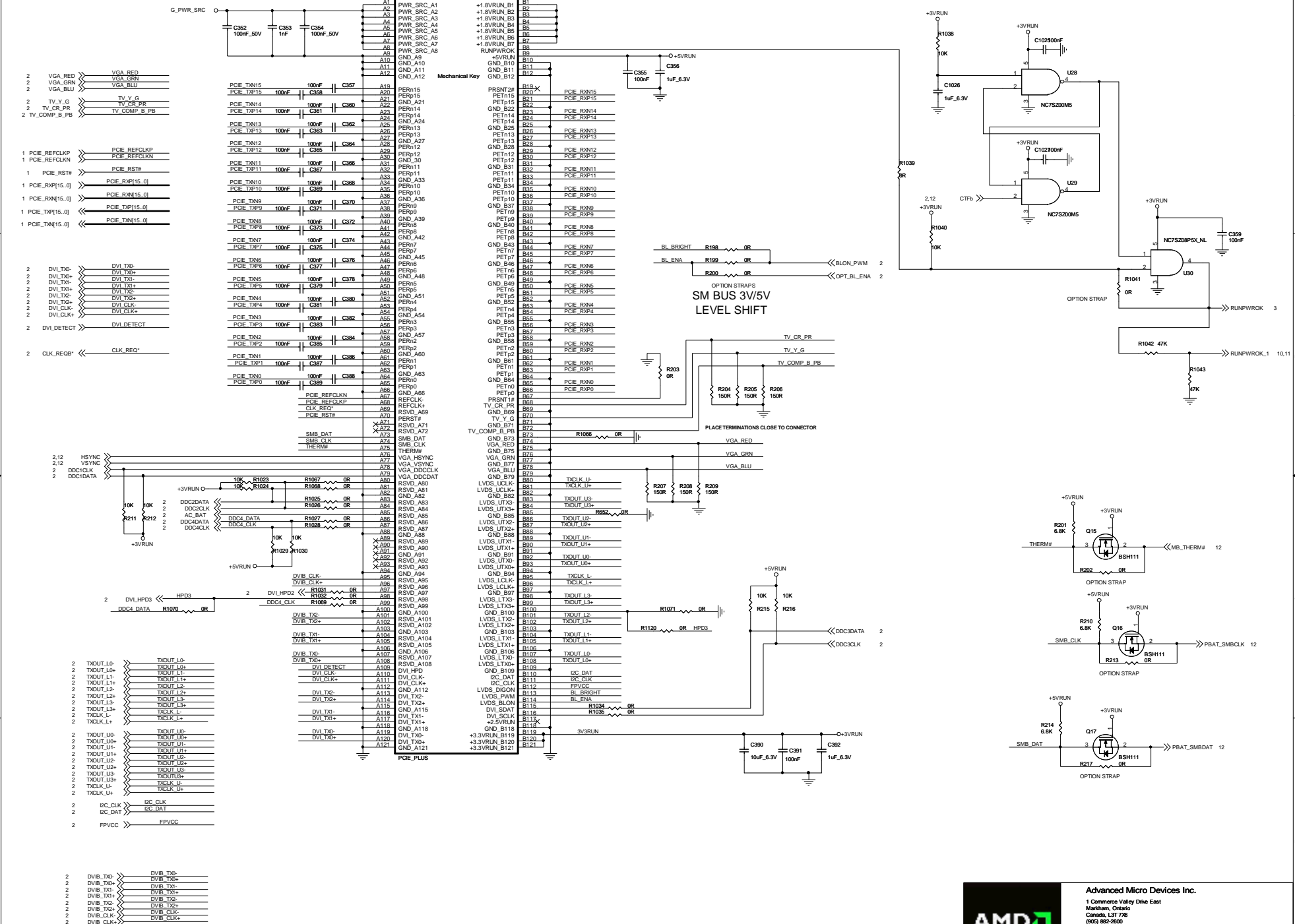
DDR2 BGA MEMORY

CHAN D RANK0 DDR2 BGA MEMORY

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PLACE CAPS ON THIS PAGE AS CLOSE TO CONNECTOR AS POSSIBLE

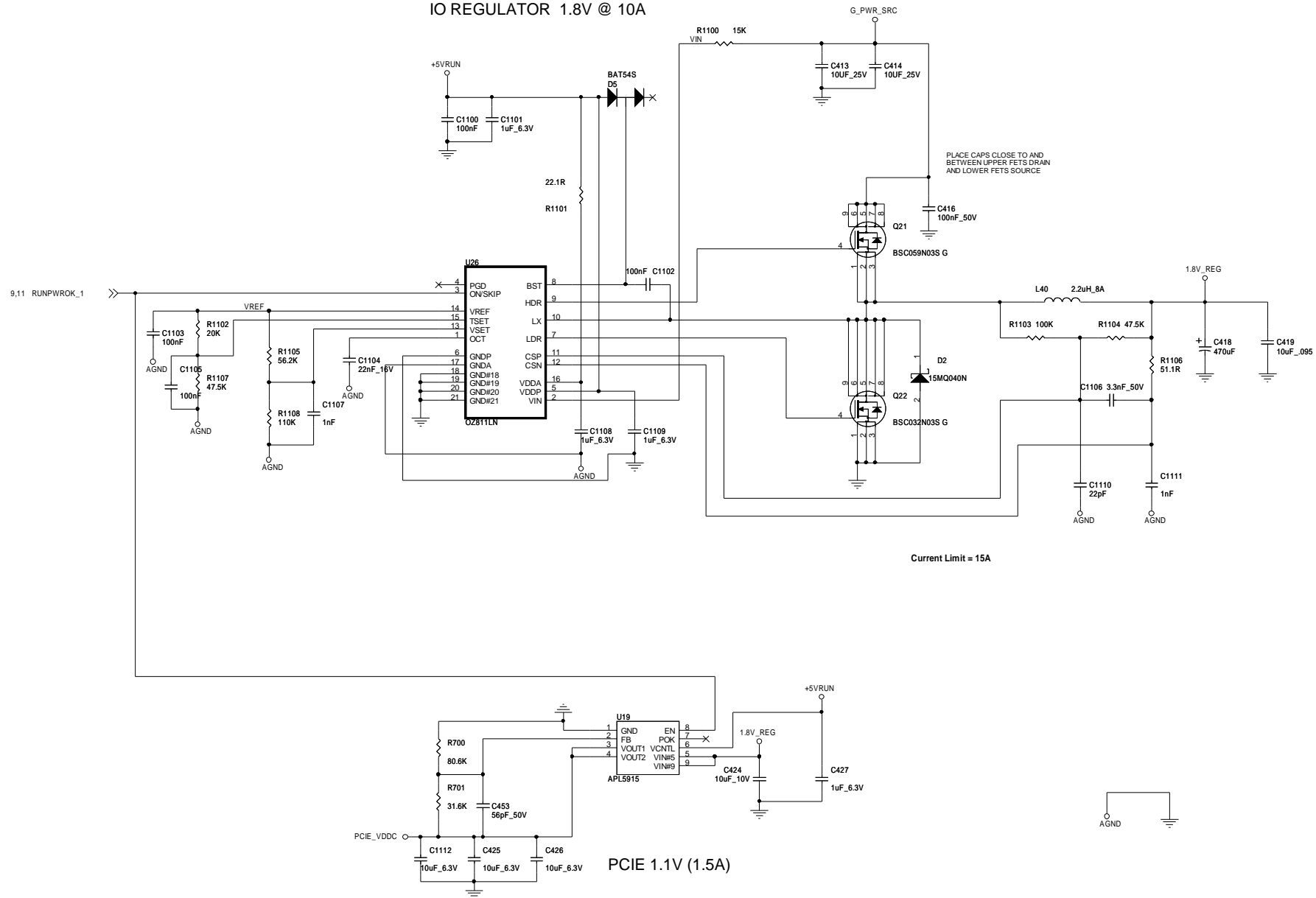



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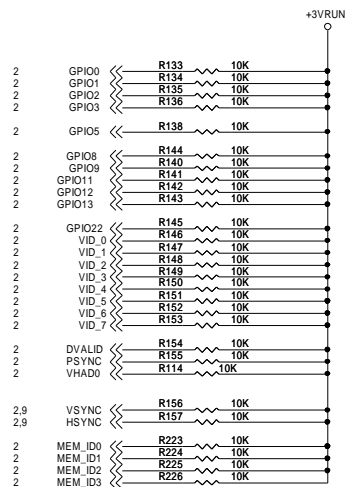
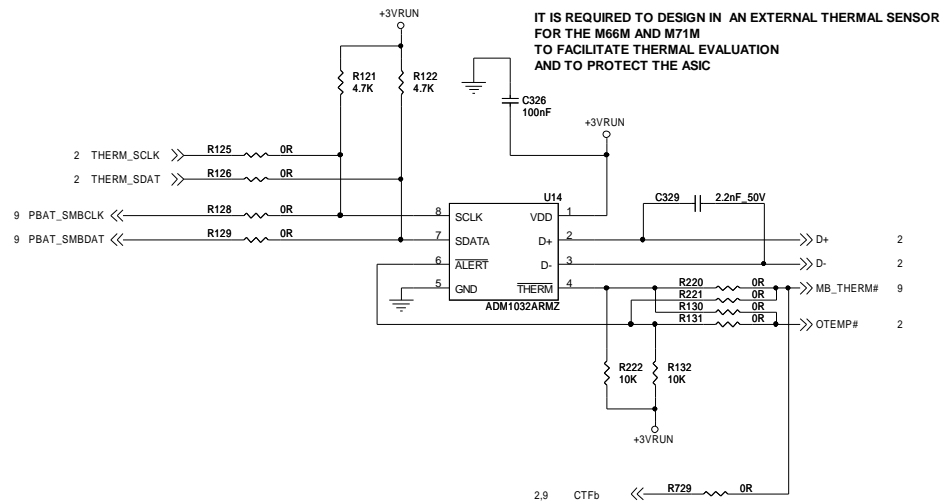
IO REGULATOR 1.8V @ 10A





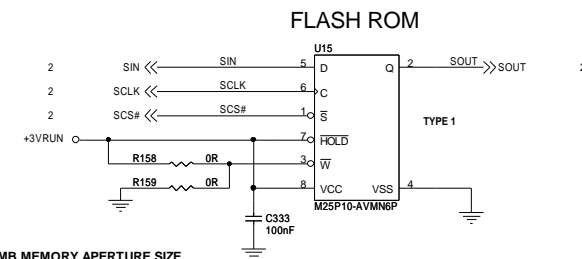
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CONFIGURATION STRAPS				RECOMMENDED SETTINGS	
ALLOW FOR PULLUP PADS FOR THESE STRAPS AND IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET				0= DO NOT INSTALL RESISTOR 1= INSTALL 10K RESISTOR X = DESIGN DEPENDANT NA = NOT APPLICABLE	
STRAPS	PIN	DESCRIPTION OF DEFAULT SETTINGS	M66M,M71M	M72M,M76M	
BIF_MSI_DIS	VID1	MESSAGE SIGNAL INTERRUPT ENABLED	NA	0	
BIF_64BAR_EN_A	VID5	64 BIT BARS DISABLED	NA	0	
TX_PWRS_ENB	GPIO0	PCIE FULL TX OUTPUT SWING	X	X	
TX_DEEMPH_EN	GPIO1	PCIE TRANSMITTER DE-EMPHASIS ENABLED	1	1	
PLL_IBIAS_RD_0	GPIO5	(M66/71)BIAS CURRENT FOR PCIE PHY PLL LSBIT (M72/76)RSVD	1	0	
BIOS_ROM_EN	GPIO_22_ROMCSB	ENABLE EXTERNAL BIOS ROM	1	1	
ROMIDCFG(3:0)	GPIO[13:11,9]	SERIAL ROM TYPE OR MEMORY APERATURE SIZE SELECT	X X X X	X X X X	
VIP_DEVICE_STRAP_ENA	VSYNC	IGNORE VIP DEVICE STRAPS	X	X	
BIF_VGA_DIS	PSYNC	VGA ENABLED	NA	0	
MEM_TYPE	UNUSED GPIO	MEMORY TYPE,MAKE AND SIZE INFO	X X X	X X X	

ATI RESERVED CONFIGURATION STRAPS											
ALLOW FOR PULLUP PADS FOR THESE STRAPS AND IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET											
VID0	VID2	VID3	VID4	VID6	VID7	HSYNC	DVALID	GPIO2	GPIO3	GPIO5	GPIO6
PULLUP PADS ARE NOT REQUIRED FOR THESE STRAPS BUT IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET											
GPIO_28_TDO	GENERICC	GPIO21_BB_EN	GPIO_23_CLKREQB (DRIVES LOW DURING RESET)	H2SYNC	V2SYNC						



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5		4		3		2		1	
D			Title M8X 512MB DDR2			Schematic No. 105-B37431-00B		Date: Friday, October 19, 2007	
			REVISION HISTORY						NOTE: This schematic represents the PCB, it does not represent any specific SKU. For Stuffing options (component values, DNI, ? please consult the product specific BOM. Please contact AMD representative to obtain latest BOM closest to the application desired.
	Sch Rev	Date	REVISION DESCRIPTION						
	0 00A	04/10/06	PROTOTYPE BASED ON 105-B24731-00C						
	1 00B	02/10/07	2 Regulators were changed, CTF						
C									
B									
A									
5		4		3		2		1	