

TABLE OF CONTENTS

Page Description

Page Description

2010/11/30
 1. P.21 ADD dual VBIOS
 2. P.29 ADD 6262
 3. P.30 remove NVVDD PWM
 3. P31~P33 add V222-2.1 NVVDD
 4. P.35 add thermal IC
 5. add page number

2010/12/01
 1.change U6006 Vcc
 2. ADD MEC8
 3. page.32~33 remove NVDD mlcc
 4. remove page.34 MEC1.5
 5. remove MEC8
 6. change L18 L19 to OL4-7670001
 7. add thermal IC in PAGE.35

2010/12/02
 1. remove LB2 LB3 in PAGE.26
 2. remove MEC1.3
 3. remove M11 M12 PAGE.5
 4. L17 to OL4-7670001
 5. remove page 31.R898 R942 R253 R251
 6. C97 C94 C137 C145 C104 C118 to 0805
 7.ADD C655
 8. remove PAGE13. FBA MLCC

12/06
 1.remove R517 page25.

12/07
 1.Add C705 C689 C583 C584 C1063

12/28: remove DUAL DVI J3A/B and J1
 add J3 J7
 J2 change to miniHDMI

12/30 modify DUAL VBIOS
 1/3 REMOVE AVDD
 1/4 add I/O EMI bridge

1/13 add R942 R946

REV	VARIANT	N/PPN	ASSEMBLY
B	BASE	600-11263-BASE-QS1	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO. STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU0000	600-11263-0000-QS1	P1263 GF105-275 128MB GDDR5 32Mx32 DVI-VGA+HDMI+DP
2	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
3	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
4	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
5	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
6	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
7	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
8	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
9	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
10	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
11	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
12	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
13	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
14	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
15	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>

Title V257			
Size	Document Number	Rev	<RevCode>
Date	Thursday, January 13, 2011	Sheet	1 of 1

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	Table of Contents

NVIDIA CORPORATION			
2701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95050, USA			
NV_PN	6002-11263-BASE-QS1	PAGE	
PCB REV	P1263-A022	DATE	14-NOV-2010
BOM REV	A		

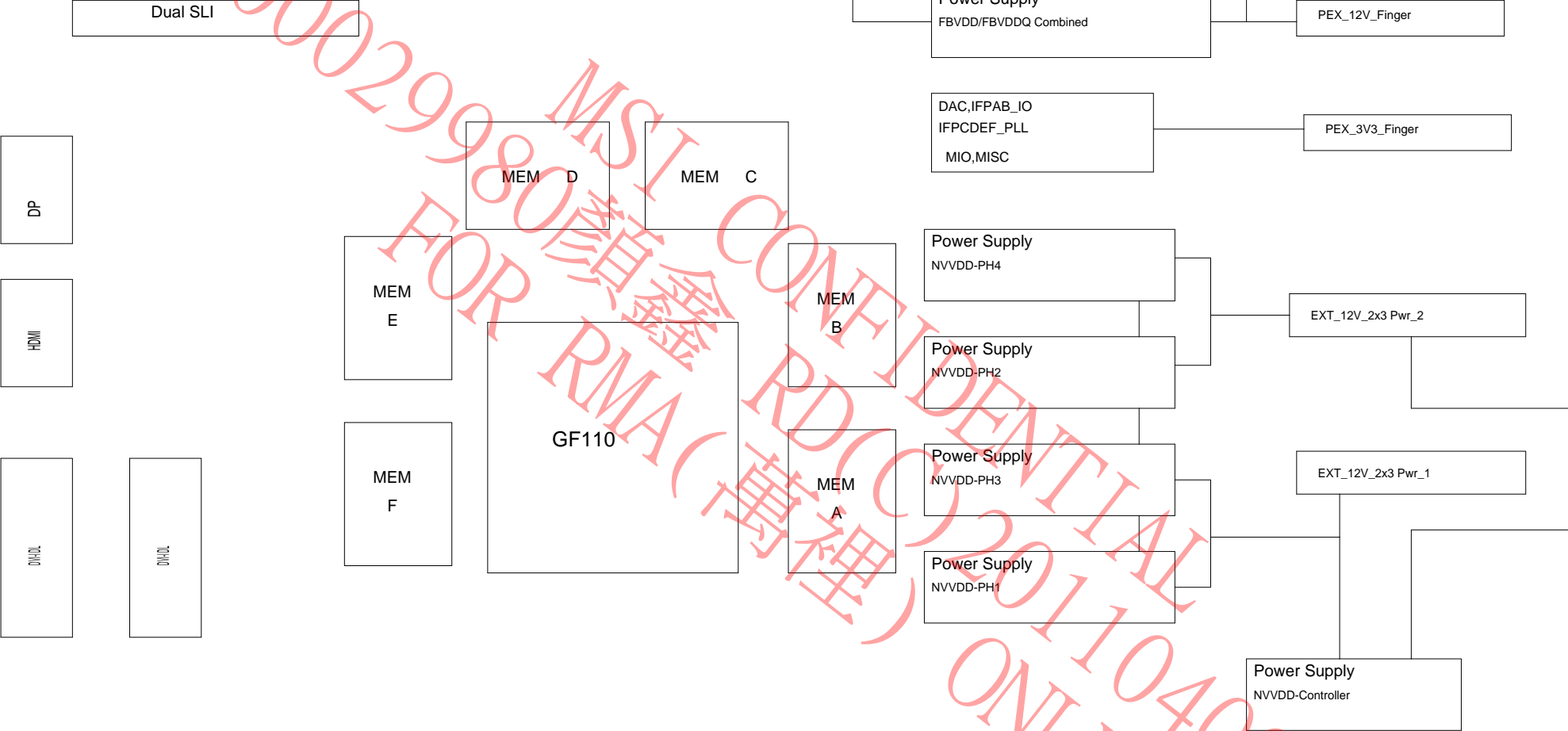


ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS". THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

Block Diagram

Power Sequence

5V - Always On
3V3 - Always On
12V_F - Always On
12V_PEX6_EXT1 - Always On
12V_PEX6_EXT2 - Always On
NVVDD -Input_Pex_Power_Rail_Enable
+ Thermal_Shutdown
FBVDD/FBVDDQ -NVVDD_PGOOD
PEX_VDD -Following FBVDDQ



Title			
V257			
Size	Document Number	Rev	<RevCode>
Customer			
Date	Tuesday, January 04, 2011	Sheet	2 of 1

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS". THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	Block Diagram

NVIDIA CORPORATION			
2701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95050, USA			
NV_PN	602-11263-BASE-QS1	PAGE	
PCB REV	P1263-A02	DATE	14-NOV-2010
BCM REV	A		

PCI Express 2.0



Title		
V267		
Size	Document Number	Rev
Custom6		<Rev Code>
Date:	Tuesday, January 04, 2011	Sheet 6 of 1

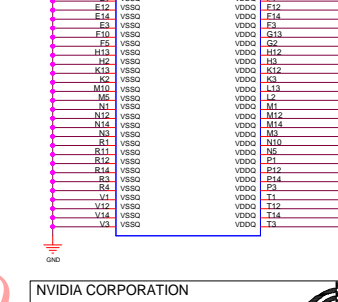
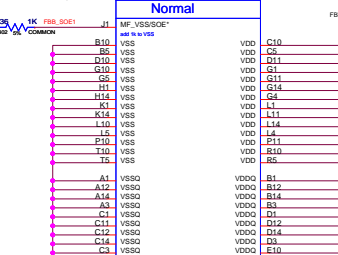
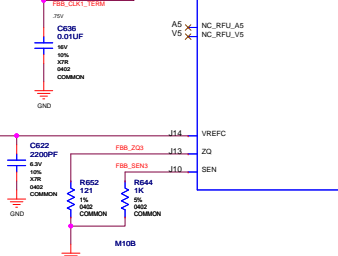
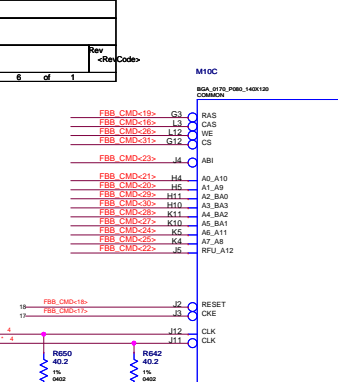
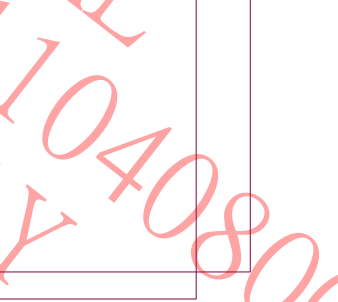
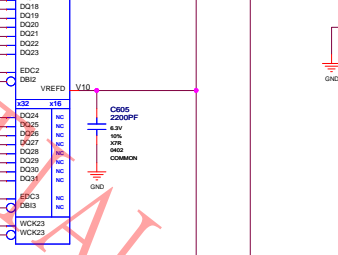
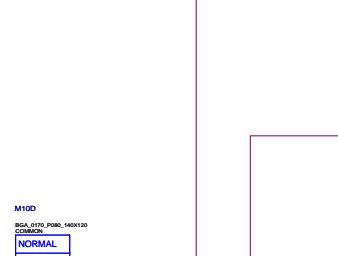
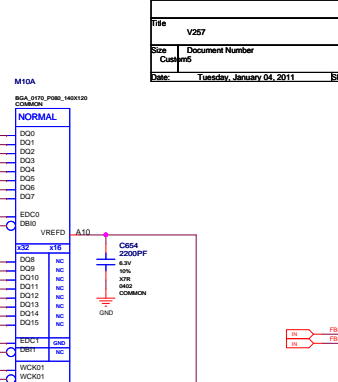
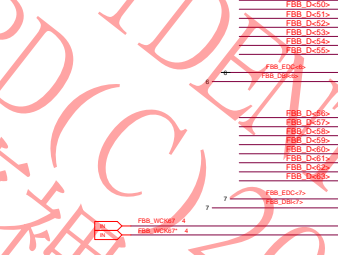
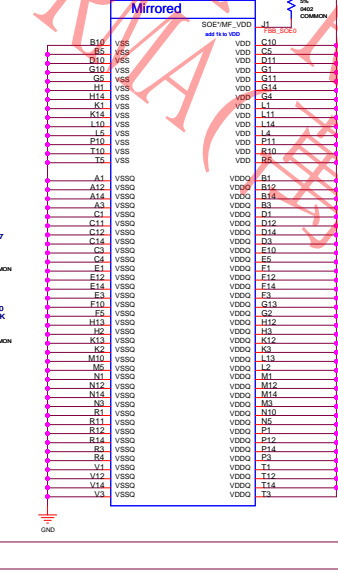
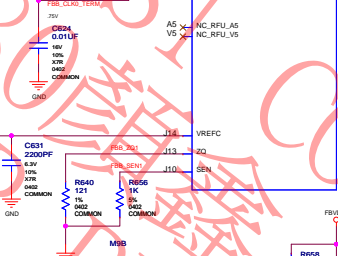
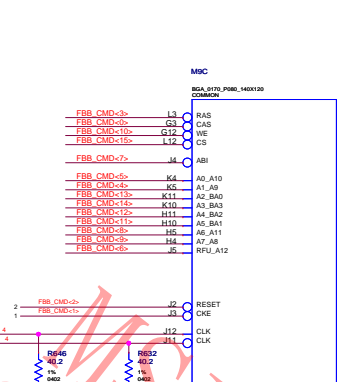
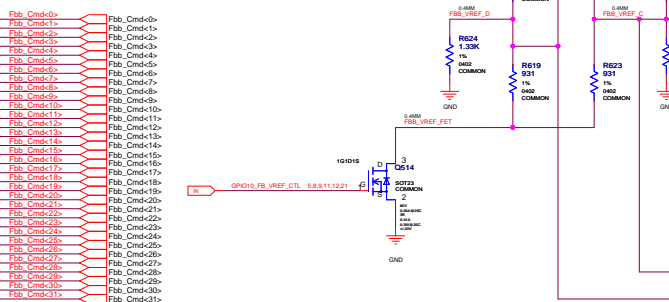
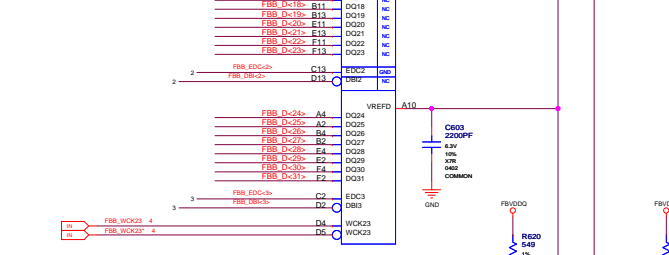
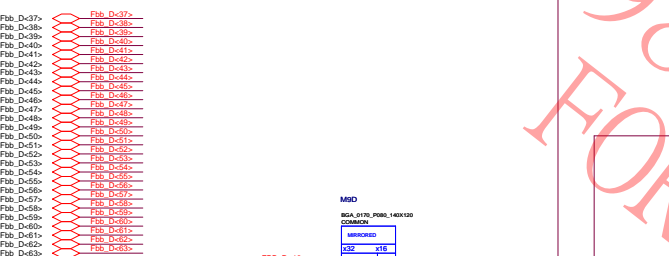
00029980 MSI CONFIDENTIAL
FOR RMA (萬裡) RD(C) 20110408001 ONLY


ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS". THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

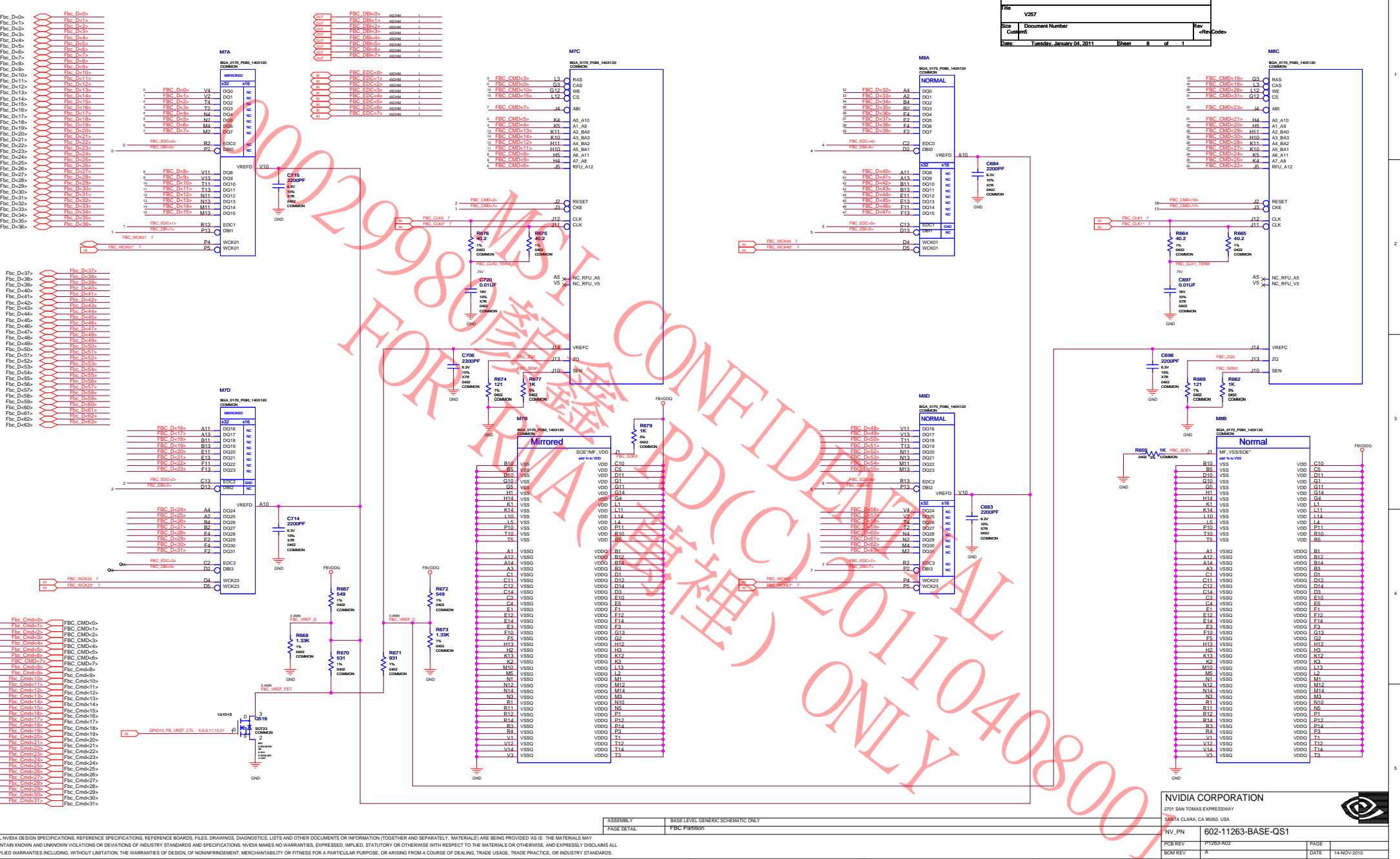
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	FBA Partition

NVIDIA CORPORATION			
2701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95050, USA			
NV_PN	602-11263-BASE-QS1		
PCB REV	P1263-A02	PAGE	
BCM REV	A	DATE	14-NOV-2010



[illegible]

NVIDIA CORPORATION			
2701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95050, USA			
NV_PN		602-11263-BASE-QS1	
PCB REV	T1263-A02	PAGE	
BOM REV	A	DATE	14-NOV-2010



ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	FBC Partition

NVIDIA CORPORATION

2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA

NV_PN	602-11263-BASE-QS1
-------	--------------------

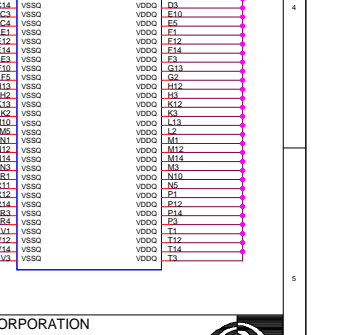
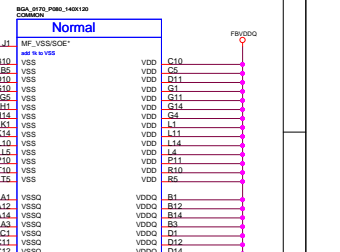
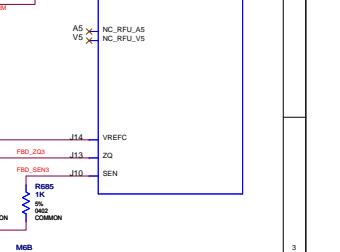
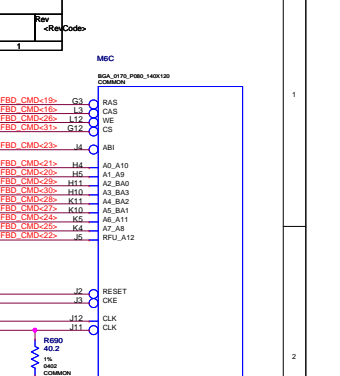
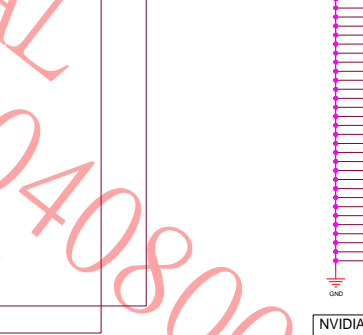
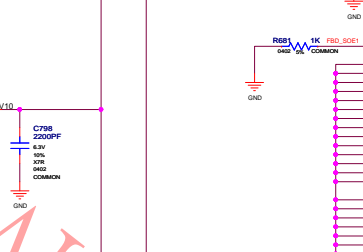
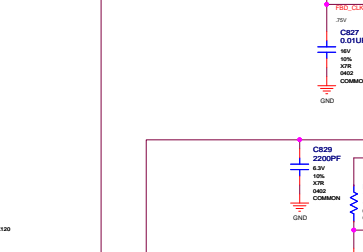
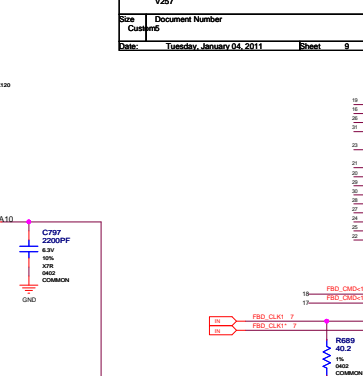
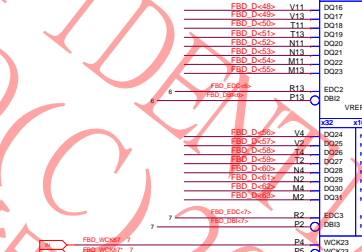
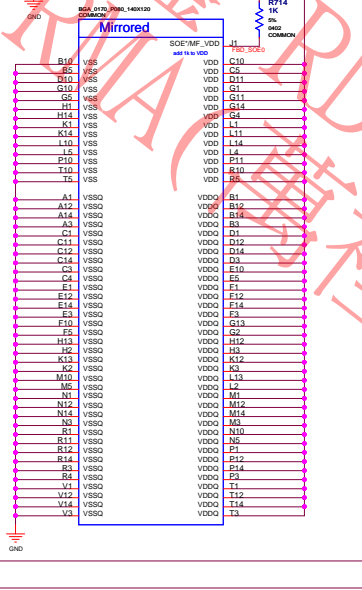
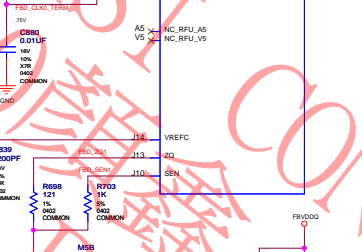
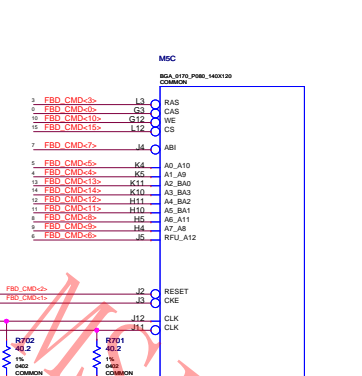
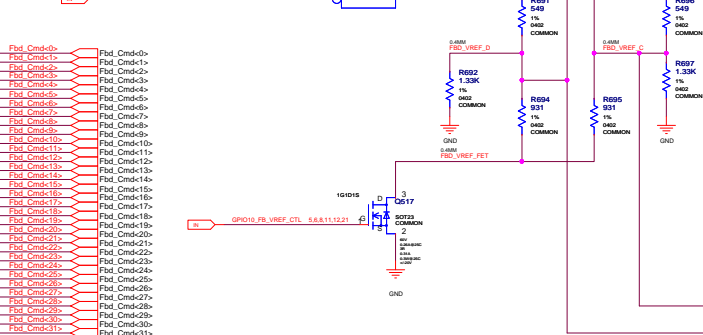
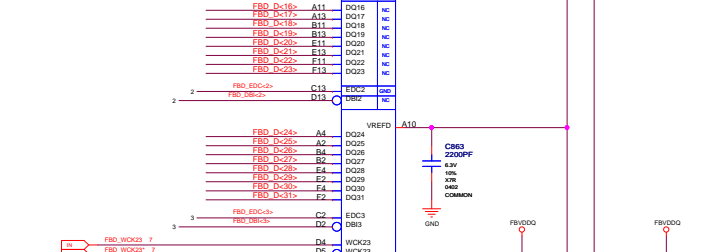
PCB REV	P1263-A02
---------	-----------


BOM REV	A
---------	---



Figure 10: Pin connections for the WCK01 module. The diagram shows two pin headers, WCK01 and WCK01, connected to various components. On the left, pins are labeled with functions like FBD D0-0, FBD D1-1, etc., and FBD WCK01-7. On the right, pins are labeled with functions like FBD D0-0, FBD D1-1, etc., and FBD WCK01-7. A central block labeled 'MSA' contains a table of pin connections. A 'VREF' section shows a voltage divider circuit with a 10k resistor and a 100k resistor connected to a 3.3V source. A 'FBD CLK0-7' section shows a 7-bit bus connection.

MSA	Pin	Function	Pin	Function
0	FBD D0-0	V4	0	FBD D0-0
1	FBD D1-1	V2	1	FBD D1-1
2	FBD D2-2	V0	2	FBD D2-2
3	FBD D3-3	T2	3	FBD D3-3
4	FBD D4-4	N2	4	FBD D4-4
5	FBD D5-5	M2	5	FBD D5-5
6	FBD D6-6	D2	6	FBD D6-6
7	FBD D7-7	M2	7	FBD D7-7
8	FBD D8-8	D2	8	FBD D8-8
9	FBD D9-9	M2	9	FBD D9-9
10	FBD D10-10	D2	10	FBD D10-10
11	FBD D11-11	M2	11	FBD D11-11
12	FBD D12-12	D2	12	FBD D12-12
13	FBD D13-13	M2	13	FBD D13-13
14	FBD D14-14	D2	14	FBD D14-14
15	FBD D15-15	M2	15	FBD D15-15
16	FBD D16-16	D2	16	FBD D16-16
17	FBD D17-17	M2	17	FBD D17-17
18	FBD D18-18	D2	18	FBD D18-18
19	FBD D19-19	M2	19	FBD D19-19
20	FBD D20-20	D2	20	FBD D20-20
21	FBD D21-21	M2	21	FBD D21-21
22	FBD D22-22	D2	22	FBD D22-22
23	FBD D23-23	M2	23	FBD D23-23
24	FBD D24-24	D2	24	FBD D24-24
25	FBD D25-25	M2	25	FBD D25-25
26	FBD D26-26	D2	26	FBD D26-26
27	FBD D27-27	M2	27	FBD D27-27
28	FBD D28-28	D2	28	FBD D28-28
29	FBD D29-29	M2	29	FBD D29-29
30	FBD D30-30	D2	30	FBD D30-30
31	FBD D31-31	M2	31	FBD D31-31
32	FBD D32-32	D2	32	FBD D32-32
33	FBD D33-33	M2	33	FBD D33-33
34	FBD D34-34	D2	34	FBD D34-34
35	FBD D35-35	M2	35	FBD D35-35
36	FBD D36-36	D2	36	FBD D36-36
37	FBD D37-37	M2	37	FBD D37-37
38	FBD D38-38	D2	38	FBD D38-38
39	FBD D39-39	M2	39	FBD D39-39
40	FBD D40-40	D2	40	FBD D40-40
41	FBD D41-41	M2	41	FBD D41-41
42	FBD D42-42	D2	42	FBD D42-42
43	FBD D43-43	M2	43	FBD D43-43
44	FBD D44-44	D2	44	FBD D44-44
45	FBD D45-45	M2	45	FBD D45-45
46	FBD D46-46	D2	46	FBD D46-46
47	FBD D47-47	M2	47	FBD D47-47
48	FBD D48-48	D2	48	FBD D48-48
49	FBD D49-49	M2	49	FBD D49-49
50	FBD D50-50	D2	50	FBD D50-50
51	FBD D51-51	M2	51	FBD D51-51
52	FBD D52-52	D2	52	FBD D52-52
53	FBD D53-53	M2	53	FBD D53-53
54	FBD D54-54	D2	54	FBD D54-54
55	FBD D55-55	M2	55	FBD D55-55
56	FBD D56-56	D2	56	FBD D56-56
57	FBD D57-57	M2	57	FBD D57-57
58	FBD D58-58	D2	58	FBD D58-58
59	FBD D59-59	M2	59	FBD D59-59
60	FBD D60-60	D2	60	FBD D60-60
61	FBD D61-61	M2	61	FBD D61-61
62	FBD D62-62	D2	62	FBD D62-62
63	FBD D63-63	M2	63	FBD D63-63
64	FBD D64-64	D2	64	FBD D64-64
65	FBD D65-65	M2	65	FBD D65-65
66	FBD D66-66	D2	66	

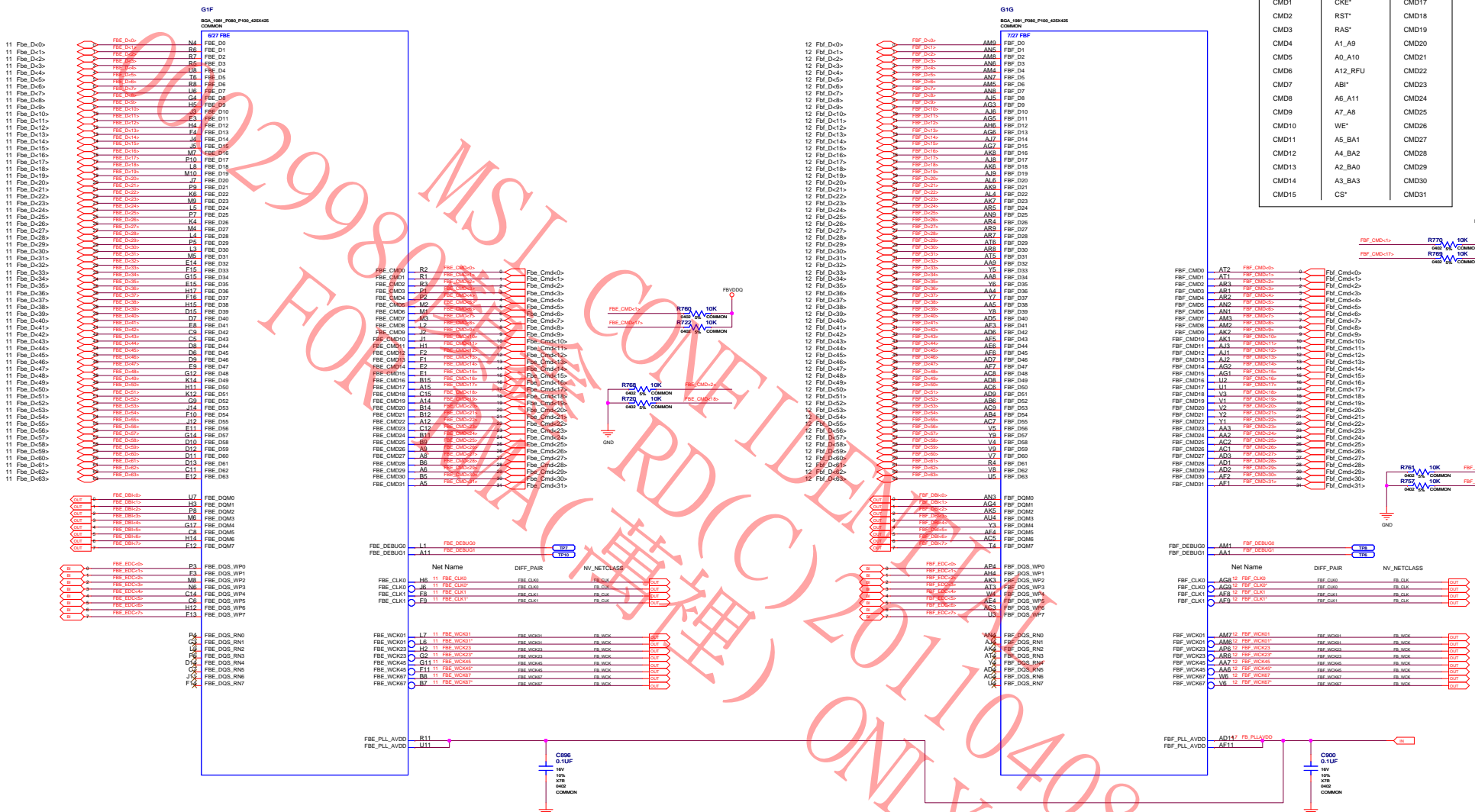


NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA			
NV_PN	602-11263-BASE-QS1		
PCB REV	P1263-A02	PAGE	
BOM REV	A	DATE	14-NOV-2010

MSJS(Andy)

GPU Framebuffer: Partition E/F

GDDR5 CMD Mapping		
0..31	CMD	32..63
CMD0	CAS*	CMD16
CMD1	CKE*	CMD17
CMD2	RST*	CMD18
CMD3	RAS*	CMD19
CMD4	A1, A9	CMD20
CMD5	A0, A10	CMD21
CMD6	A12, RFU	CMD22
CMD7	AB1*	CMD23
CMD8	A6, A11	CMD24
CMD9	A7, A8	CMD25
CMD10	WE*	CMD26
CMD11	A5, BA1	CMD27
CMD12	A4, BA2	CMD28
CMD13	A2, BA0	CMD29
CMD14	A3, BA3	CMD30
CMD15	CS*	CMD31



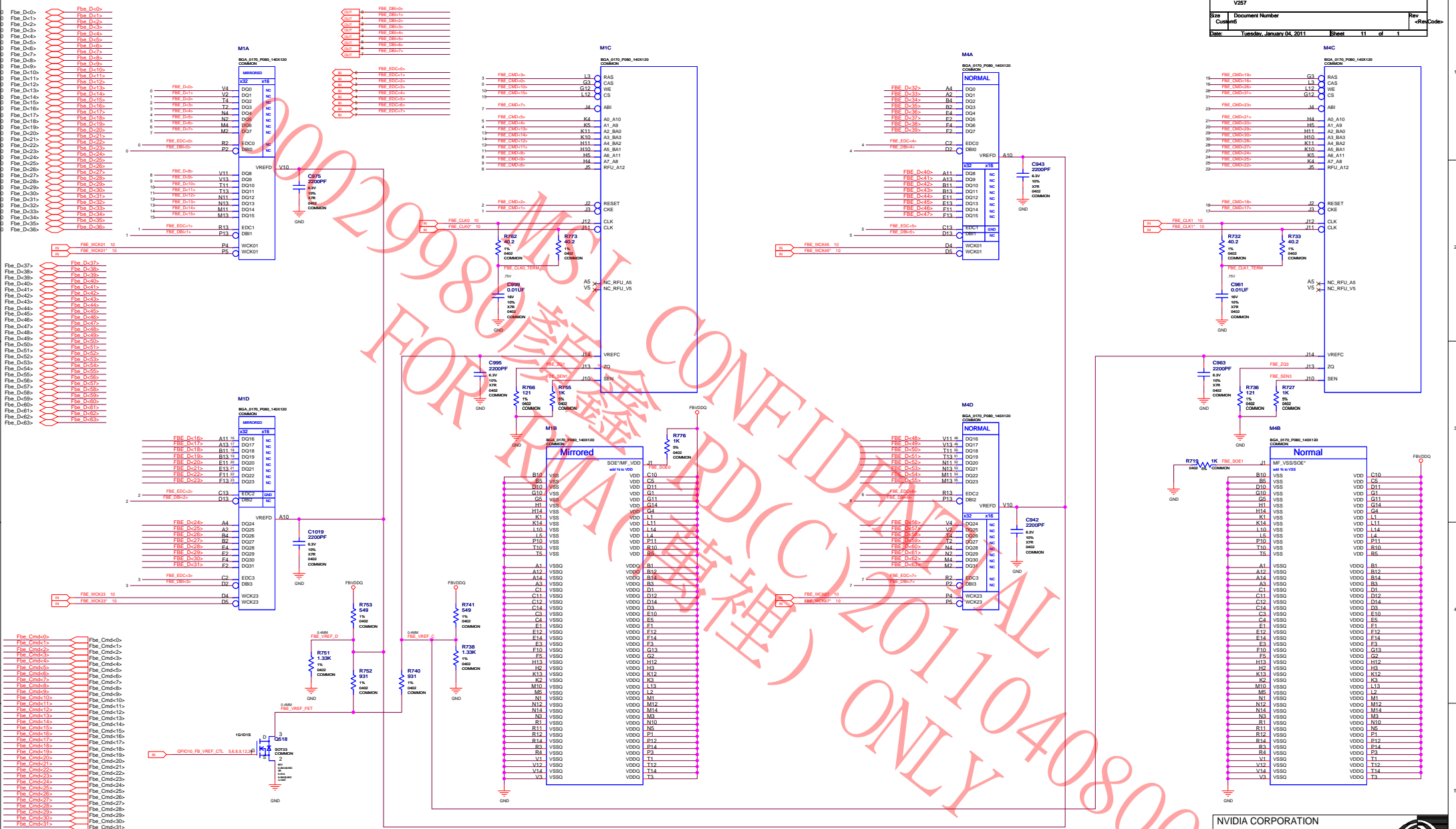
Title V257		Document Number		Rev			
Size Changed		Date		Issue Code			
Date Tuesday, January 04, 2011		Sheet 10		of 1			

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS". THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, OR INDUSTRY STANDARDS.

ASSEMBLY	GPU Framebuffer: Partition: EIF	NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA		
PAGE DETAIL		NV_PN 602-11263-BASE-QS1		
PCB REV	P1263-ADZ	PAGE		
BCM REV	A	DATE	14-NOV-2010	

MEMORY: FBE Partition

Title			
V257			
Size	Document Number		Rev
Customer			<Rev>Code>
Date:	Tuesday, January 04, 2011	Sheet	11 of 1



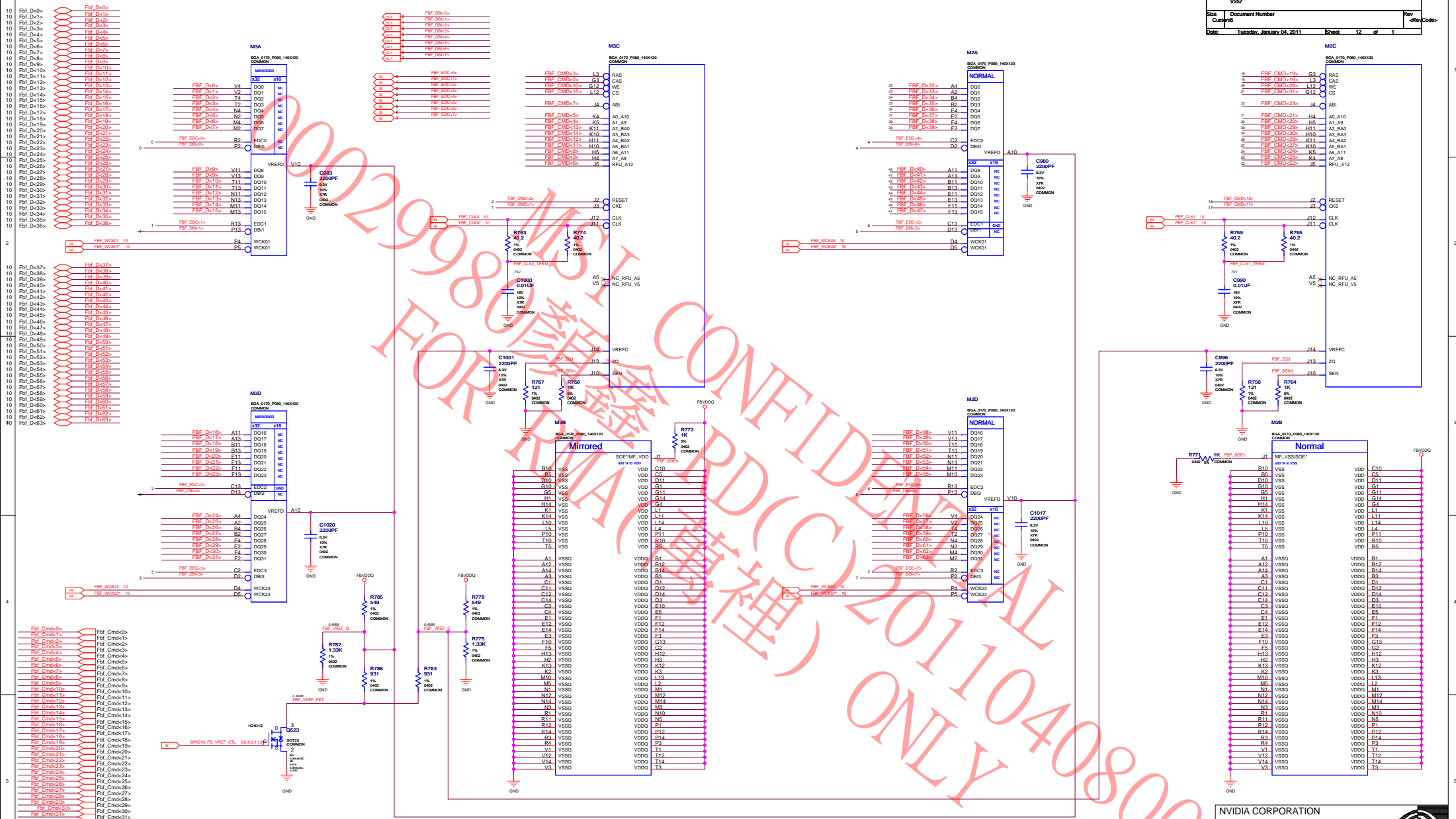
ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS". THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE, WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	FBE PARTITION

NVIDIA CORPORATION	
2701 SAN TONIA EXPRESSWAY	
SANTA CLARA, CA 95050, USA	
INV. PN	602-11263-BASE-QS1
PCB REV	P1263-002
DOCX REV	A
PAGE	14-NOV-2010

MS7S(Andy)

MEMORY: FBF Partition



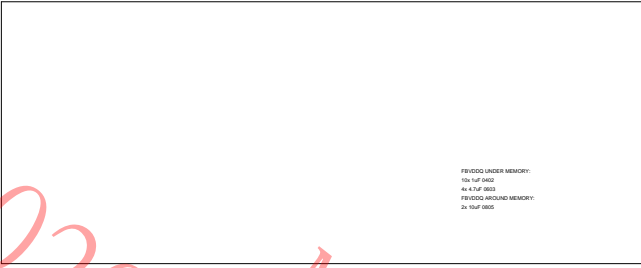
NVIDIA CORPORATION	
2701 SAN TOMAS EXPRESSWAY	
SANTA CLARA, CA 95050, USA	
INV. PN	602-11263-BASE-QS1
PCB REV	P1263-A02
BOX REV	A
PAGE	14-NOV-2010

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE, WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

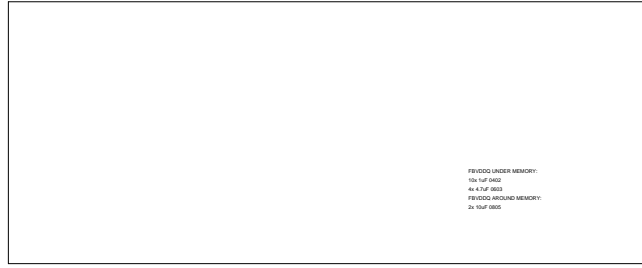
MS7S(Andy)

GDDR5 Memory Decoupling

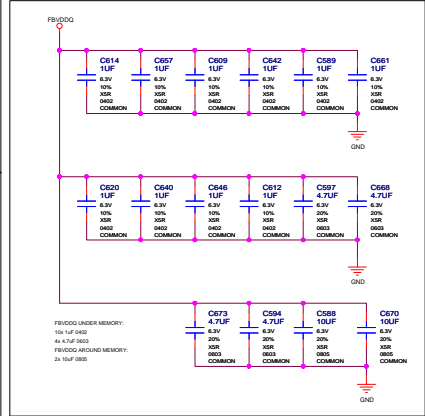
DECOUPLING: FBA<31..0>



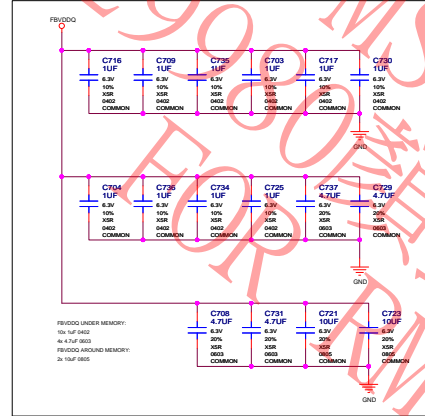
DECOUPLING: FBA<63..32>



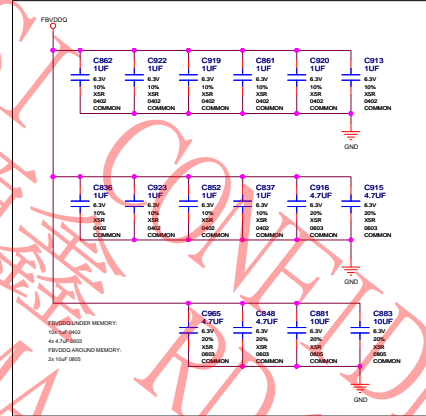
DECOUPLING: FBB<31..0>



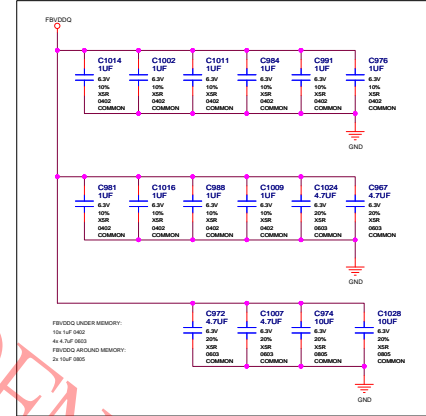
DECOUPLING: FBC<31..0>



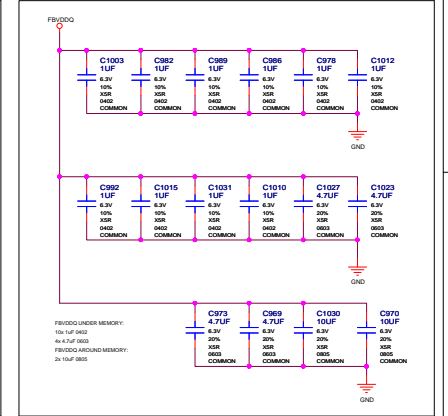
DECOUPLING: FBD<31..0>



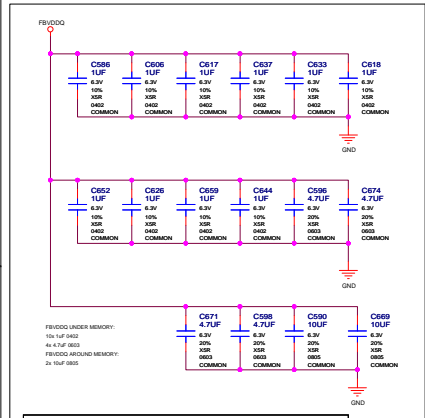
DECOUPLING: FBE<31..0>



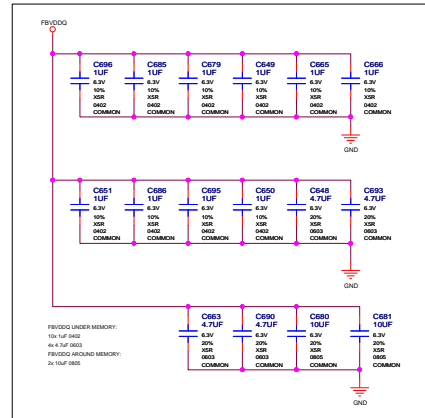
DECOUPLING: FBF<31..0>



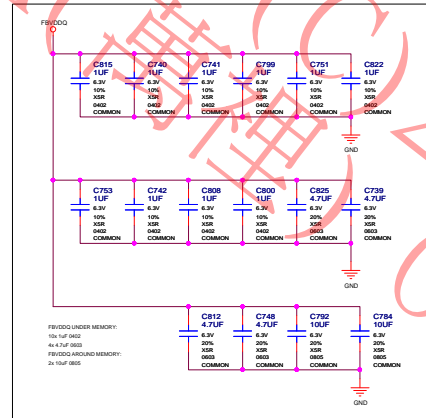
DECOUPLING: FBB<63..32>



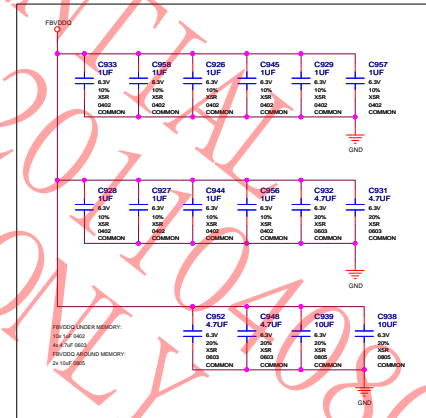
DECOUPLING: FBC<63..32>



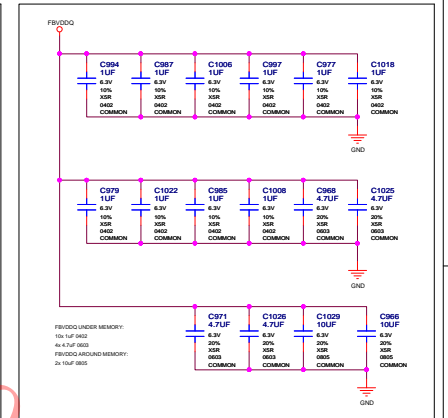
DECOUPLING: FBD<63..32>



DECOUPLING: FBE<63..32>



DECOUPLING: FBF<63..32>



Title	V287
Size	Document Number
Category	Rev
Date	Tuesday, January 04, 2011
Sheet	13 of 1

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS". THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE, WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	GDDR5 Memory Decoupling

NVIDIA CORPORATION

2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA

INV_PN 602-11263-BASE-QS1

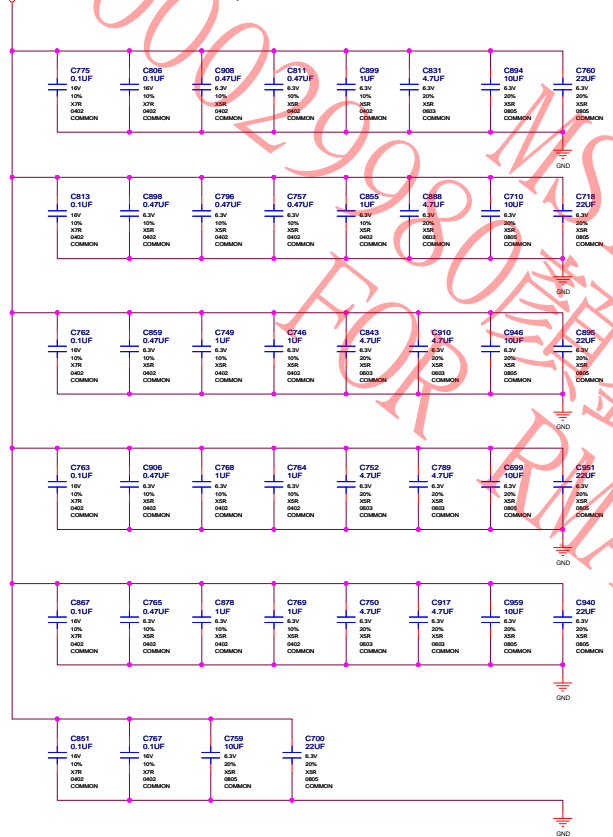
PCB REV P1263-A02

BOX REV A

PAGE	14
DATE	14-NOV-2010

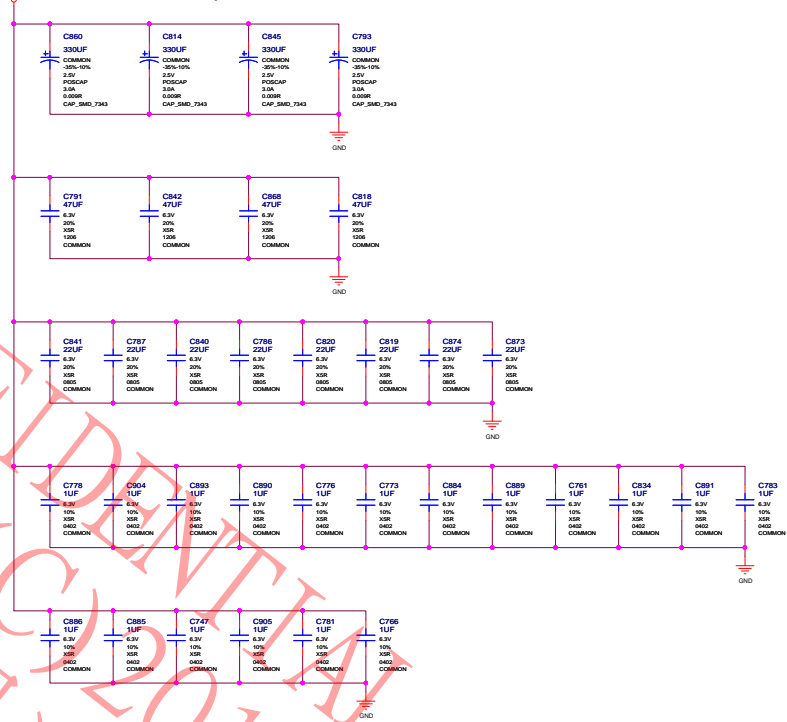
GPU DECOUPLING (NVVDD,FBVDDQ)

FBVDDQ Decap under GPU



FBVDDQ under GPU
0.1uF 0402 X7R x8
0.47uF 0402 X5R x8
1uF 0402 X5R x8
FBVDDQ around GPU
4.7uF 0603 X5R x8
10uF 0805 X5R x6
22uF 0805 X5R x6

NVVDD Decap under GPU



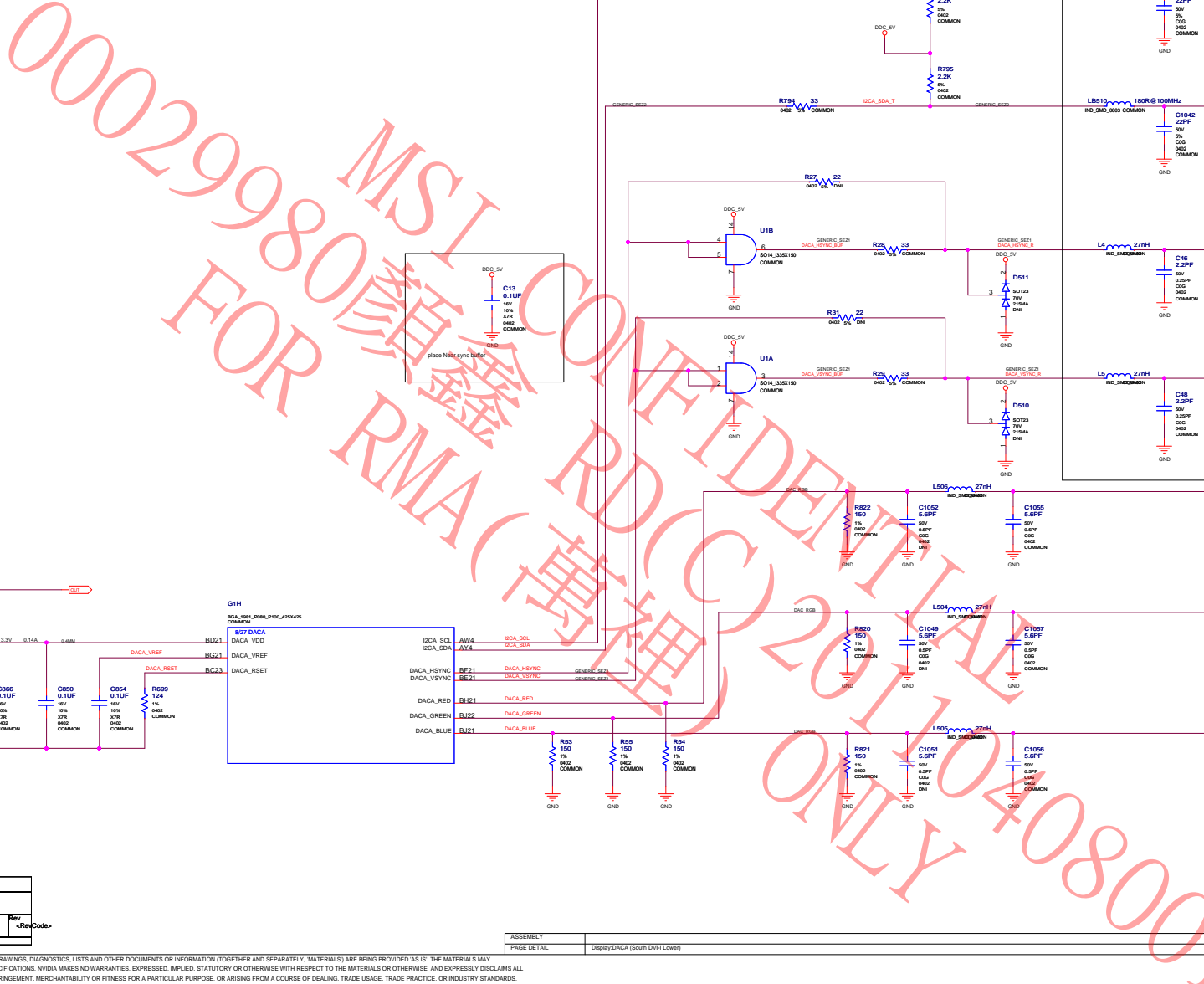
Title		V257
Site	Document Number	Rev
Customer		<RevCode>
Date	Tuesday, January 04, 2011	Sheet 14 of 1

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	GPU Decoupling(NVVDD,FBVDDQ)

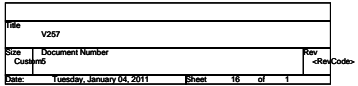
NVIDIA CORPORATION	
2701 SAN TOMAS EXPRESSWAY	
SANTA CLARA, CA 95050, USA	
INV_PN	602-11263-BASE-QS1
PCB REV	P1263-A02
BCM REV	A
PAGE	
DATE	14-NOV-2010



ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.



ASSEMBLY	
PAGE DETAIL	Display:DACA (South DVI-I Lower)



ASSEMBLY	
PAGE DETAIL	Display:DACB (South DVI-I Upper


NV_PN	602-11263-BASE-QS1
-------	--------------------

PCB REV	P1263-A02
---------	-----------

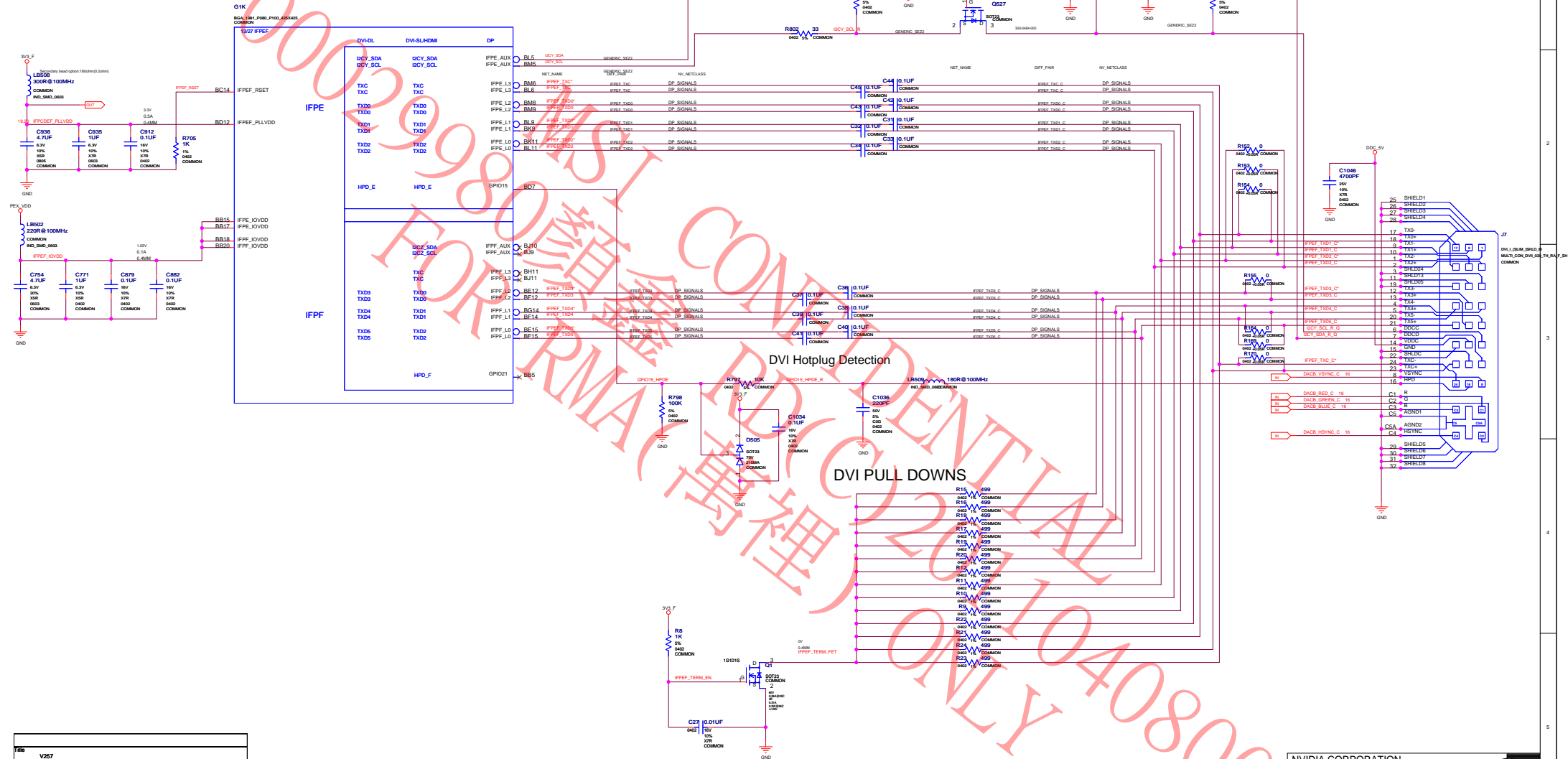
BOM REV	A	DATE	14-NOV-2010
---------	---	------	-------------

	H
--	---

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	IFP A/B Interface (South DVI Lower)

NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA			
NV_PN		602-11263-BASE-QS1	
PCB REV	P1263-A0Z	PAGE	
BOM REV	A	DATE	14-NOV-2010


IFP E/F Interface (South DVI-I Upper)



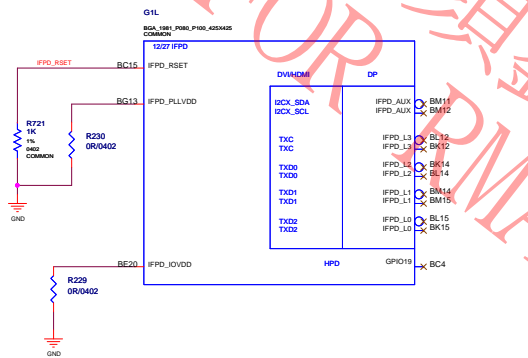
Title				V257			
Size	Document Number						Rev
Custom6							<Rev Code>
Date:	Tuesday, January 04, 2011			Sheet	18	of	1

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	IFP E/F Interface(South DVI-I Upper

NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA			
NV_PN		602-11263-BASE-QS1	
PCB REV	P1263-A02	PAGE	
BOM REV	A	DATE	14-NOV-2010


- 1.DP AUX to DP connector: AUX AC coupled
- 2.DP AUX to DP-DVI dongle: AUX pass through



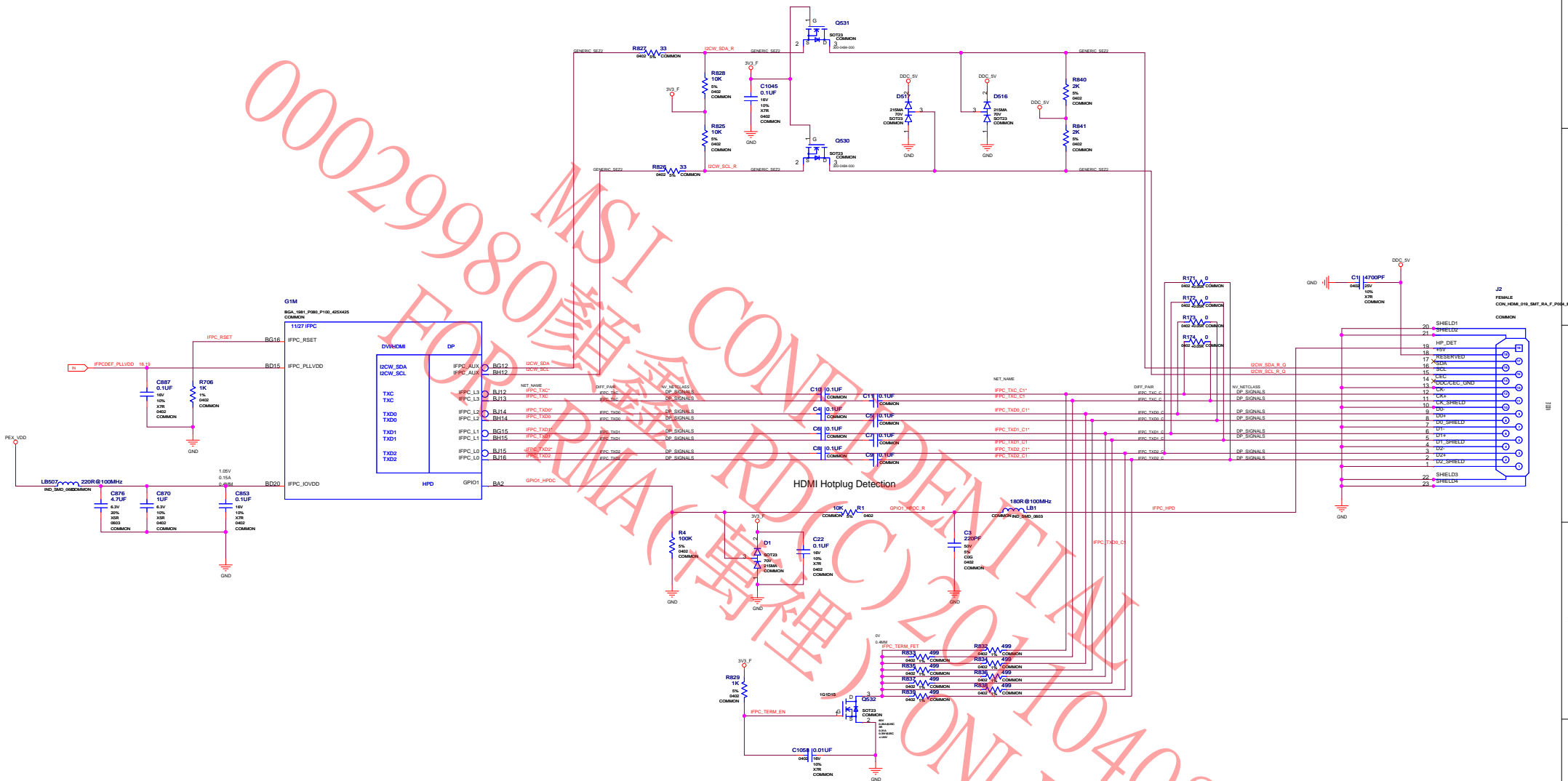
Title V257			
Size Custom6			Rev <Rev Code>
Date Tuesday, January 04, 2011		Sheet 19 of 1	

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	
PAGE DETAIL	IFPD (North DP)

NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA			
NV_PN		602-11263-BASE-QS1	
PCB REV	P1263-A02	PAGE	
BOM REV	A	DATE	14-NOV-2010

IFPC Interface (Mid HDMI)



File	V267
Size	Document Number
Date	Tuesday, January 04, 2011
Sheet	20 of 1
Rev	<rev>
Code	

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS". THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	IFPC Interface (Mid HDMI)

NVIDIA CORPORATION

2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA

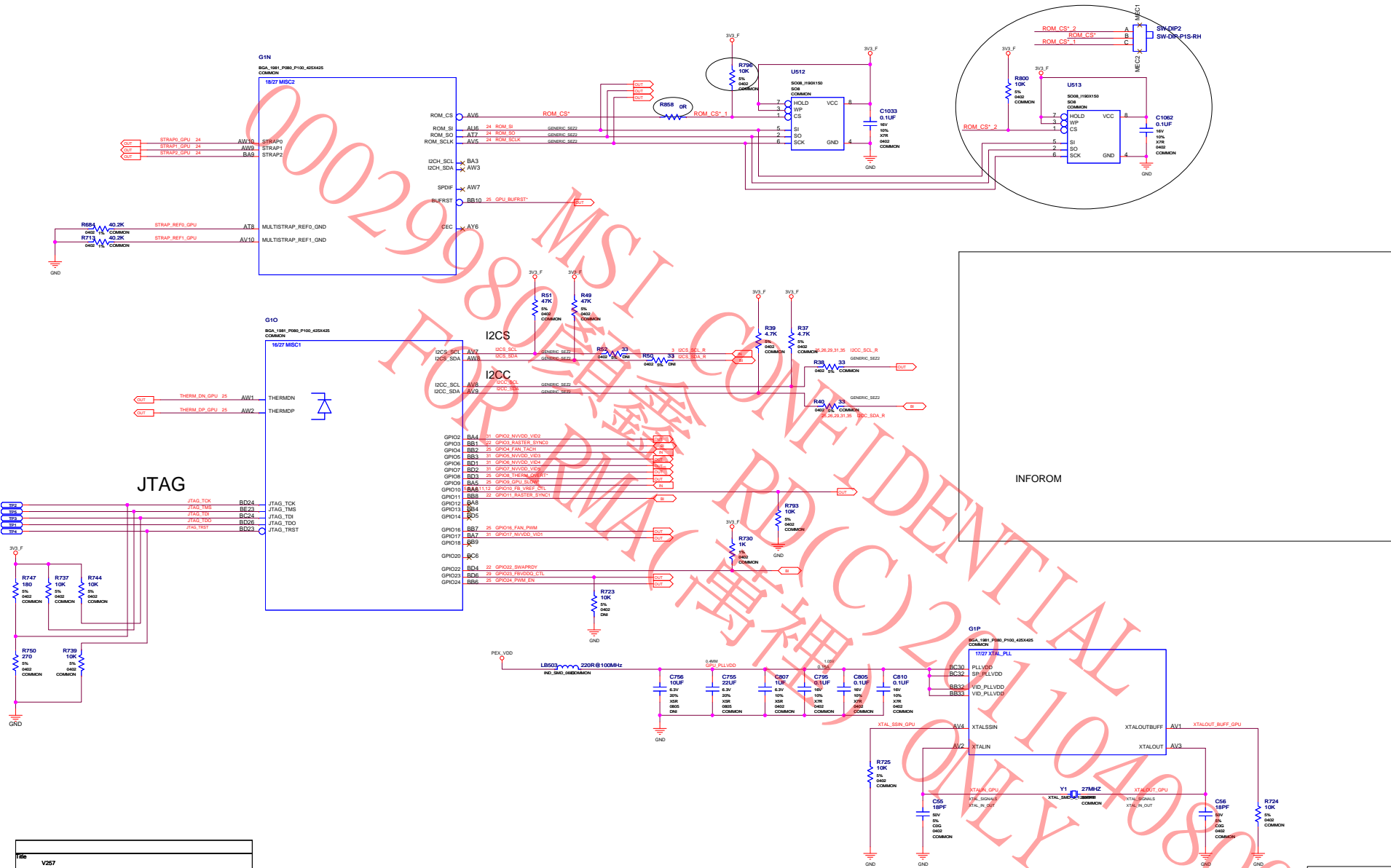
NV_PN 602-11263-BASE-QS1

PCB REV P1263-A02

BCM REV A

PAGE	
DATE	14-NOV-2010

MISC: GPIO, I2C, ROM, and XTAL



File	V257
Size	Document Number
Customer	Rev
Date	Tuesday, January 24, 2011
Sheet	21 of 1

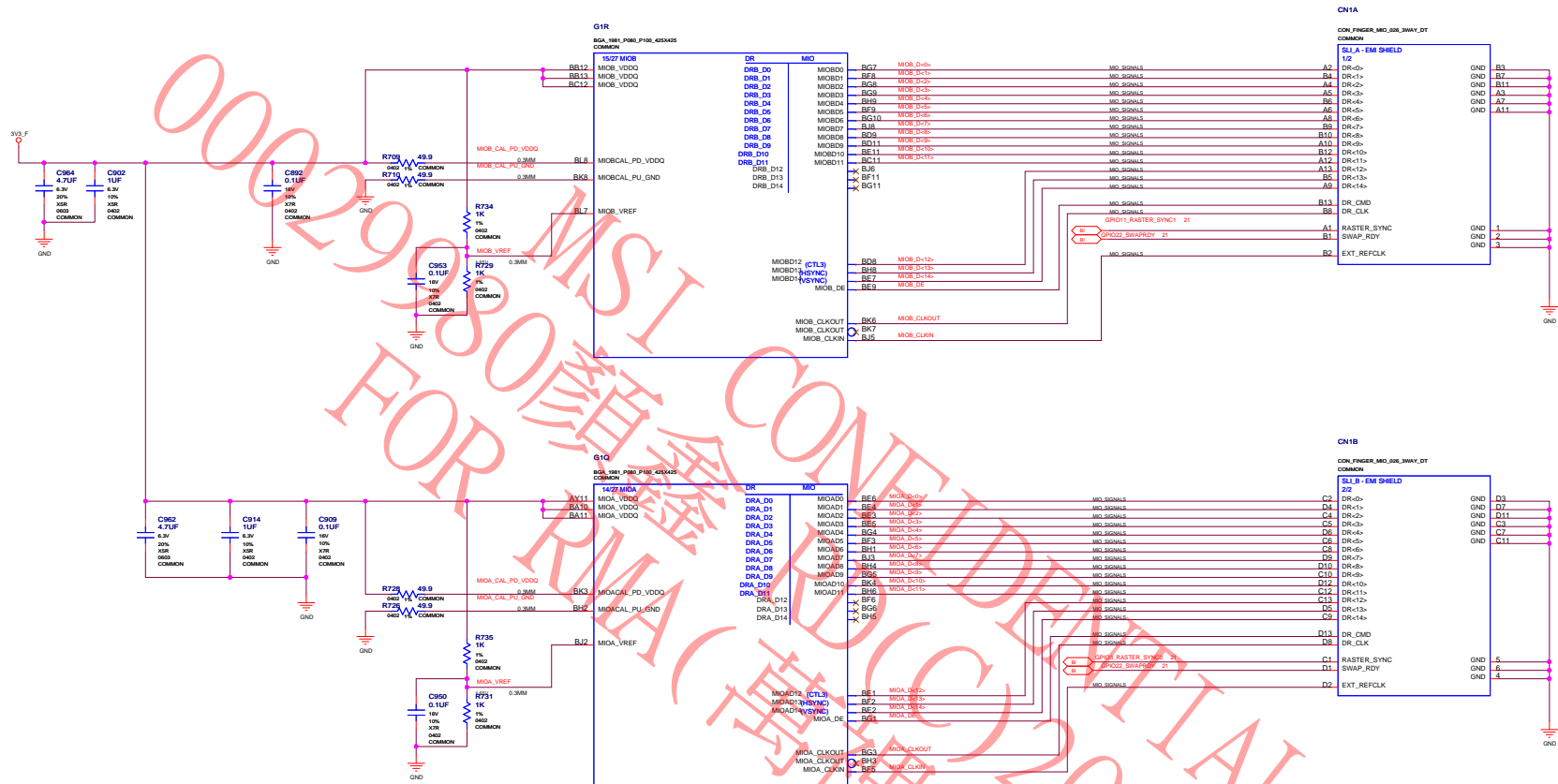
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	MISC: GPIO, I2C, ROM, and XTAL

NVIDIA CORPORATION	
2701 SAN TOMAS EXPRESSWAY	
SANTA CLARA, CA 95050, USA	
NV_PN	602-11263-BASE-QS1
PCB REV	P1263-A02
BCM REV	A
PAGE	
DATE	14-NOV-2010



ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.


Multi-use IO(MIO) Interface



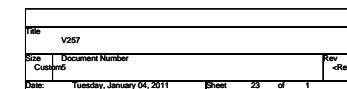
File V257			
Size	Document Number		Rev
Customs			<Rev Code>
Date:	Tuesday, January 04, 2011	Sheet 22 of 1	

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	Multi-use IO(MIO) Interface

NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA			
NV_PN		602-11263-BASE-QS1	
PCB REV	P1263-A02	PAGE	
BOM REV	A	DATE	14-NOV-2010

G1S
BIGA_1981_P080_P100_425X425



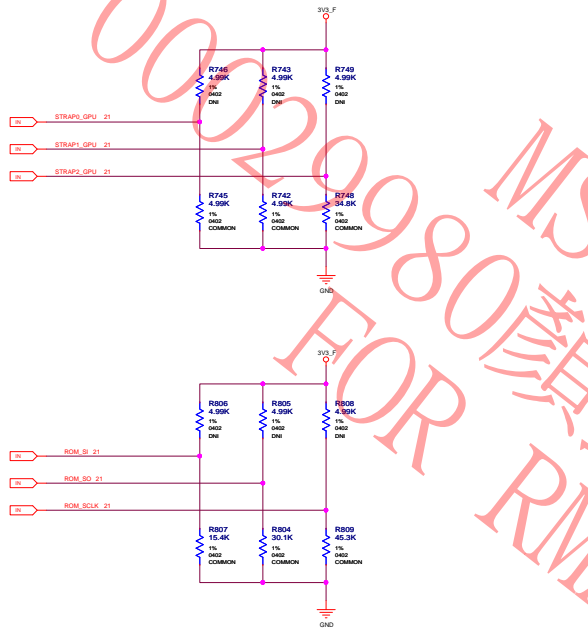
PCB REV	P1263-A02	PAGE	
BOM REV	A	DATE	14-NOV-2010

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS". THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	GPU POWER CONNECTIONS

MSJS(Andy)

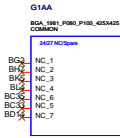
GPU Strapping Configuration



ROM_SI	RAMCFG[0]	0010 32Mx32 GDDR5 320-68 Hynix
	RAMCFG[1]	0011 32Mx32 GDDR5 320-68 Samsung
	RAMCFG[2]	0110 64Mx32 GDDR5 320-68 Hynix
	RAMCFG[3]	0111 64Mx32 GDDR5 320-68 Samsung
ROM_SO	VGA_DEVICE	1 0=Disabled(3D Only) 1=Enabled(VGA Display Device)
	SMB_ALT_ADDR	0
	FB[0]_BAR_SIZE	1 128MB
	XCLK_417	0
ROM_SCLK	PEX_PLL_EN_TERM100	1 ENABLE 0 DISABLED
	SLOT_CLK_CFG	1 ENABLE
	SUB_VENDOR	1 Dedicated BIOS
	PCI_DEVID_EXT	0 0x108
STRAP0	USER_BIT [3..0]	0000 => 5K PD Default
STRAP1	3GIO_PADCFG_LUT_ADR	0000 => 5K PD Desktop Default
STRAP2	PCI_DEVID [3..0]	0110 => 35K PD For 0x1086

	GND	3V3
5k	0000	1000
10k	0001	1001
15k	0010	1010
20k	0011	1011
25k	0100	1100
30k	0101	1101
35k	0110	1110
45k	0111	1111

Title			
V257			
Size	Document Number	Rev	
Cust006		<Rev Code>	
Date	Tuesday, January 24, 2011	Sheet	24 of 1



ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS". THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	GPU Strapping Configuration

NVIDIA CORPORATION			
2701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95050, USA			
NV_PN	602-11263-BASE-QS1		
PCB REV	P1263-A02	PAGE	
BCM REV	A	DATE	14-NOV-2010

ASSEMBLY	
PAGE DETAIL	MISC: Thermal / BACKDRIVE



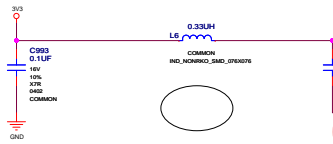
PCB REV	P1263-A02
---------	-----------

DATE	14-NOV-2010
------	-------------

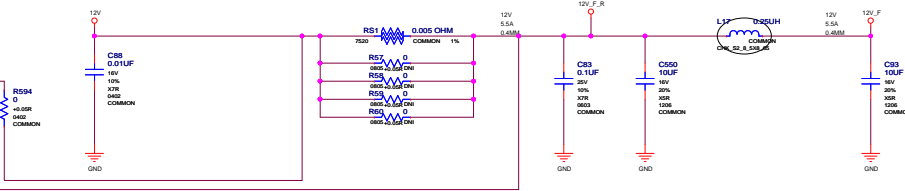
MSJS(Andy)

Power Supply: PEX PS INPUT, DETECTION LOGIC

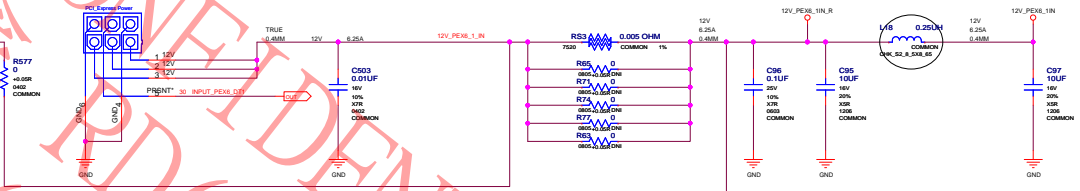
PEX_3V3 INPUT - 10W



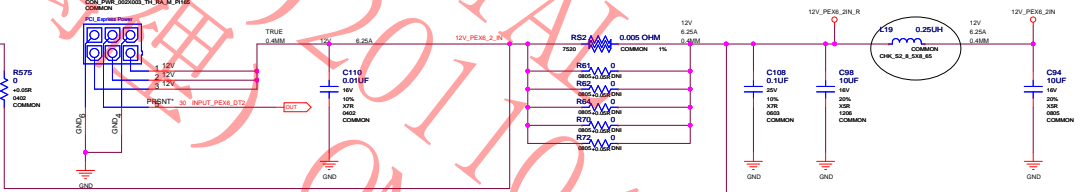
PEX_12V INPUT - 66W



PEX6 INPUT 1 - 2x3 PCIE CON 75W



PEX6 INPUT 2 - 2x3 PCIE CON 75W



File	V257
Size	Document Number
Count	Rev
Date	Tuesday, January 04, 2011
Sheet	26 of 1

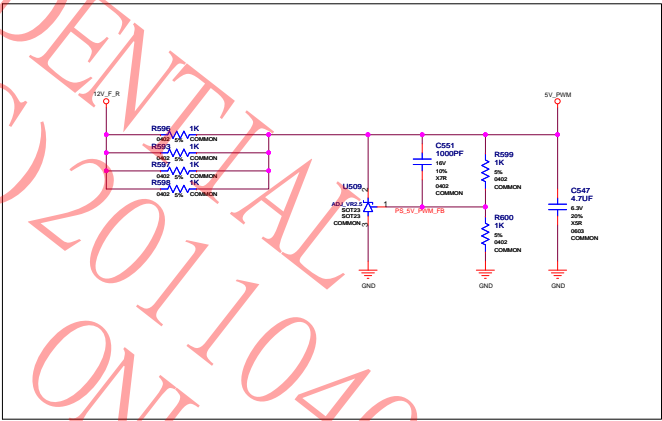
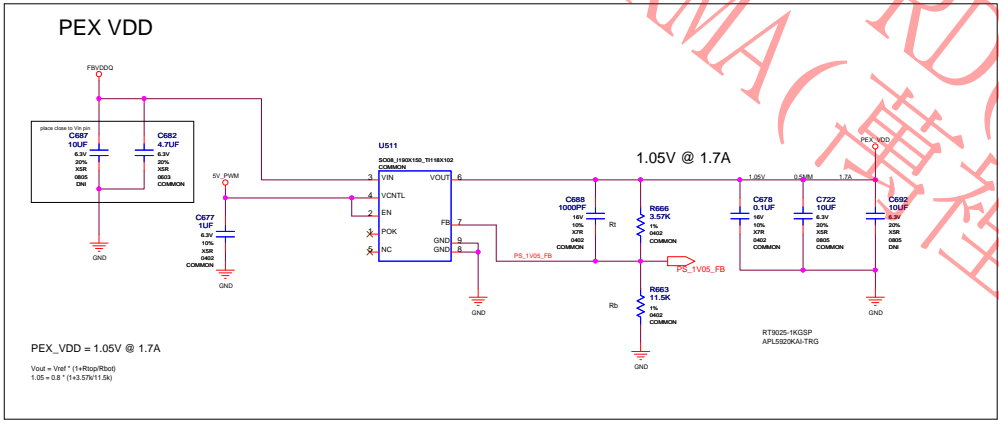
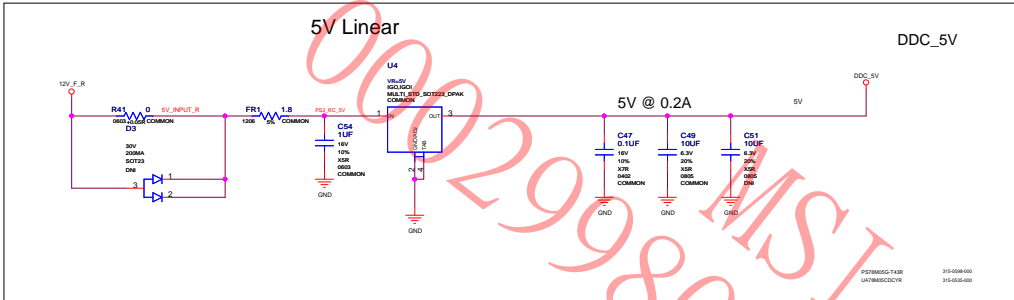
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	Power Supply PEX PS INPUT DETECTION LOGIC

NVIDIA CORPORATION	
2701 SAN TOMAS EXPRESSWAY	
SANTA CLARA, CA 95050, USA	
INV_PN	602-11263-BASE-QS1
PCB REV	P1263-A02
BCM REV	A
PAGE	14-NOV-2010



ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

Power Supply: DDC5V,PEX_VDD,5V_PWM,AVDD



Title	V257
Size	Document Number
Customer	Rev
Date	Tuesday, January 04, 2011
Sheet	27 of 1

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	Power Supply: DDC5V, PEX_VDD, 5V_PWM, AVDD
PAGE DETAIL	

NVIDIA CORPORATION	
2701 SAN TOMAS EXPRESSWAY	
SANTA CLARA, CA 95050, USA	
REV_PN	602-11263-BASE-QS1
PCB REV	P1263-A02
BCM REV	A
PAGE	
DATE	14-NOV-2010

00029980 MSI CONFIDENTIAL
FOR RMA (RD(C) 20110408001 ONLY

Title		
V257		
Size	Document Number	Rev
A	5	<RevCode>
Date:	Tuesday, January 04, 2011	Sheet 28 of 1

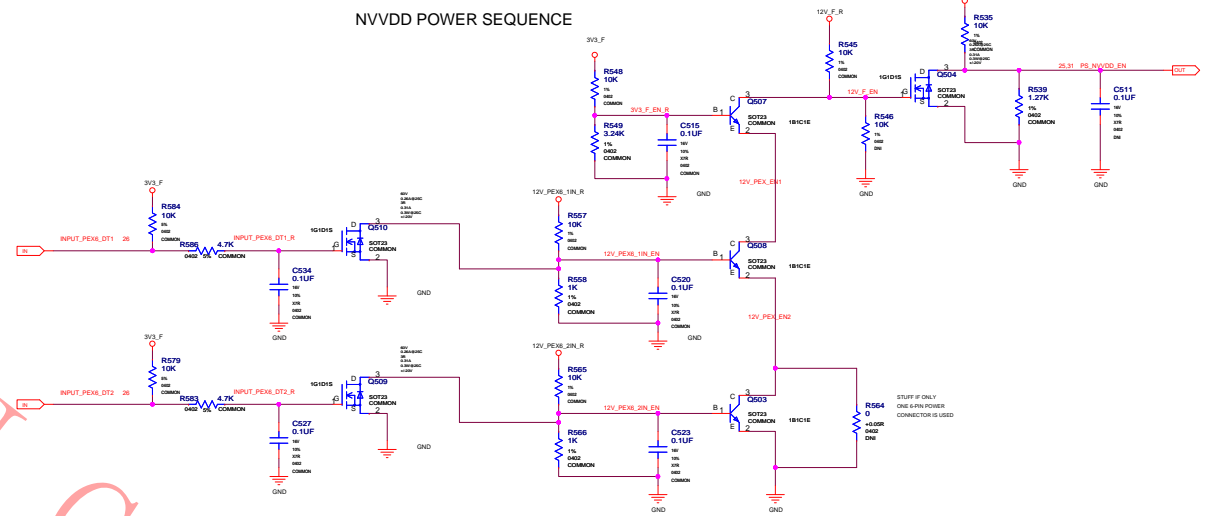
[illegible]

ASSEMBLY	
PAGE DETAIL	Power Supply: FBVDDQ



NVVDD POWER SEQUENCE

GPIO7 VID_5	GPIO6 VID_4	GPIO5 VID_3	GPIO2 VID_2	GPIO17 VID_1	VRM11.0 VID Setting
0	0	0	0	1	1.20000V
0	0	0	1	0	1.18750V
0	0	0	1	1	1.17500V
0	0	1	0	0	1.16250V
0	0	1	0	1	1.15000V
0	0	1	1	0	1.13750V
0	0	1	1	1	1.12500V
0	1	0	0	0	1.11250V
0	1	0	0	1	1.10000V
0	1	0	1	0	1.08750V
0	1	0	1	1	1.07500V
0	1	1	0	0	1.06250V
0	1	1	0	1	1.05000V
0	1	1	1	0	1.03750V
0	1	1	1	1	1.02500V
1	0	0	0	0	1.01250V
1	0	0	0	1	1.00000V
1	0	0	1	0	0.98750V
1	0	0	1	1	0.97500V
1	0	1	0	0	0.96250V
1	0	1	0	1	0.95000V
1	0	1	1	0	0.93750V
1	0	1	1	1	0.92500V
1	1	0	0	0	0.91250V
1	1	0	0	1	0.90000V
1	1	0	1	0	0.88750V
1	1	0	1	1	0.87500V
1	1	1	0	0	0.86250V
1	1	1	0	1	0.85000V
1	1	1	1	0	0.83750V
1	1	1	1	1	0.82500V



Title			
V257			
Size	Document Number		Rev
Custom6			<Rev
Date:	Tuesday, January 04, 2011	Sheet	30 of 1

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE. NVIDIA DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	Power Supply, NVVDD Controller

RLIM1=5.62K,RLIM2 = 28.7K to set SW FREQ=300KHz, OCP=400A

2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA



NV_PN	602-11263-BASE-QS1		
PCB REV	P1263-A02	PAGE	
BOM REV	A	DATE	14-NOV-2010

Power Supply: NVDD Regulator

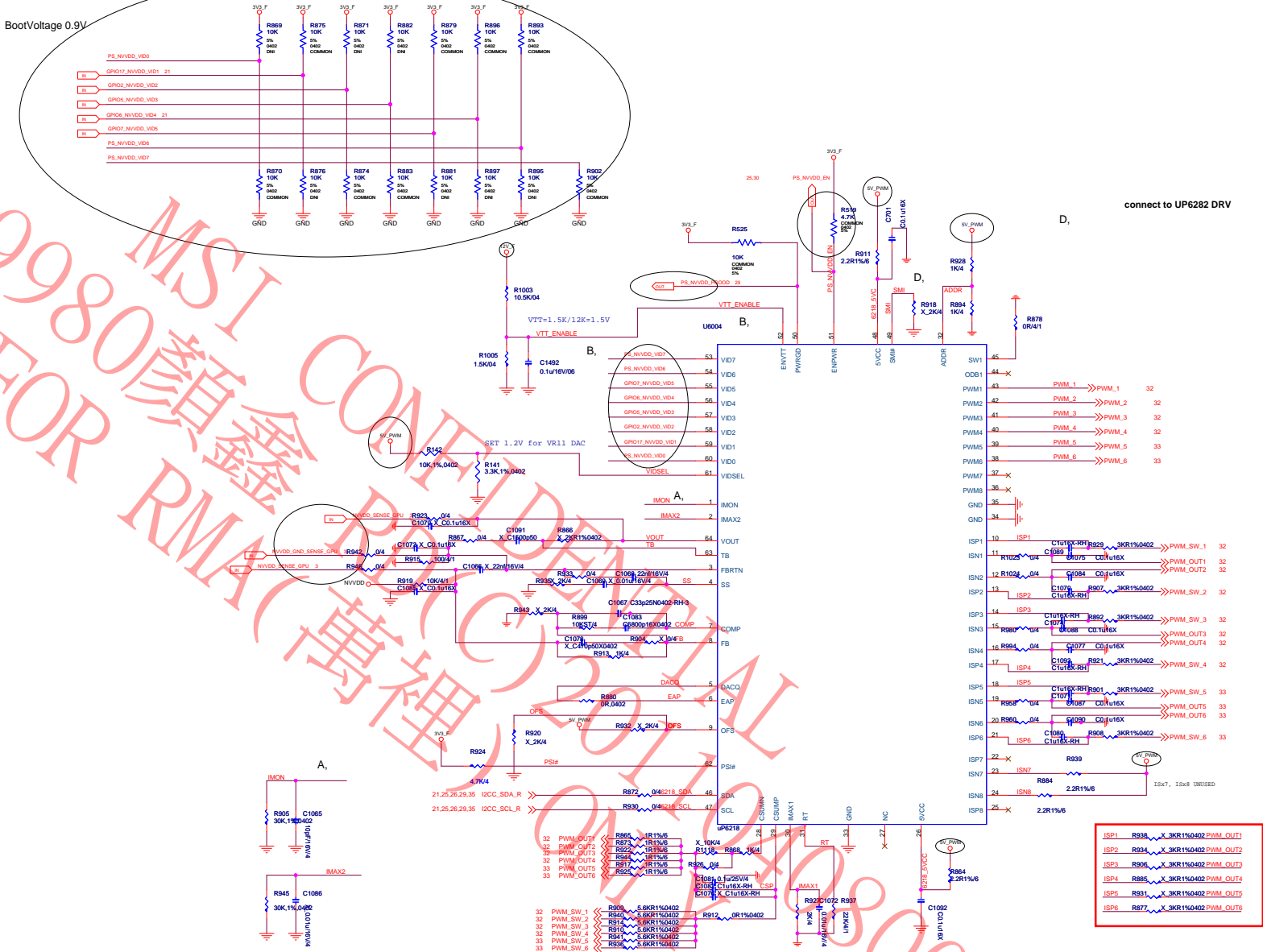
VID Table

GPIO7 VID_5	GPIO6 VID_4	GPIO5 VID_3	GPIO2 VID_2	GPIO17 VID_1	VOUT
0	0	0	0	0	1.21250V
0	0	0	0	0	1.20000V
0	0	0	0	1	1.18750V
0	0	0	1	1	1.17500V
0	0	1	0	0	1.16250V
0	0	1	0	1	1.15000V
0	0	1	1	0	1.13750V
0	0	1	1	1	1.12500V
0	1	0	0	0	1.11250V
0	1	0	0	1	1.10000V
0	1	0	1	0	1.08750V
0	1	0	1	1	1.07500V
0	1	1	0	0	1.06250V
0	1	1	0	1	1.05000V
0	1	1	1	0	1.03750V
0	1	1	1	1	1.02500V
1	0	0	0	0	1.01250V
1	0	0	0	1	1.00000V
1	0	0	1	0	0.98750V
1	0	0	1	1	0.97500V
1	0	1	0	0	0.96250V
1	0	1	0	1	0.95000V
1	0	1	1	0	0.93750V
1	0	1	1	1	0.92500V
1	1	0	0	0	0.91250V
1	1	0	0	1	0.90000V
1	1	0	1	0	0.88750V
1	1	0	1	1	0.87500V
1	1	1	0	0	0.86250V
1	1	1	0	1	0.85000V
1	1	1	1	0	0.83750V
1	1	1	1	1	0.82500V

File				
V267				
Size				
Customer				
Date: Thursday, January 13, 2011				
Sheet 31 of 1				
Rev				
<Rev Code>				

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, MATERIALS) ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS AND OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

BootVoltage 0.9V



1	
2	
3	
4	12 6.2
5	



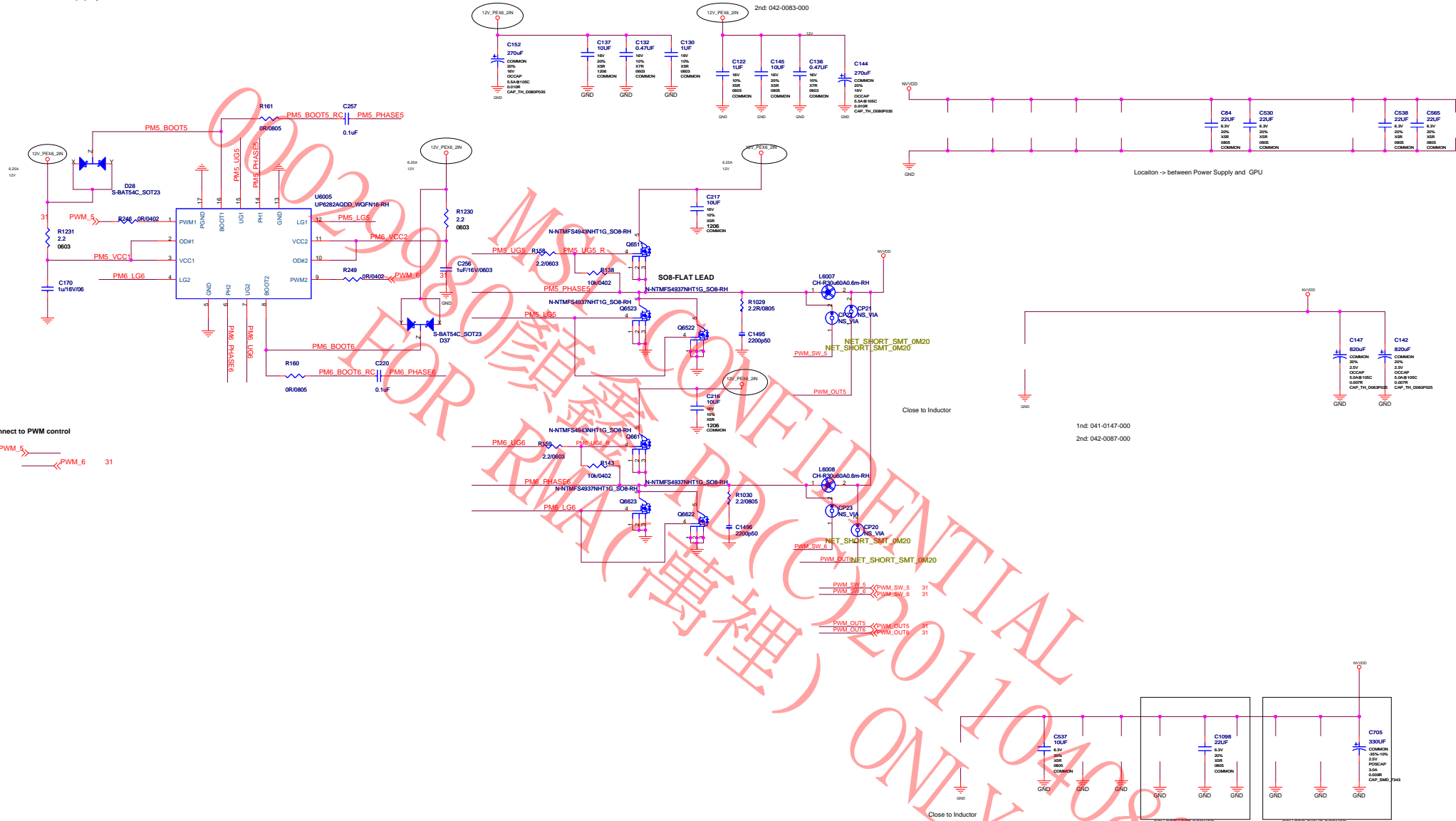
1	
2	
3	
4	12 6.2
5	

1	
2	
3	
4	12 6.2
5	

1	
2	
3	
4	12 6.2
5	

1	
2	
3	
4	12 6.2
5	

Power Supply: NVVDD PHASE 5, 6



Title				V257			
Size		Document Number					Rev
Custom6							<Rev
Date:	Tuesday, January 04, 2011		Sheet	33	of	1	

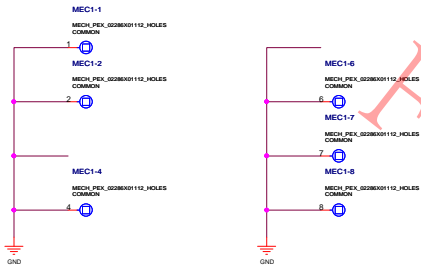
ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Power Supply: NVVDD PHASE 3, 4

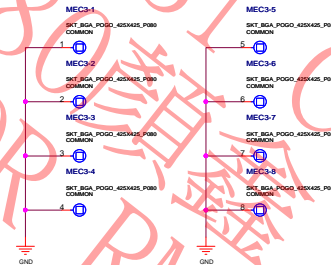
NVIDIA CORPORATION
2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA

NV_PN	600-11025-BASE-SCH		
PCB REV	P1025-C00	PAGE	
BOM REV	A	DATE	12-JAN-2010

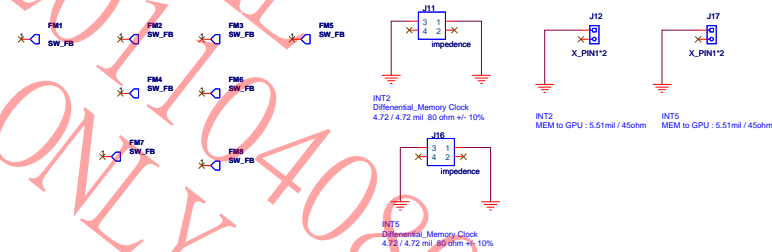
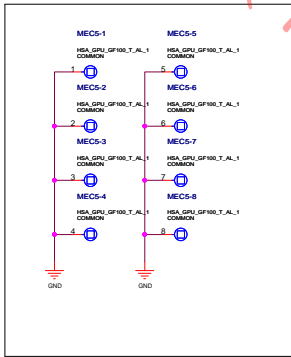
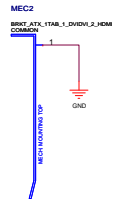
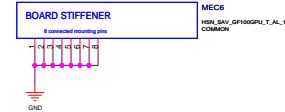
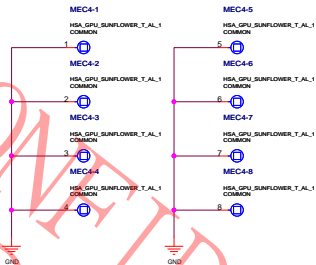
BASEPLATE MOUNTING HOLES



SOCKET (FOR LAB USE ONLY)



FANSINK HOLE

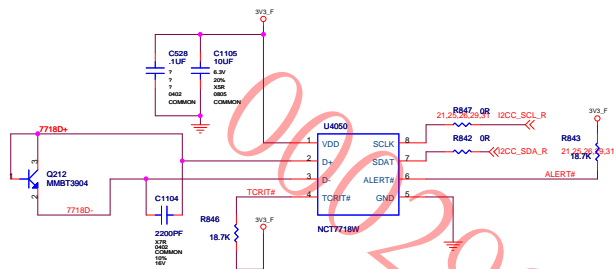


Title			
V257			
Size	Document Number	Rev	<RevCode>
Custom			
Date	Tuesday, January 05, 2011	Sheet	34 of 1

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED AS IS. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	Thermal/Mechanical

NVIDIA CORPORATION			
2701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95050, USA			
INV_PN	602-11263-BASE-QS1	PAGE	
PCB REV	P1263-A02	DATE	14-NOV-2010
BCM REV	A		



MSI CONFIDENTIAL
 29980 顏金 RD(C) 20110408001
 FOR RMA(萬裡) ONLY

Title		
V257		
Size	Document Number	Rev
Custpm6		<Rev Code>
Date: Tuesday, January 04, 2011 Sheet 35 of 1		