P2010-A02 75W/100W GK107/GF108 128-BIT GDDR5 in x32/x16 MODE DVI-I-DL + DVI-D-DL/HDMI +mHDMI/DP TABLE OF CONTENTS 2012/07/05 MS-V280-1.0 changed list Page Description Page Description Table of Contents PCI Express 12 Remove colay DP (J1) GPU Frame Buffer Partition A/B Remove colay HDMI (J5) 14 FBA Partition [31..0] 17 Remove 2pin fan control,add 4pin fan control X2,remove all bumper FBA Partition [63..32] FBB Partition [31..0] Romove U501 PEX_VDD / U503 PEX_PLL / U3 18 FBB Partition [63..32] 21 Change FBVDDQ PWM to UP1529 GPU FB Decoupling 21 Add PSI control circuit DACA VGA 10 IFPAB TMDS Change PWM to 4-phase (ON Semiconductor/NCP5395GMNR2G) 22 11 IFPC mHDMI Add 6262 voltage control circuit, thermal shutdown circuit and NVVDD power sequence 25 IFPC DP 12 IFPEF TMDS 13 IFPE Long HDMI Core Decoupling, GPU VDD_3V3 17 Straps, Thermal, Mechanical 18 Power Supply I: Misc Power Power supply II:PEX_VDD 20 FBVDDQ NVVDD CTRL 21 NVVDD MOS 22 12V EXT / 12V PEX Power Select Circuit

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