

P413: G86, DDR2 MEMORY 32MX16/16Mx16

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REV HISTORY

REV	VARIANT	N/P/N	ASSEMBLY
B	BASE	600-10413-xxxx-000	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	9K0000	600-10413-0000000	P413, G86-300, 64 BIT DDR2 16Mx16 MEMORY, VGA+DVI+HDMI
2	9K0001	600-10413-0001000	P413, G86-300, 64 BIT DDR2 32Mx16 MEMORY, VGA+DVI+HDMI
3	9K0000	600-10413-0000000	P413, G79-300, 64 BIT DDR2 16Mx16 MEMORY, VGA+DVI+HDMI
4	9K0001	600-10413-0001000	P413, G79-300, 64 BIT DDR2 32Mx16 MEMORY, VGA+DVI+HDMI
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9	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
10	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
11	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
12	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
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PAGE DETAIL	P413 Overview

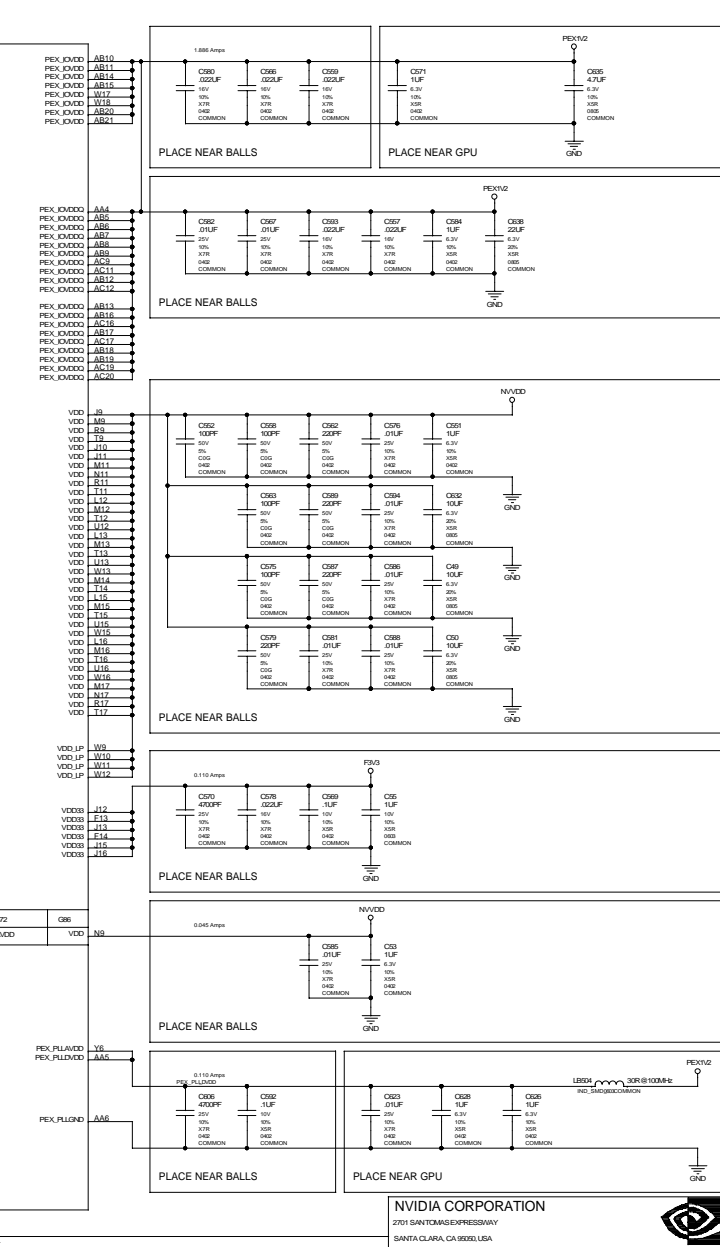
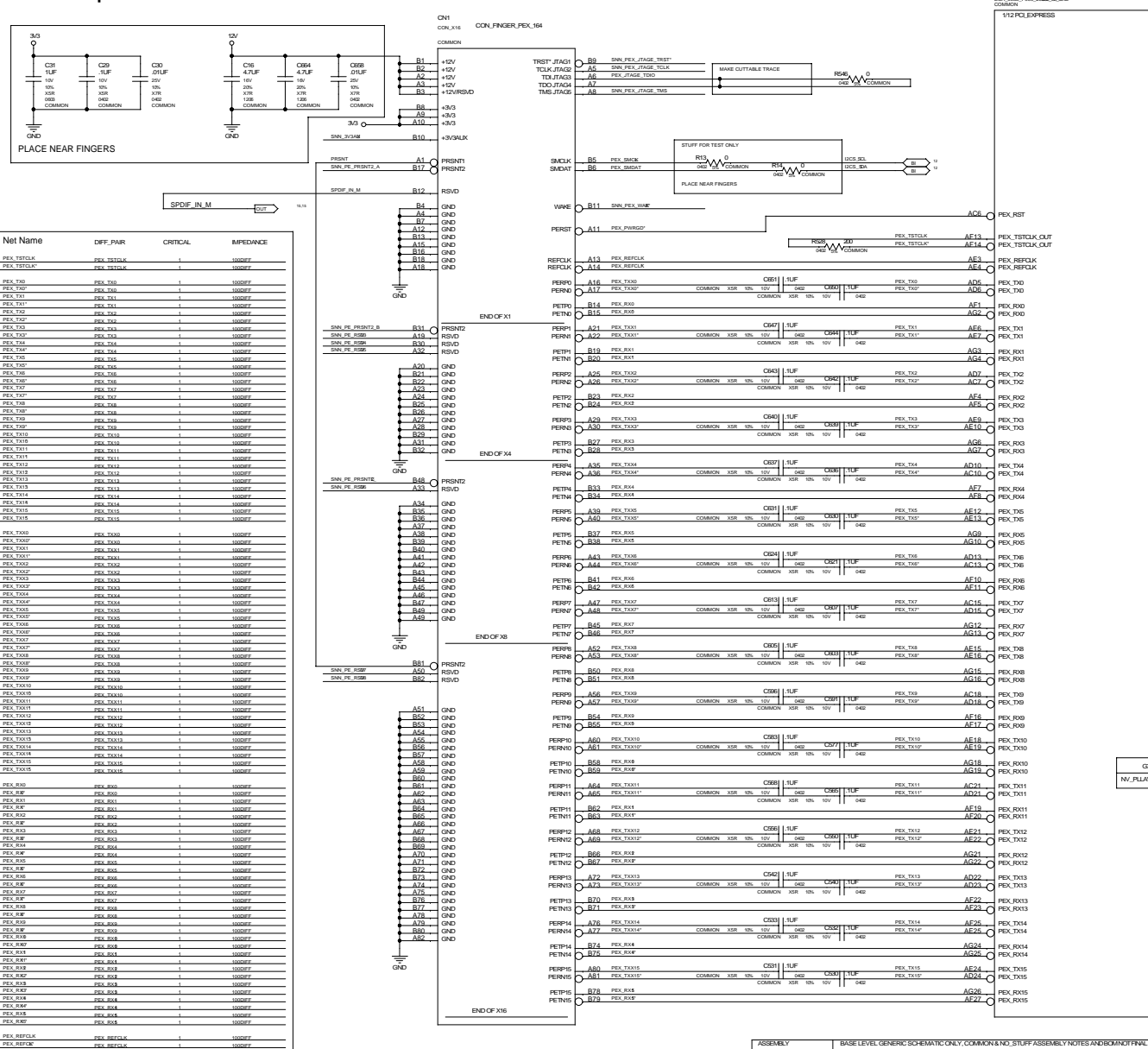
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## PCI Express Interface



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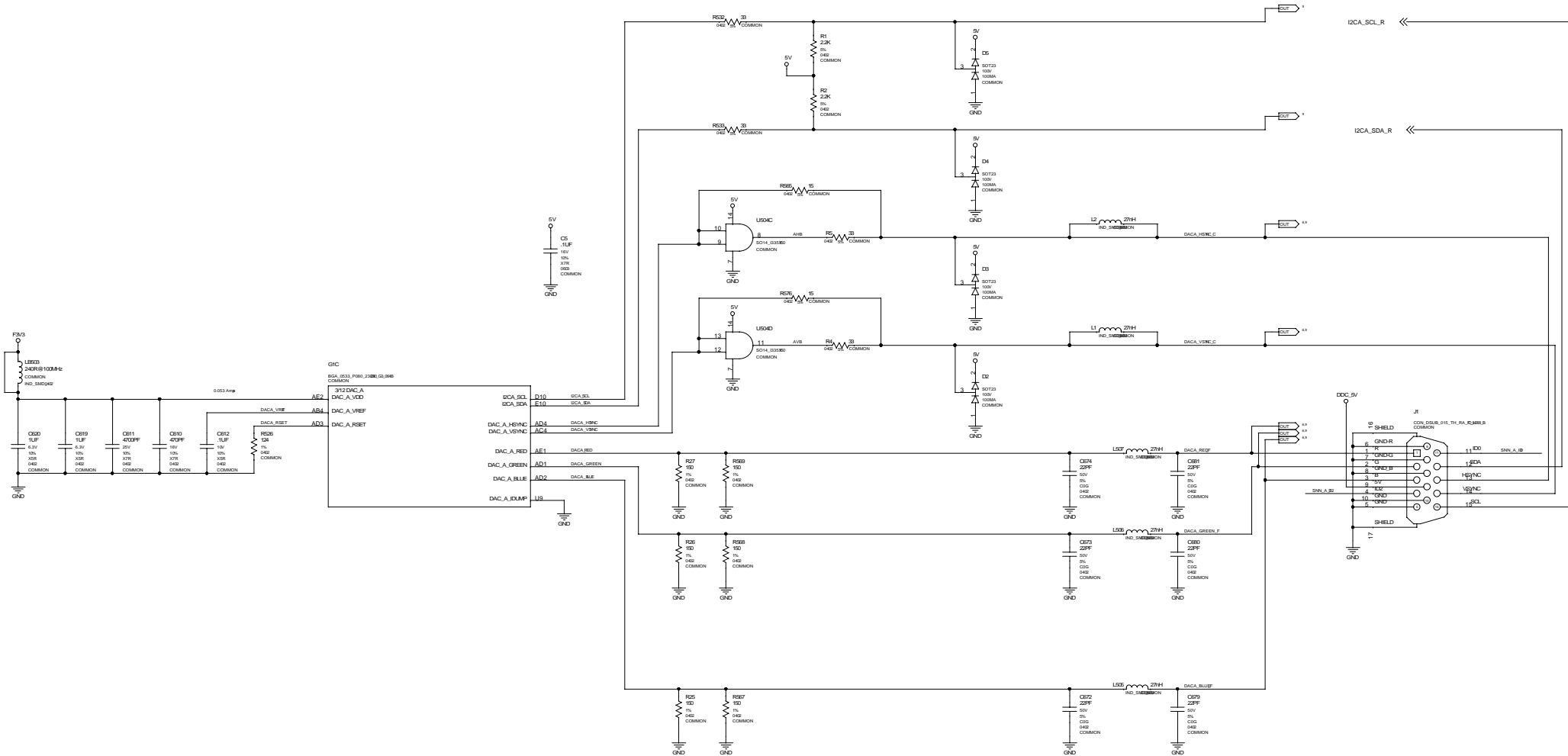






DACA, Slim DB15 Connector

Net Name	MIN LINE WIDTH	CRITICAL	IMPEDANCE
10 DACA_VREF	12MIL		
10 DACA_VSET	12MIL		
10 DACA_VHNC		2	100OHM
10 DACA_VSNC		2	100OHM
10 DACA_VHNC_C		2	100OHM
10 DACA_VSNC_C		2	100OHM
10 DACA_RED		1	100OHM
10 DACA_GREEN		1	100OHM
10 DACA_BLUE		1	100OHM
10 DACA_RED_F		1	100OHM
10 DACA_GREEN_F		1	100OHM
10 DACA_BLUE_F		1	100OHM



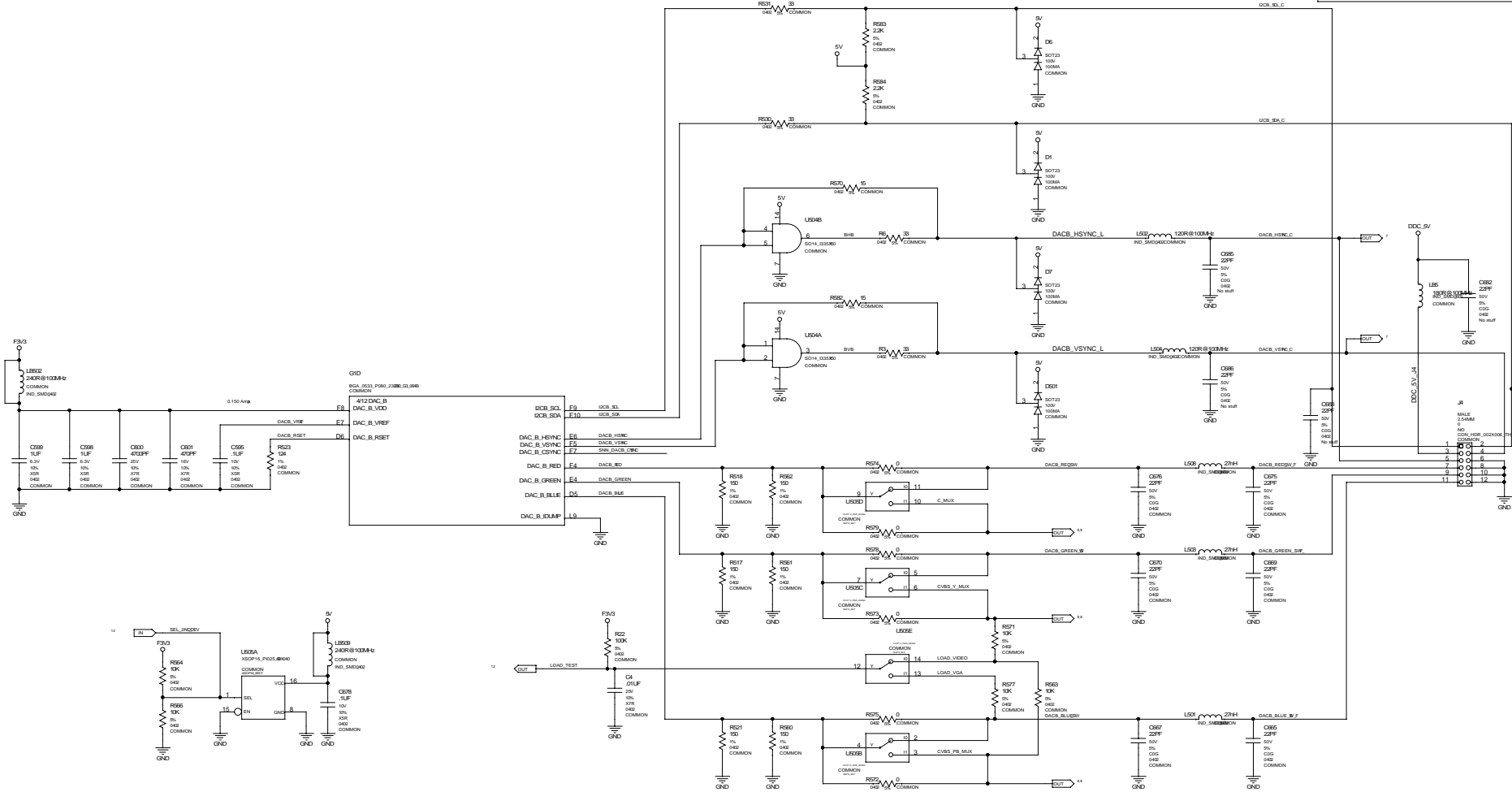
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ASSEMBLY: DACA LEVEL 0 GENERIC SCHEMATIC ONLY; COMMON & NO STUFF ASSEMBLY NOTES AND NOT FINAL  
PAGE DETAIL: DACA\_SSD01E1C000000

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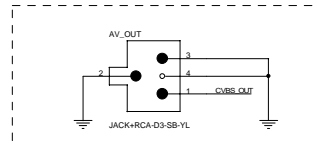
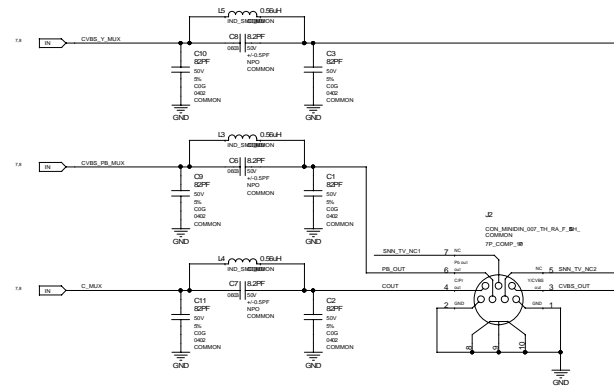
DACB, MUX, 2x6 Header

Net Name	MIN LINE WIDTH	CRITICAL	IMPEDANCE
1) DACB_VREF	15MIL		
2) DACB_RST	15MIL		
3) DACB_HSYNC		2	100OHM
4) DACB_VSYNC		2	100OHM
5) DACB_HSYNC_C		2	100OHM
6) DACB_VSYNC_C		2	100OHM
7) DACB_RED		1	100OHM
8) DACB_GREEN		1	100OHM
9) DACB_BLUE		1	100OHM
10) DACB_RED_N_F		1	100OHM
11) DACB_GREEN_N_F		1	100OHM
12) DACB_BLUE_N_F		1	100OHM
13) DACB_RED0V		1	100OHM
14) DACB_GREEN0V		1	100OHM
15) DACB_BLUE0V		1	100OHM



## MiniDIN Connector

	Net Name	CRITICAL	IMPEDANCE
7.9	C_MUX	1	50Ω(2)
7.9	CVBS_Y_MUX	1	50Ω(2)
7.9	CVBS_PB_MUX	1	50Ω(2)
7.9	C_OUT	1	50Ω(2)
7.9	CVBS_OUT	1	50Ω(2)
7.9	PB_OUT	1	50Ω(2)



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1. Name of the person	2. Date of birth	3. Place of birth	4. Nationality	5. Date of entry	6. Date of departure	7. Date of return	8. Date of exit	9. Date of re-entry	10. Date of re-exit	11. Date of re-entry	12. Date of re-exit	13. Date of re-entry	14. Date of re-exit	15. Date of re-entry	16. Date of re-exit	17. Date of re-entry	18. Date of re-exit	19. Date of re-entry	20. Date of re-exit	21. Date of re-entry	22. Date of re-exit	23. Date of re-entry	24. Date of re-exit	25. Date of re-entry	26. Date of re-exit	27. Date of re-entry	28. Date of re-exit	29. Date of re-entry	30. Date of re-exit	31. Date of re-entry	32. Date of re-exit	33. Date of re-entry	34. Date of re-exit	35. Date of re-entry	36. Date of re-exit	37. Date of re-entry	38. Date of re-exit	39. Date of re-entry	40. Date of re-exit	41. Date of re-entry	42. Date of re-exit	43. Date of re-entry	44. Date of re-exit	45. Date of re-entry	46. Date of re-exit	47. Date of re-entry	48. Date of re-exit	49. Date of re-entry	50. Date of re-exit	51. Date of re-entry	52. Date of re-exit	53. Date of re-entry	54. Date of re-exit	55. Date of re-entry	56. Date of re-exit	57. Date of re-entry	58. Date of re-exit	59. Date of re-entry	60. Date of re-exit	61. Date of re-entry	62. Date of re-exit	63. Date of re-entry	64. Date of re-exit	65. Date of re-entry	66. Date of re-exit	67. Date of re-entry	68. Date of re-exit	69. Date of re-entry	70. Date of re-exit	71. Date of re-entry	72. Date of re-exit	73. Date of re-entry	74. Date of re-exit	75. Date of re-entry	76. Date of re-exit	77. Date of re-entry	78. Date of re-exit	79. Date of re-entry	80. Date of re-exit	81. Date of re-entry	82. Date of re-exit	83. Date of re-entry	84. Date of re-exit	85. Date of re-entry	86. Date of re-exit	87. Date of re-entry	88. Date of re-exit	89. Date of re-entry	90. Date of re-exit	91. Date of re-entry	92. Date of re-exit	93. Date of re-entry	94. Date of re-exit	95. Date of re-entry	96. Date of re-exit	97. Date of re-entry	98. Date of re-exit	99. Date of re-entry	100. Date of re-exit
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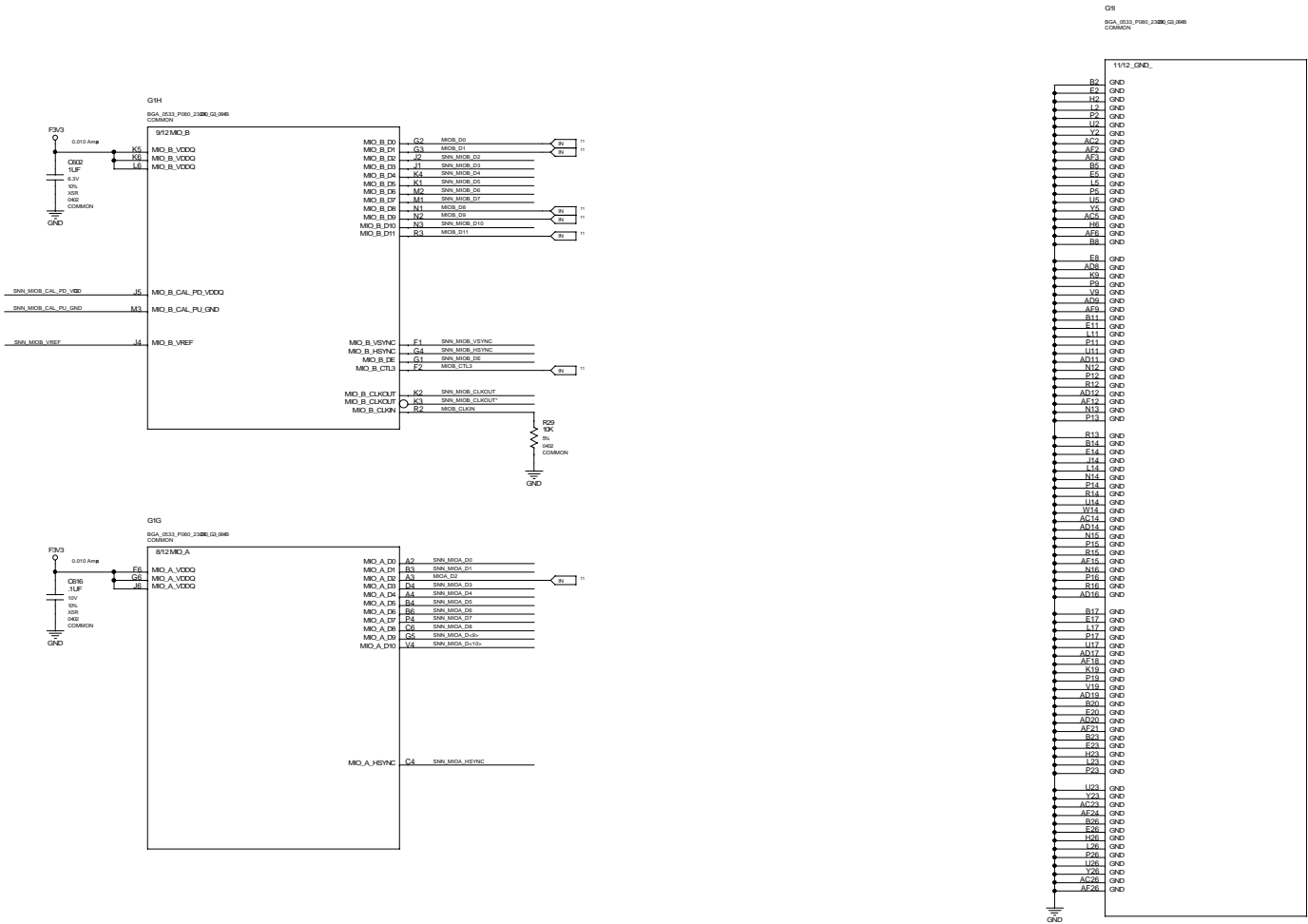
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PAGE DETAIL	MiniDIN Connector





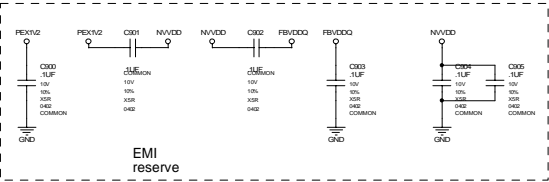
MIOA, MIOB Interface



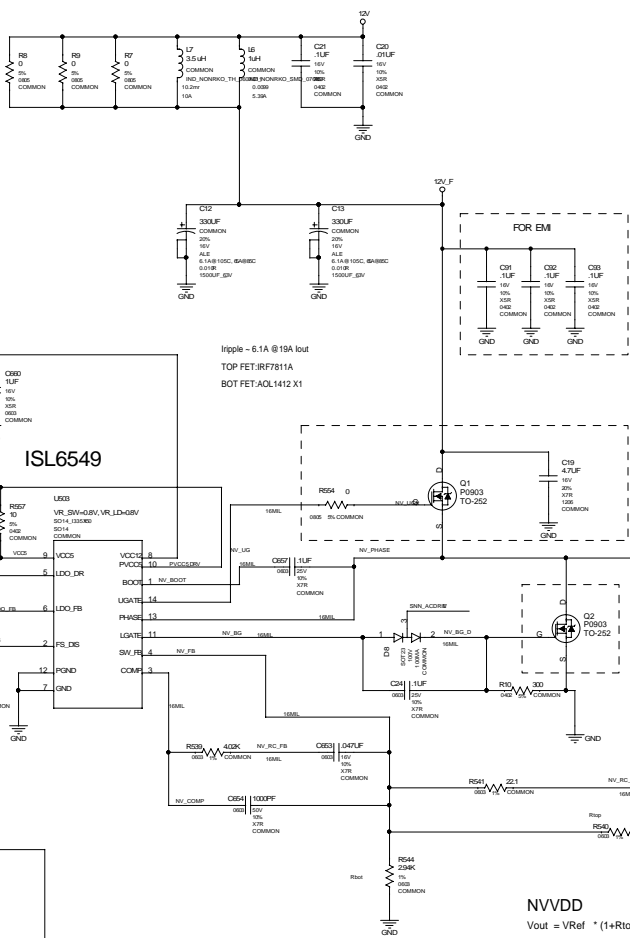
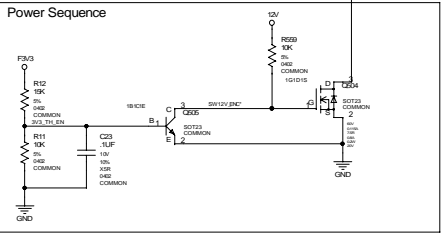
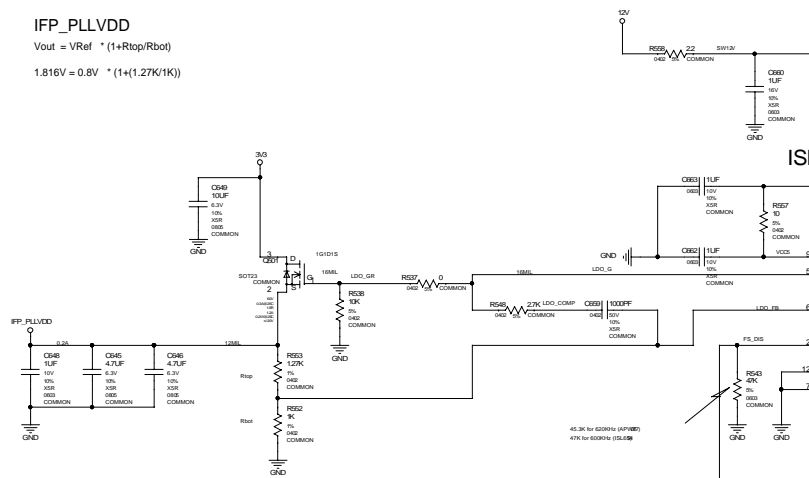




Power Supply I: NVVDD, PLLVDD

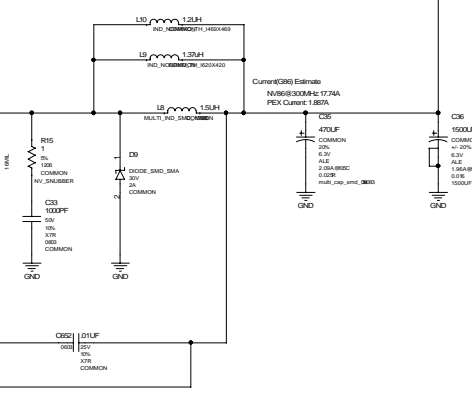
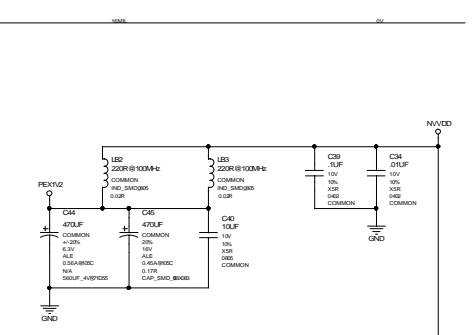


IFP\_PLLVDD  
 $V_{out} = V_{Ref} \cdot (1 + R_{top}/R_{bot})$   
 $1.816V = 0.8V \cdot (1 + (1.27K/1K))$



NVVDD  
 $V_{out} = V_{Ref} \cdot (1 + R_{top}/R_{bot})$   
 $1.20V = 0.8V \cdot (1 + (1.54K/3.09K))$  (ISL6549)  
 $1.22V = 0.8V \cdot (1 + (1.54K/2.94K))$  (ISL6549)

Net Name	LINE_WIDTH	Current	Voltage
12V_F	12V	3A	1.2V
NVVDD	NVVDD	2A	1.2V
PEXVDD	PEXVDD	2A	1.2V



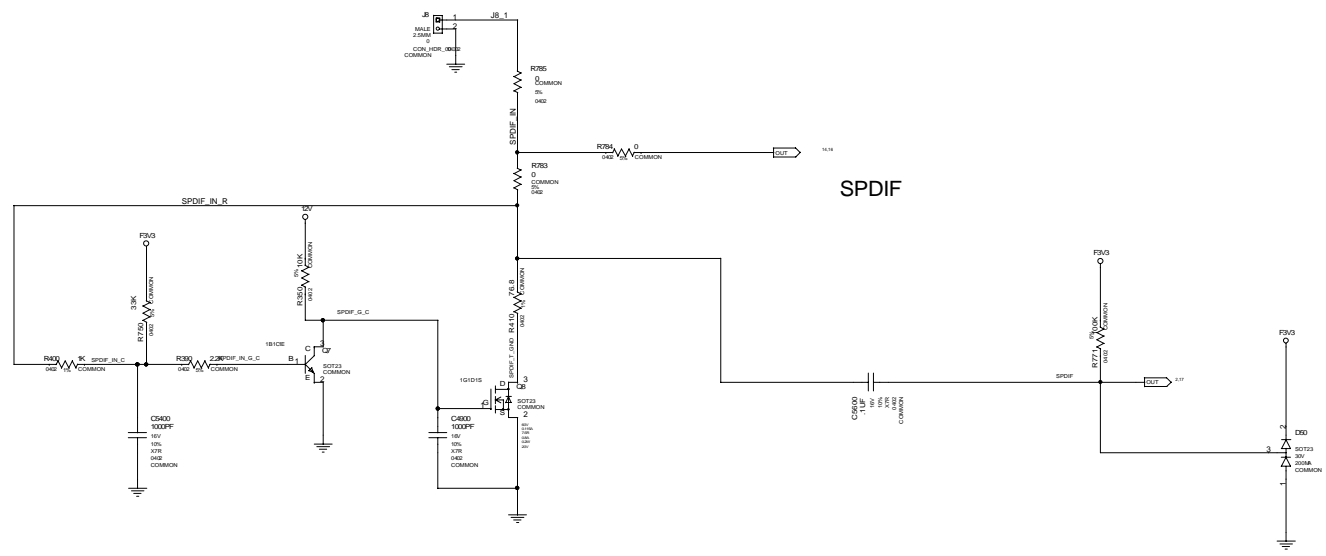




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