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D							D
C							C
B							B
A							A

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SHEET: TOC

DATE: Fri Jun 21 05:25:22 2019REV: 1.00

SHEET NUMBER: 1 OF 35

DOCUMENT NUMBER: 105\_0199w\_008

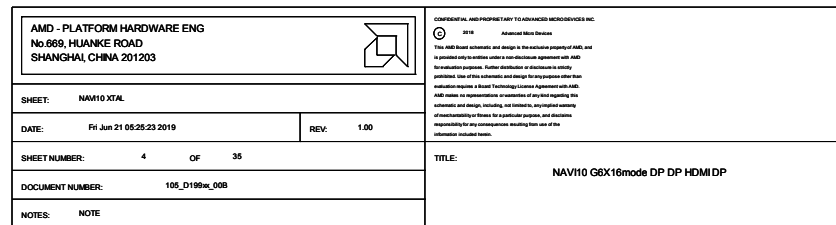
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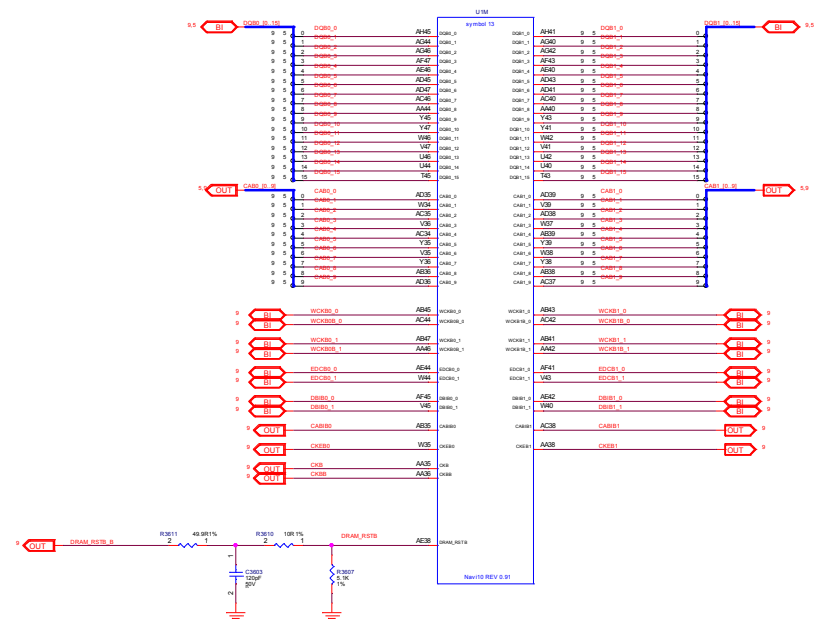
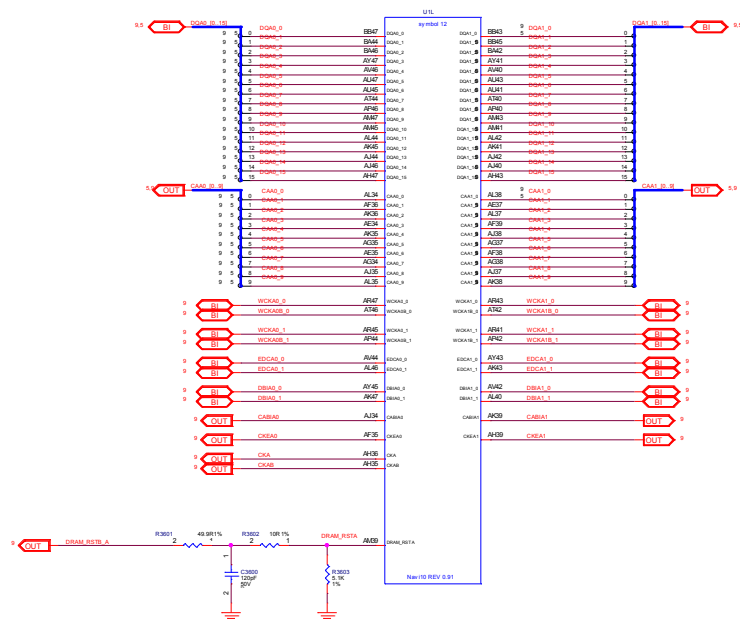
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TITLE:  
NAV110 G8X16mode DP DP HDMI DP









SHEET: NAV10 MEM CHAB

DATE: Fri Jun 21 05:25:24 2019

SHEET NUMBER: 5 OF 35

DOCUMENT NUMBER: 105\_D199xx\_00B

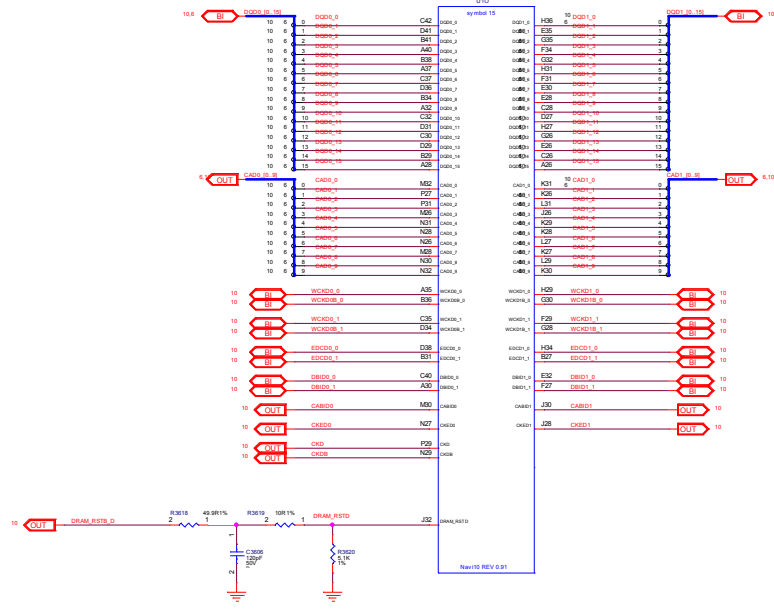
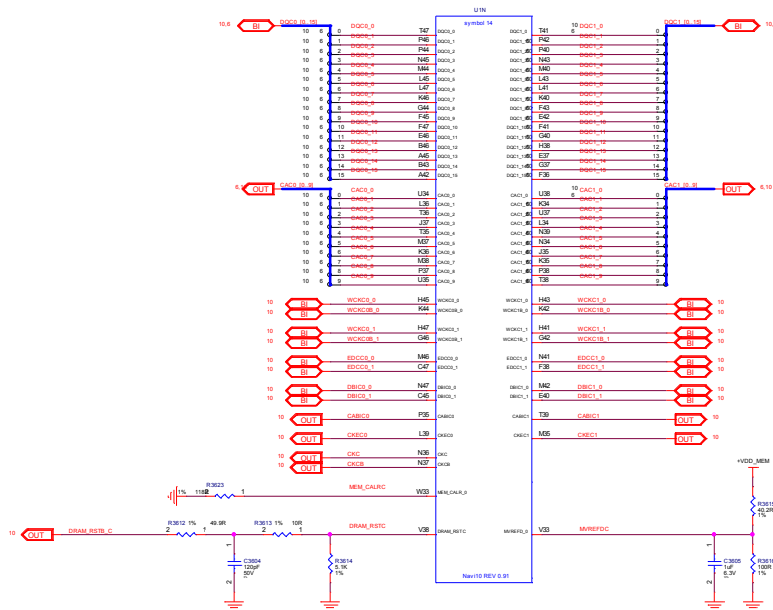
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SHEET: NAV10 MEM CHCD

DATE: Fri Jun 21 05:25:24 2019

SHEET NUMBER:

DOCUMENT NUMBER: 105\_D199xx\_00B

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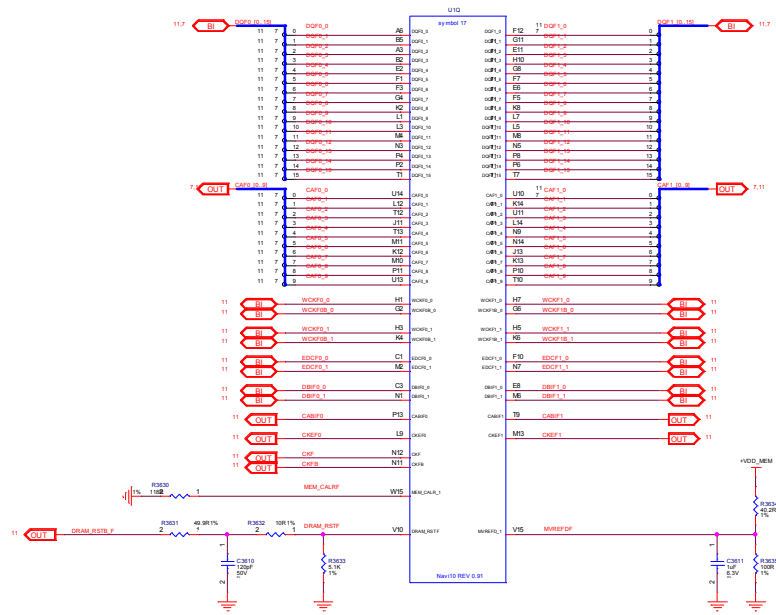
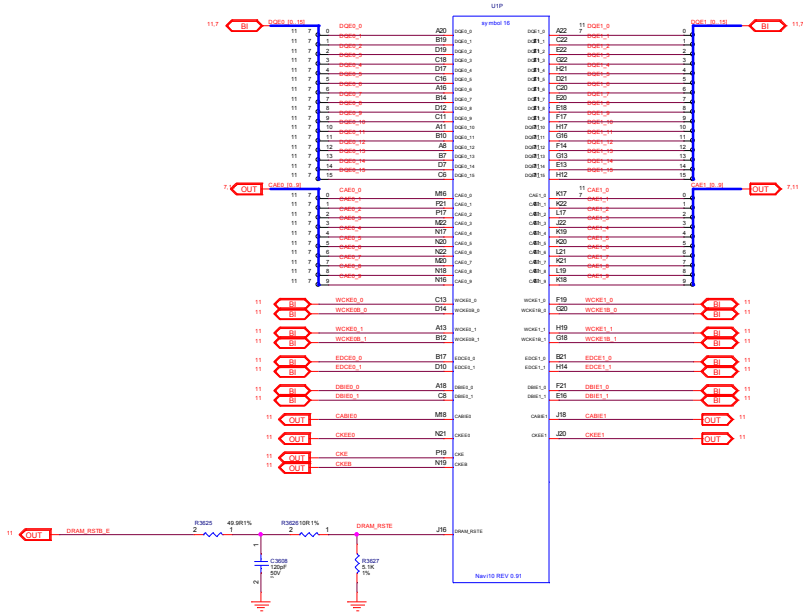


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SHANGHAI, CHINA 201203

SHEET: NAVI10 MEM CHEF

DATE: Fri Jun 21 05:25:25 2019

REV: 1.00

SHEET NUMBER: 7 OF 35

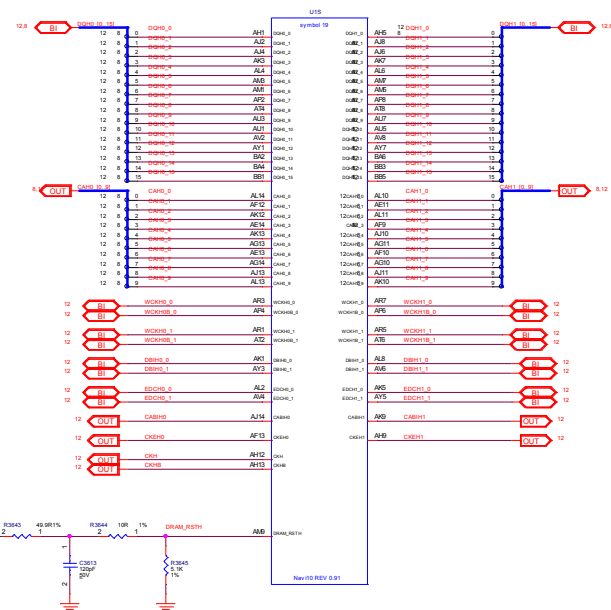
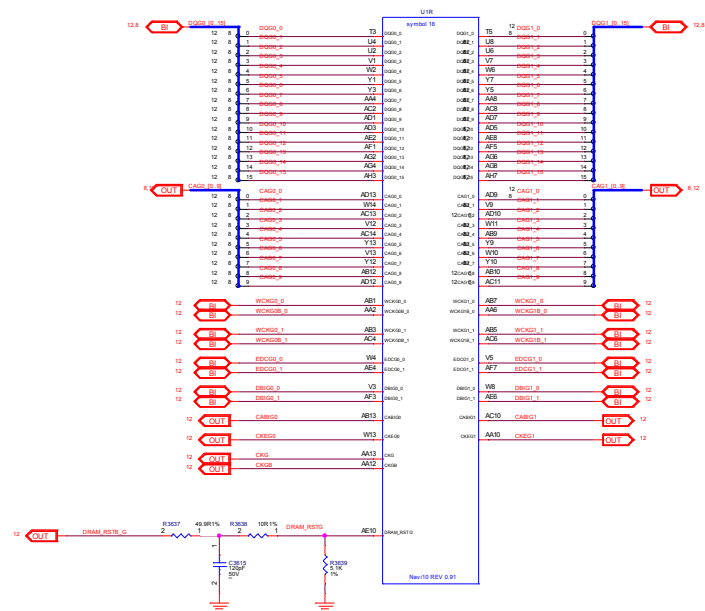
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SHEET: NAW10 MEM CHGH

DATE: Fri Jun 21 05:25:26 2019

SHEET NUMBER: 8 OF 35

DOCUMENT NUMBER: 105\_D199xx\_00B

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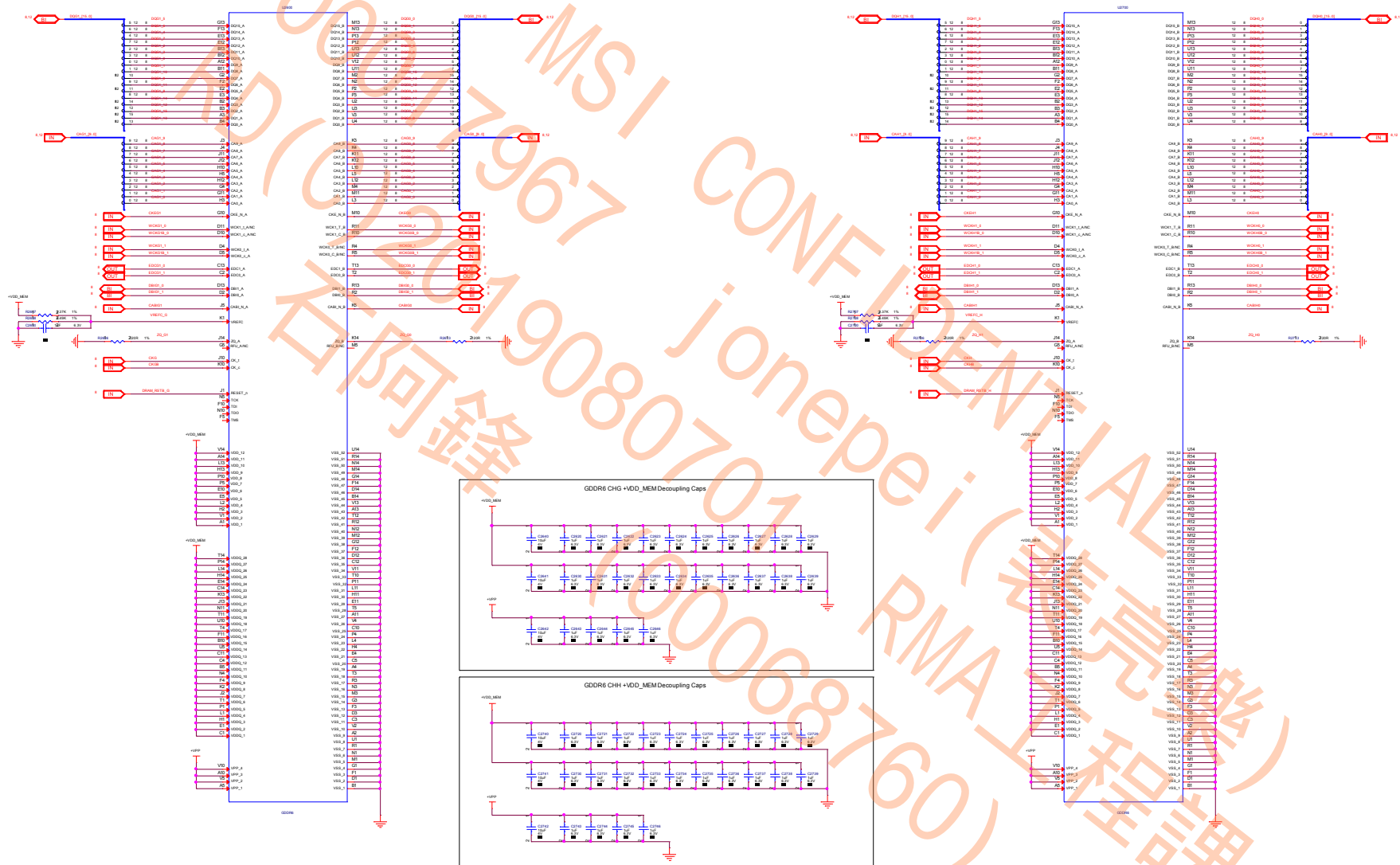
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SHANGHAI, CHINA 201203

SHEET: GDDR6 MEM CHGH

DATE: Fri Jun 21 03:45:58 2019

SHEET NUMBER: 12 OF 35

DOCUMENT NUMBER: 105\_0198x6\_888

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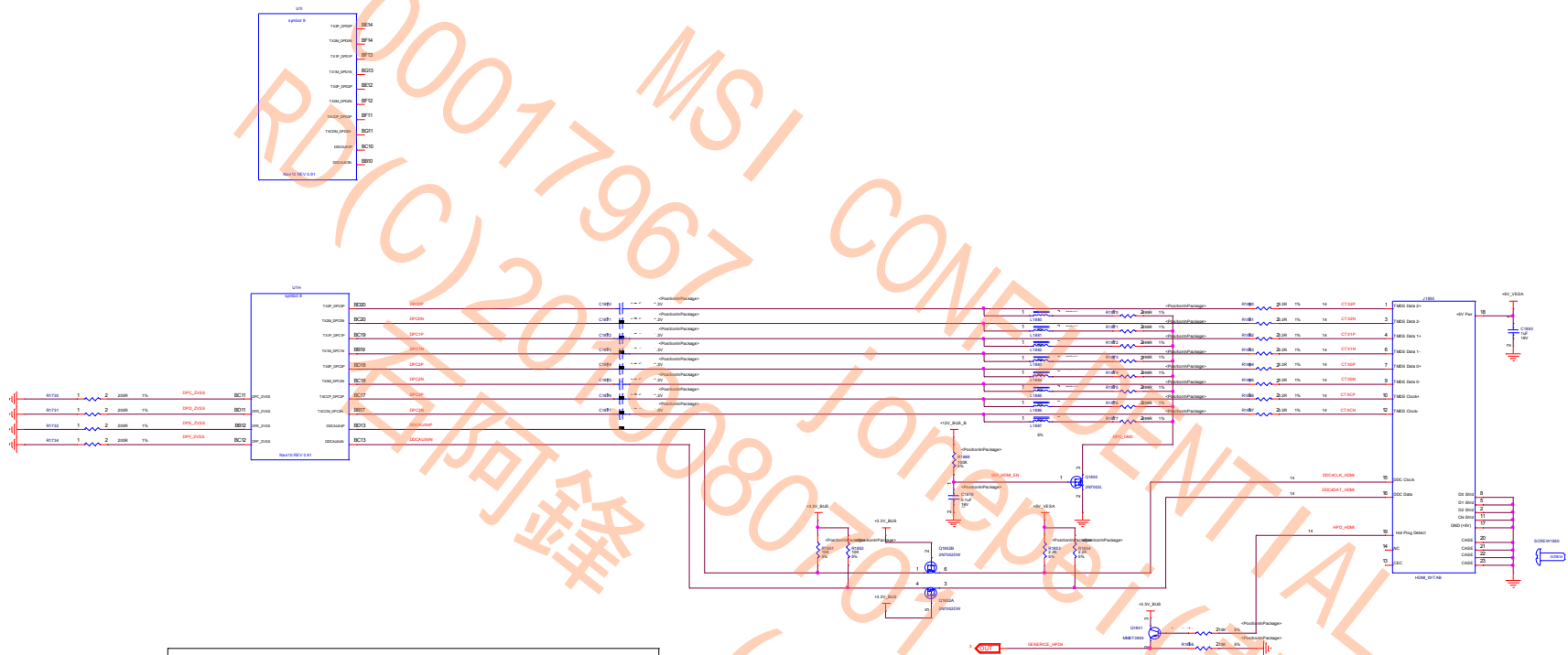



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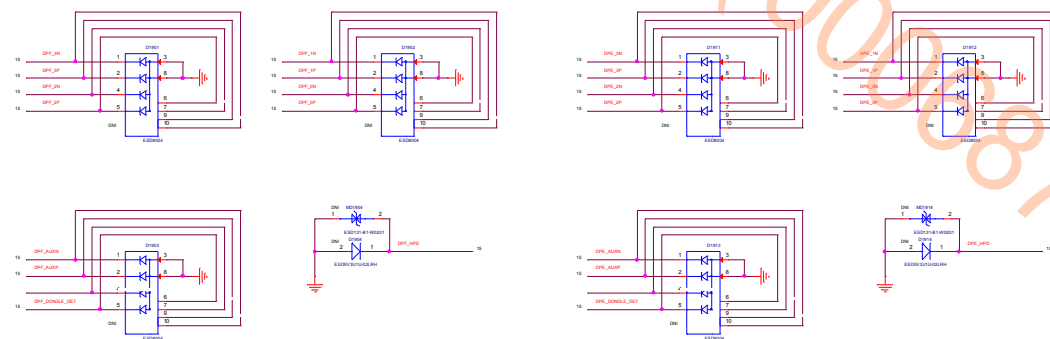
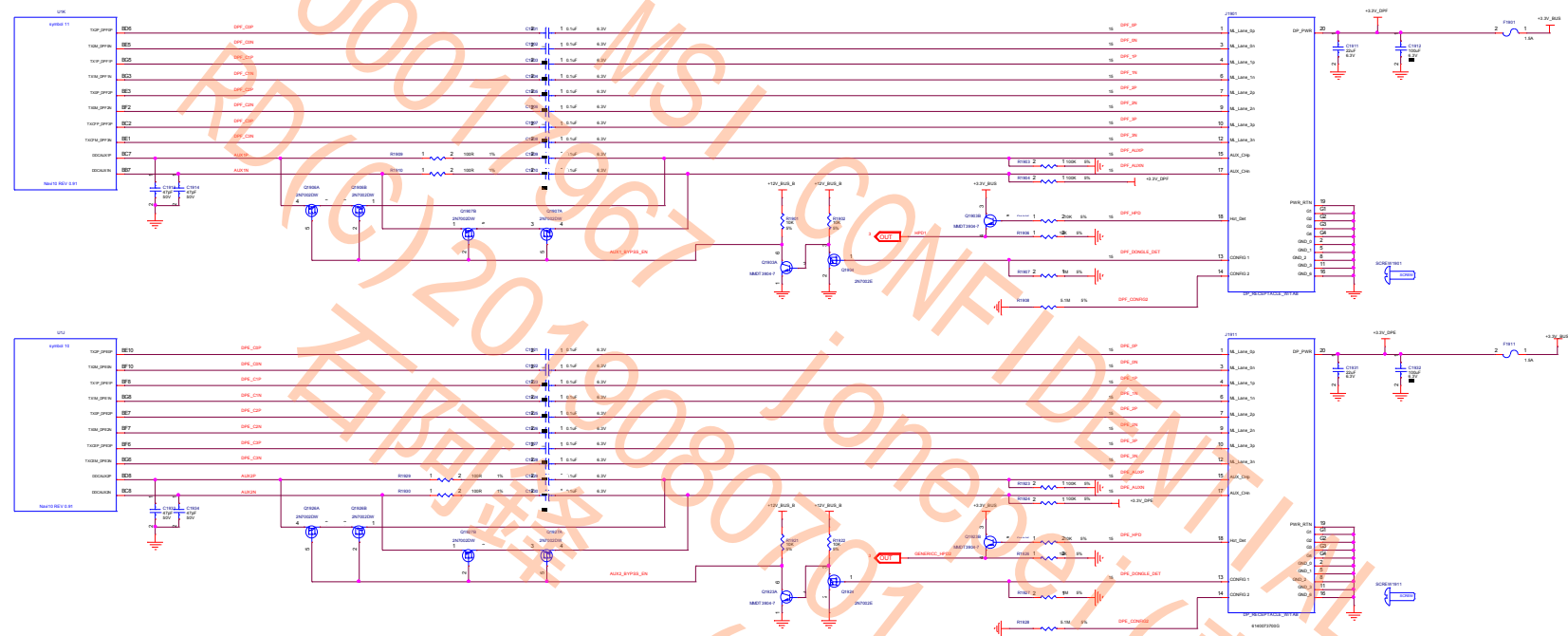
TITLE: NAVI10 G6X16mode DP DP HDMI DP






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DATE: Fri Jun 21 03:45:48 2016	REV: 1.00	TITLE:  NAV110 G6X16mode DP DP HDMI DP		
SHEET NUMBER: 14 OF 35		DOCUMENT NUMBER: 105_D1999v0_008		
NOTES: NOTE				

(9) NAVI10 TMDP E/F



<b>AMD - PLATFORM HARDWARE ENG</b> <b>No.669, HUANG ROAD</b> <b>SHANGHAI, CHINA 201203</b>		
<b>SHEET: NAVI10 TMOPEF - DP DP</b>		
<b>DATE: F4 Jun 21 03:45:49 2010</b>	<b>REV: 1.00</b>	
<b>SHEET NUMBER: 15 OF 35</b>		
<b>DOCUMENT NUMBER: 105_0199xx_008</b>		
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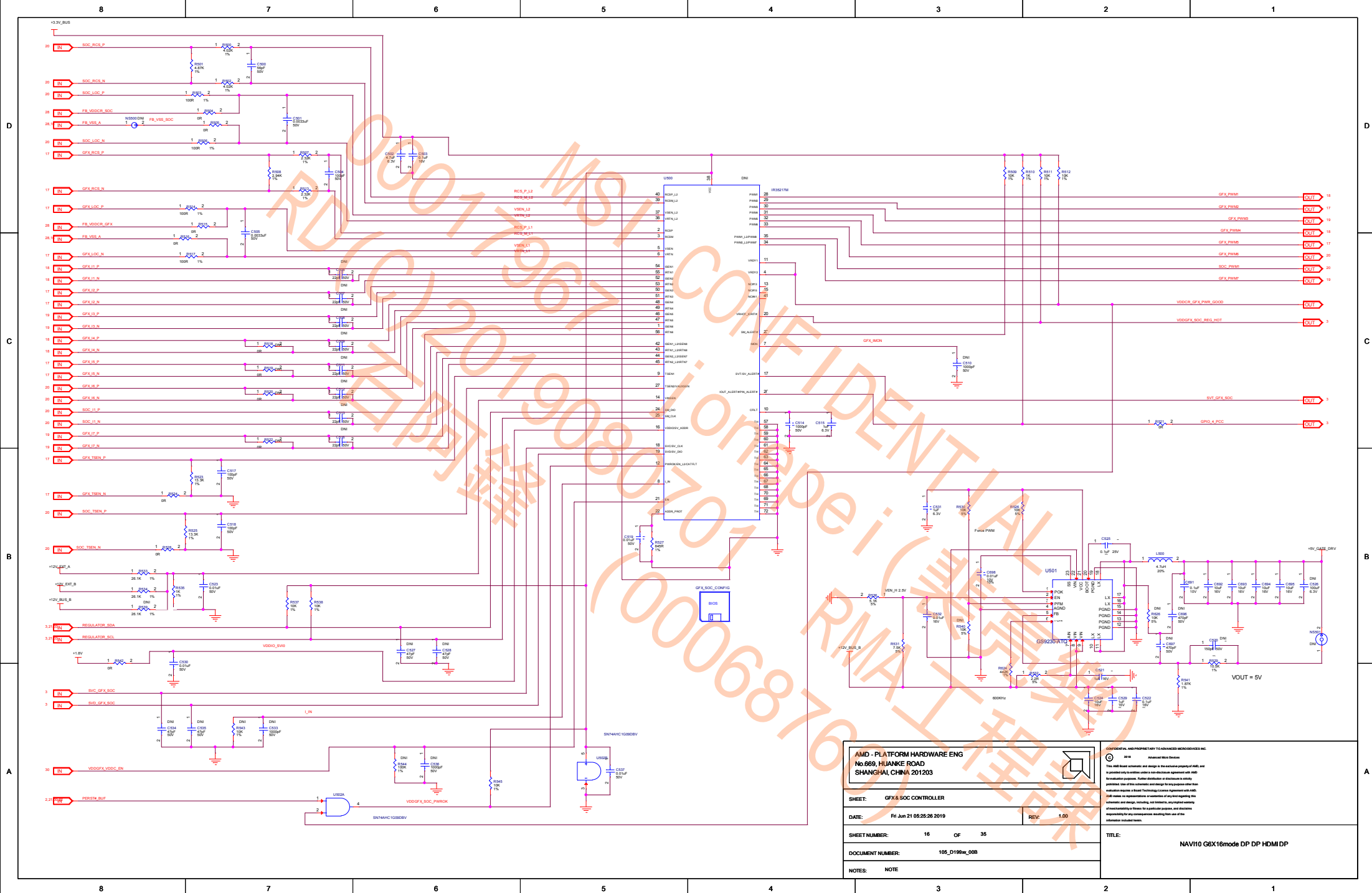
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TITLE:	NAV110 G6X16mode DP DP HDMI DP
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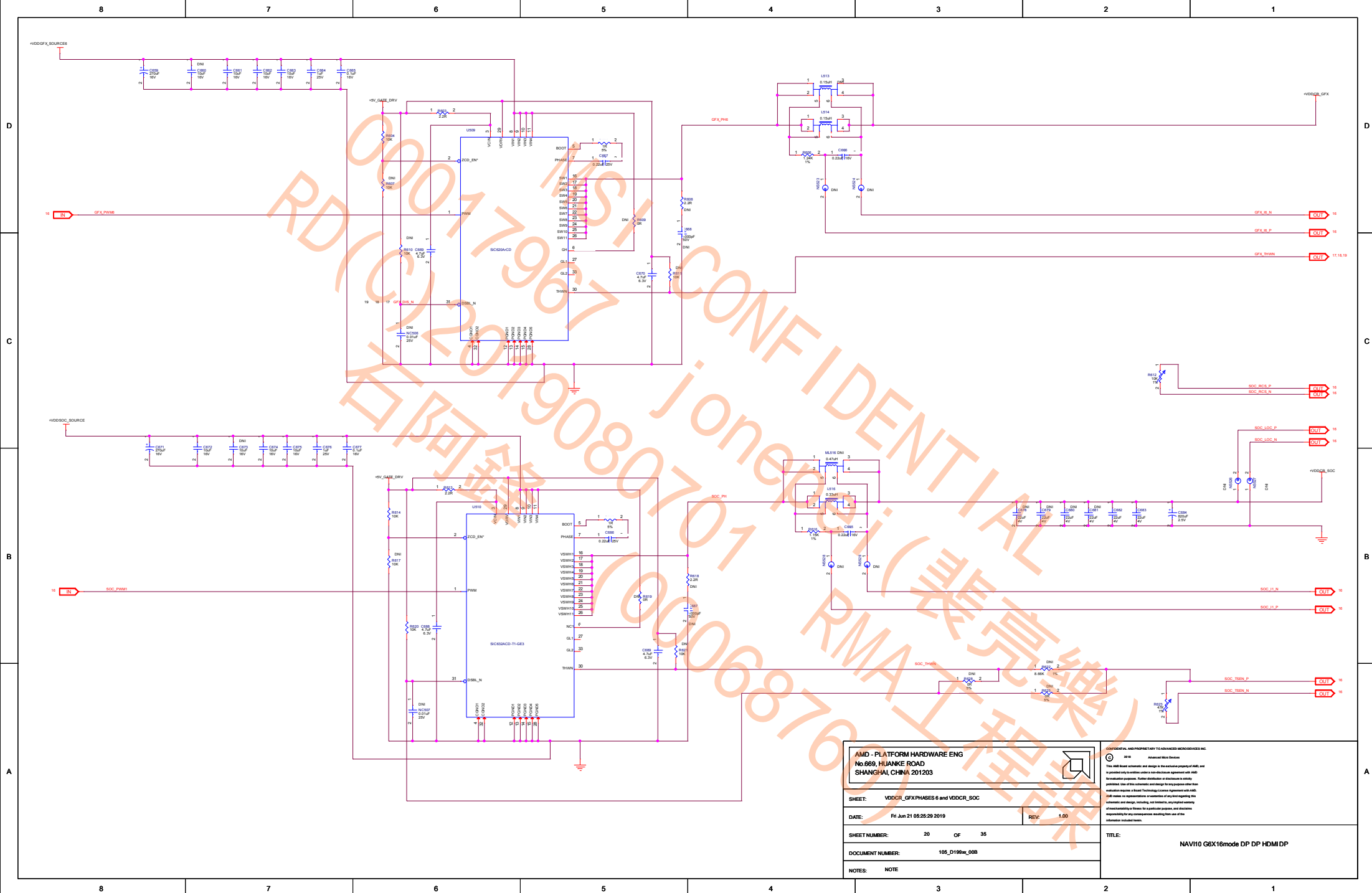




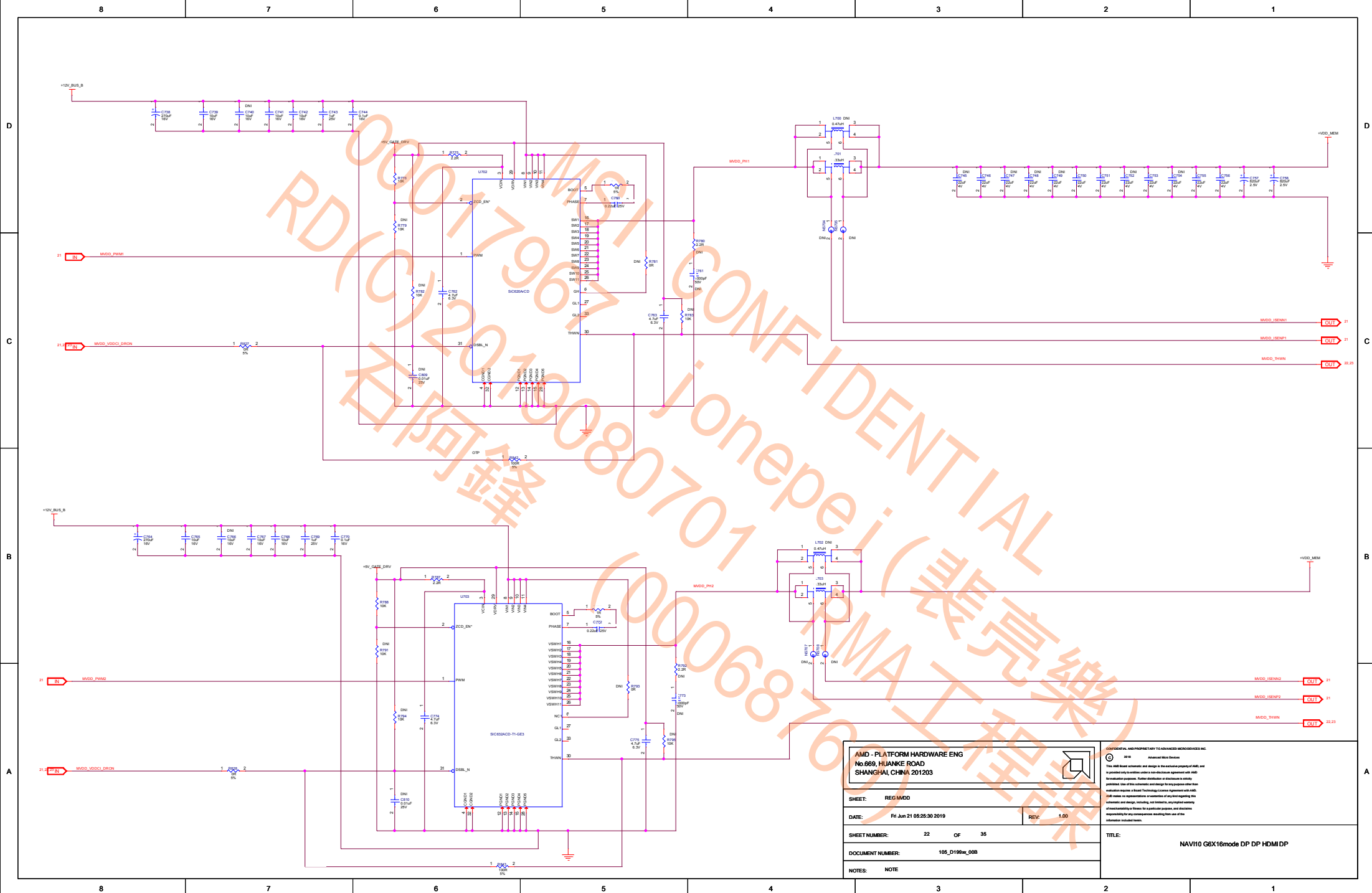






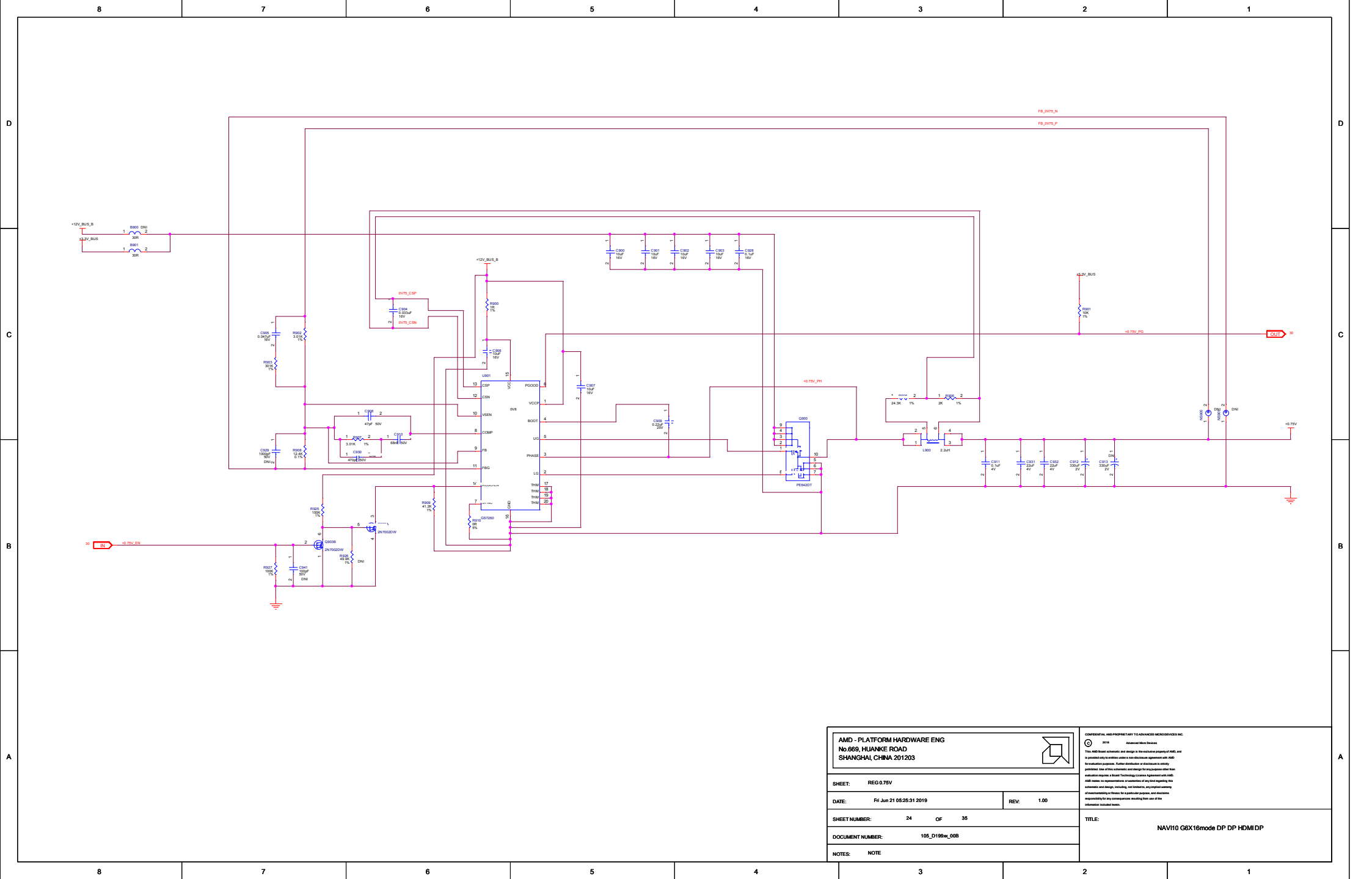






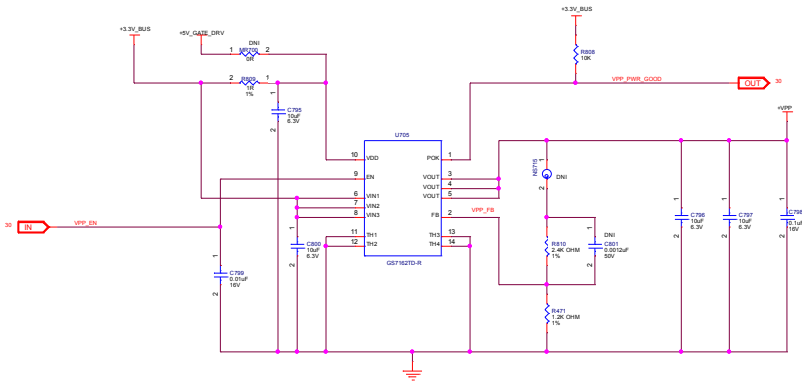
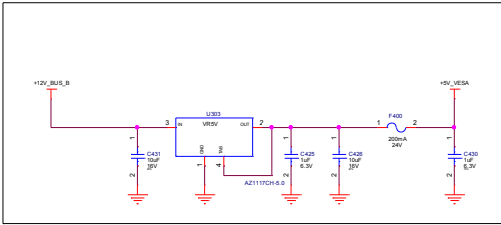





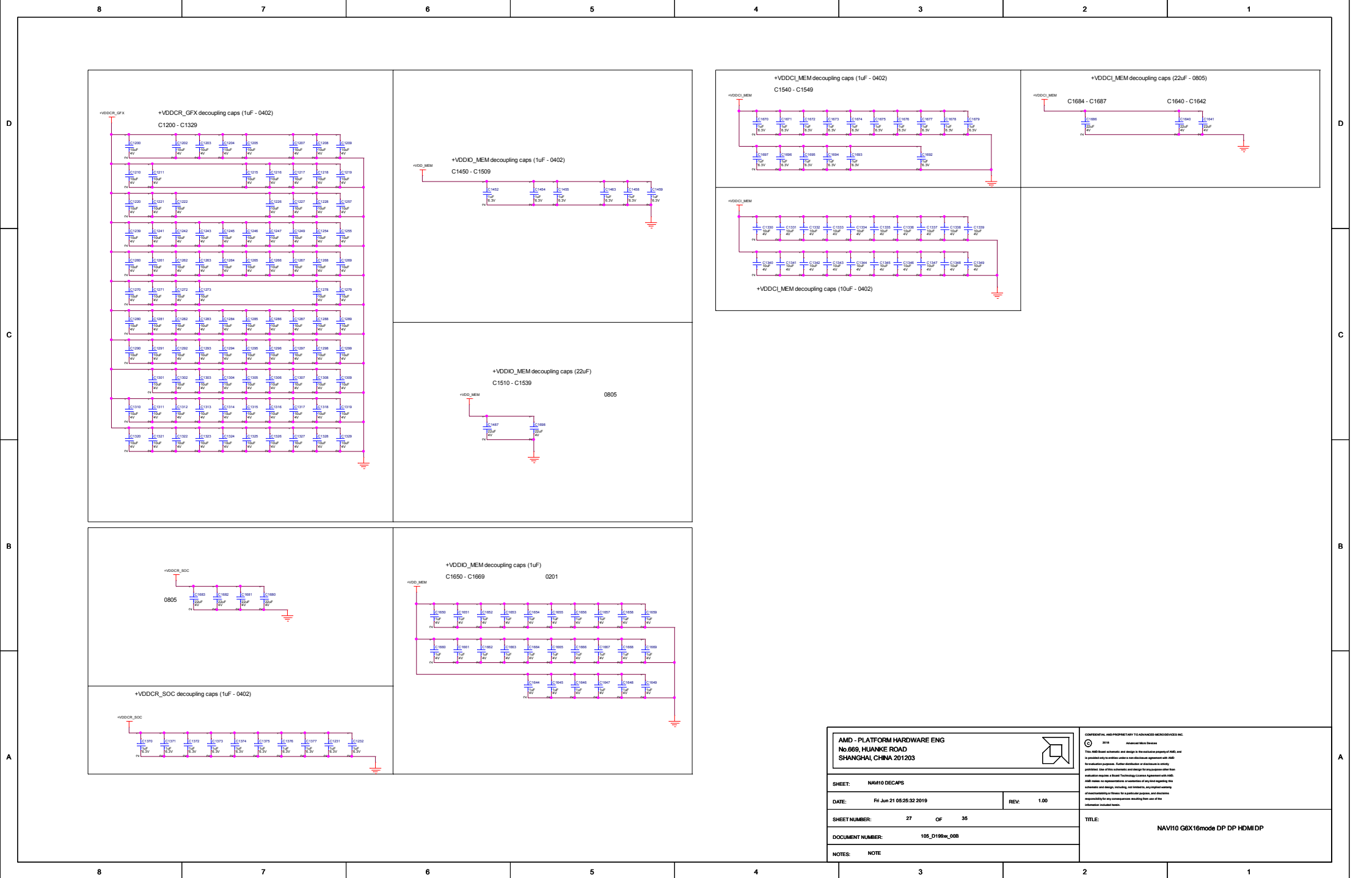


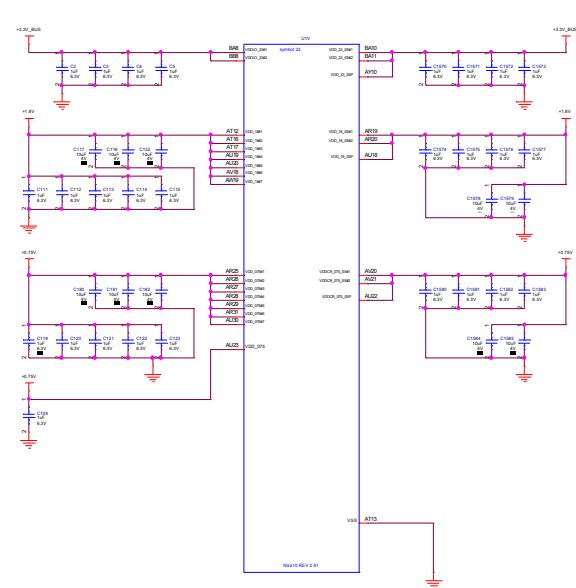



REGULATOR FOR +5V RAILS  
IOUT = 50mA



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SHEET: SMALL RAIL REGULATORS		REV: 1.00			
DATE: Fri Jun 21 05:25:32 2019				TITLE:	
SHEET NUMBER: 26 OF 35				NAVI10 G8X16mode DP DP HDMI DP	
DOCUMENT NUMBER: 105_0199w_008					
NOTES: NOTE					

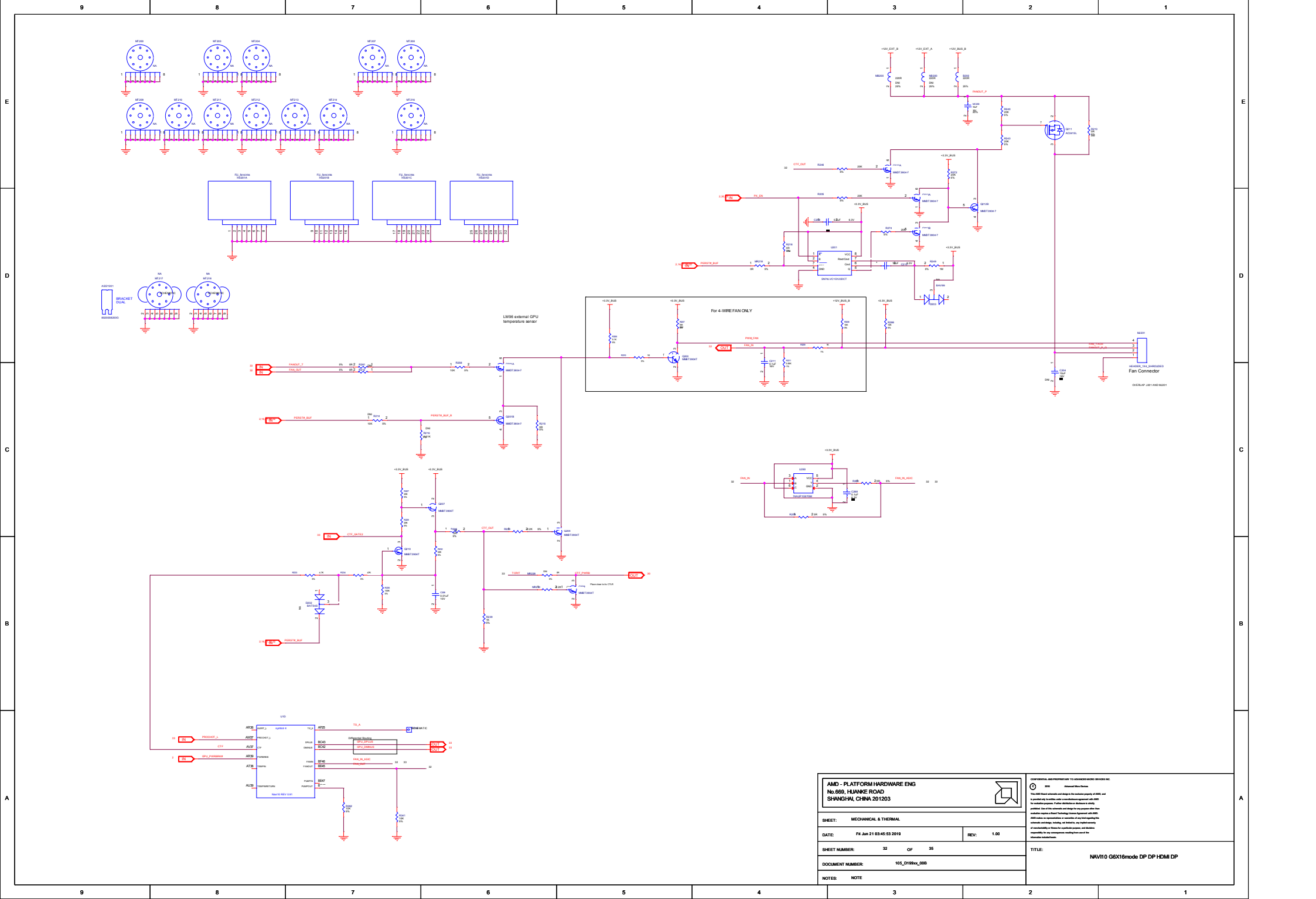




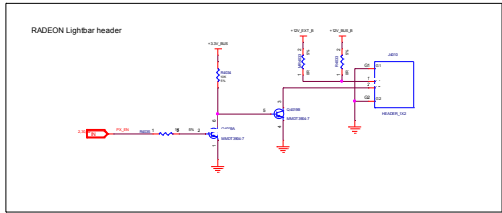
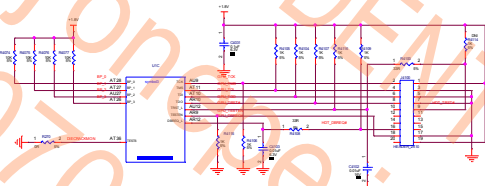
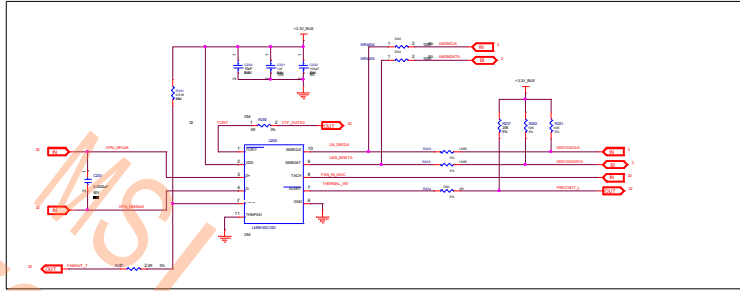
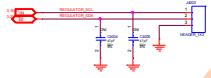
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SHEET: NAV10 POWER				① 8000 Advanced Data Sheet	
DATE: Feb Jan 21 05:25:33 2015		REV: 1.00		<p>           This AMD document and design the minimum pinout of 8000, and is provided only for reference and is not a contractual agreement with AMD or its customers. AMD reserves the right to make changes to this document without notice and without obligation. AMD makes no representation or warranty regarding the accuracy, completeness, or timeliness of the information and design for its purposes other than to provide customers with a general understanding of the information and design. AMD makes no representation or warranty of any kind regarding the accuracy, completeness, or timeliness of the information and design. AMD makes no representation or warranty of any kind regarding the accuracy, completeness, or timeliness of the information and design. AMD makes no representation or warranty of any kind regarding the accuracy, completeness, or timeliness of the information and design.         </p>	
SHEET NUMBER 28 OF 35				TITLE: NAV10 G6X1mode DP DP HDMI DPM	
DOCUMENT NUMBER 101_0199xx_000					
NOTES: NOTE					











NAME: PLATFORM HARDWARE ENG NUMBER: HANKE HEAD BRANCH: CHINA 201233			
SHEET: 000001		REV: 1.00	
DATE: Fri Jan 21 09:45:01 2011		TITLE: NAVITO GSK16node EP12PHEM DP	
SHEET NUMBER: 33		DOCUMENT NUMBER: 10011000_000	



## REVISION HISTORY

ENGINEER: Peak, Dong

	NOTES
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**NOTE**

105\_D199xx\_00B

DATE: Fri Jun 21 03:44:58 2019

SHEET NUMBER: 35 OF 35

REV: 1.00

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石阿鋒 (00068760)