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Gigabyte Confidential

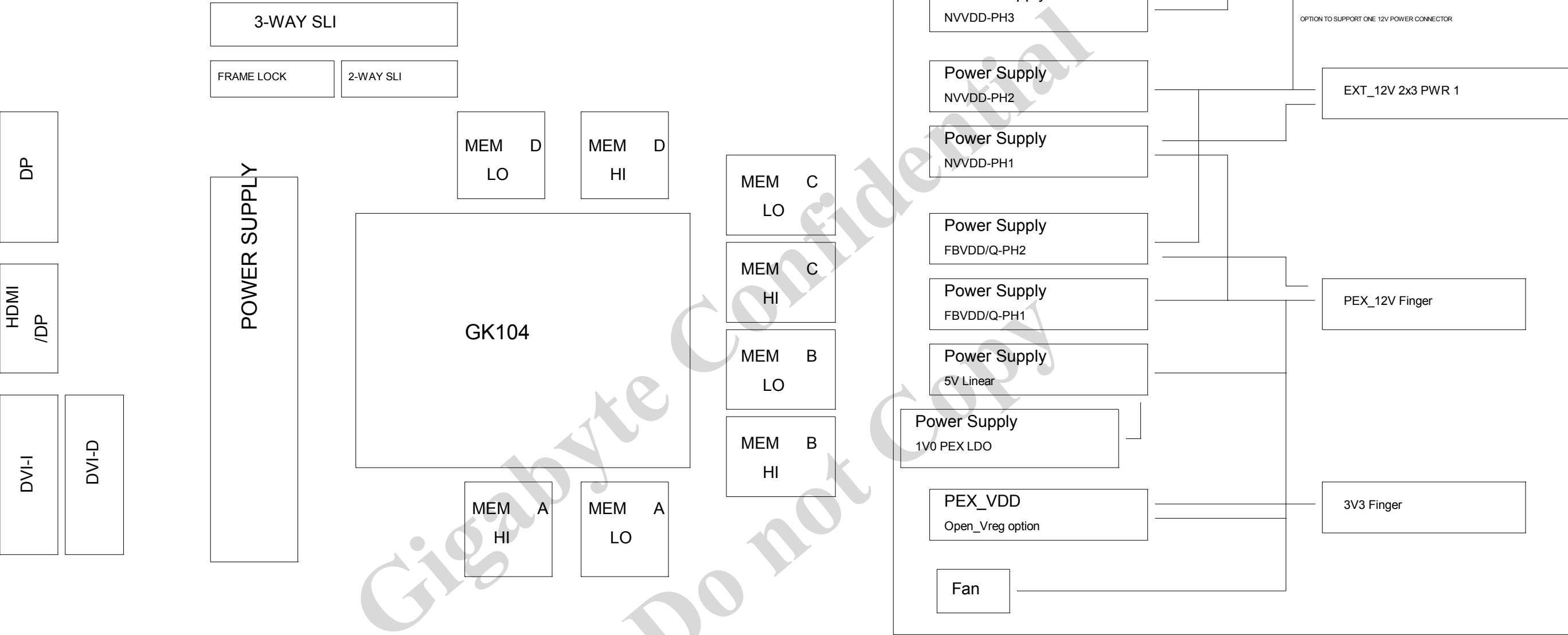
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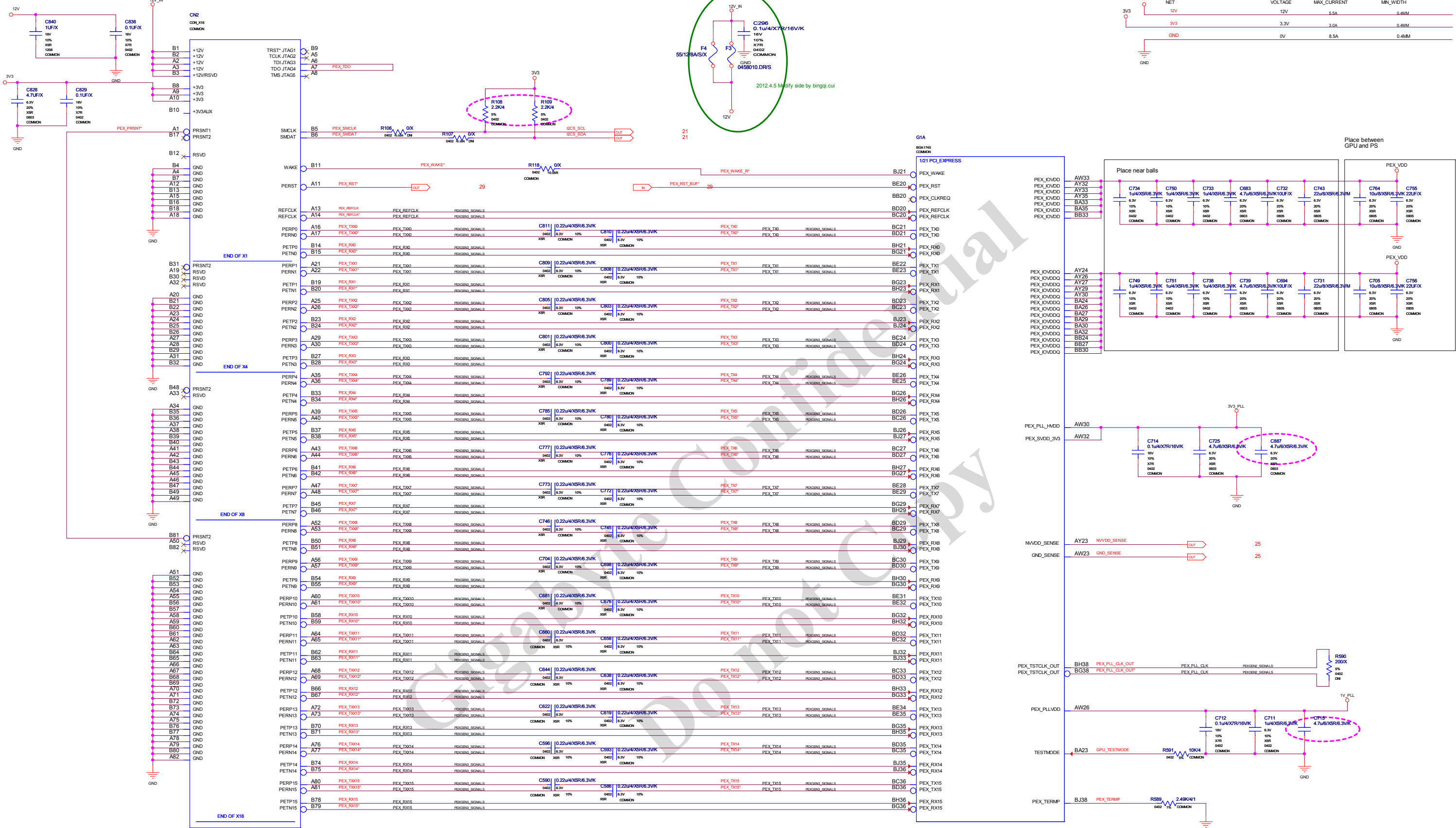


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Block Diagram

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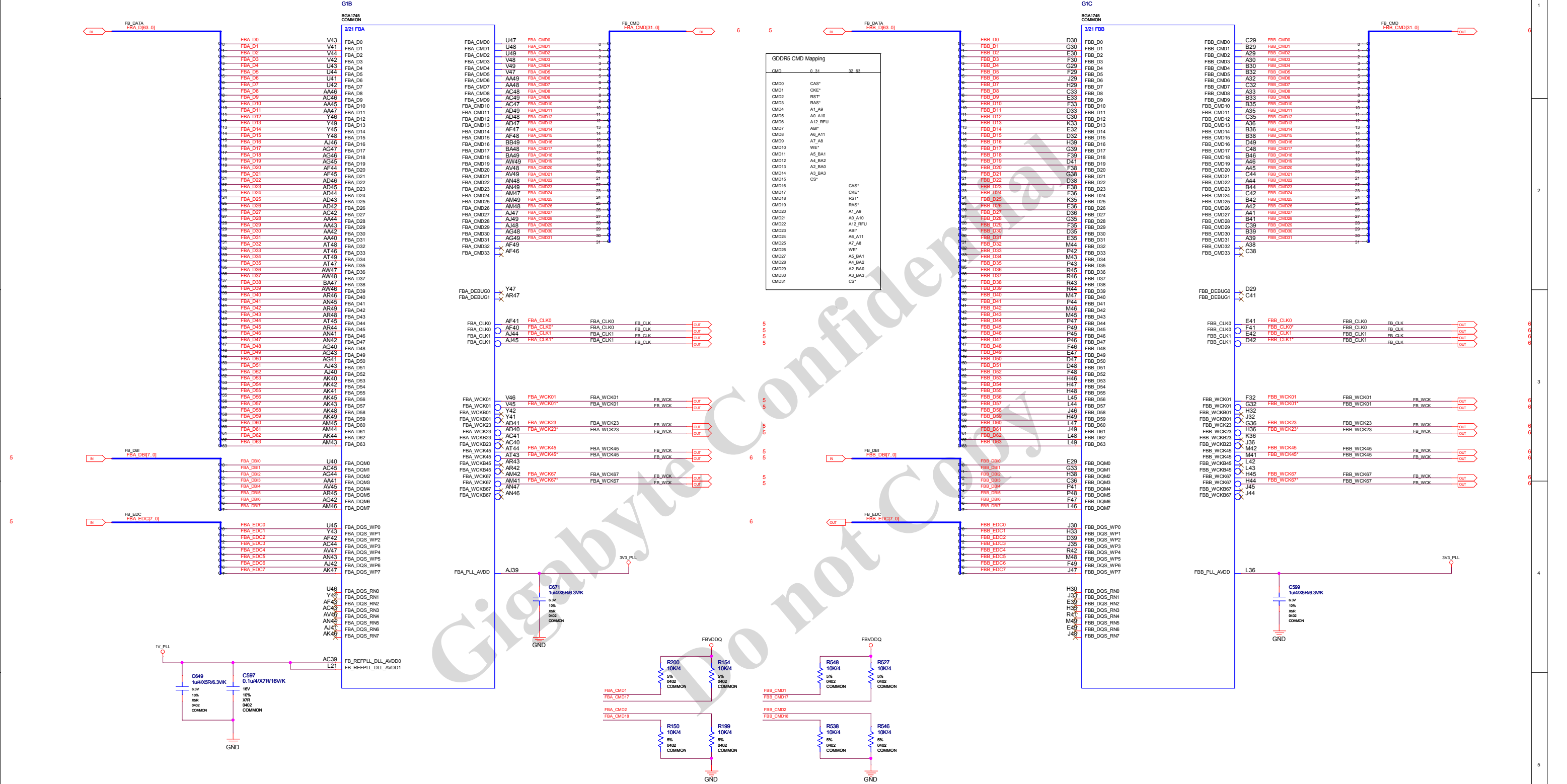


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PCI Express

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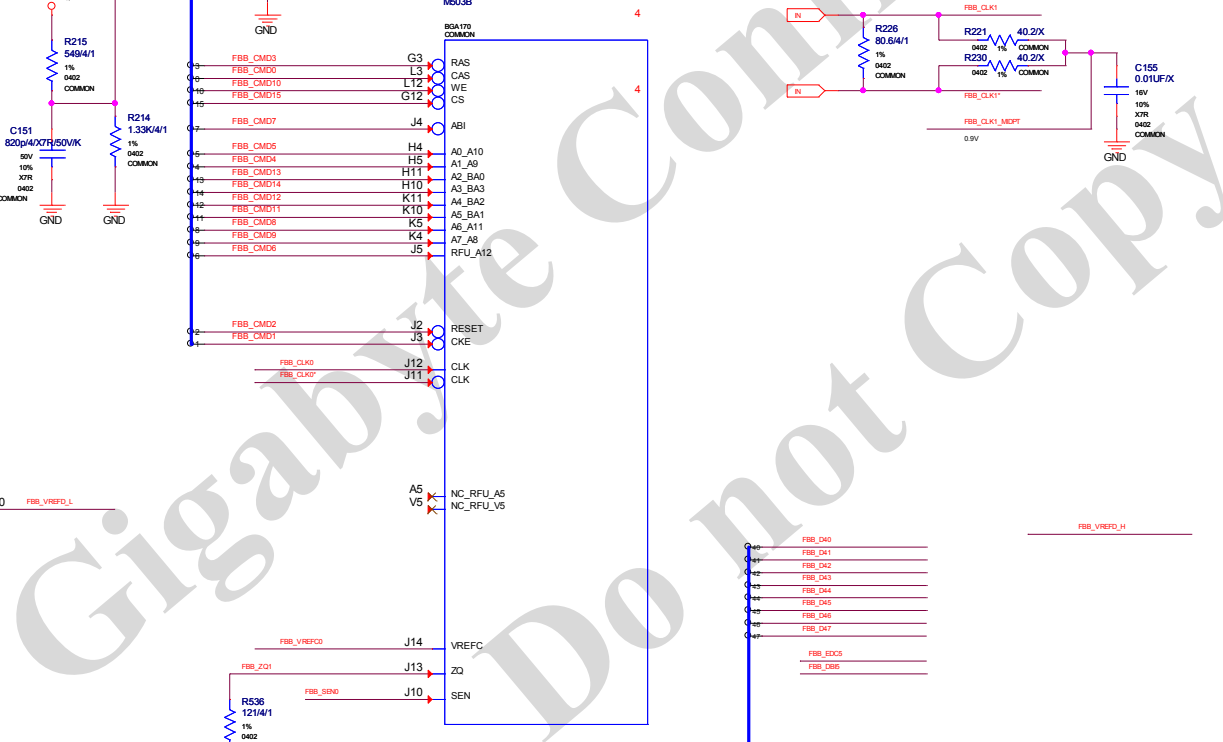


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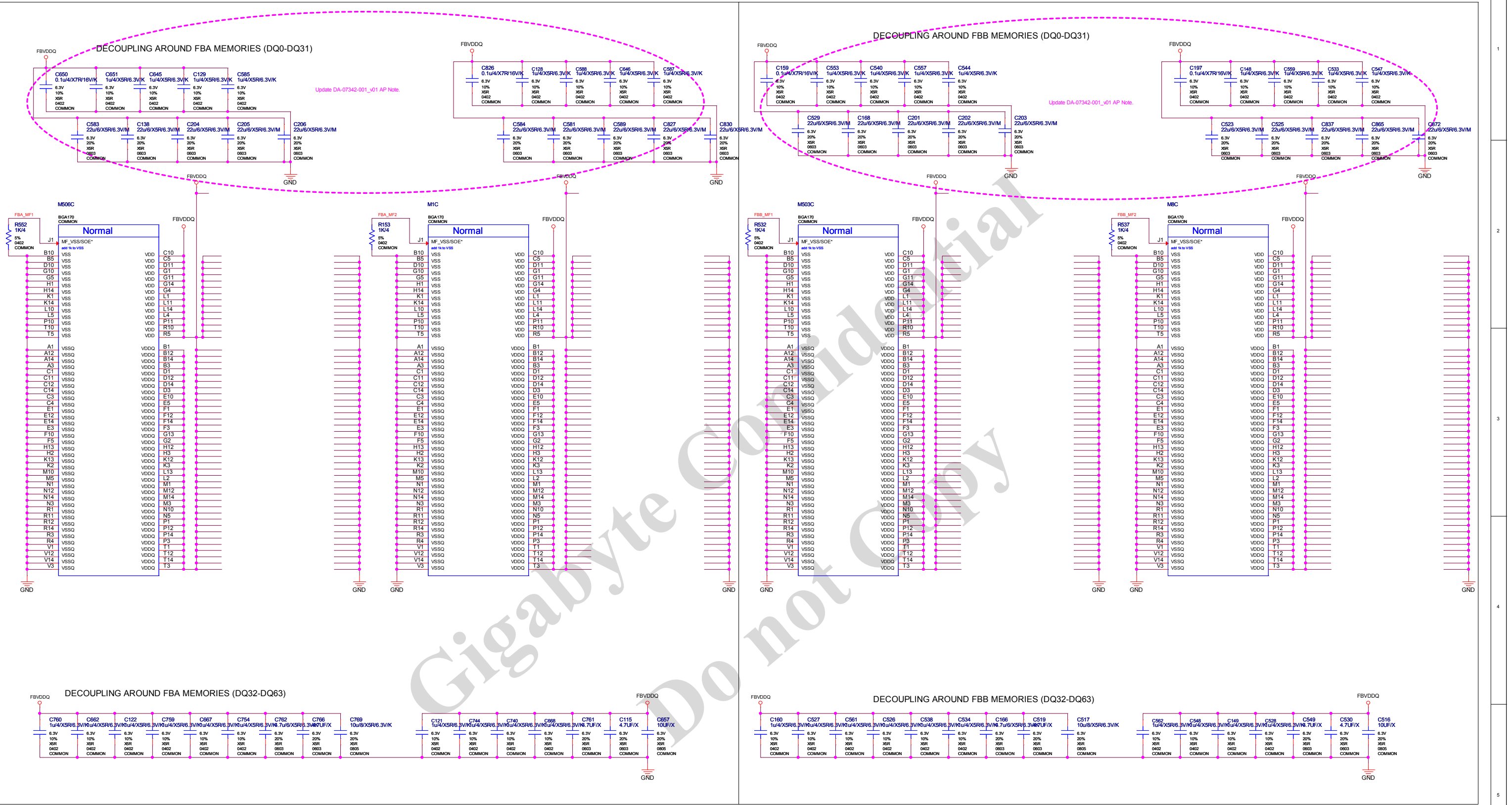
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GPU_FB_AB

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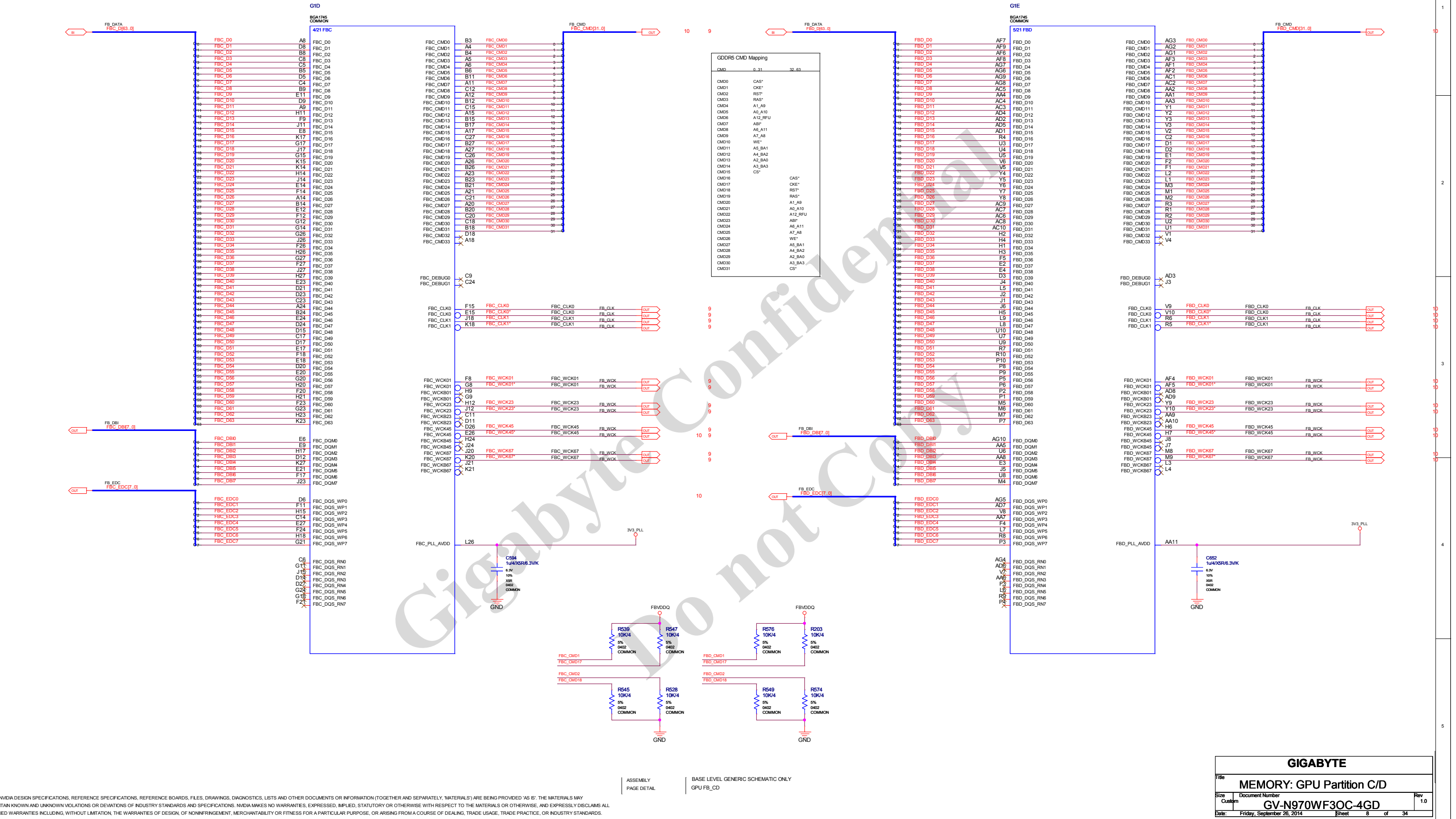
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MEM_FB_AB_PWR

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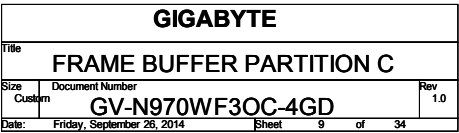


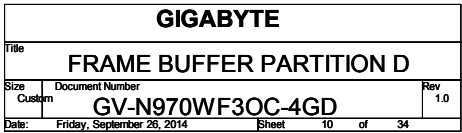
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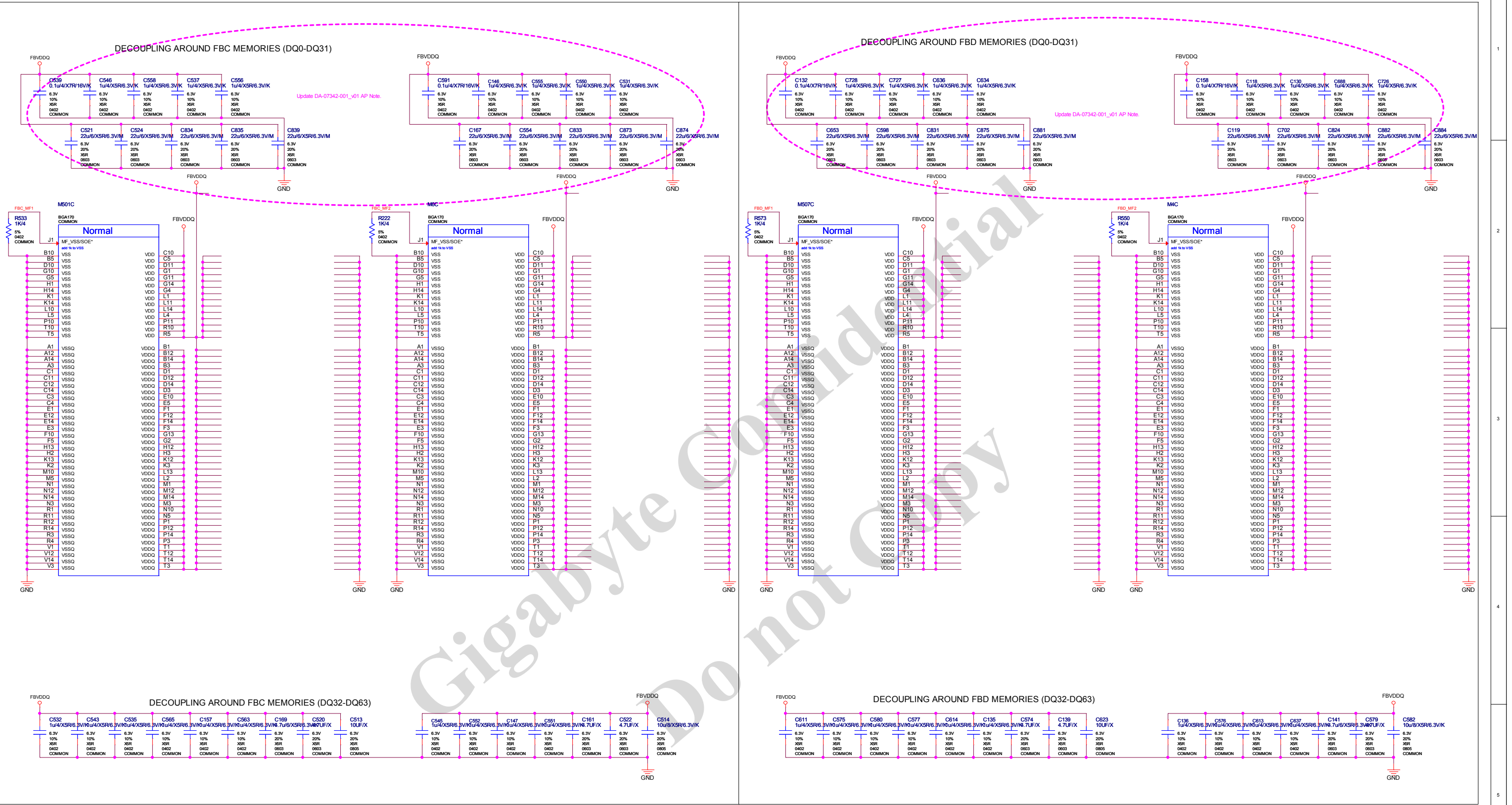
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GPU_FB_CD

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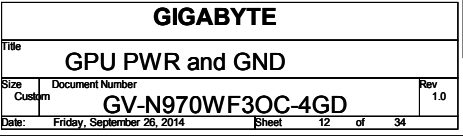


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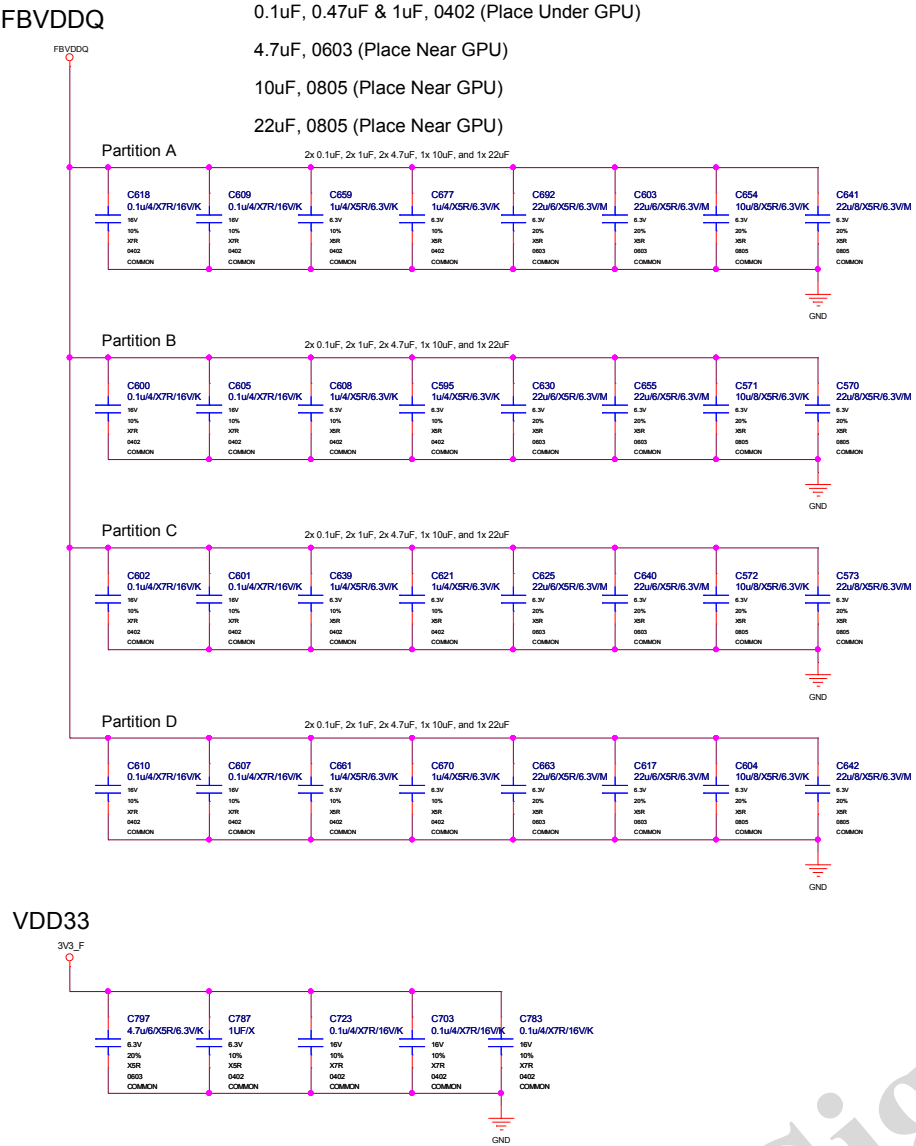
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MEM_FB_CD_PWR

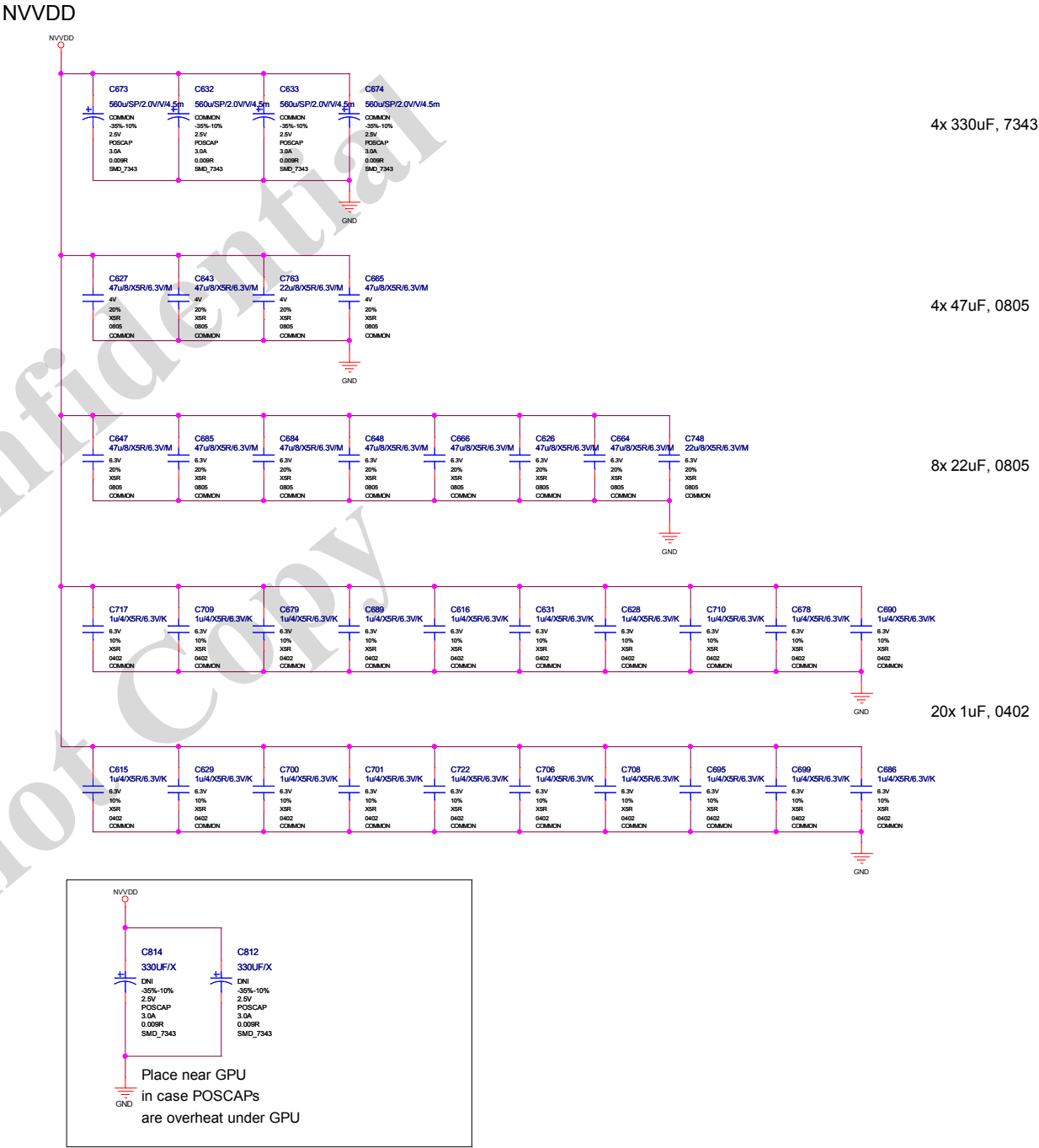
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FRAME BUFFER PARTITION C/D DECOUPLING			
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Based on GB2-X GDDR5 FBVDDQ Decap Guideline



NVVDD Decoupling caps. Place under GPU.

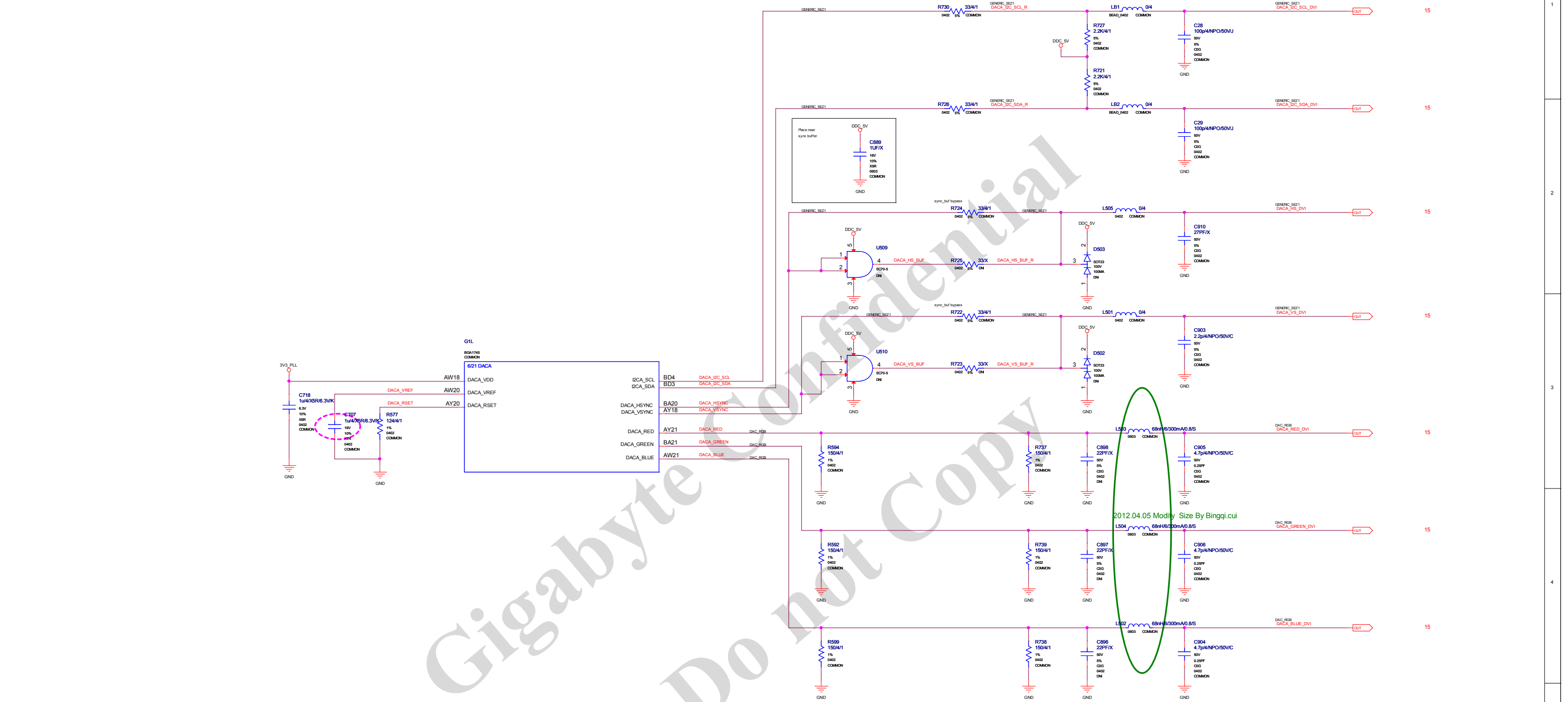


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GPU Decoupling

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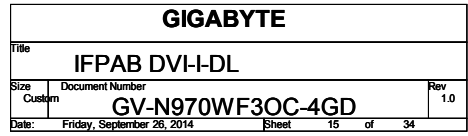


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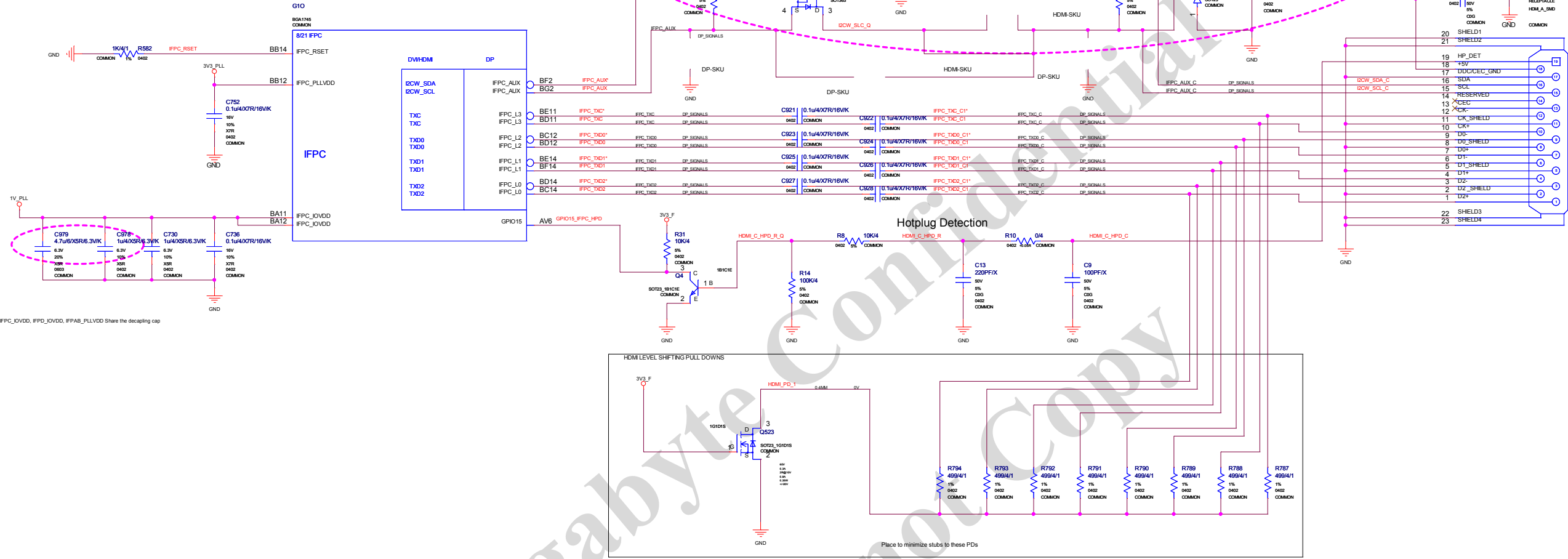
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DACA Interface

GIGABYTE			
Title DACA Interface			
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Two cases to be considered:
1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through

* I2C to DDC level switching

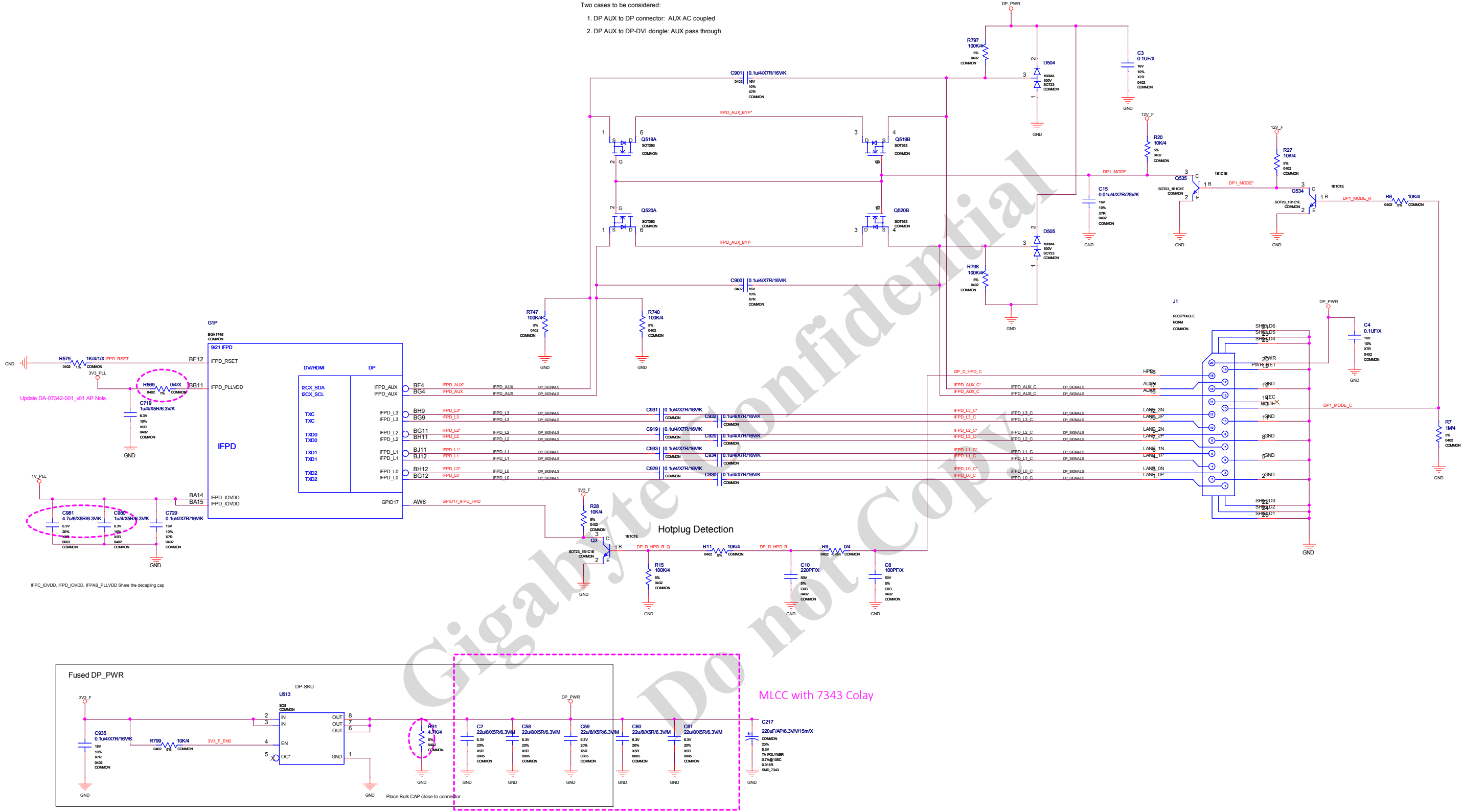


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IFPC HDMI/DP

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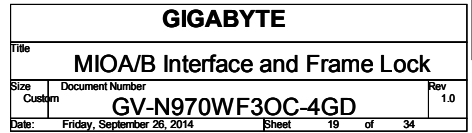
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 - 2. DP AUX to DP-DVI dongle: AUX pass through

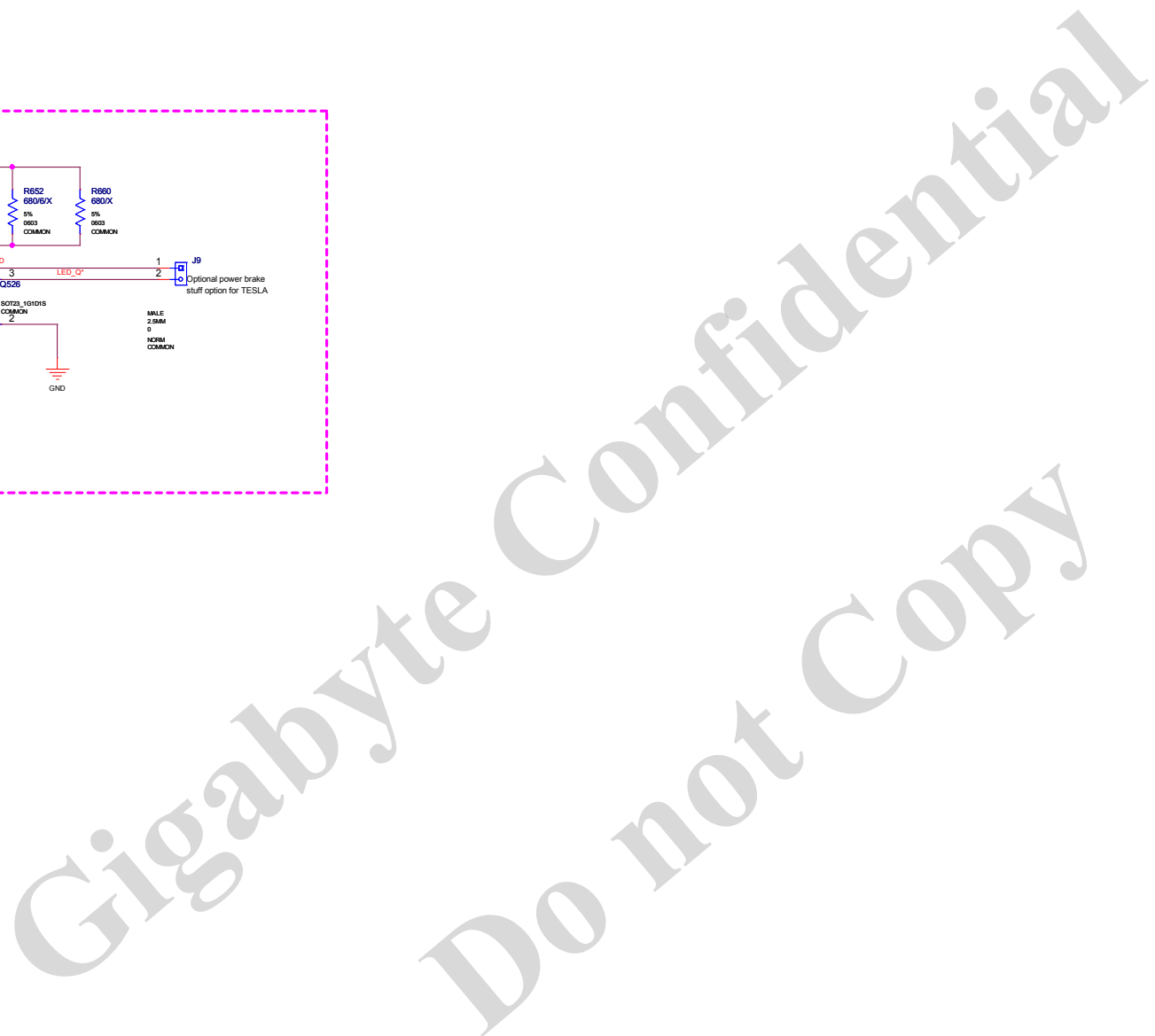


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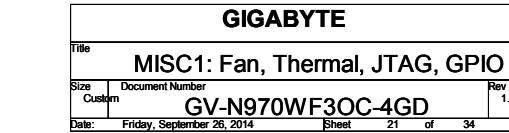
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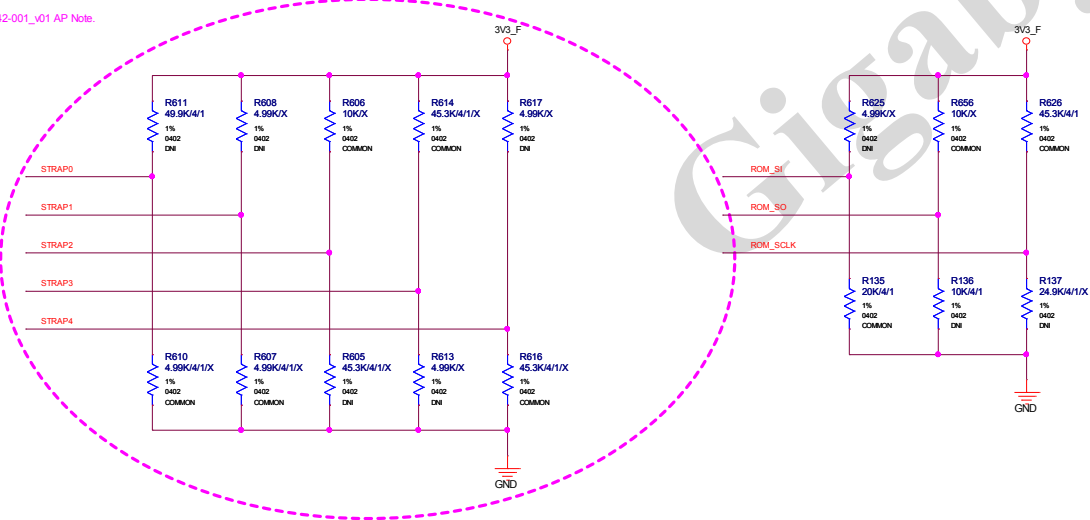
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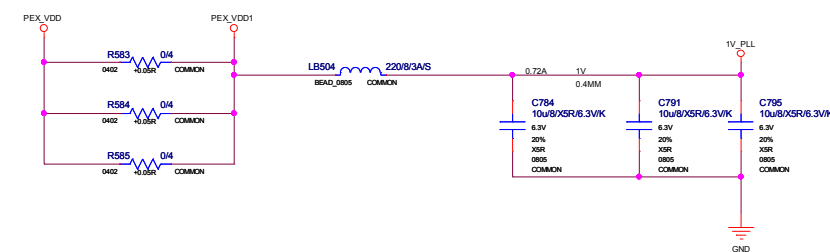
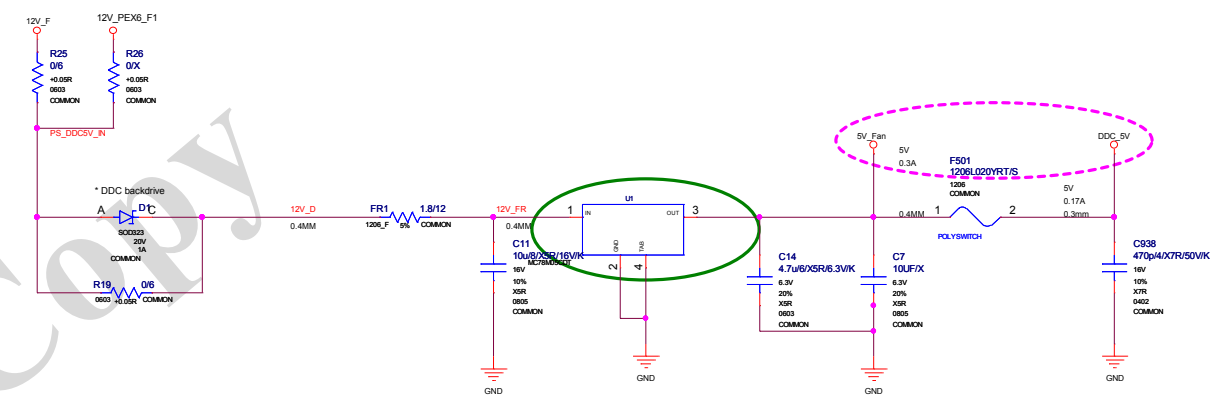
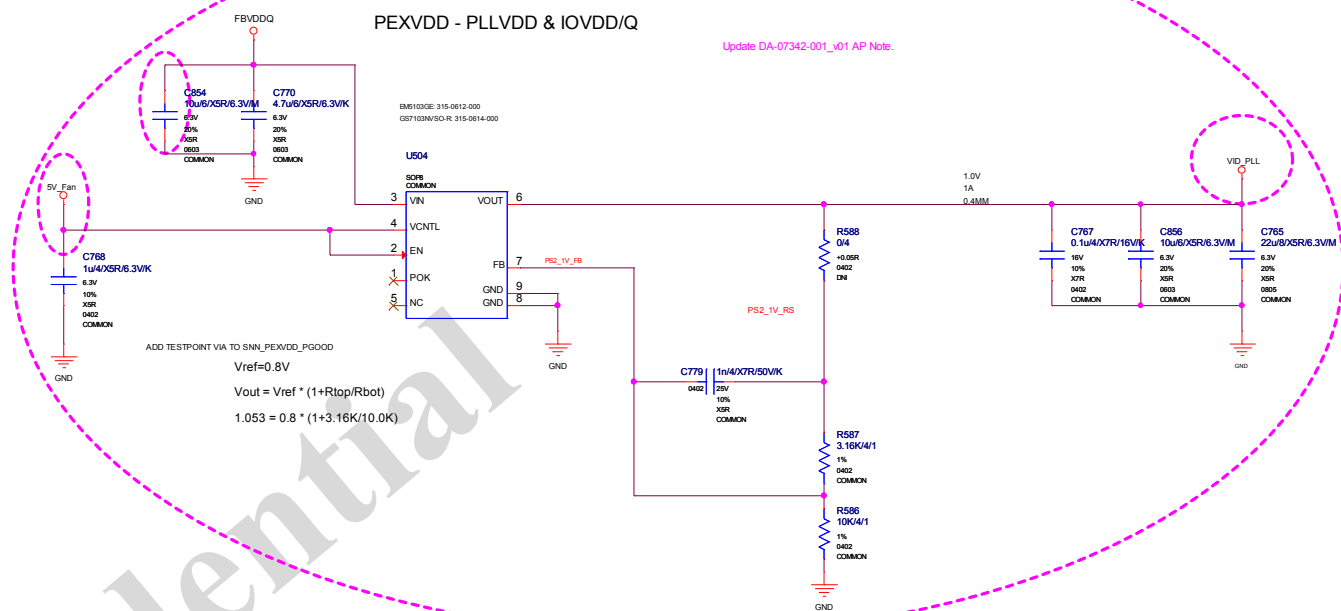


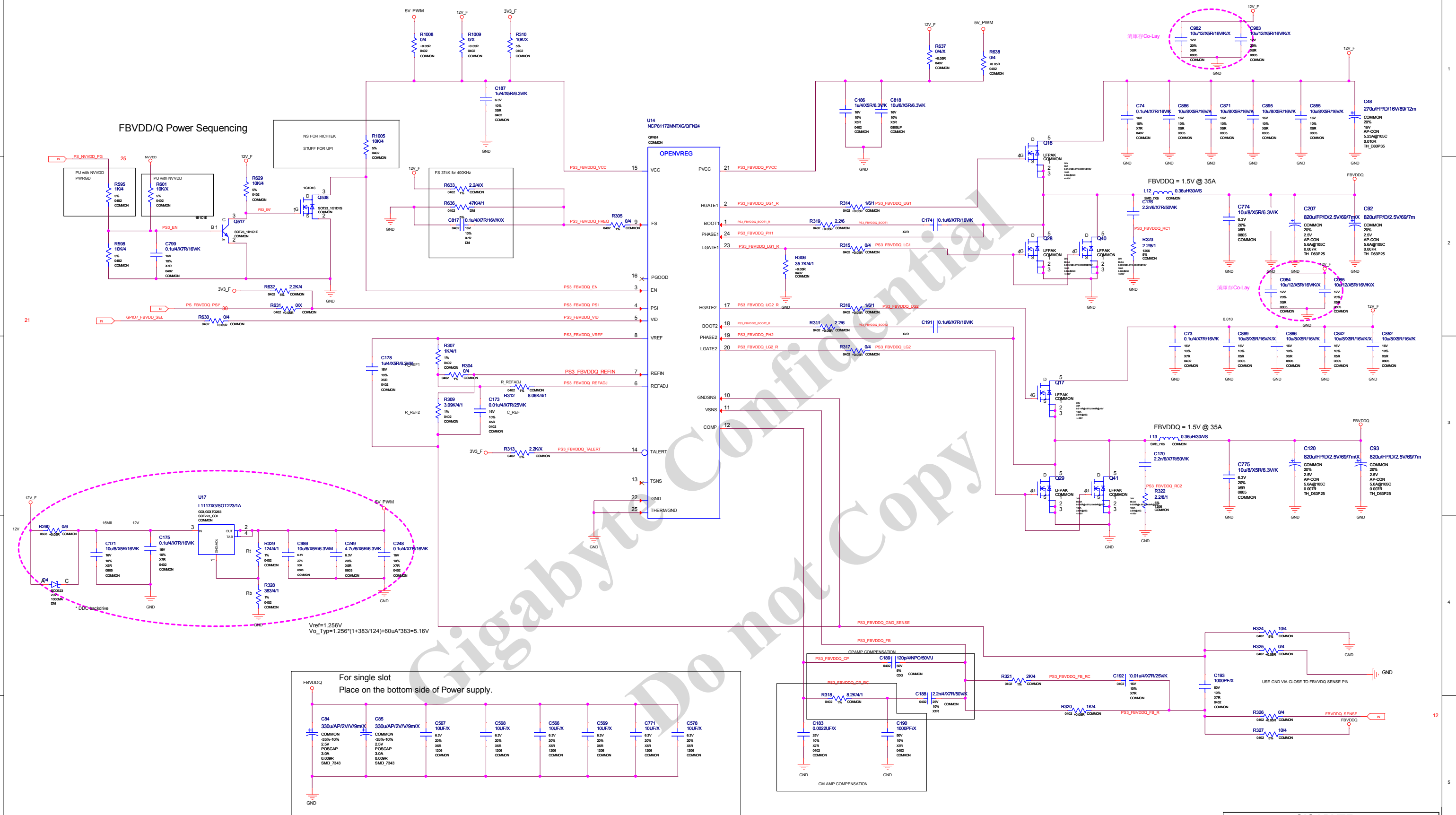
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STRAP1	3GIO_PADCFG_LUT_ADR*	0000*	5K PD Desktop*
STRAP2	PCI_DEVID [3:0]*	0111 - (0x1187)*	10K PU -325 GPU*
STRAP3	SOR_EXPOSED [3:0]*	1111*	45K PU*
STRAP4	DP_PLL_VDD_33V*	1* FOR 3_3V*	
	PEX_MAX_SPEED*	1* FOR GEN2/3*	45K PD*
	PEX_SPD_CHANGE_GEN3*	1* ENABLED*	
	*		
ROM_SI	RAMCFG[0]*	0*	
	RAMCFG[1]*	1*	64Mx32 256 bit hynix for SKEL primary memory
	RAMCFG[2]*	1*	35K PD*
	RAMCFG[3]*	0*	
ROM_SO	VGA_DEVICE*	1*	
	SMB_ALT_ADDR*	0*	30k PD*
	FB[0]_APERTURE_SIZE*	1* For 128MB*	
	FB[1]_APERTURE_SIZE*	0* For 128MB*	
ROM_SCLK	PEX_PLL_EN_TERM100*	0* DISABLE*	
	PCI_DEVID_EXT[5]*	0* For 0x1189*	25K PD*
	SUB_VENDOR*	1* Dedicated BIOS*	
	PCI_DEVID_EXT[4]*	0* For 0x1189*	

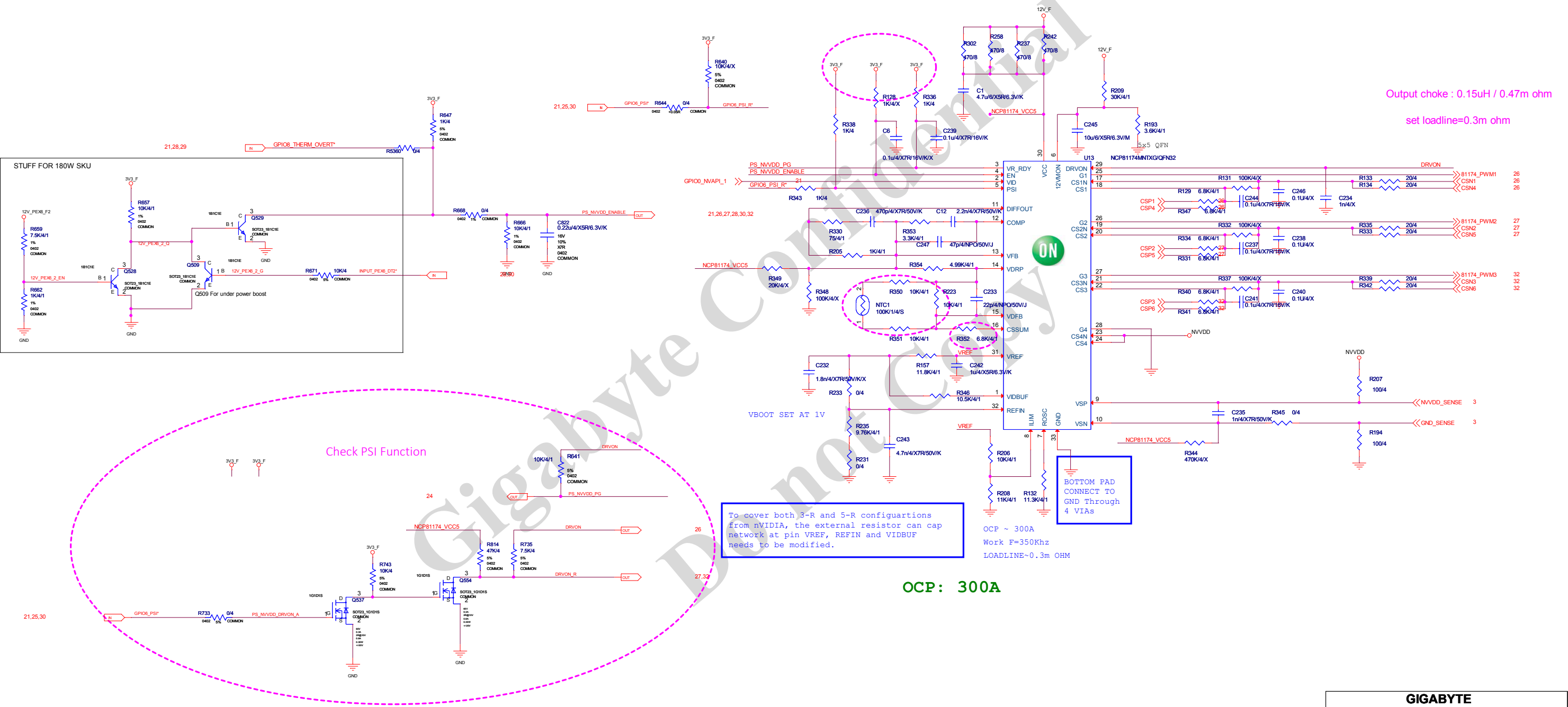
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BINARY BRINGUP	NC
MULTI-LEVEL	40.2k 1% TO GND

Update DA-07342-001_v01 AP Note.







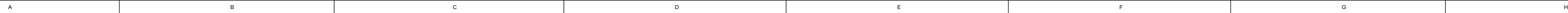


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PS: NVVDD Controller

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POWER SUPPLY: NVVDD Controller			
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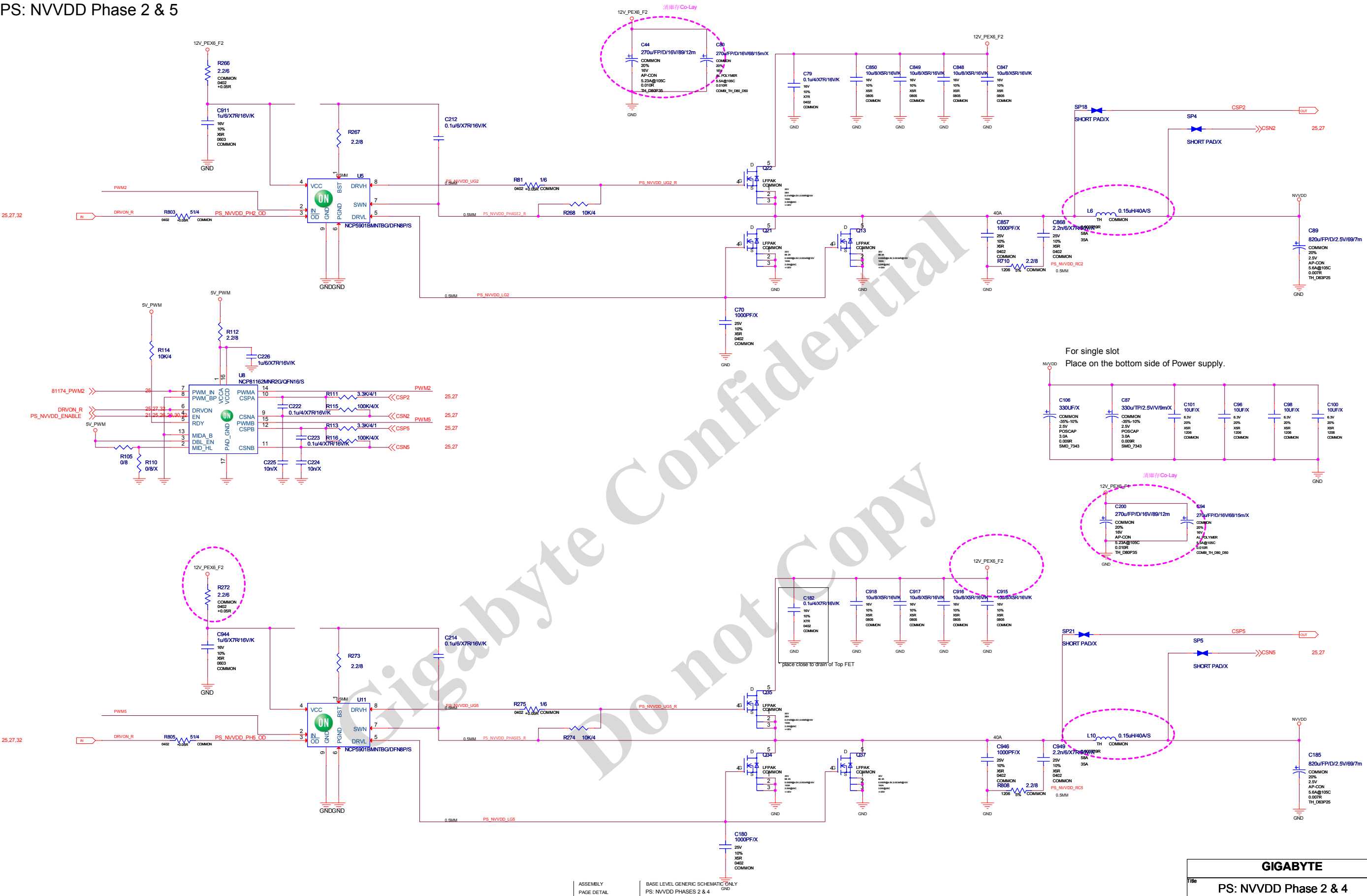


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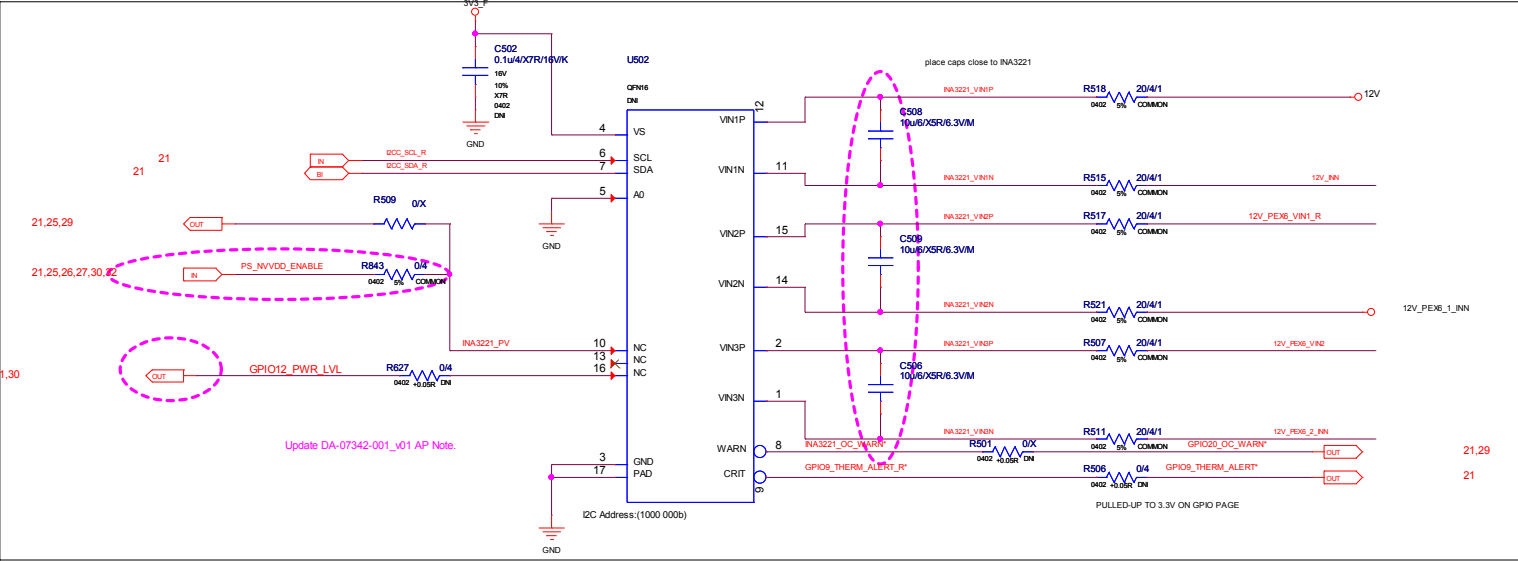
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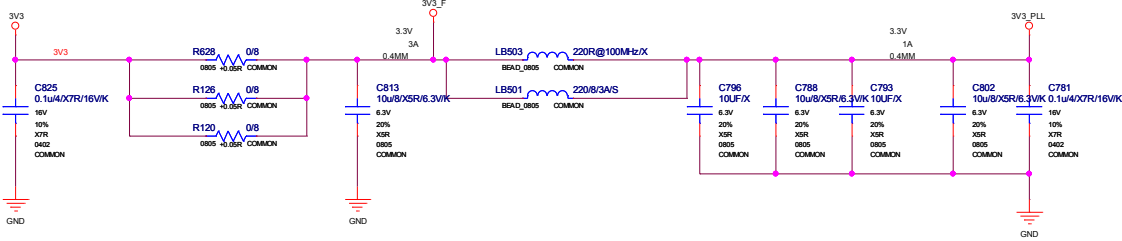
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PS: NVVDD PHASES 2 & 4

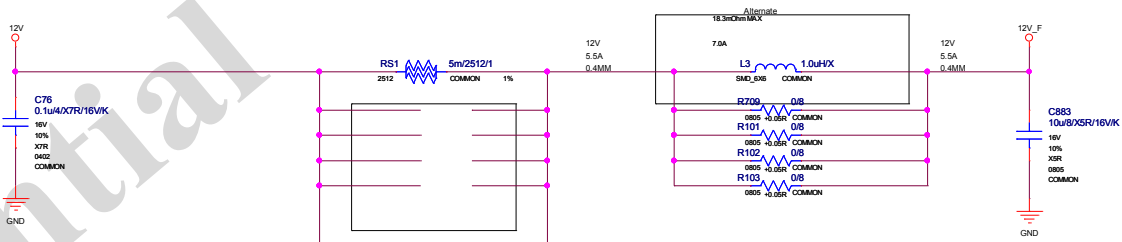
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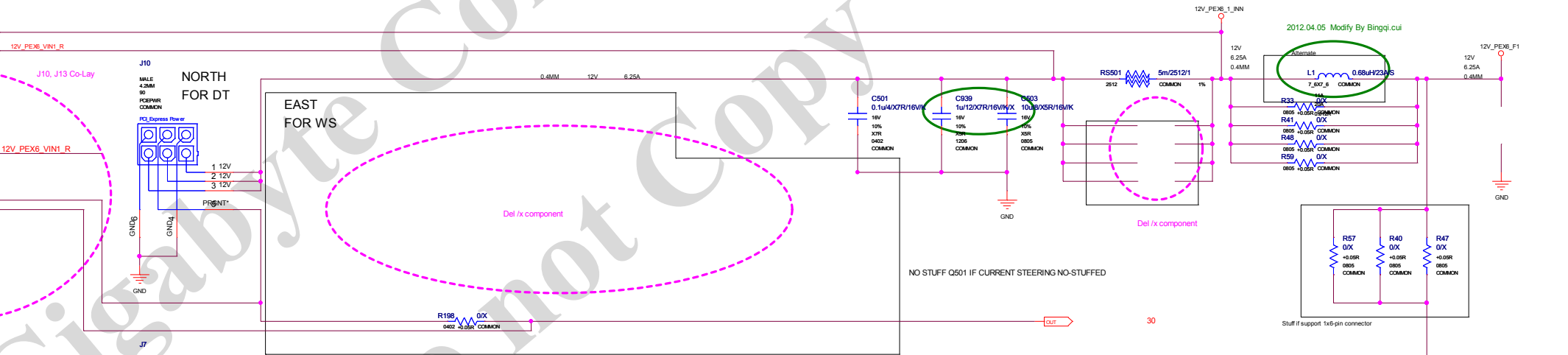
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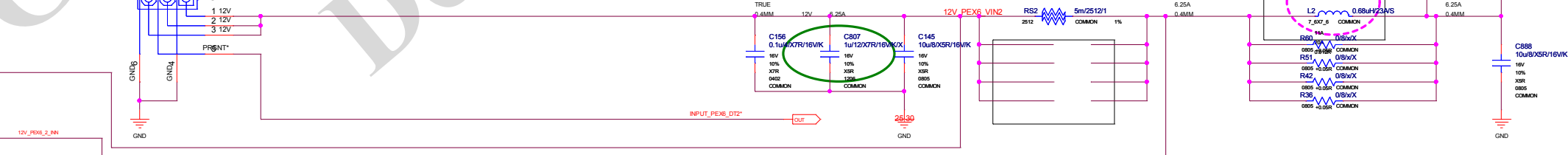
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PEX6 INPUT 1 - 2x3 PCIE CON 75W



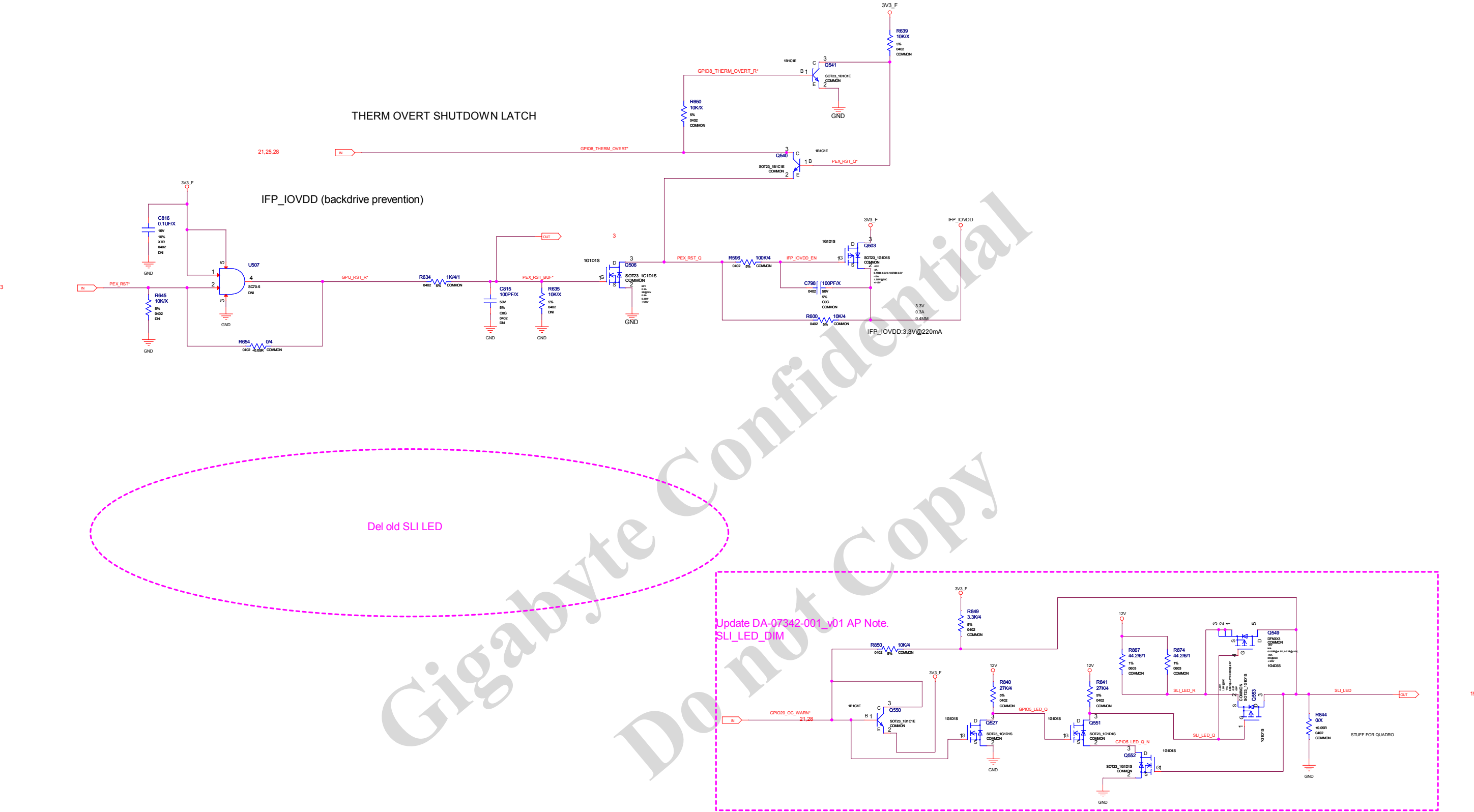
PEX6 INPUT 2 - 2x3 PCIE CON 75W



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PS: Inputs, Filtering, and Monitoring

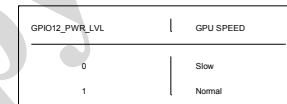
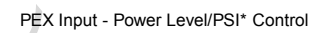
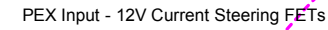
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Title	Inputs, Filtering, and Monitoring		
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ASSEMBLY PAGE DETAIL
BASE LEVEL GENERIC SCHEMATIC ONLY
PS: Thermal Shutdown & Lighted SLI DRIVER

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Title	Thermal Shutdown& Light SLI DRIVER		
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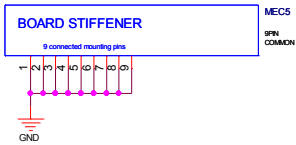


ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	PS: 12V Current Steering

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Title 12V Current Steering			
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Brackets:

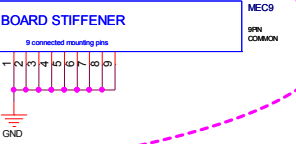
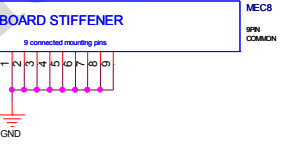
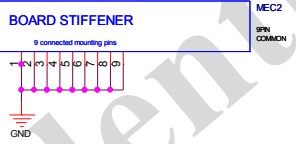
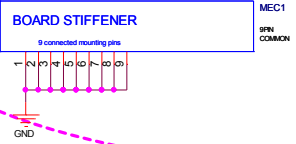
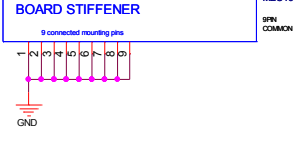
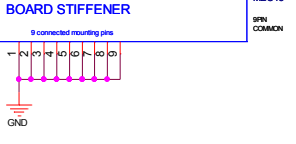
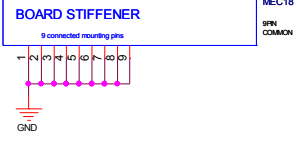
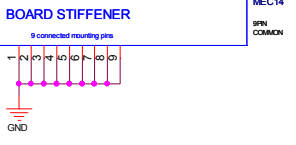
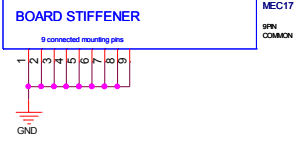
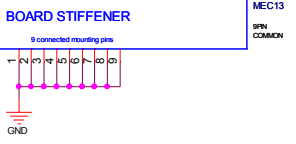
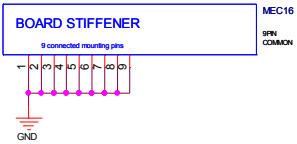
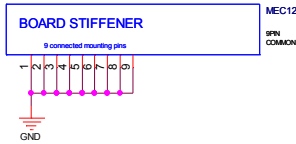
MH-C236D118-8



GPU Stiffener:

For GPU

MOUNTING HOLES FOR HS:



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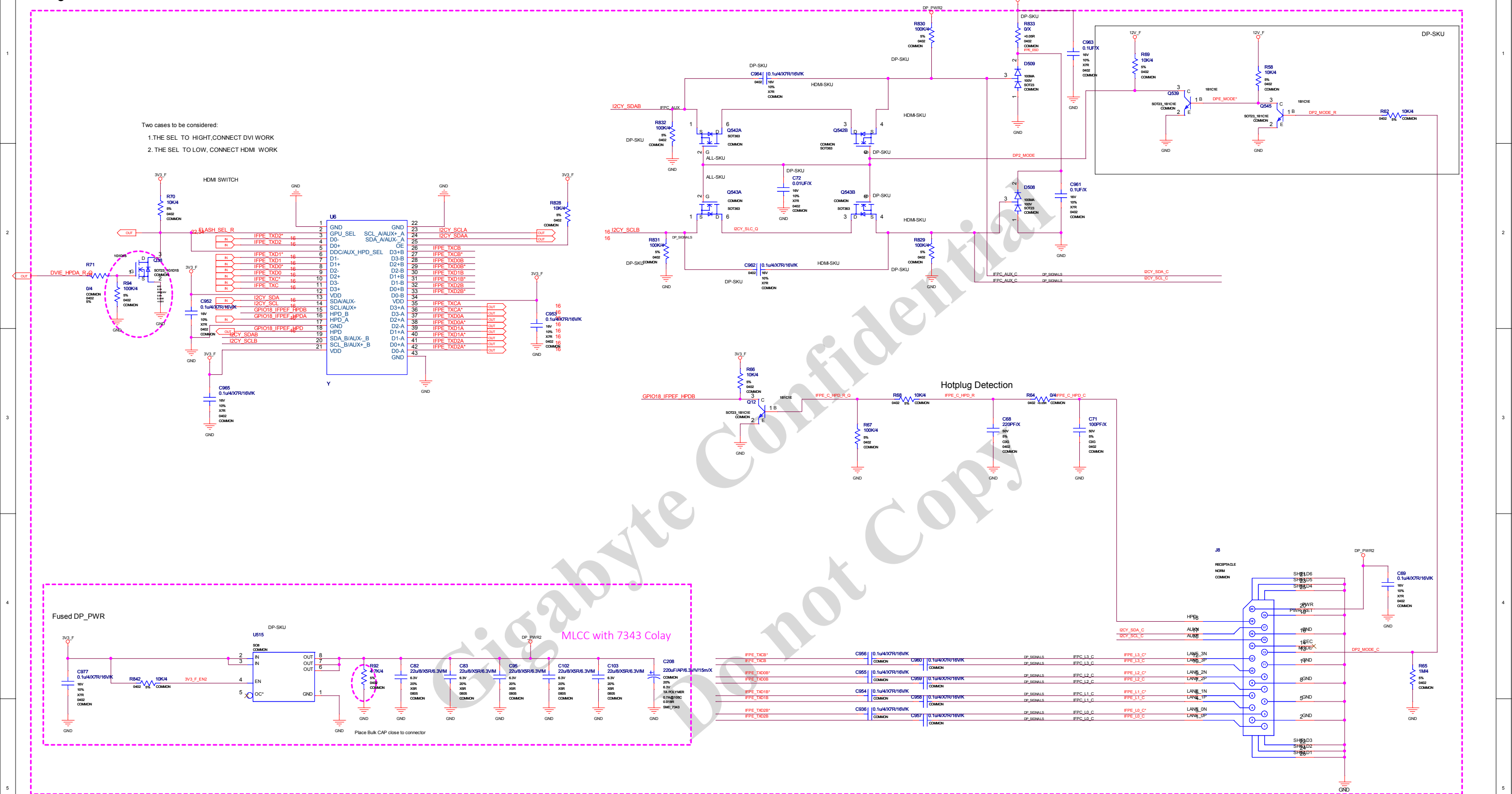
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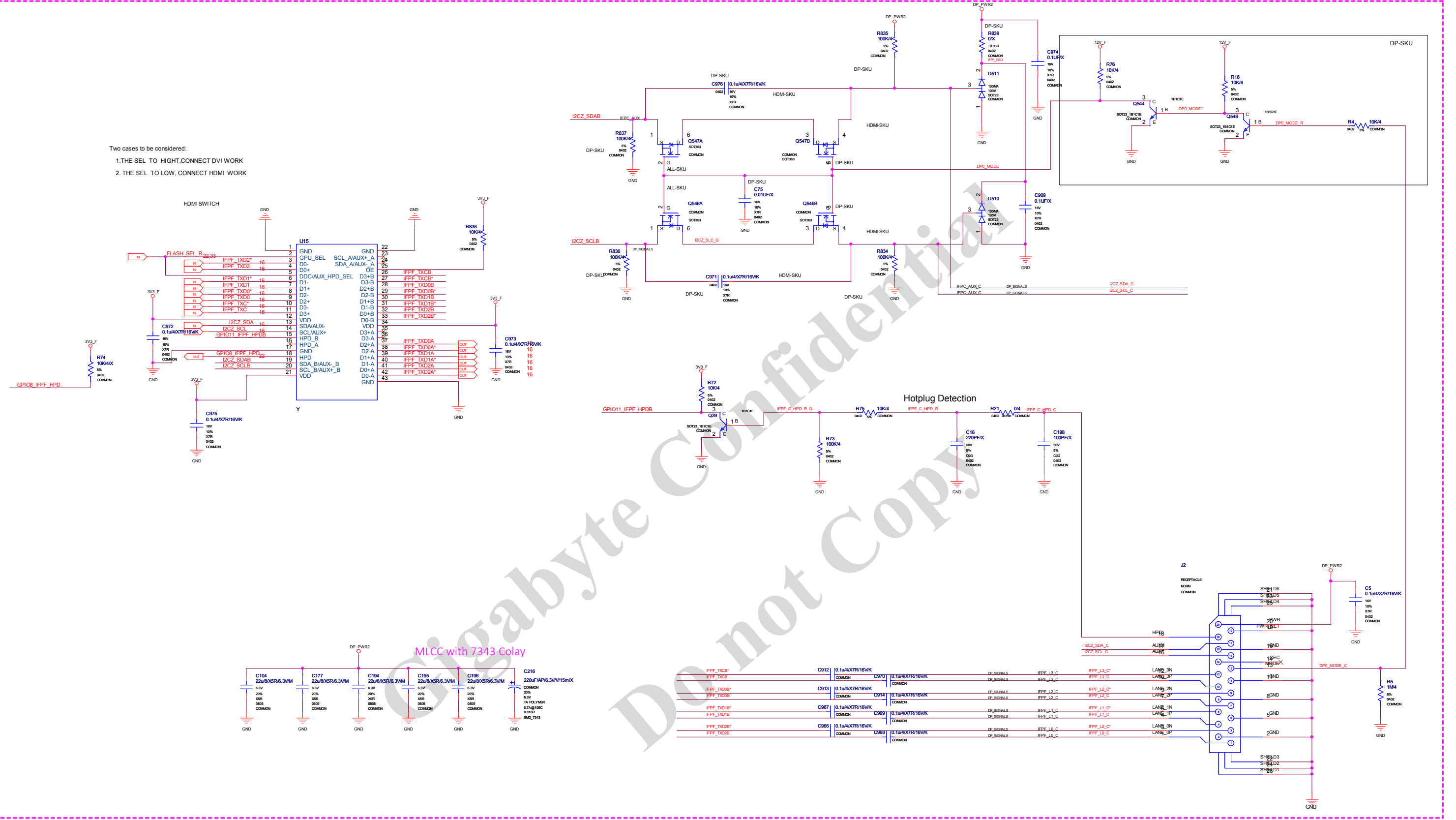
BASE LEVEL GENERIC SCHEMATIC ONLY
MECH

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Title			
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