
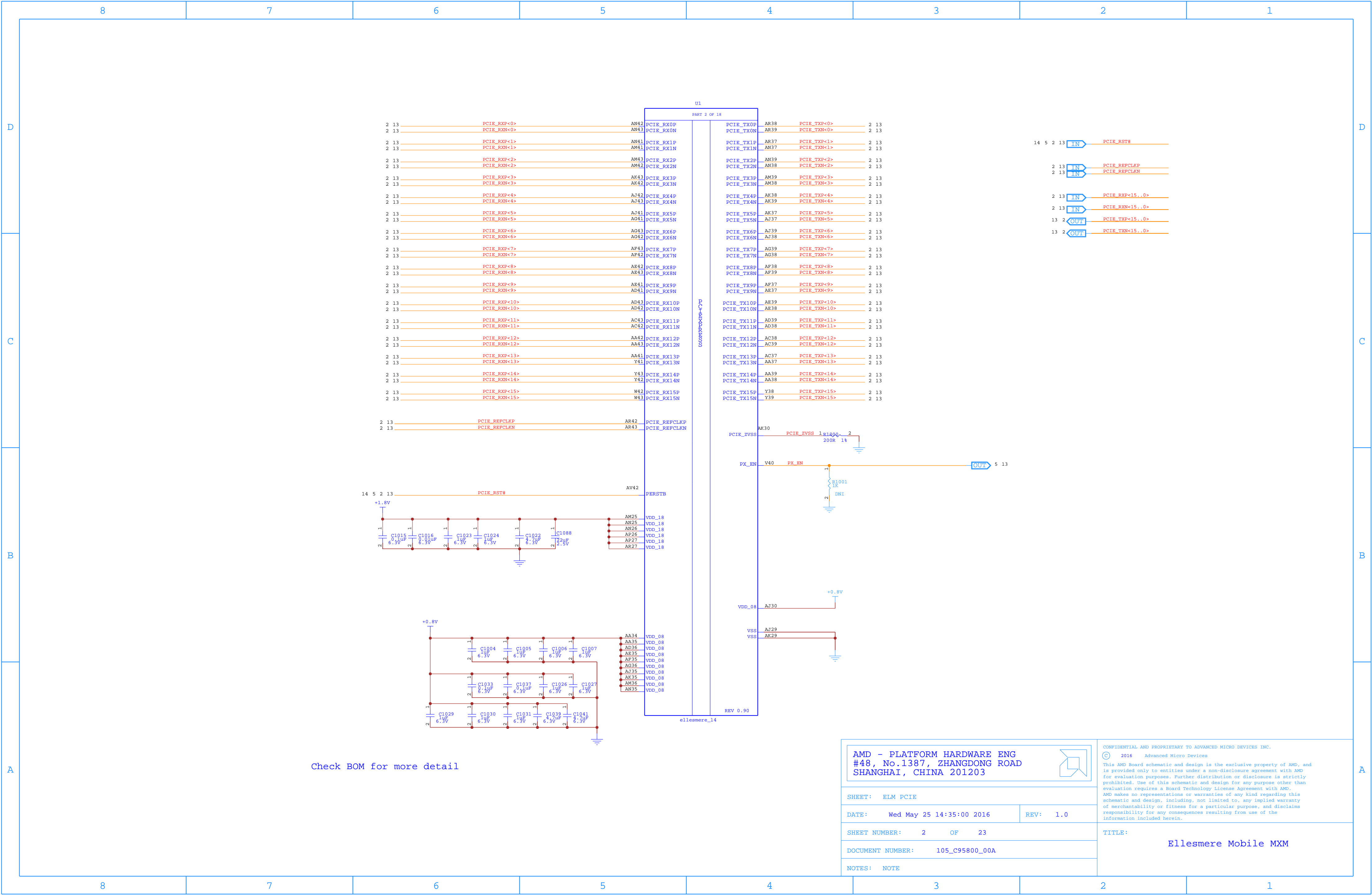


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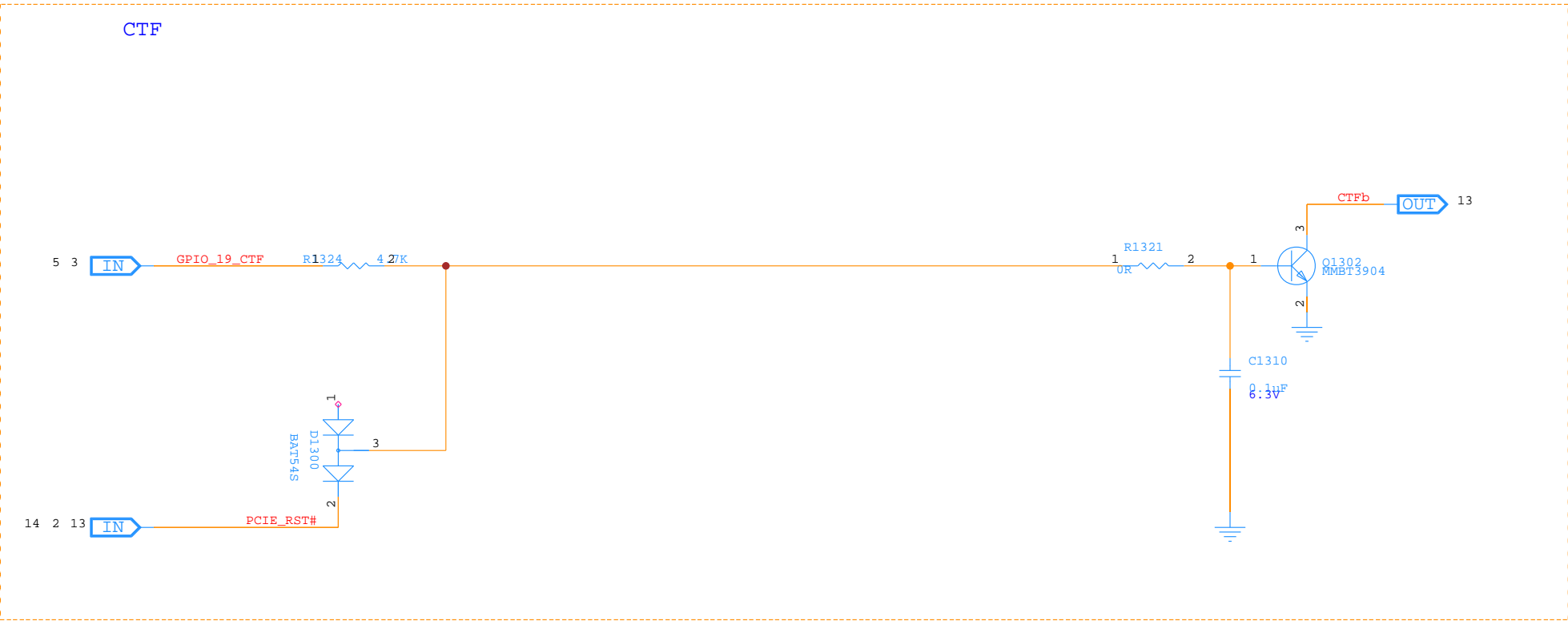
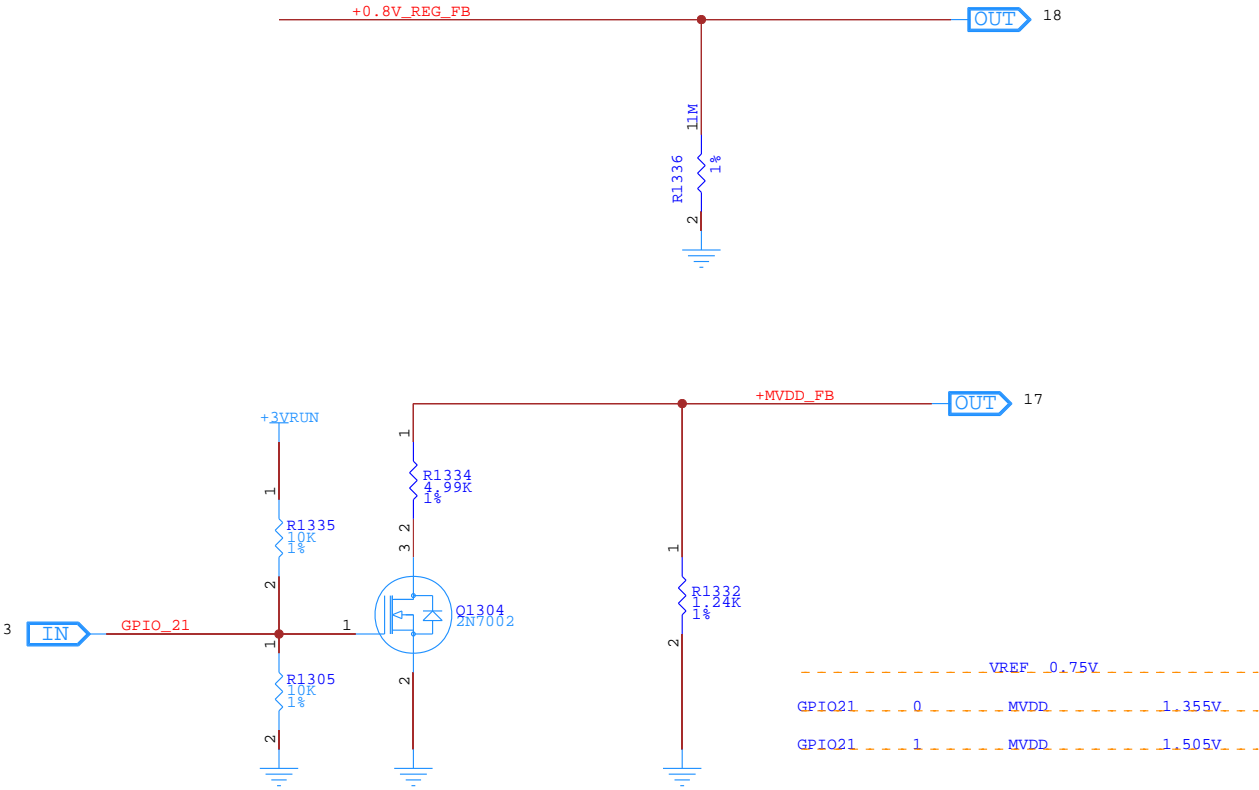
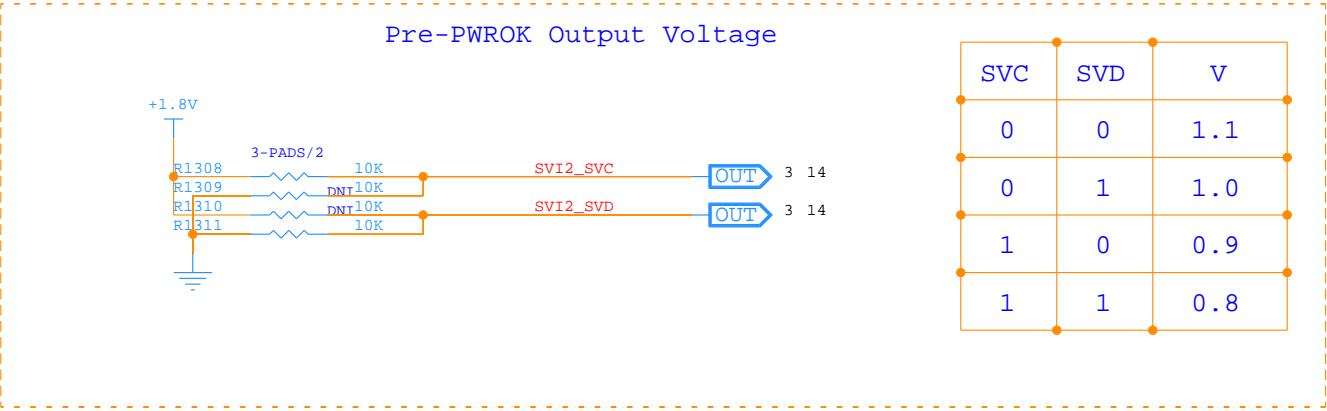
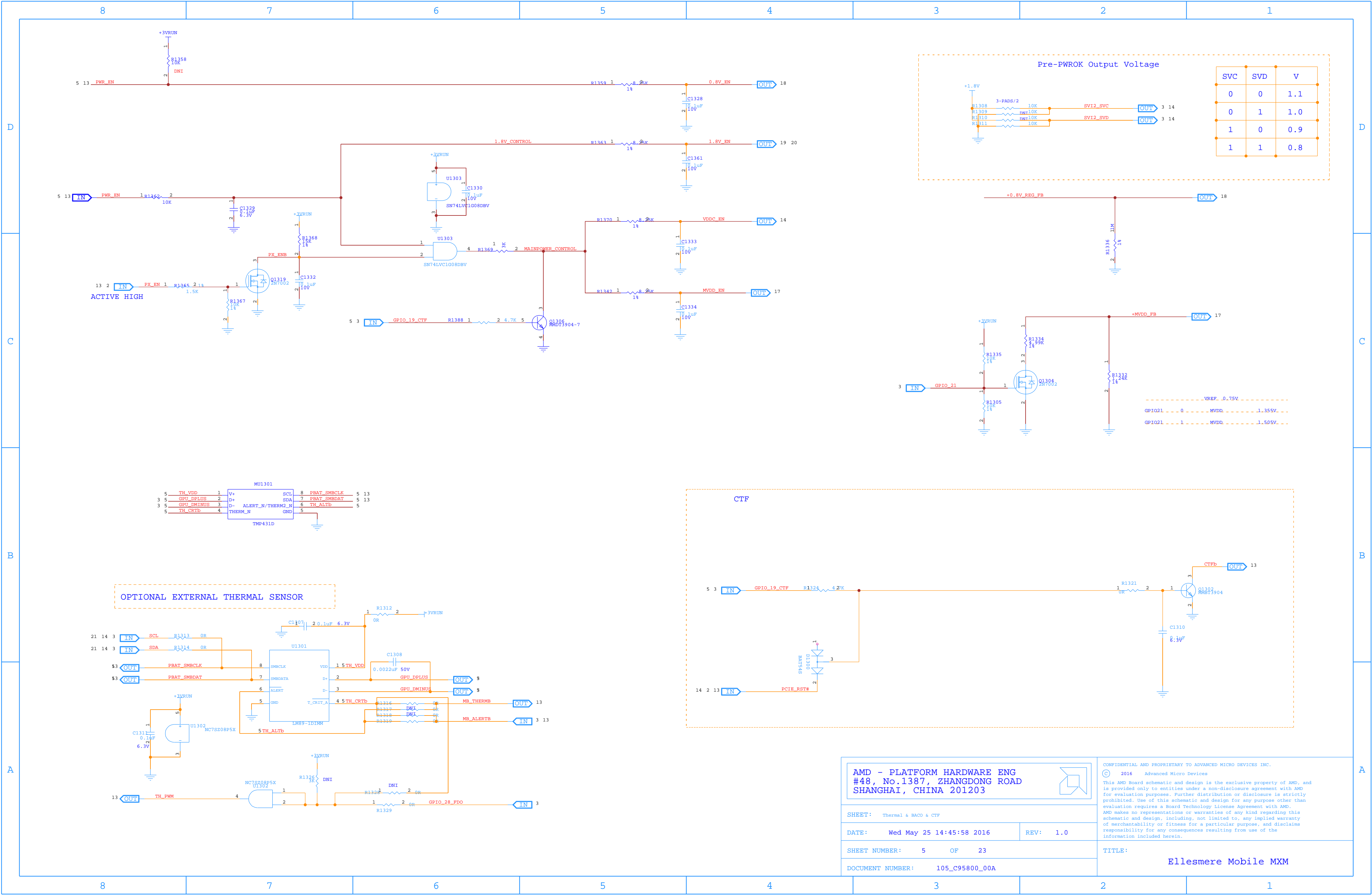
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2	ELM PCIE
3	ELM Main
4	ELM CLK
5	Thermal & BACO & CTF
6	ELM GND
7	ELM POWER
8	ELM MEM CH AB
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13	MXM3.1 Interface
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22	Block Diagram
23	REVISION HISTORY

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SHEET: TOC			TITLE: Ellesmere Mobile MXM	
DATE: Mon Mar 30 09:36:44 2015	REV: 1.0			
SHEET NUMBER: 1 OF 23				
DOCUMENT NUMBER: 105_C95800_00A				
NOTES: NOTE				



Check BOM for more detail

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SHEET: ELM PCIE		TITLE: Ellesmere Mobile MXM	
DATE: Wed May 25 14:35:00 2016		REV: 1.0	
SHEET NUMBER: 2 OF 23			
DOCUMENT NUMBER: 105_C95800_00A			
NOTES: NOTE			



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SHEET: Thermal & BACO & CTF

DATE: Wed May 25 14:45:58 2016

SHEET NUMBER: 5 OF 23

DOCUMENT NUMBER: 105_c95800_00A

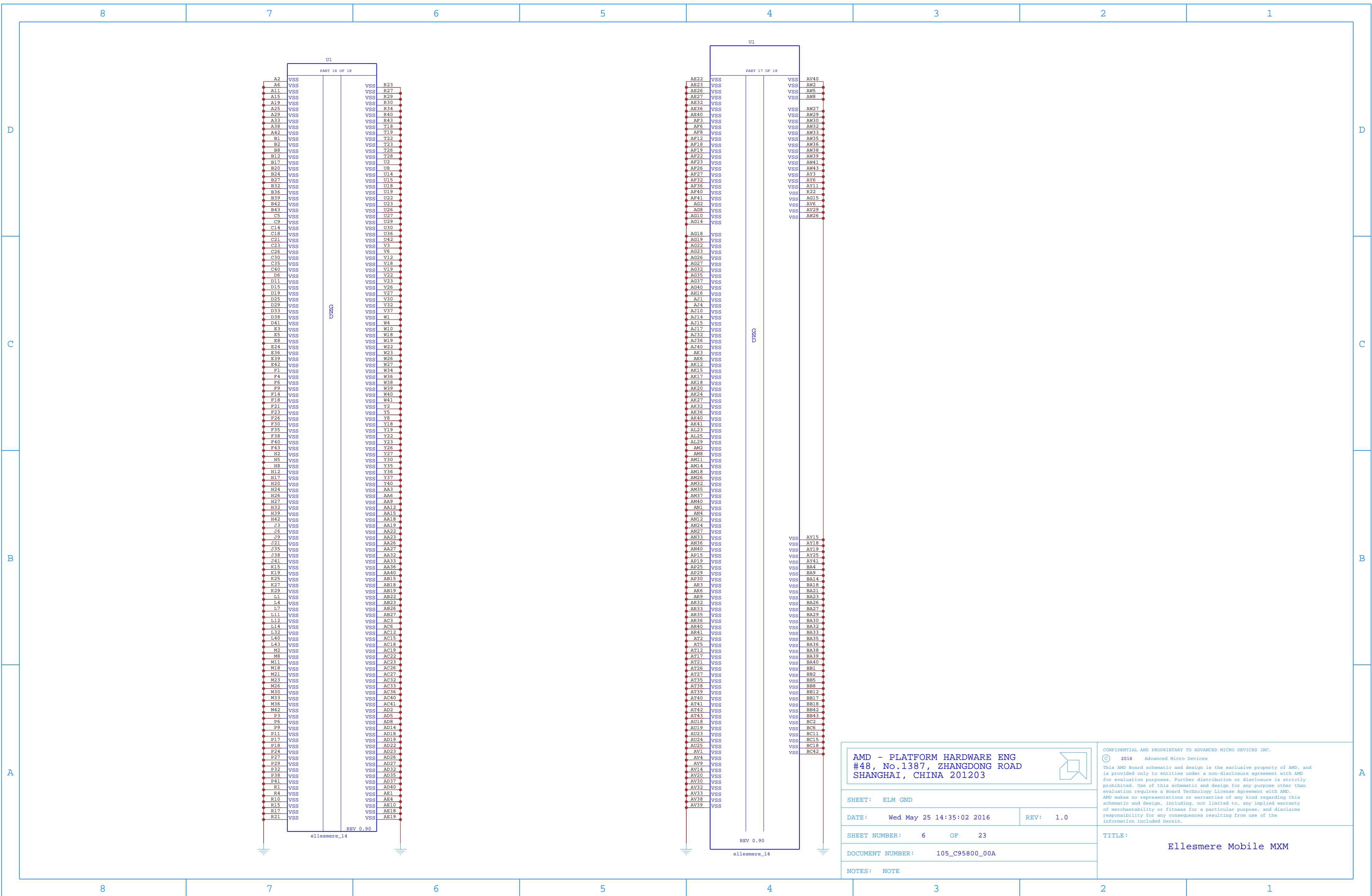
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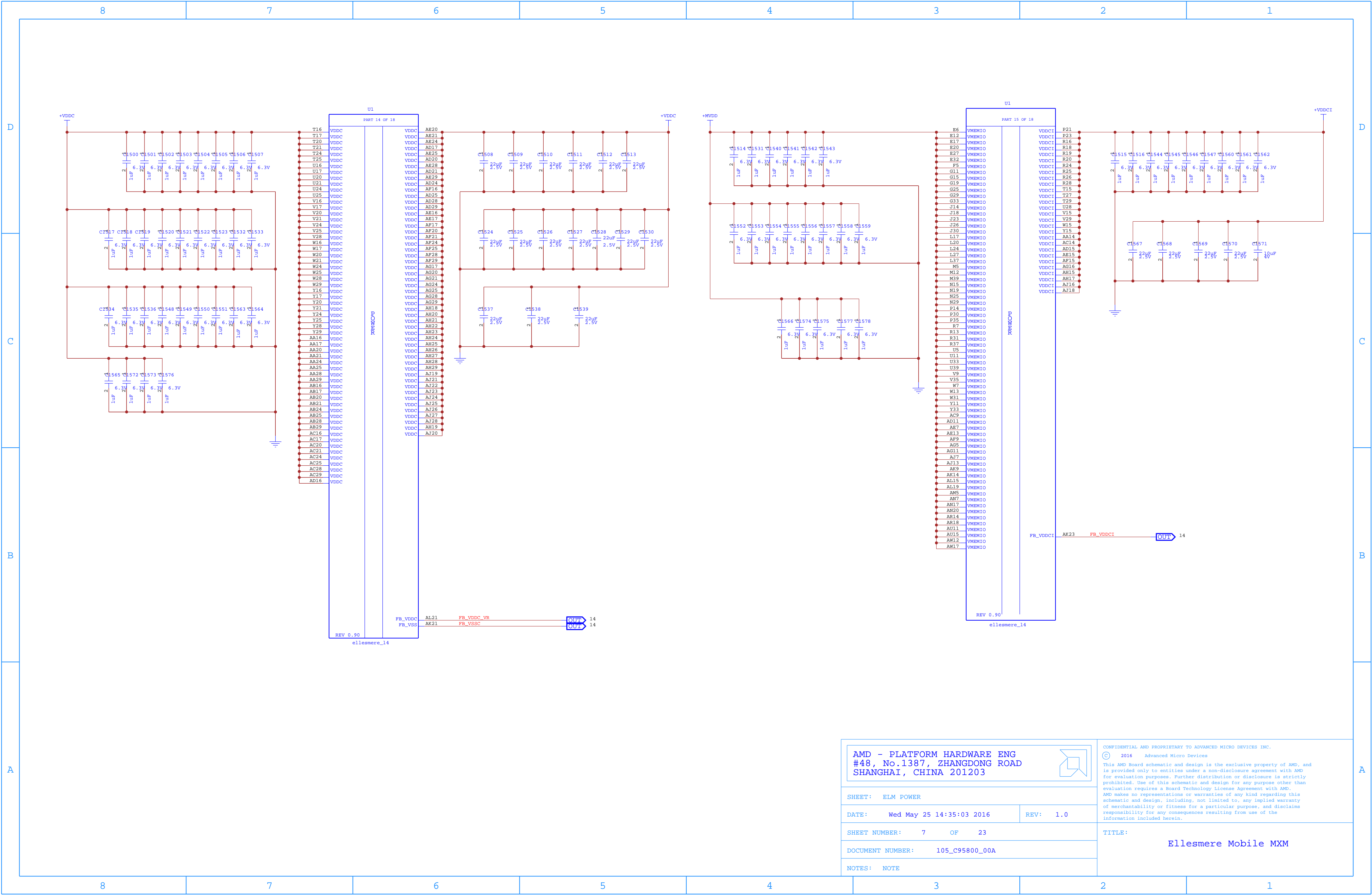
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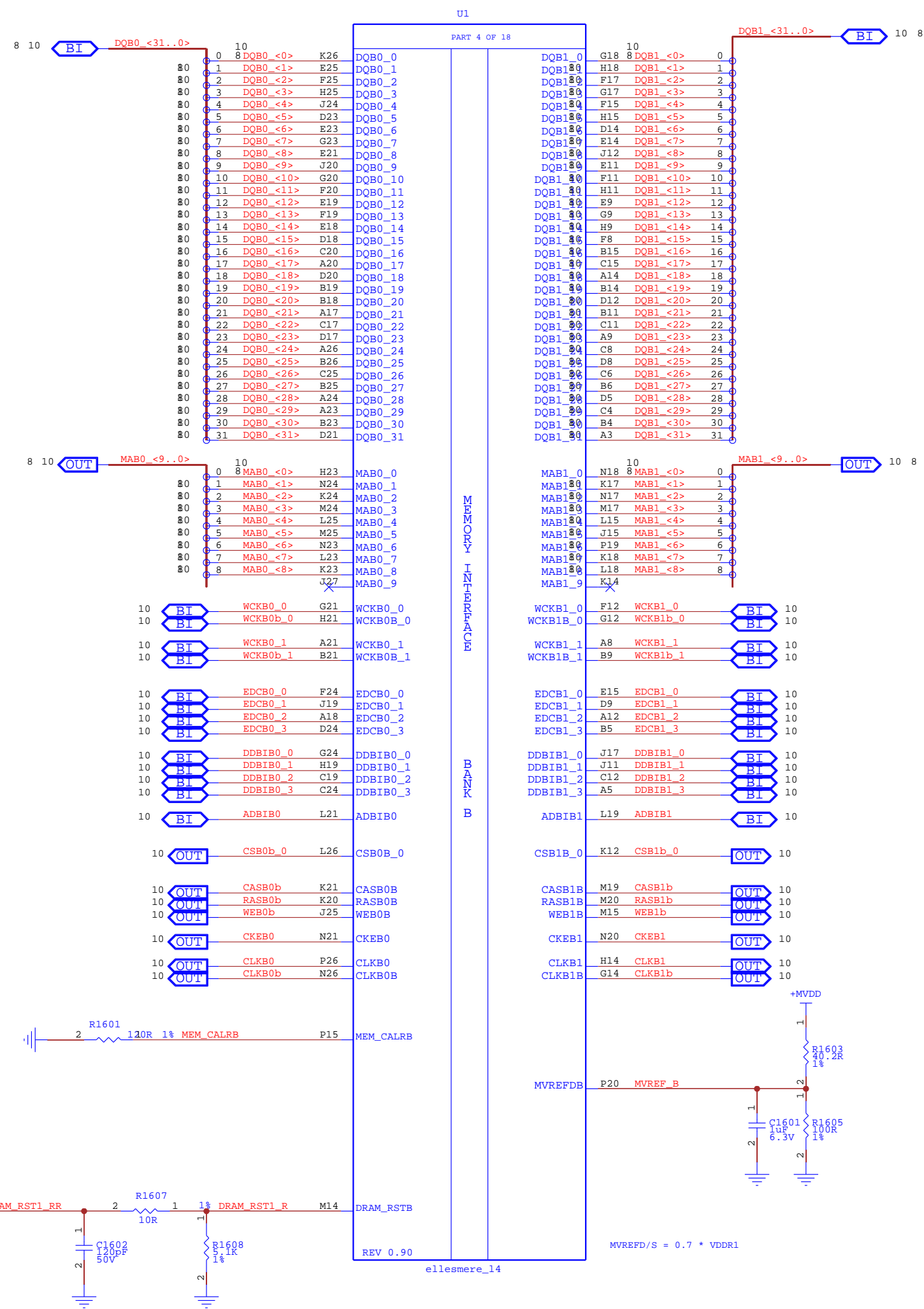
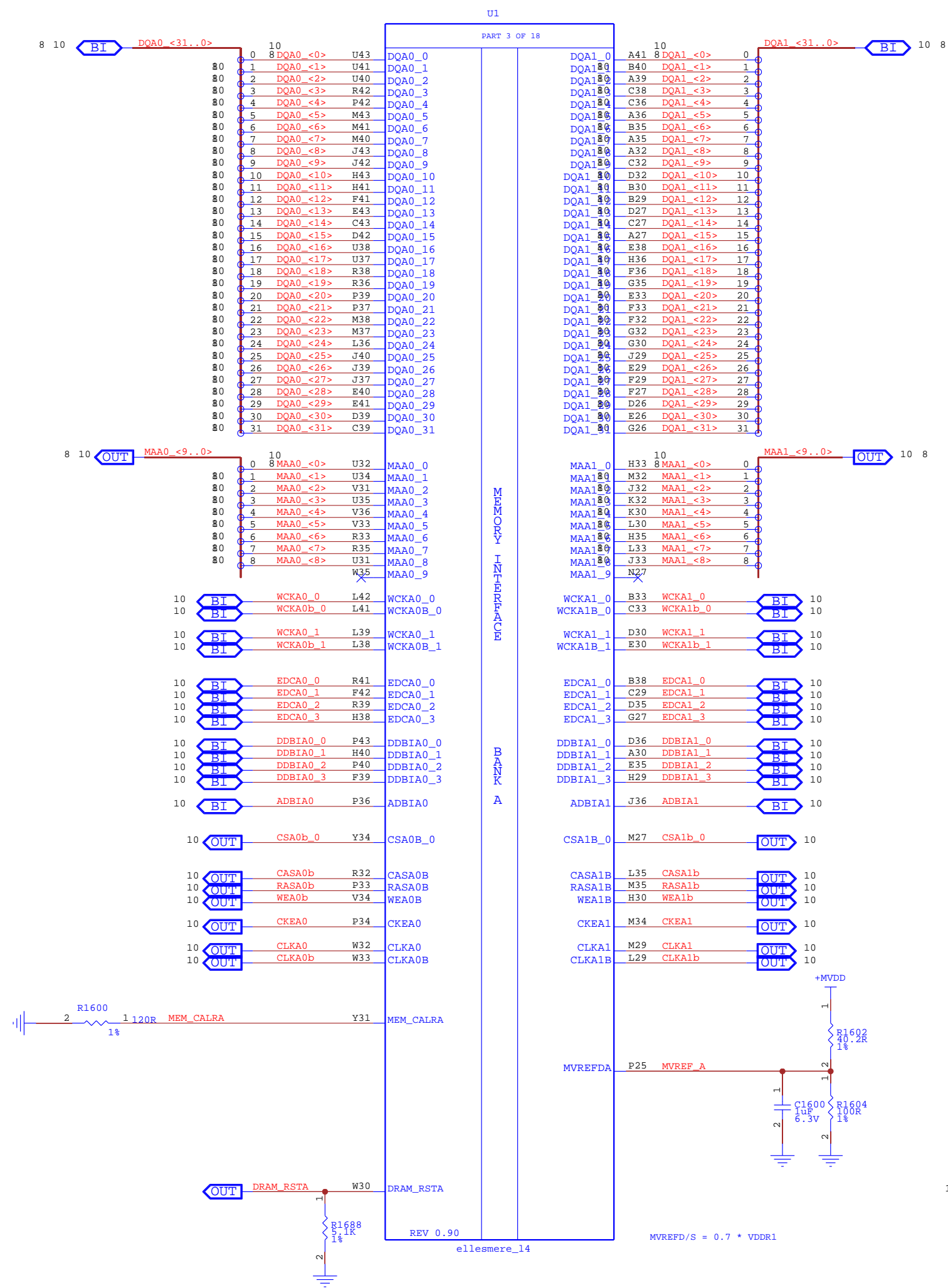
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SHEET: ELM MEM CH AB

DATE: Wed May 25 14:35:03 2016

SHEET NUMBER: 8 OF 23

DOCUMENT NUMBER: 105_C95800_00A

NOTES: NOTE

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Ellesmere Mobile MXM

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SHEET: GDDR5 x32 CH CD

DATE: Wed May 25 14:35:05 2016

REV: 1.0

SHEET NUMBER: 11 OF 23

DOCUMENT NUMBER: 105_C95800_00A

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Ellesmere Mobile MXM

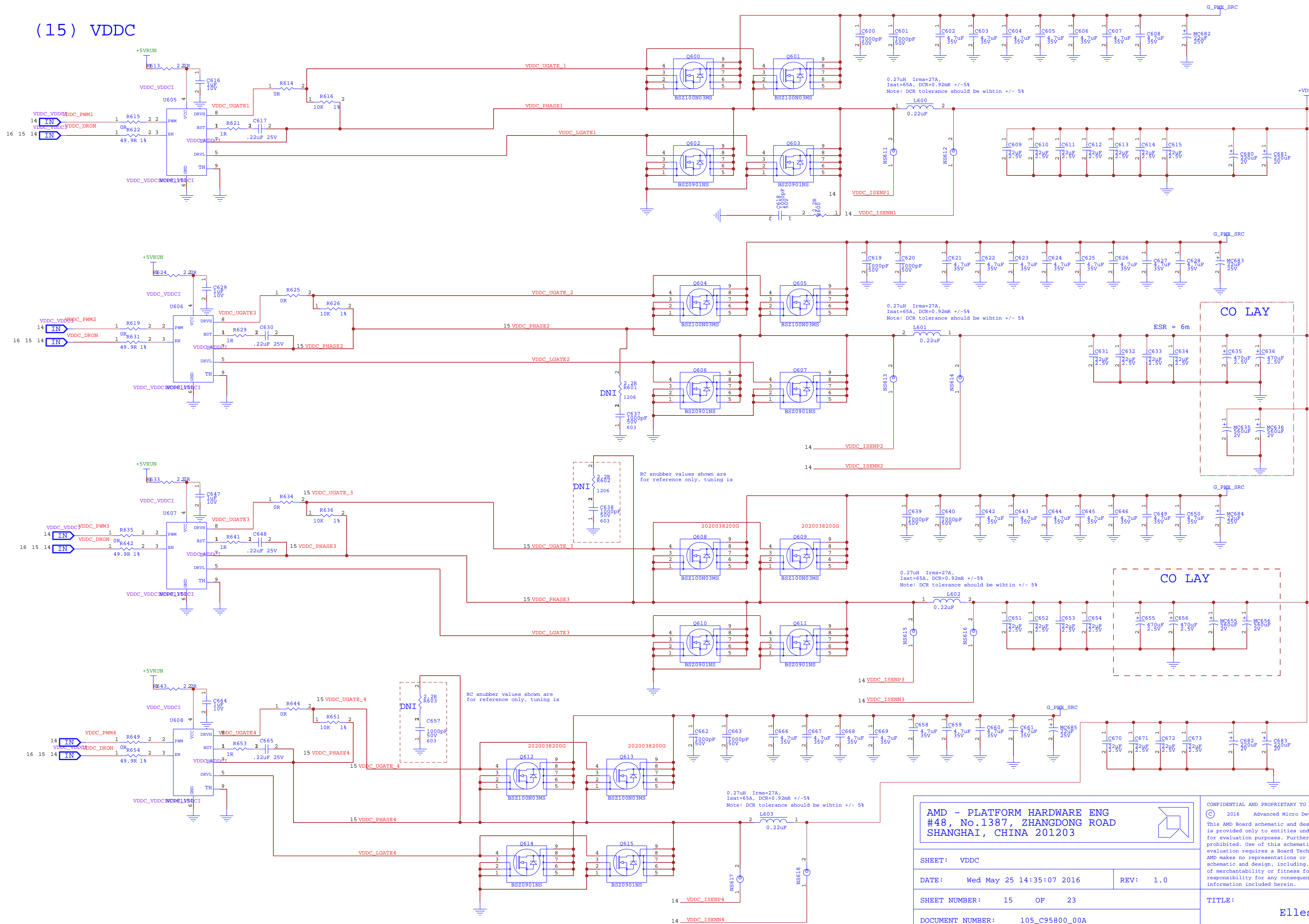
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DOCUMENT NUMBER: 105_C95800_00A

(15) VDDC




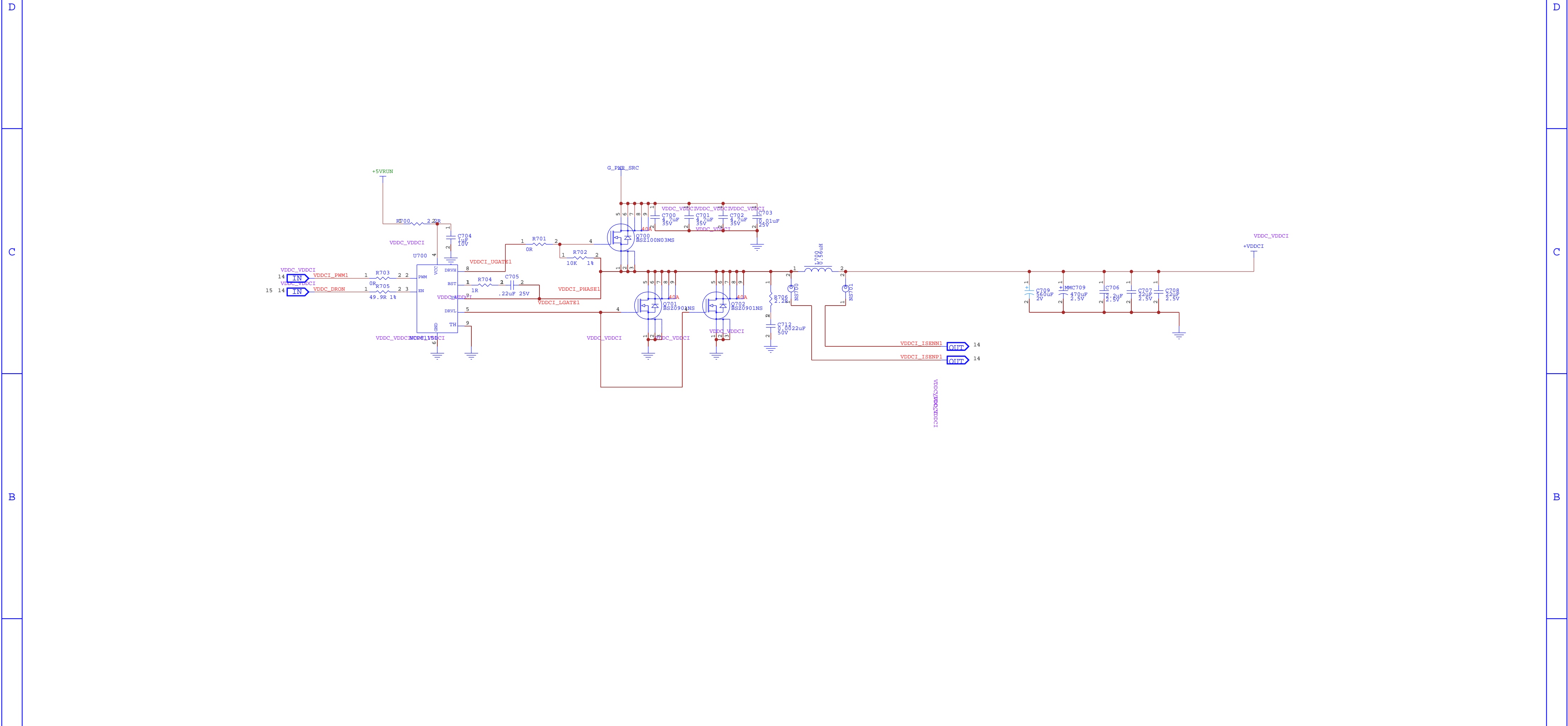
AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: VDDC	
DATE: Wed May 25 14:35:07 2016	REV: 1.0
SHEET NUMBER: 15 OF 23	
DOCUMENT NUMBER: 105_C95800_00A	
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Ellesmere Mobile MXM

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REV: 1.0

DOCUMENT NUMBER: 105_C95800_00A

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	<div>SHEET NUMBER: 16 OF 23</div>	
	<div>DOCUMENT NUMBER: 105_C95800_00A</div>	

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B

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C

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REV: 1.0

TITLE:

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D



B

A



REV: 1.0

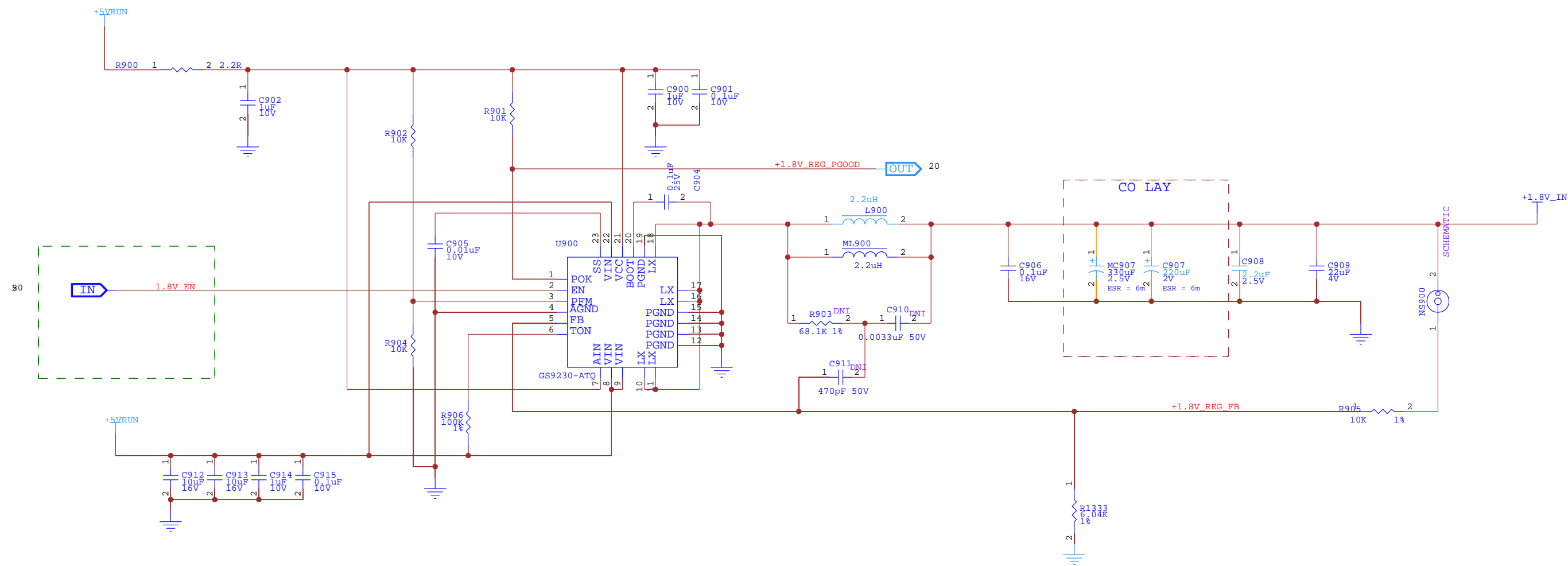
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(19) 1.8V REG



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DATE: Wed May 25 14:35:09 2016		REV: 1.0	
SHEET NUMBER: 19 OF 23			
DOCUMENT NUMBER: 105_C95800_00A			

D



C


C

B

B

A

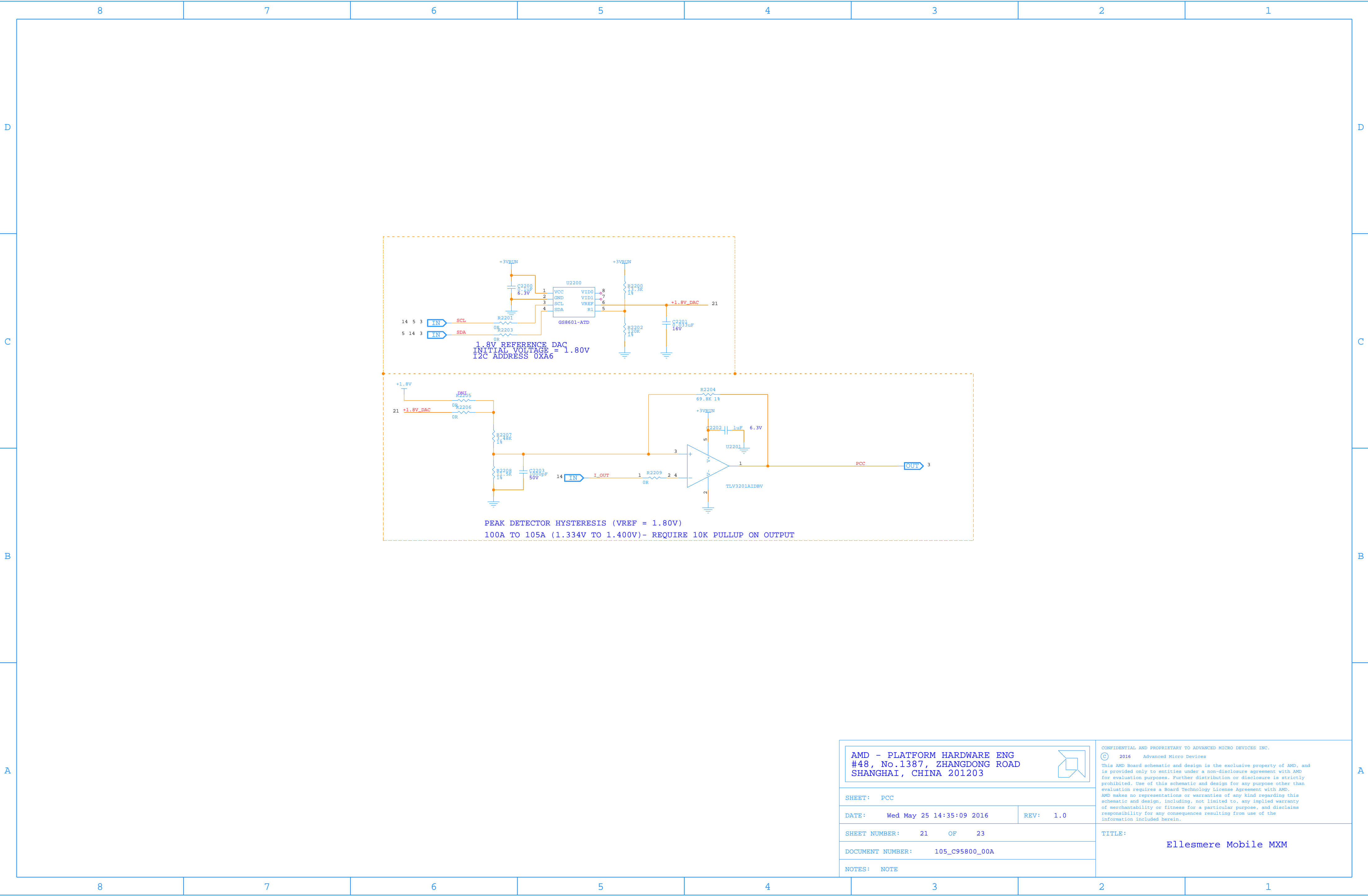
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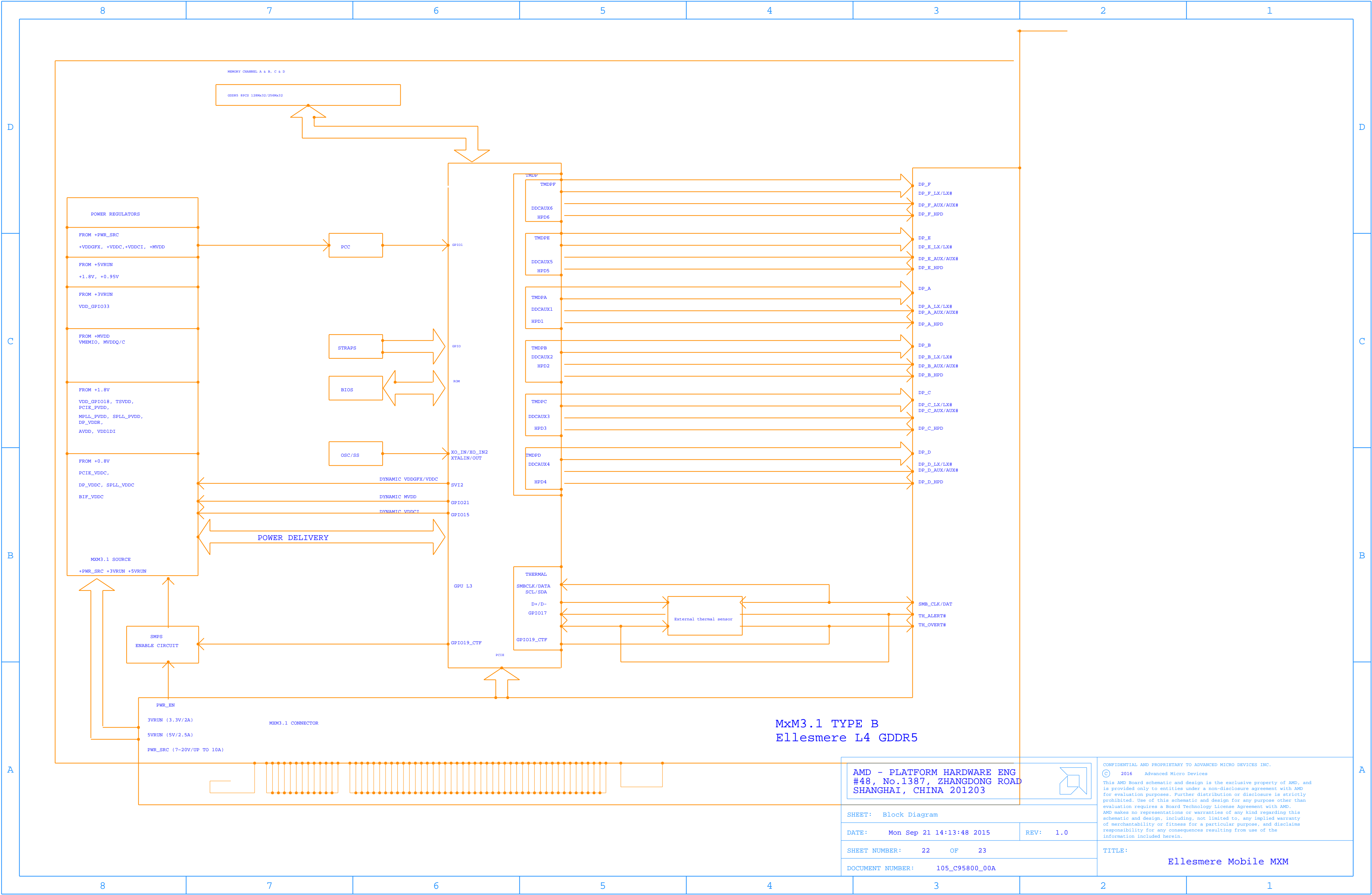


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REV: 1.0

TITLE: Ellesmere Mobile MXM





MxM3.1 TYPE B
Ellesmere L4 GDDR5

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SHEET NUMBER: 22 OF 23					
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