

P2010-B01

75W/80W/100W GK107/GK106/GM107 128-BIT GDDR5 in x32/x16 MODE
DVI-I-DL + DVI-D-DL/HDMI + mHDMI/DP

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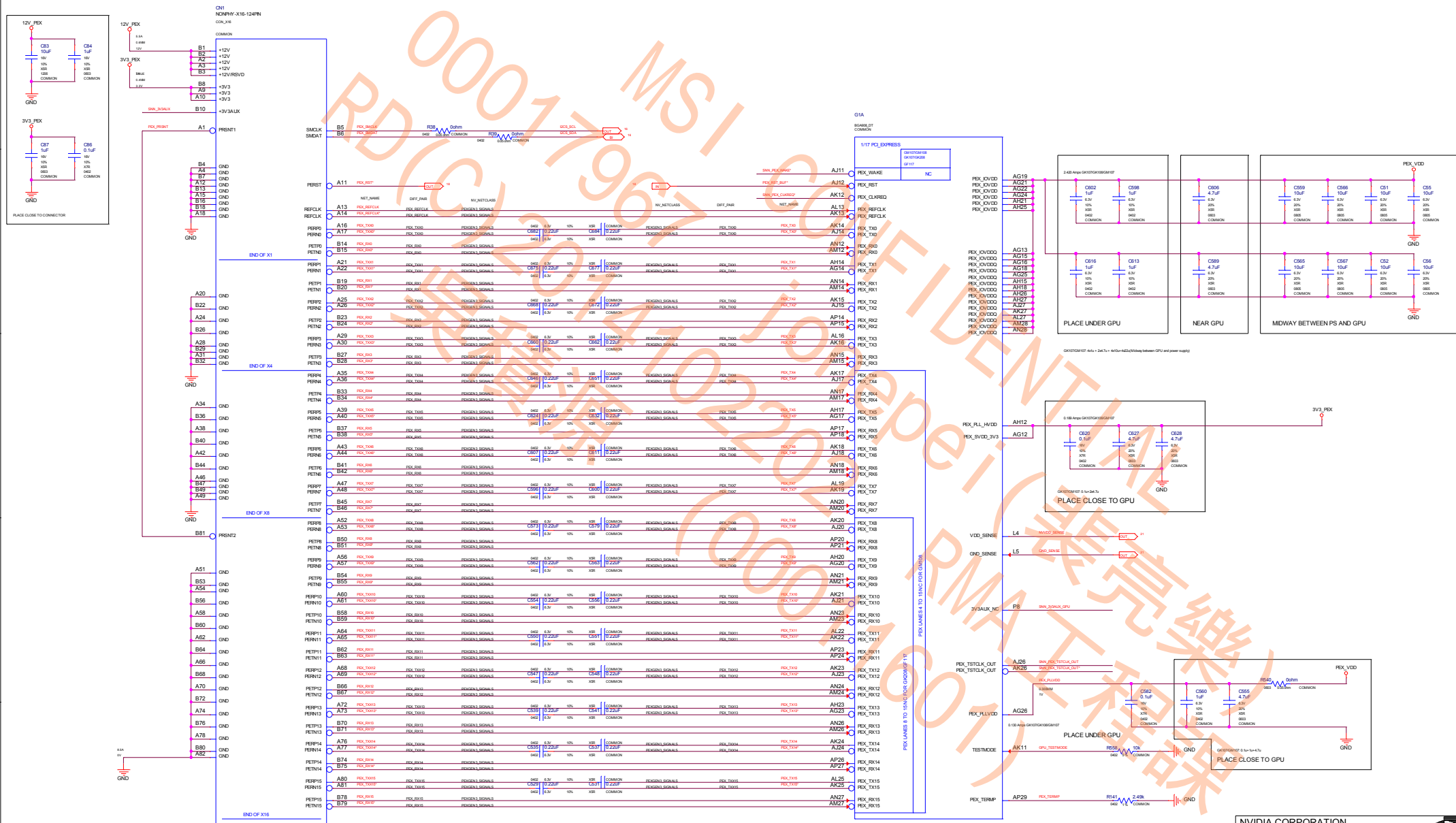
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PCI Express



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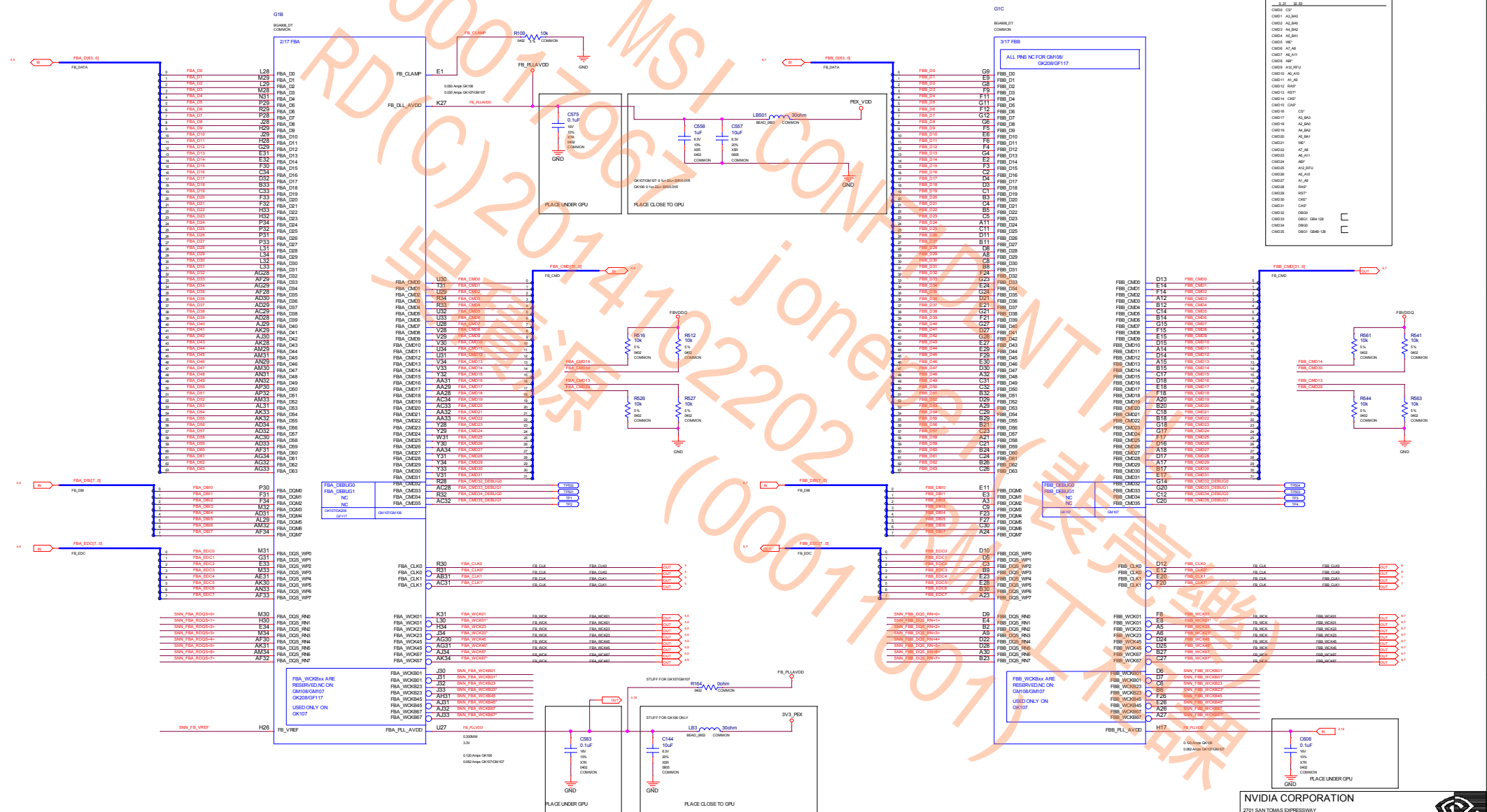
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GPU Frame Buffer Partition A/B



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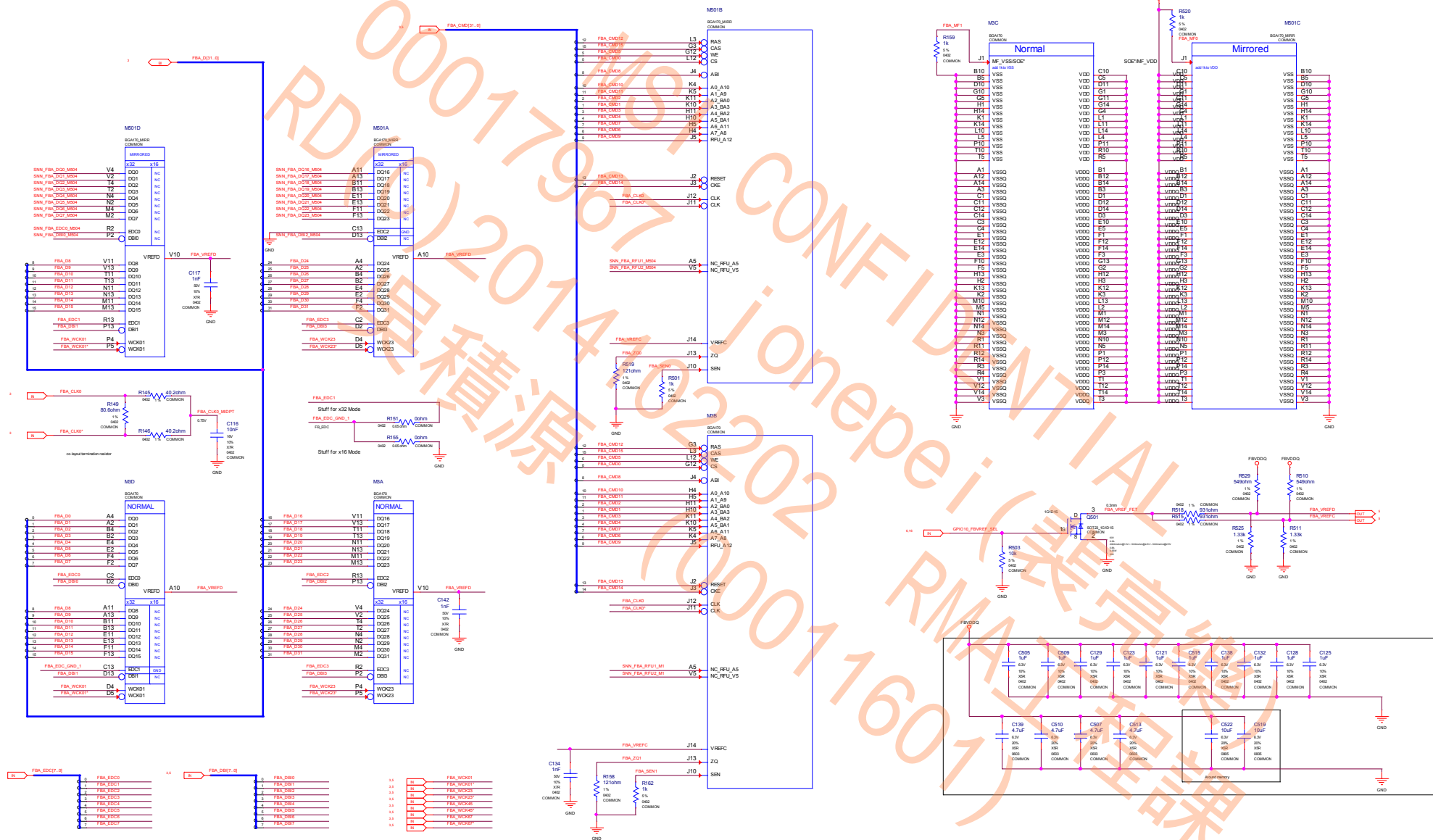


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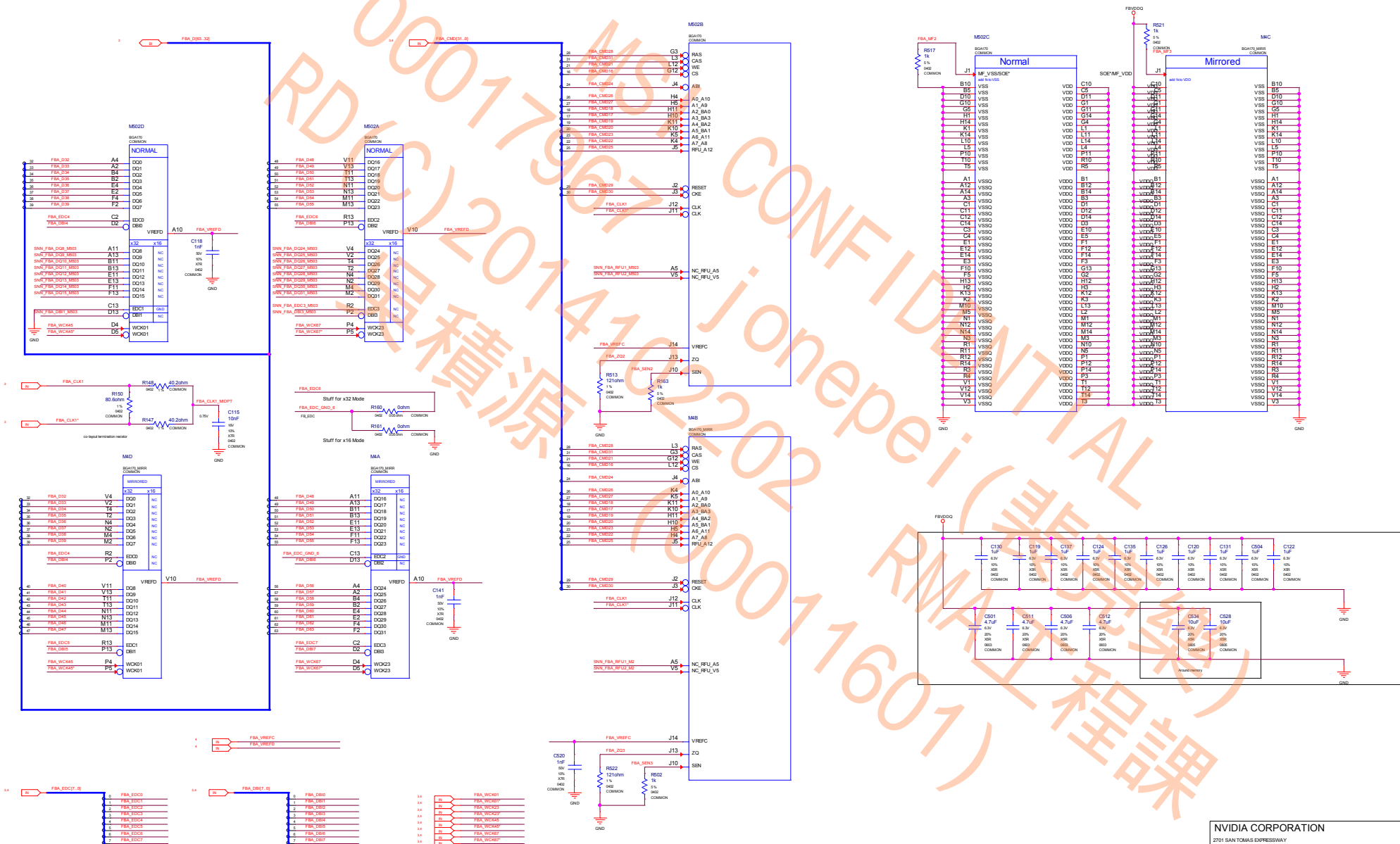
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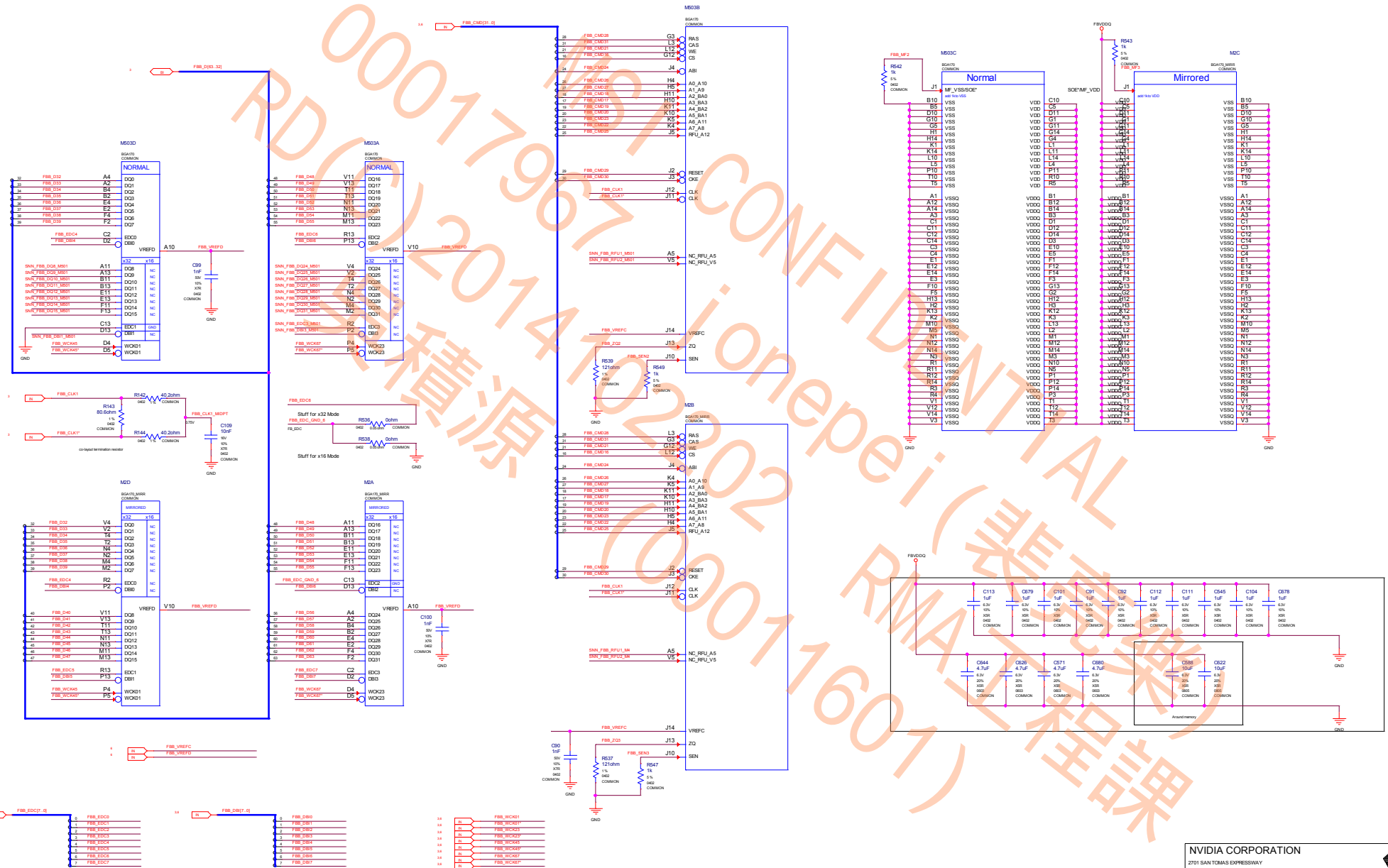
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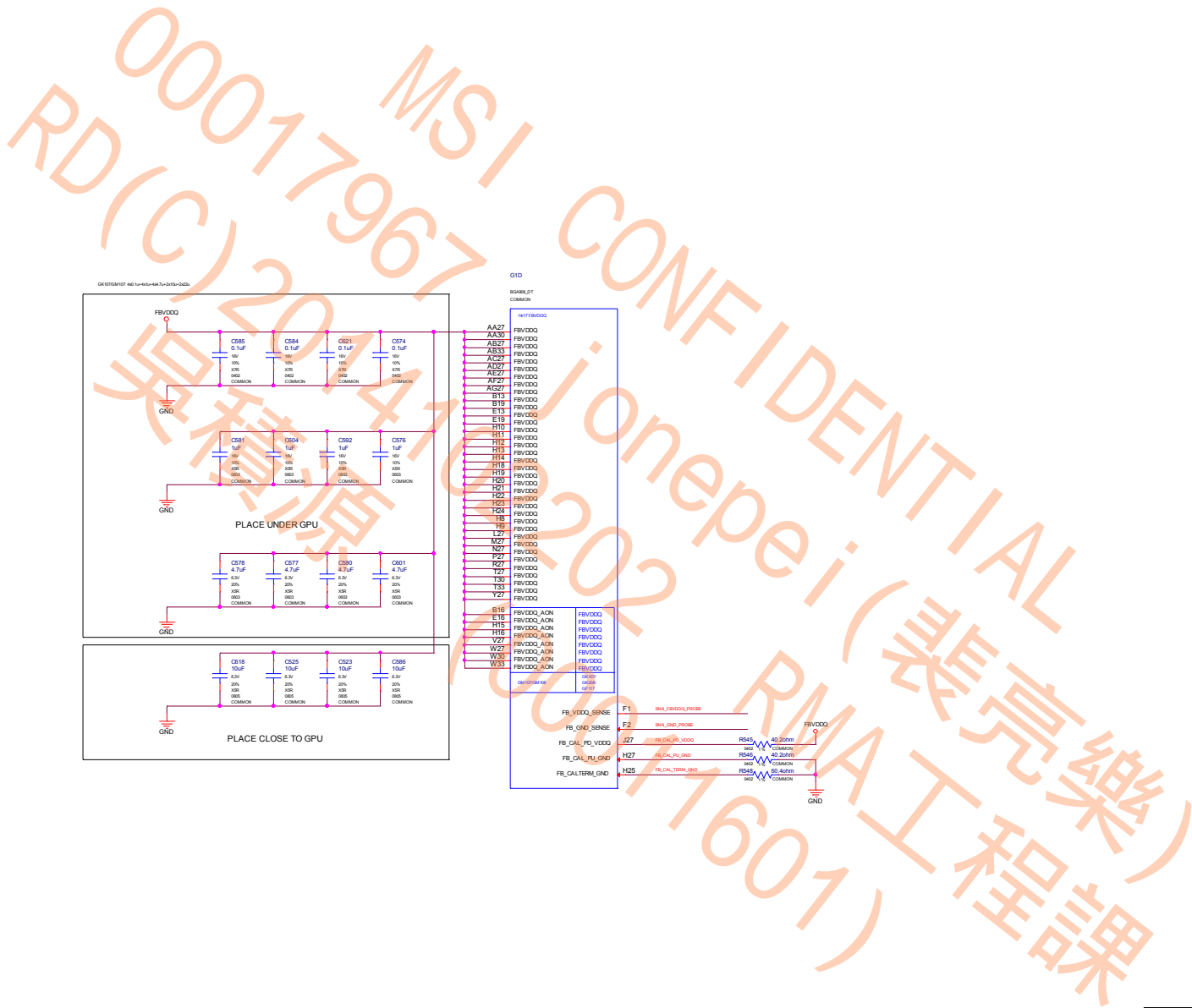
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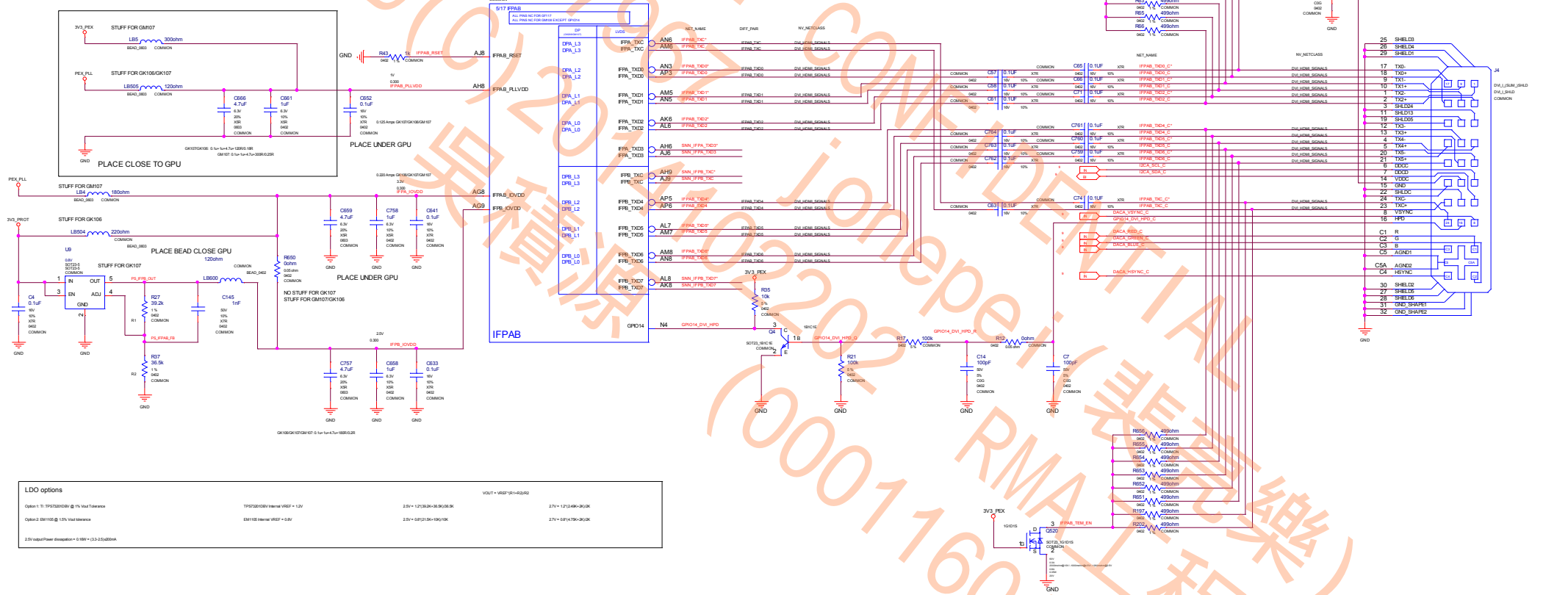


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LDO options		VOUT = VREF*(1+R2/R1)	
Option 1: 1.1V/100mA @ 1% Volt.Tolerance	TPS70000 Internal VREF = 1.2V	2.5V = 1.2V*(1+36.9K/30K)	2.7V = 1.2V*(48+30K)
Option 2: 0.8V/100 @ 1% Volt.Tolerance	ESD100 Internal VREF = 0.8V	2.5V = 0.8V*(21.5K/10K)	2.7V = 0.8V*(39K+30K)
2.5V output Power dissipation = 0.10W = 1.1V x 0.10A			

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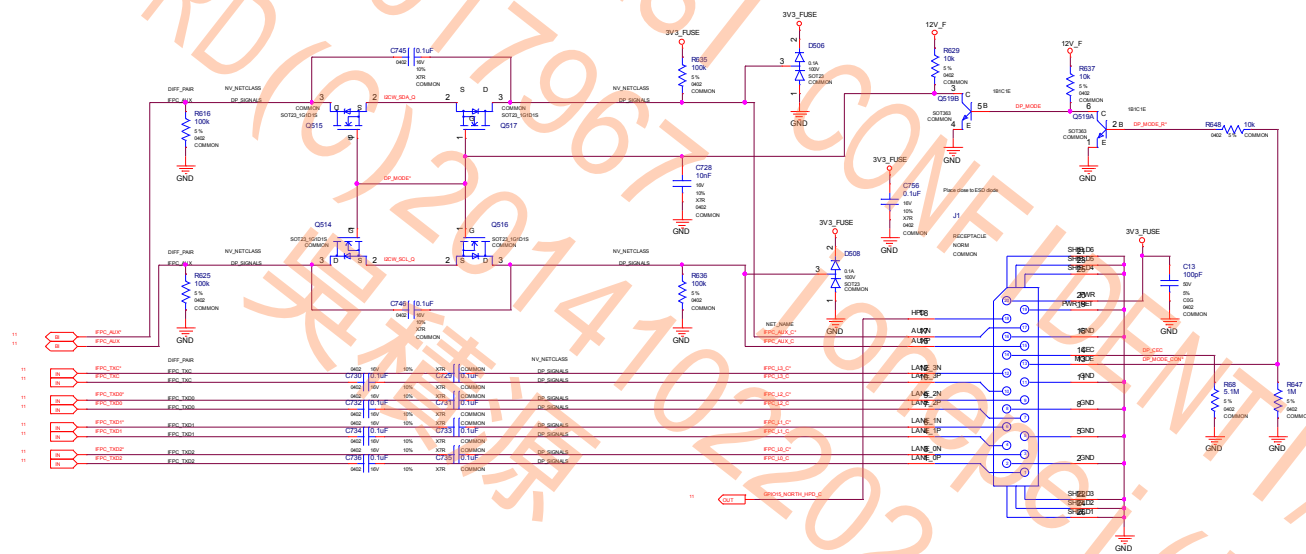
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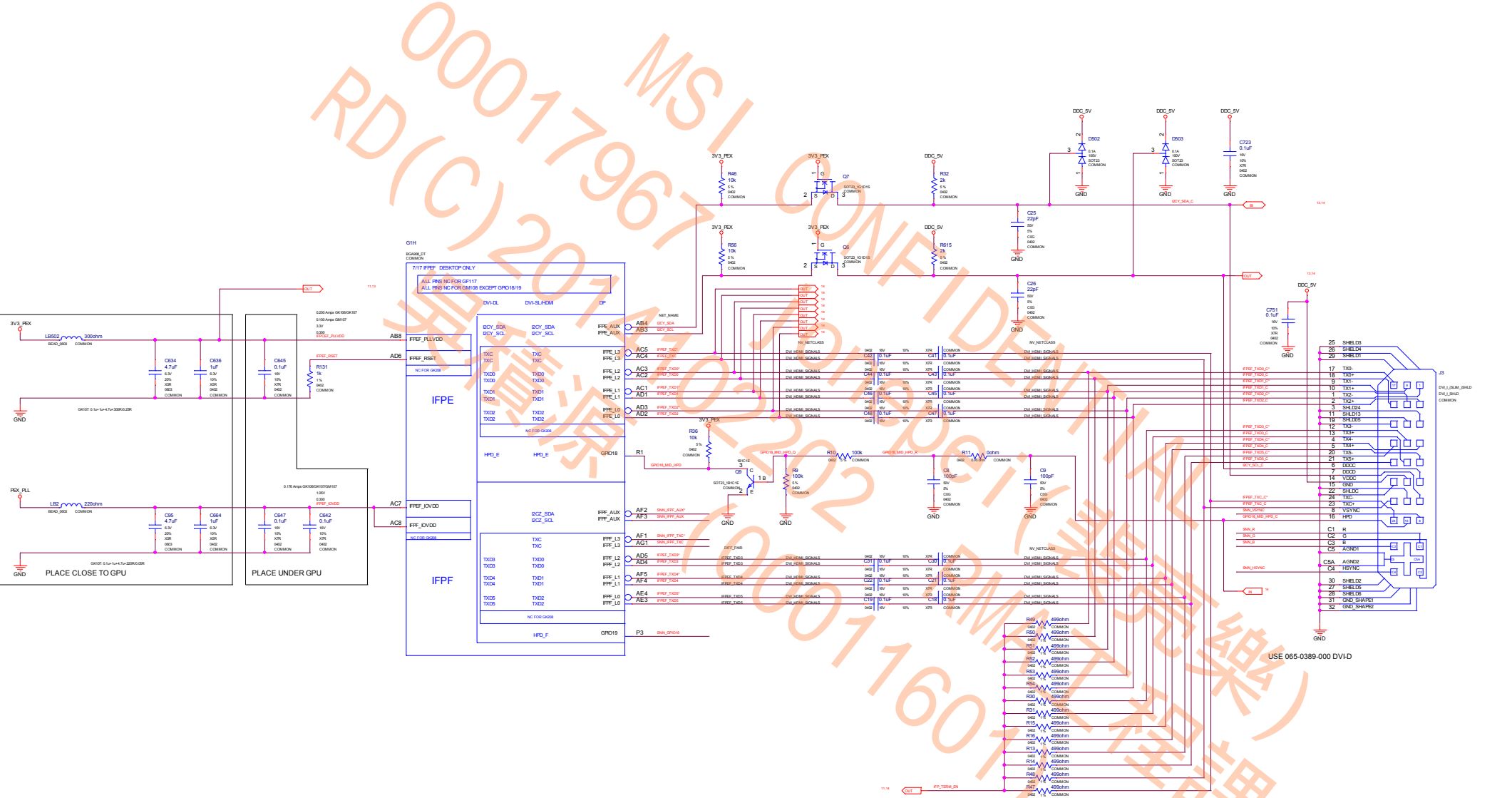
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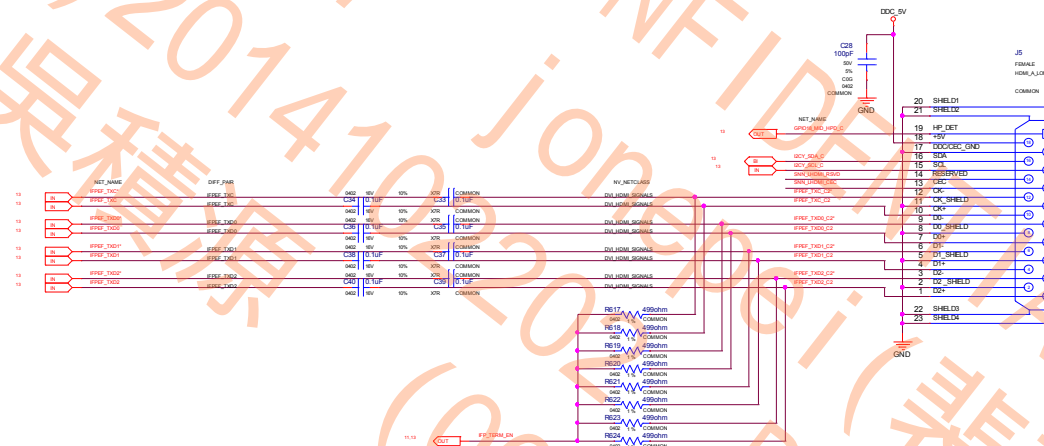
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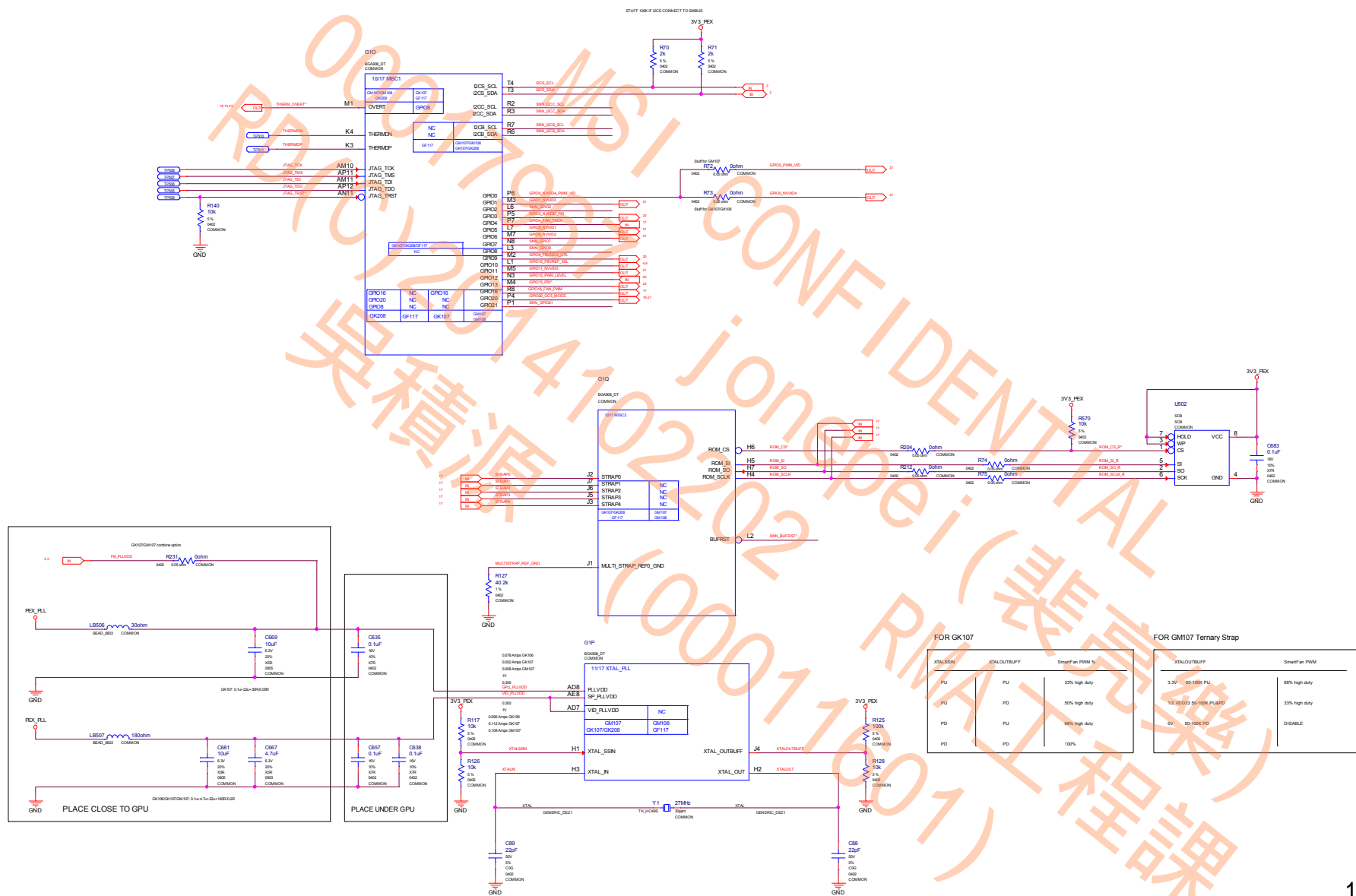




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Misc Interfaces



14ci203

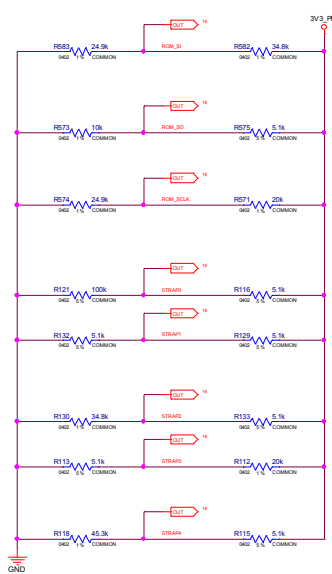
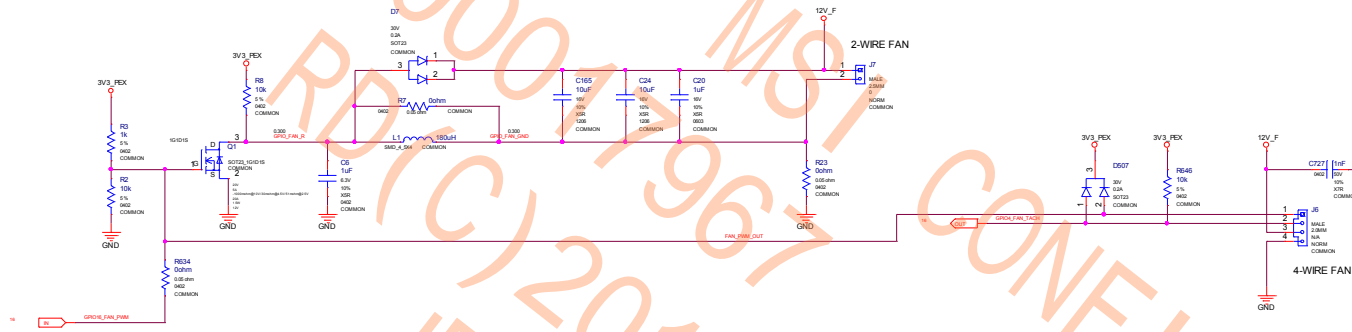
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GK107 Straps

Bit Signal		Values	
05	RAMPQ1Q2	0000	0000
06	RAMPQ1Q2	0001	0000
07	RAMPQ1Q2	0010	0000
08	RAMPQ1Q2	0011	0000
09	RAMPQ1Q2	0100	0000
10	RAMPQ1Q2	0101	0000
11	RAMPQ1Q2	0110	0000
12	RAMPQ1Q2	0111	0000
13	FBQ1	01	0000
14	FBQ2	01	0000
15	SMALL_A1_ADDR	0	ENABLED
16	VGA_DEVICE	0	ENABLED
17	POL_DEVICE_EXT	0	INT
18	SUB_VENDOR	0	NO_BIOS
19	POL_DEVICE_EXT_BITS	0	INT
20	PREL_A1_EN100MHz	0	ENABLED
21	USERQ3	0000	INT
22	USERQ2		
23	USERQ1		
24	USERQ0		
25	IOPL_PIOQ1Q2_LUT_ADDR0	0000	0000
26	IOPL_PIOQ1Q2_LUT_ADDR0	0001	0000
27	IOPL_PIOQ1Q2_LUT_ADDR0	0010	0000
28	IOPL_PIOQ1Q2_LUT_ADDR0	0011	0000
29	IOPL_PIOQ1Q2_LUT_ADDR0	0100	0000
30	IOPL_PIOQ1Q2_LUT_ADDR0	0101	0000
31	IOPL_PIOQ1Q2_LUT_ADDR0	0110	0000
32	IOPL_PIOQ1Q2_LUT_ADDR0	0111	0000
33	POL_DEVICE0	0000	0000
34	POL_DEVICE0	0100	0000
35	POL_DEVICE0		
36	POL_DEVICE0		
37	IOPL_EXP0	0	ENABLED
38	IOPL_EXP0	0	ENABLED
39	IOPL_EXP0	0	ENABLED
40	IOPL_EXP0	0	ENABLED
41	IOPL_EXP0	0	ENABLED
42	IOPL_EXP0	0	ENABLED
43	IOPL_EXP0	0	ENABLED
44	IOPL_EXP0	0	ENABLED
45	IOPL_EXP0	0	ENABLED
46	IOPL_EXP0	0	ENABLED
47	IOPL_EXP0	0	ENABLED
48	IOPL_EXP0	0	ENABLED
49	IOPL_EXP0	0	ENABLED
50	IOPL_EXP0	0	ENABLED
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96	IOPL_EXP0	0	ENABLED
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98	IOPL_EXP0	0	ENABLED
99	IOPL_EXP0	0	ENABLED
100	IOPL_EXP0	0	ENABLED
101	IOPL_EXP0	0	ENABLED
102	IOPL_EXP0	0	ENABLED

GM107 Straps

Bit Signal		Values			
03	RMW_CFG	0000	GRAB2 (Gbit) Name	0000	Reserved and
		0001	GRAB2 (Gbit) Enable	0001	Reserved and
		0010	GRAB2 (Gbit) Sampling	0010	Reserved and
04	RMW_CFG	0011	Reserved and	0100	Reserved and
		0100	GRAB2 (Gbit) Type1	0100	Reserved and
		0101	GRAB2 (Gbit) Type2	0101	Reserved and
		0110	GRAB2 (Gbit) Sampling	0110	Reserved and
		0111	Reserved and	0111	Reserved and
05	DEV0_SEL	0	DEV0 A	1	DEV0 B
06	PCIE_CFG	0	DESKTOP	1	LAPTOP
07	SMR_ACT_ACK0	0	DISABLED	1	ENABLED
08	VGA_DEV00	0	DISABLED	1	ENABLED
09	SCR0_EXP0MSD	0	DISABLED	1	ENABLED
10	SCR0_EXP0MSD	0	DISABLED	1	ENABLED
01	SCR1_EXP0MSD	0	DISABLED	1	ENABLED
02	SCR1_EXP0MSD	0	DISABLED	1	ENABLED

TERMINAL STRAP

23H	GCE_ENABLE	50 100K PU ONLY
19H	FCR_ENABLE	50 100K PU AND
04H	GCE_ENABLE	50 100K PU ONLY

Multilevel Straps

0000	5.1K to GND	1000	5.1K to VCC
0001	10K to GND	1001	10K to VCC
0010	15K to GND	1010	15K to VCC
0011	20K to GND	1011	20K to VCC
0100	24.9K to GND	1100	24.9K to VCC
0101	30.1K to GND	1101	30.1K to VCC
0110	34.8K to GND	1110	34.8K to VCC
0111	45.3K to GND	1111	45.3K to VCC

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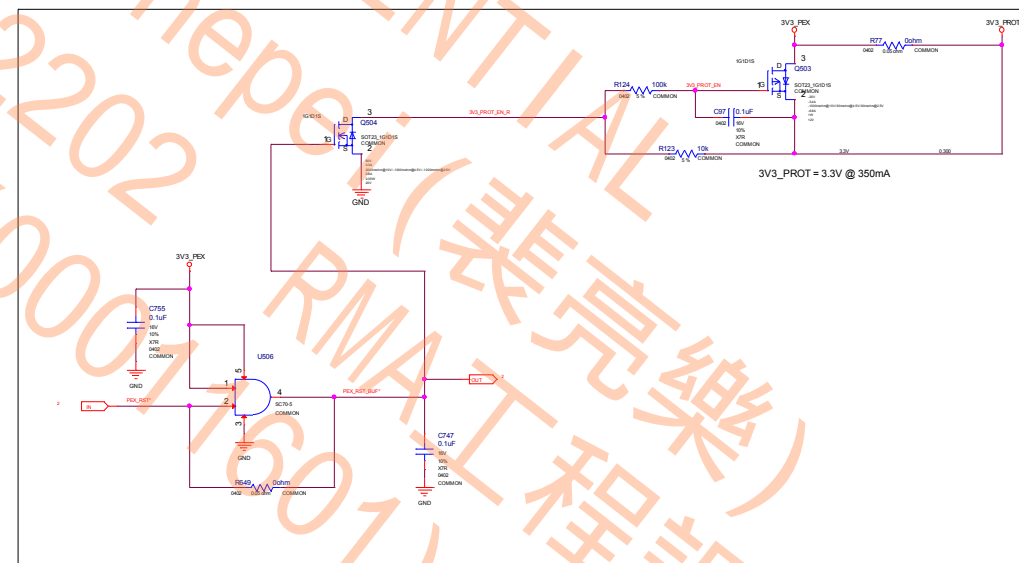
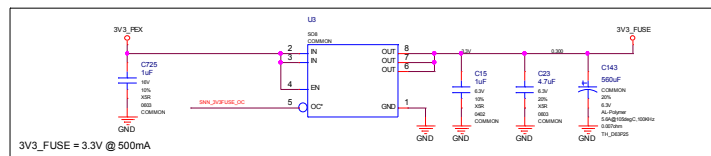
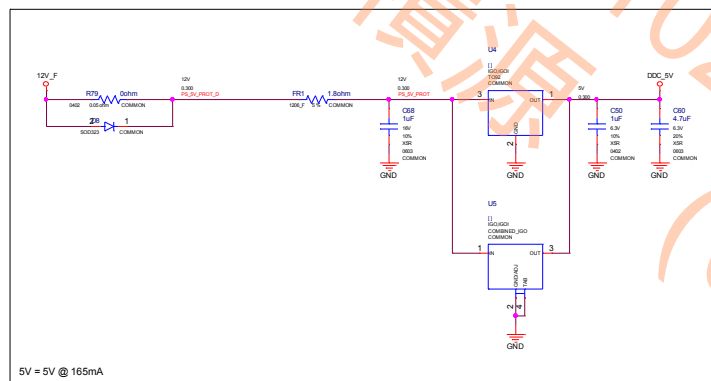
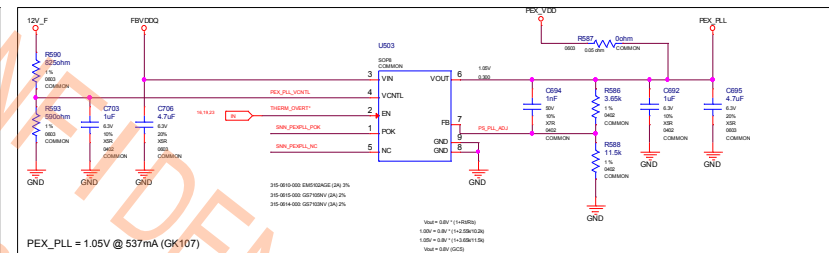
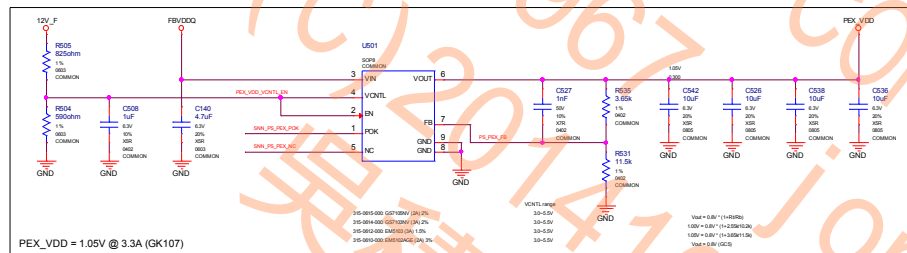
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Power Supply I: Misc Power/PEXVDD LDO(Optional)




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PAGE DETAIL	Power Supply 1: Misc Power/PEXVDD LDO (Optional)

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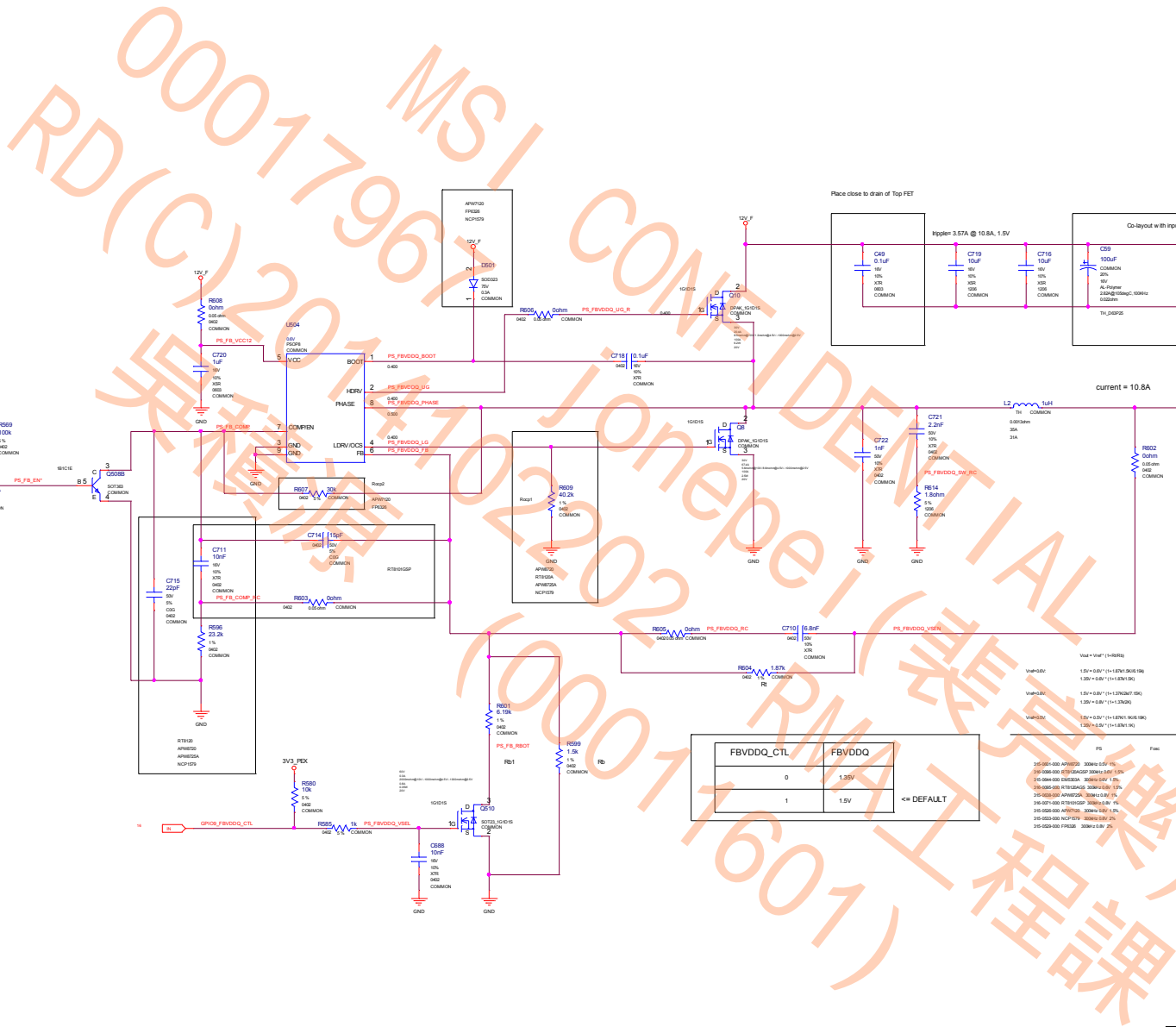


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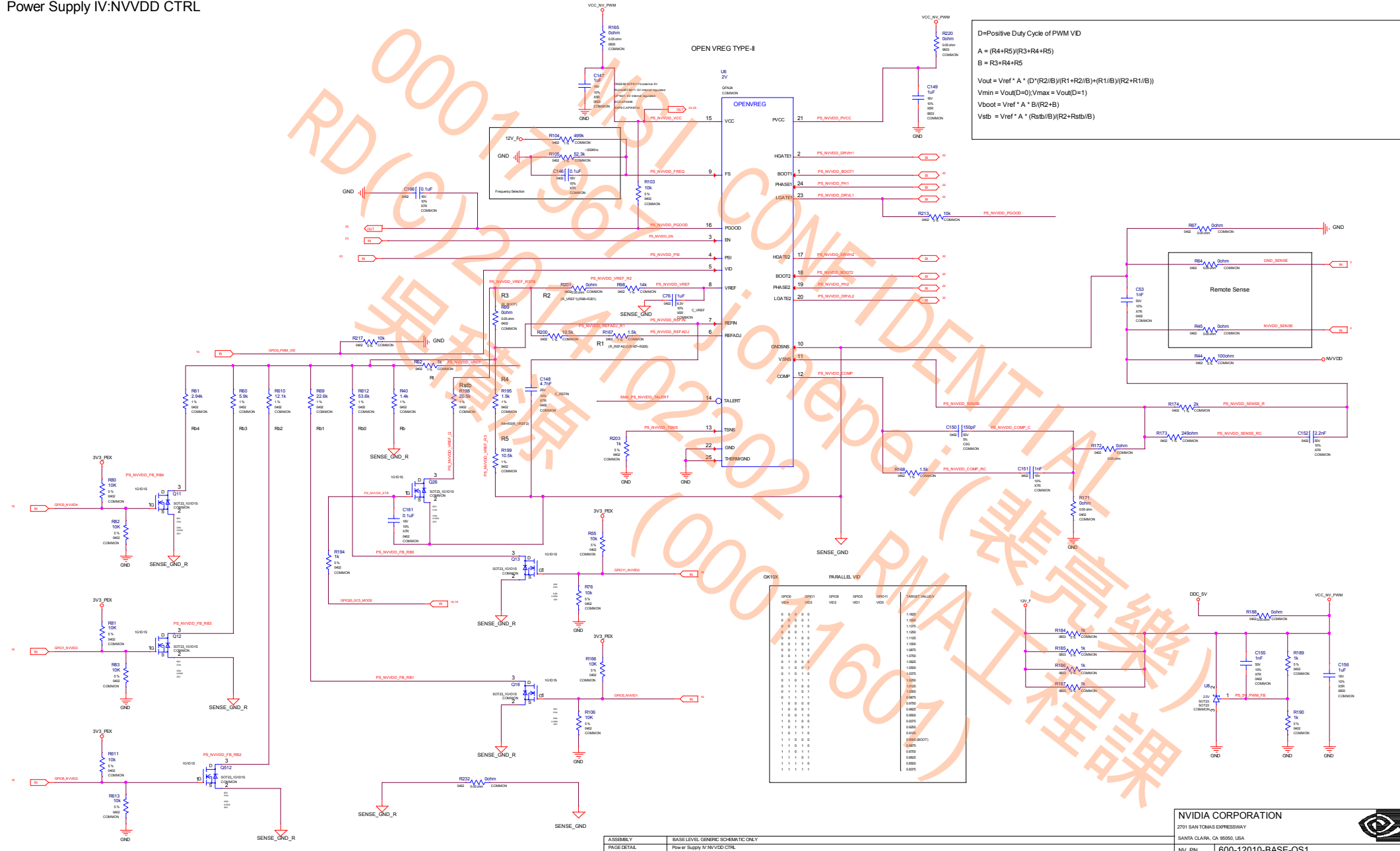
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Power Supply IV:NVVDD CTRL



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ASSEMBLY: BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL: Power Supply IV:NVVDD CTRL

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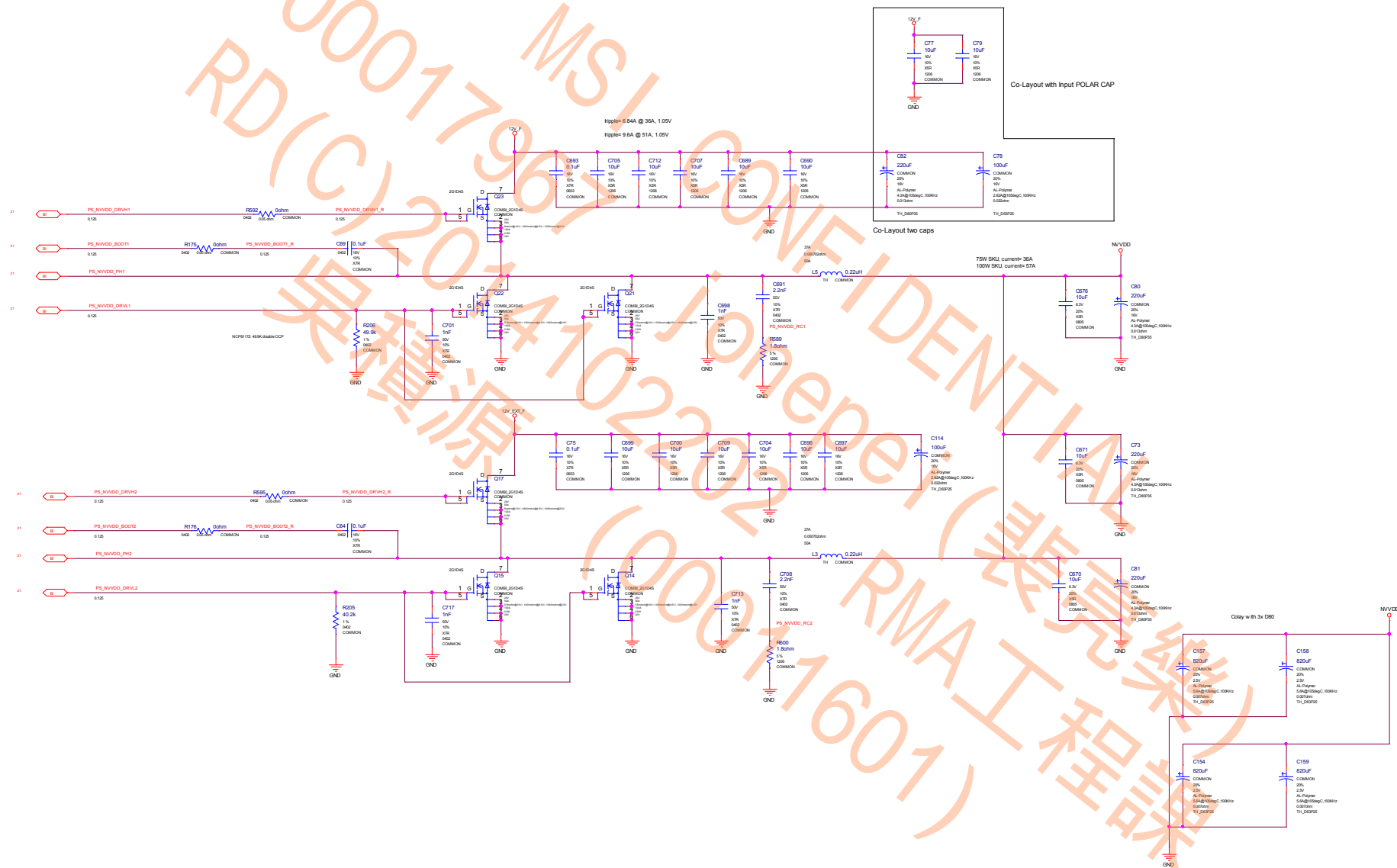
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NVVDD MOSFET



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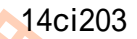
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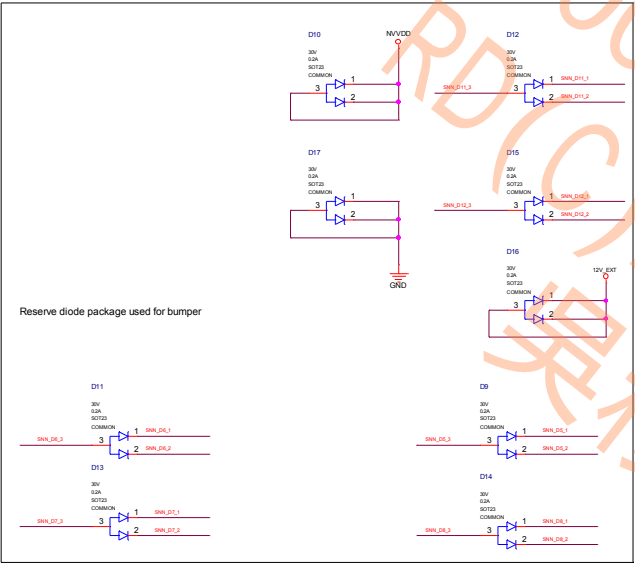
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PEX_12V INPUT - 66W



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