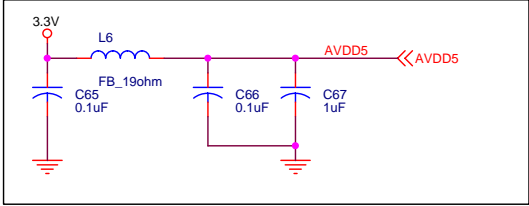
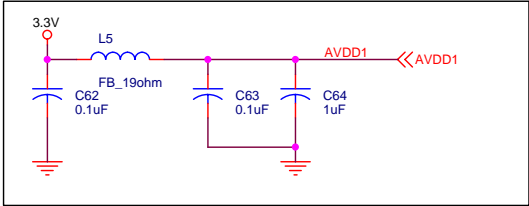
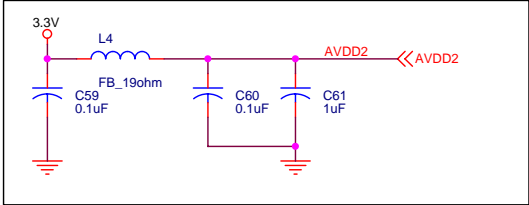
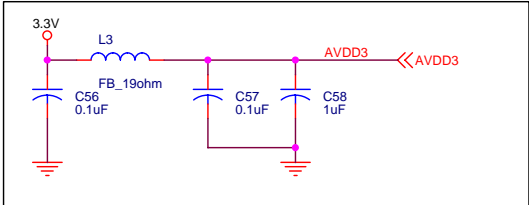
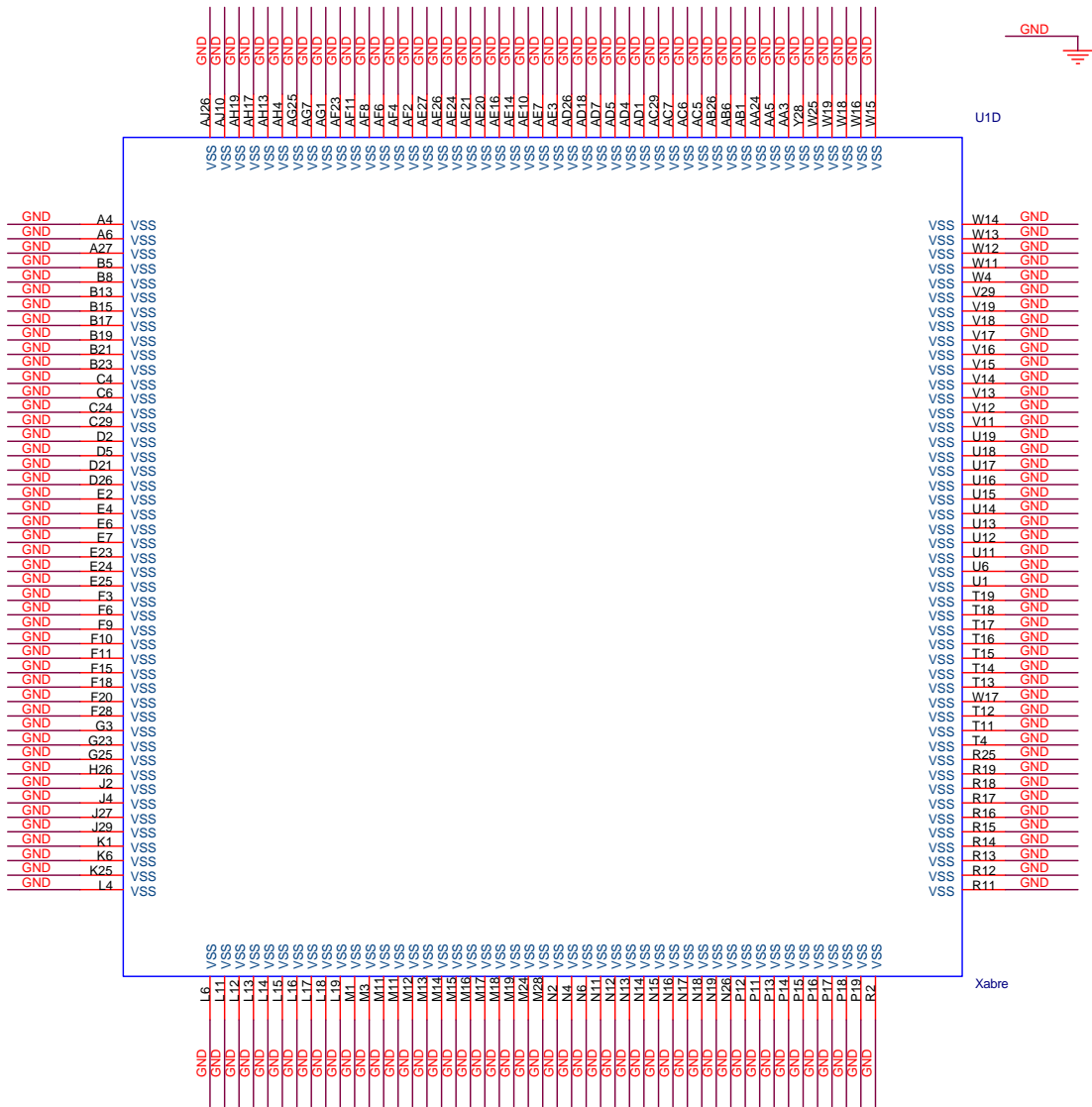
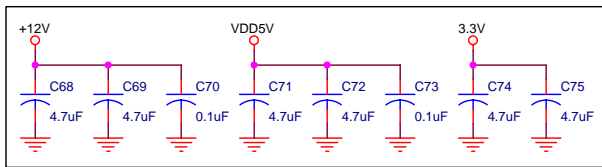


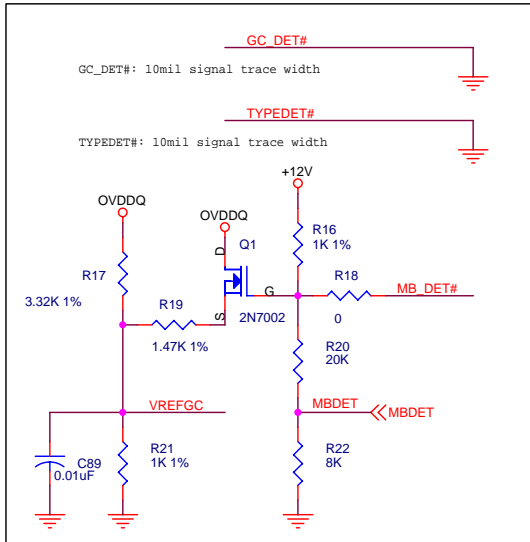
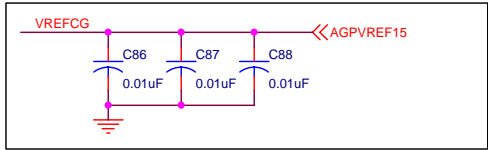
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Title		
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Size	Document Number	Rev
B	Xabre 8MX16 DDR (Xabre008)	1.0
Date:	星期一, 十二月 16, 2002	Sheet 3 of 10



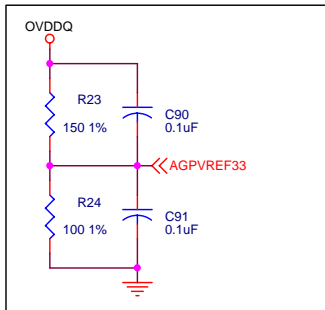
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B	Xabre 8MX16 DDR (Xabre008)	1.0
Date:	星期一, 十二月 16, 2002	Sheet 4 of 10



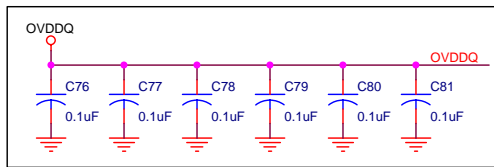
Bypass capacitor for AGP connector



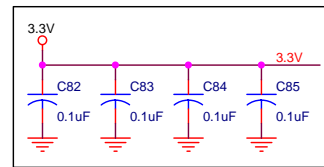
1.5V level for AGP4X AND AGP8X Modes



3.3V level for AGP2X Mode



Bypass capacitor for AGP connector

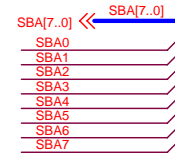
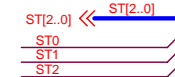
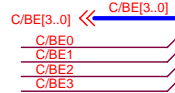
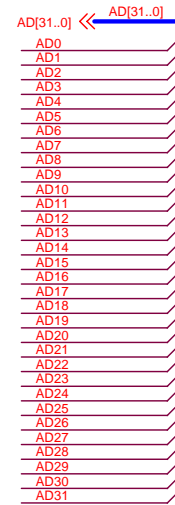
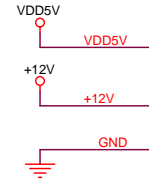
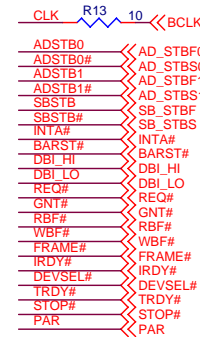


Bypass capacitor for AGP connector

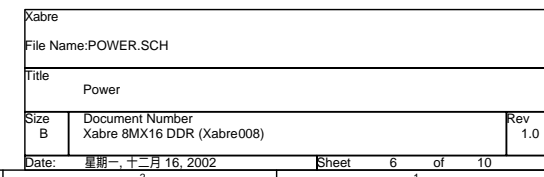
VDD5V	B01	OVRCNT	A01	+12V
VDD5V	B02	5.0V	A02	TYPEDET#
VDD5V	B03	5.0V	A03	GC_DET#
GND	B04	USB+	A04	GND
GND	B05	GND	A05	INTA#
CLK	B06	INTB#	A06	INTA#
REQ#	B07	CLK	A07	BARST#
3.3V	B08	REQ	A08	GNT#
ST0	B09	VCC3.3	A09	ST1
ST2	B10	ST0	A10	MB_DET#
RBF#	B11	ST2	A11	DBI_HI
GND	B12	RBF	A12	DBI_LO
DBI_LO	B13	GND	A13	GND
SBA0	B14	DBI_LO	A14	WBF#
3.3V	B15	SBA0#	A15	SBA1
SBA2	B16	VCC3.3	A16	3.3V
SBSTB	B17	SBA2#	A17	SBA3
GND	B18	SB_STBF	A18	SBSTB#
SBA4	B19	GND	A19	GND
SBA6	B20	SBA4#	A20	SBA5
B21	B21	SBA6#	A21	SBA7
B22	B22	RESERVED	A22	GND
B23	B23	GND	A23	GND
B24	B24	3.3AUX	A24	GND
B25	B25	VCC3.3	A25	GND
B26	B26	VCC3.3	A26	AD30
AD31	B27	AD31	A27	AD28
AD29	B28	VCC3.3	A28	3.3V
AD27	B29	VCC3.3	A29	AD26
AD25	B30	AD26	A30	AD24
GND	B31	GND	A31	GND
ADSTB1	B32	AD_STF1	A32	ADSTB1#
AD23	B33	AD23	A33	C#BE3
OVDDQ	B34	VDDQ1.5	A34	OVDDQ
AD21	B35	AD21	A35	AD22
GND	B36	AD19	A36	AD20
AD17	B37	GND	A37	GND
C#BE2	B38	AD17	A38	AD18
OVDDQ	B39	C#BE2	A39	AD16
IRDY#	B40	VDDQ1.5	A40	OVDDQ
B41	B41	FRAME	A41	FRAME#
B42	B42	RES#KEY	A42	GND
B43	B43	GND#KEY	A43	GND
B44	B44	RES#KEY	A44	GND
B45	B45	3.3#KEY	A45	TRDY#
DEVSEL#	B46	DEVSEL	A46	STOP#
OVDDQ	B47	VDDQ1.5	A47	GND
GND	B48	PERR	A48	PAR
GND	B49	SERR	A49	PAR
C#BE1	B50	AD15	A50	AD15
OVDDQ	B51	VDDQ1.5	A51	OVDDQ
AD14	B52	AD14	A52	AD13
AD12	B53	AD11	A53	AD11
GND	B54	GND	A54	GND
AD10	B55	GND	A55	GND
AD8	B56	AD9	A56	AD9
OVDDQ	B57	AD8	A57	C#BE0
ADSTB0	B58	VDDQ1.5	A58	OVDDQ
AD7	B59	AD_STF0	A59	ADSTB0#
GND	B60	AD7	A60	AD8
AD5	B61	GND	A61	GND
AD3	B62	AD5	A62	AD4
OVDDQ	B63	AD3	A63	AD2
AD1	B64	VDDQ1.5	A64	OVDDQ
VREFCG	B65	AD1	A65	AD0
AGPREFCG	B66	AGPREFCG	A66	VREFCG

AGP3.0-B

AGP3.0-A

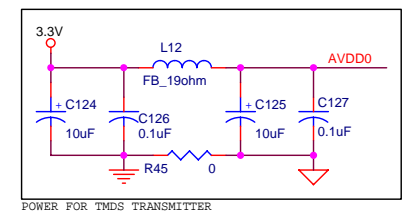
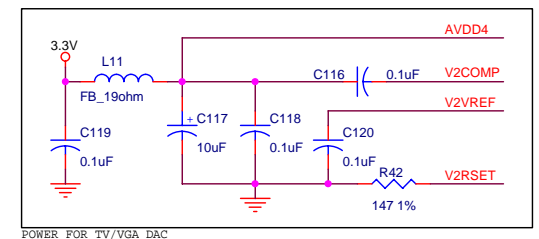
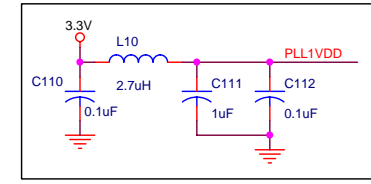
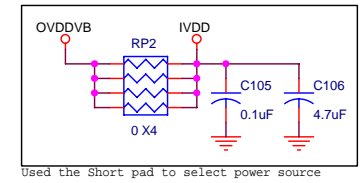
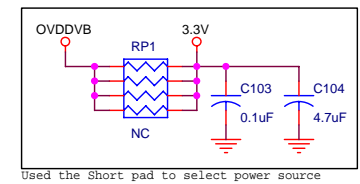
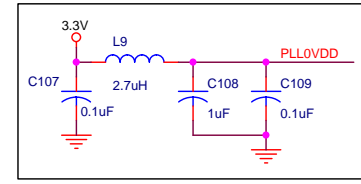
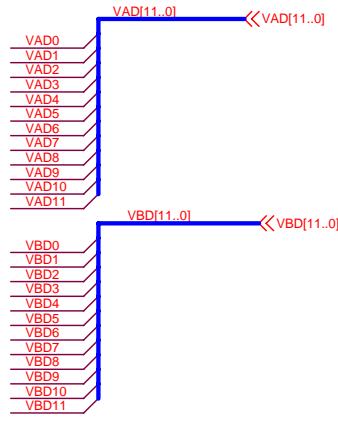
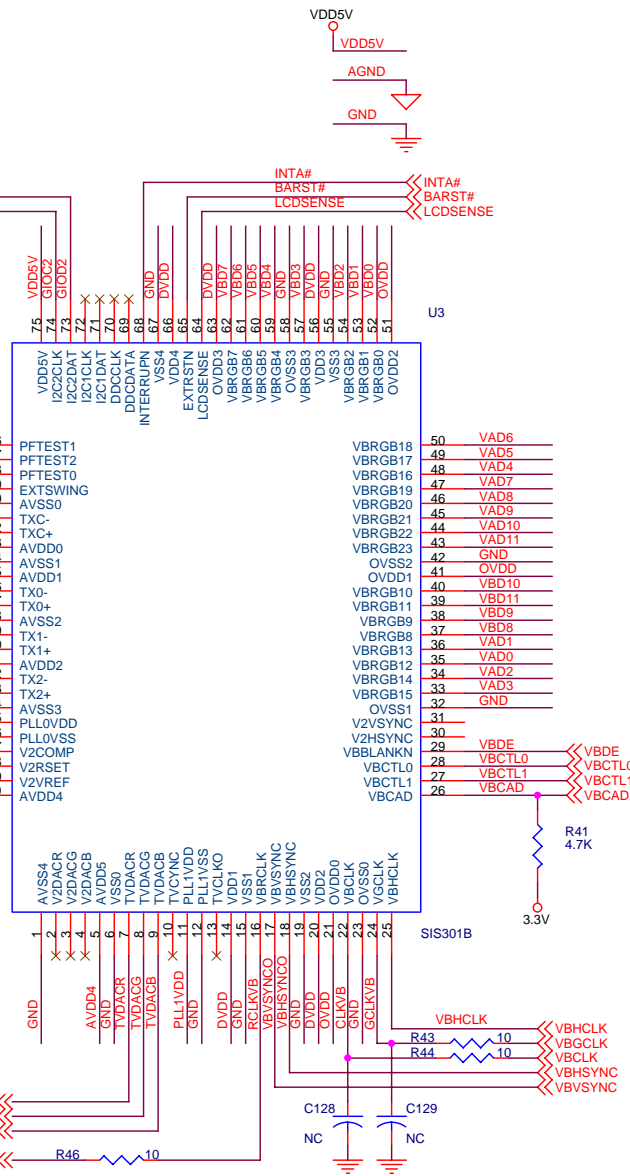
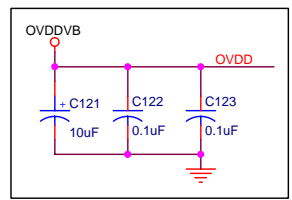
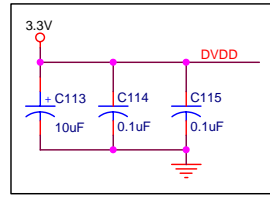
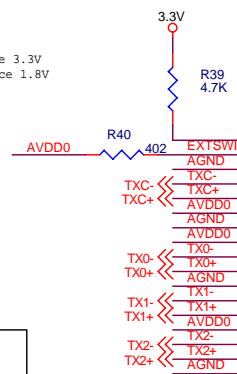


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File Name:AGP8X.SCH		
Title AGP		
Size B	Document Number Xabre 8MX16 DDR (Xabre008)	Rev 1.0
Date: 星期一, 十二月 16, 2002	Sheet 5	of 10

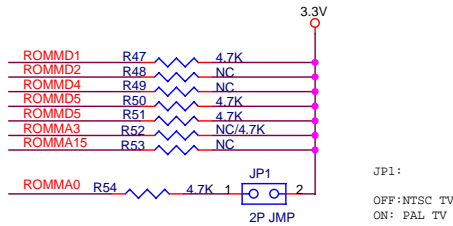




R39:
NC: VB interface 3.3V
4.7K: VB interface 1.8V

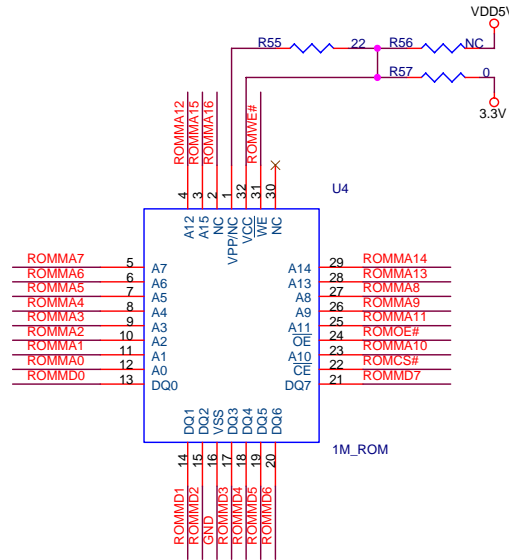
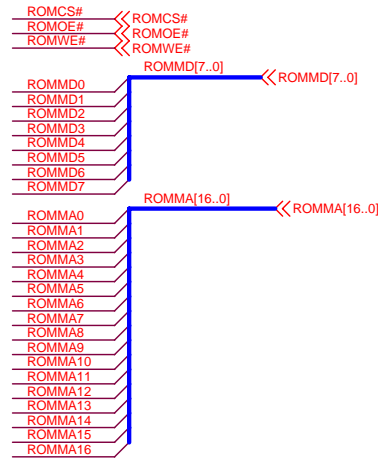
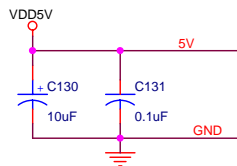


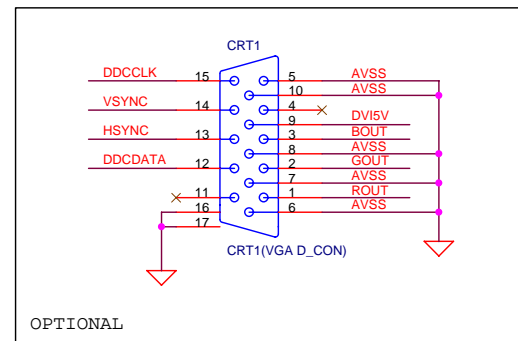
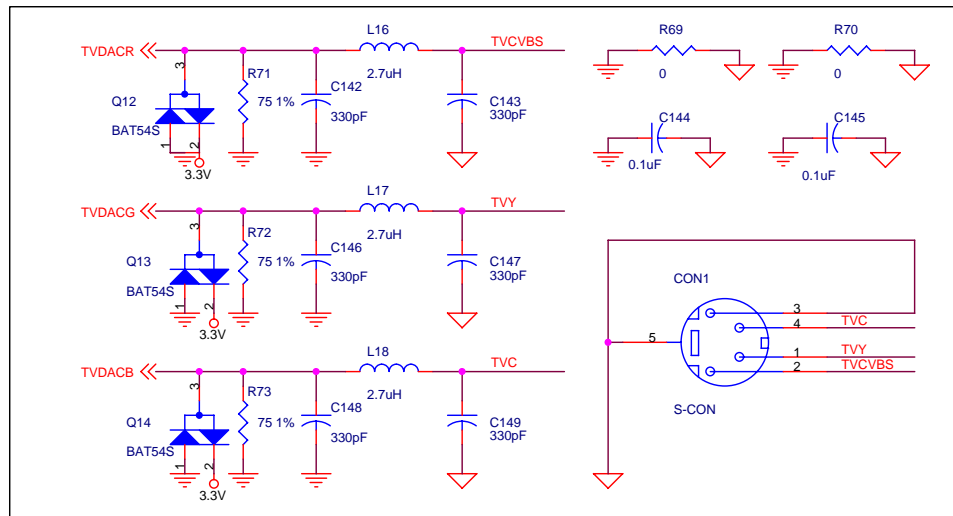
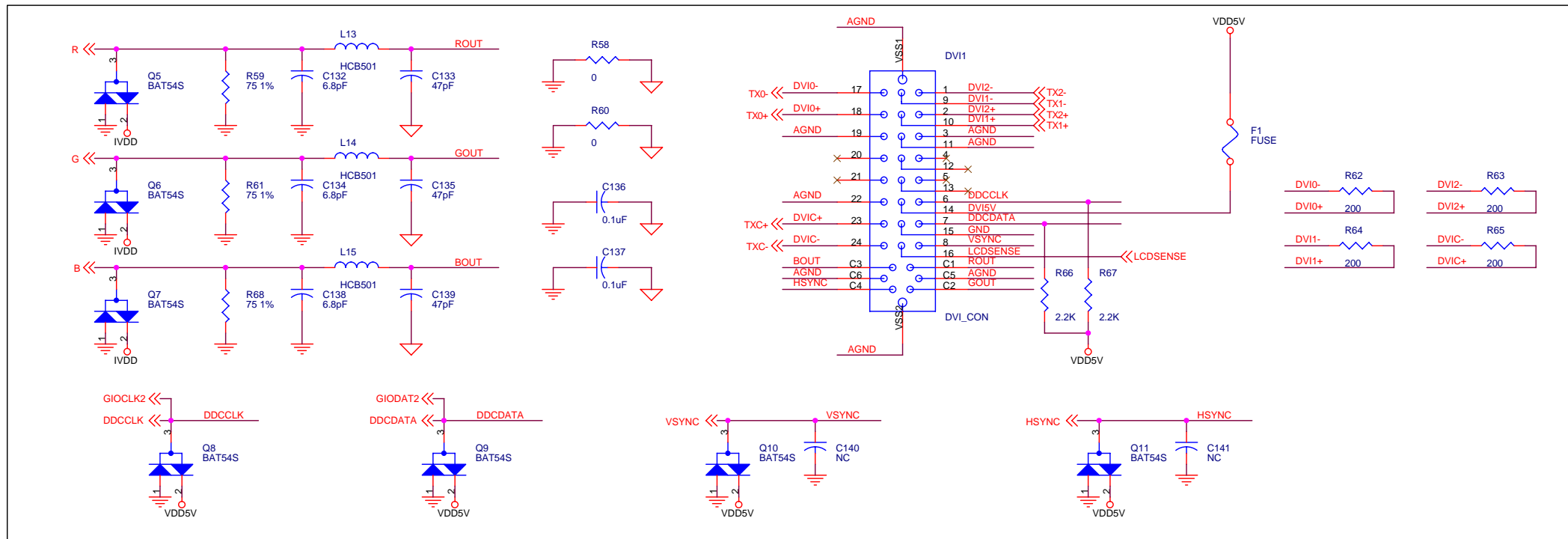
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Title		
SiS301B		
Size	Document Number	Rev
B	Xabre 8MX16 DDR (Xabre008)	1.0
Date:	星期一, 十二月 16, 2002	Sheet 7 of 10



JP1:
OFF:NTSC TV
ON: PAL TV

ROMMA[3] : 0 INTA DISABLE
 1 INTA ENABLE
ROMMA[15] : 0 AGP SIDE-BAND FUNCTION ENABLE
 1 AGP PIPELINE FUNCTION ENABLE
ROMMD[1:0] : 00 SDRAM
 01 Reserved
 10 DDR SDRAM(one DQS control 8-bit DQ type DDR)
 11 DDR SDRAM(one DQS control 32-bit DQ type DDR)
ROMMD[2] : Reserved
ROMMD[5:4] : 00 AGP(2.0) 2X MODE/AGP(3.0) 4x mode
 01 AGP(2.0) 1X MODE/AGP(3.0) 8X mode
 10 AGP(2.0) 4X MODE/agg(3.0) 4/8X mode
 11 PCI MODE/Reserved





Xabre		
File Name:OUTPUT.SCH		
Title		
OUTPUTS		
Size	Document Number	Rev
B	Xabre 8MX16 DDR (Xabre008)	1.0
Date:	星期一, 十二月 16, 2002	Sheet 9 of 10

