

P2131_A00

GK208 GDDR5 256X32Bit

DVI-I/SlimVGA + HDMI

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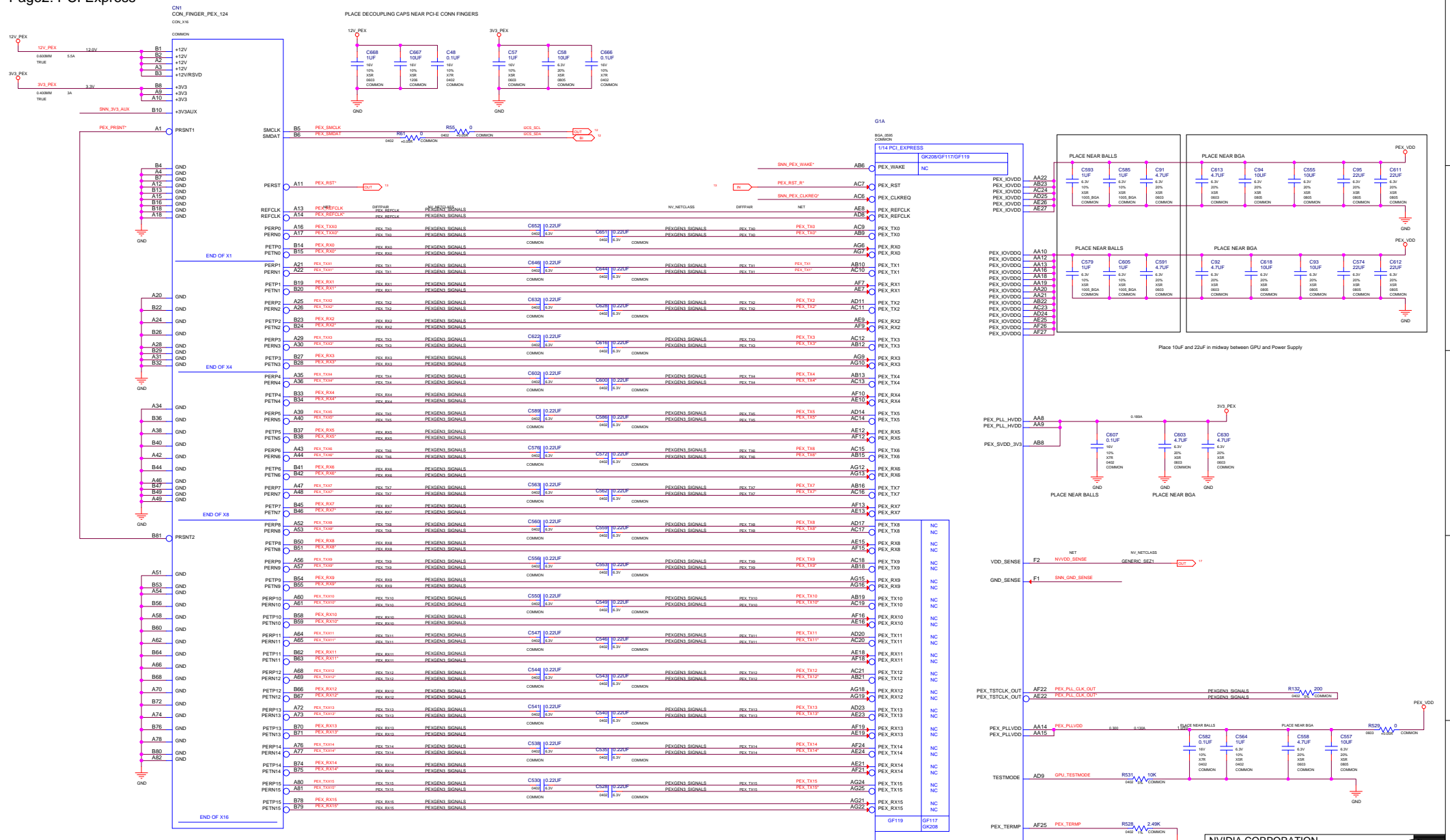
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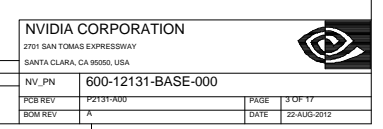
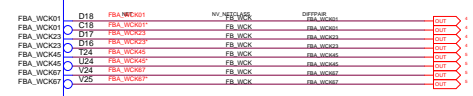
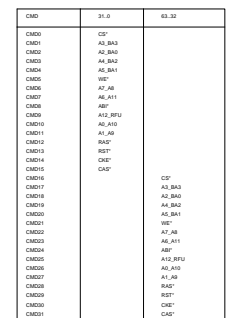
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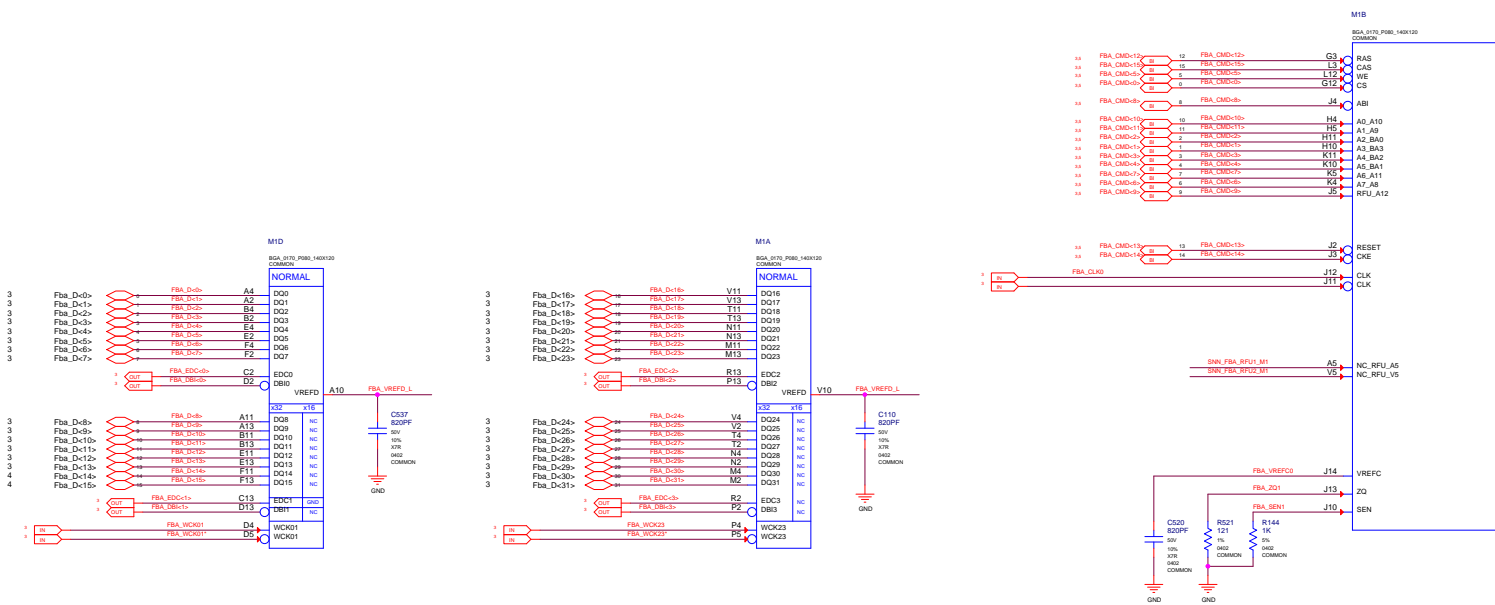
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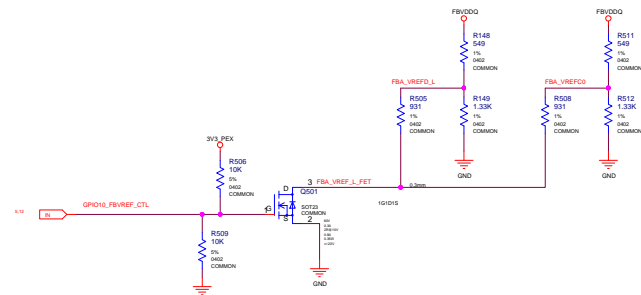
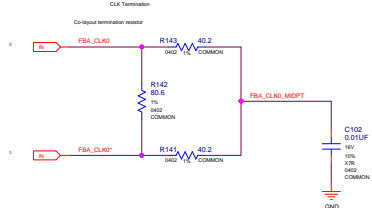
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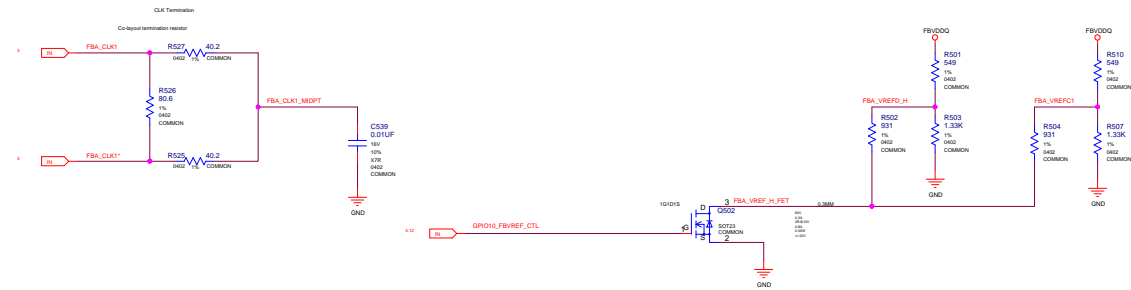
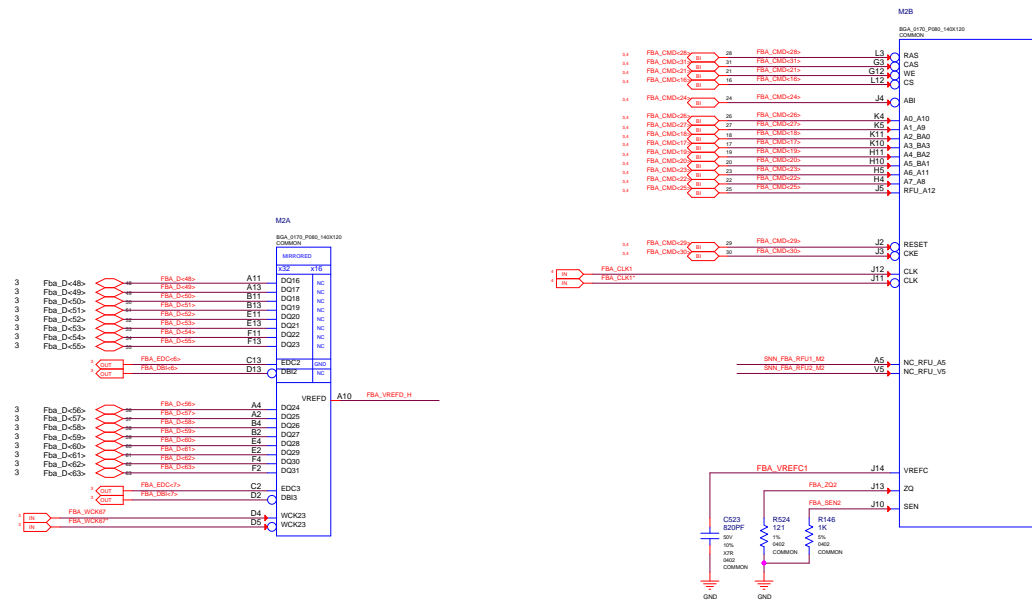




QND	31.0	63.32
QND0	C0	
QND1	A3, BA3	
QND2	A2, BA0	
QND3	A4, BA2	
QND4	A5, BA1	
QND5	0E7	
QND6	A7, A8	
QND7	AE, A11	
QND8	4B7	
QND9	A12, RFU	
QND10	A0, A9	
QND11	A1, A9	
QND12	8A07	
QND13	8E7	
QND14	CAD7	
QND15	CAD7	
QND16	C0	
QND17	A3, BA3	
QND18	A2, BA0	
QND19	A4, BA2	
QND20	A5, BA1	
QND21	0E7	
QND22	A7, A8	
QND23	AE, A11	
QND24	4B7	
QND25	A12, RFU	
QND26	A0, A9	
QND27	A1, A9	
QND28	8A07	
QND29	8E7	
QND30	CAD7	
QND31	CAD7	



OID	SP-0	KS-30
CM05	CS'	
CM01	AS, BA3	
CM02	AS, BA0	
CM03	AA, BA2	
CM04	AS, BA1	
CM06	WE'	
CM07	AS, AB	
CM08	AA, AB1	
CM09	AB'	
CM10	AA2, BPU	
CM11	AS, AB	
CM12	BA2'	
CM13	BE1'	
CM14	CH'	
CM15	CA2'	
CM16		CS'
CM17		AS, BA3
CM18		AS, BA0
CM19		AA, BA2
CM20		AS, BA1
CM21		WE'
CM22		AS, AB
CM23		AA, AB1
CM24		AB'
CM25		AA2, BPU
CM26		AS, AB
CM27		AA2
CM28		BA2'
CM29		BE1'
CM30		CH'
CM31		CA2'



ASSEMBLY	
PAGE DETAIL	FB Partition A bits 63..32



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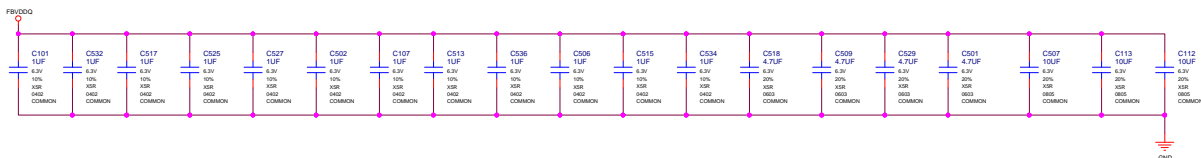
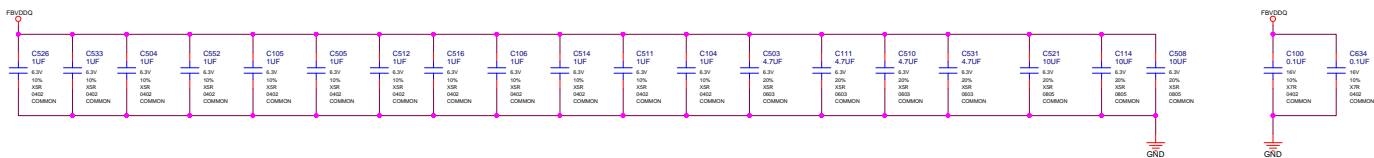
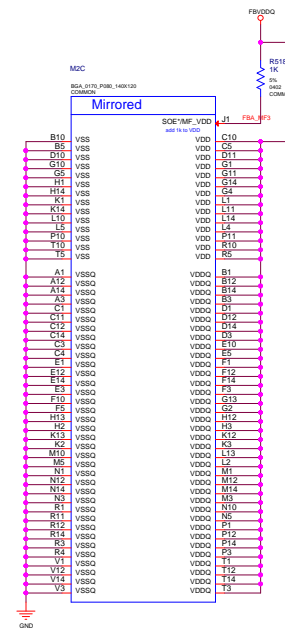
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PLACE LARGER CAPACITORS SLIGHTLY FARTHER AWAY

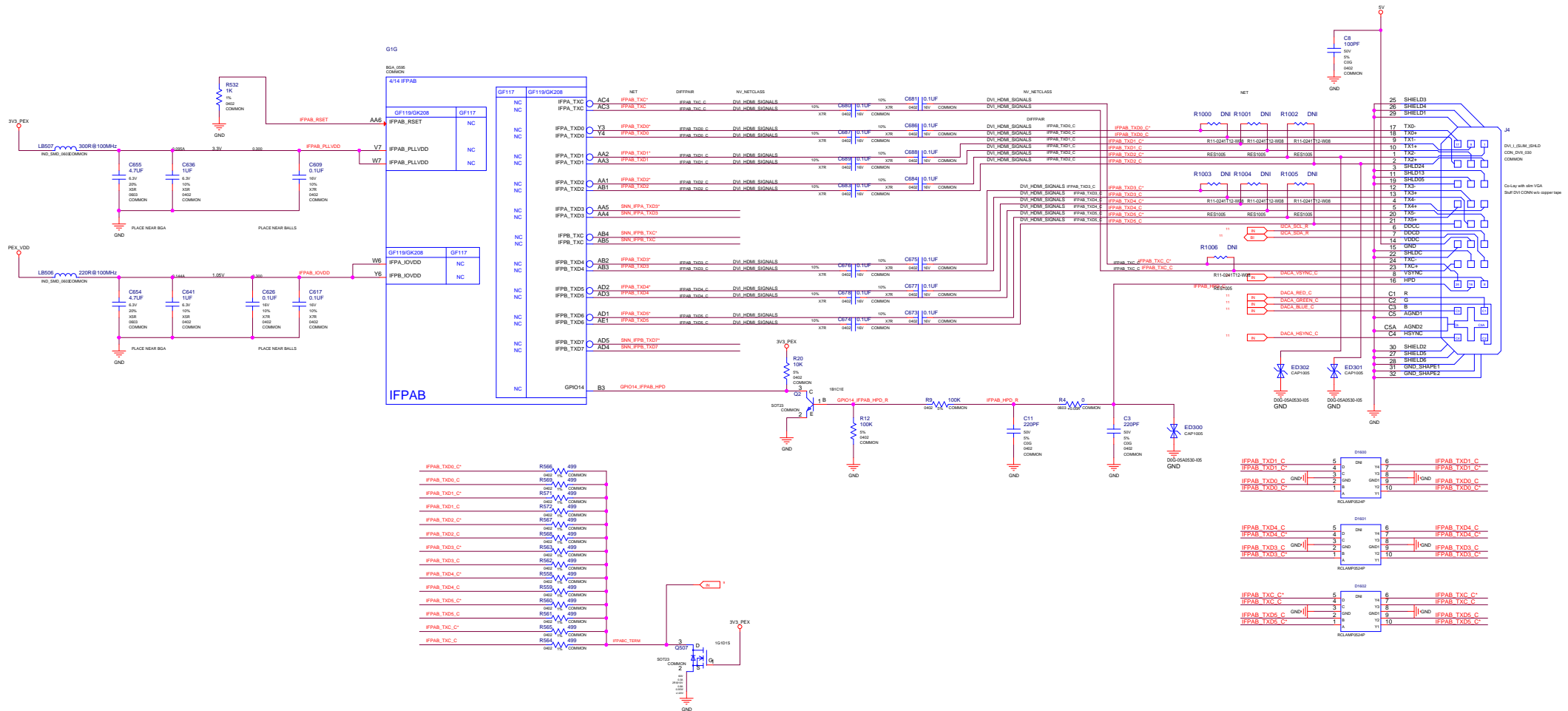
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PAGE DETAIL	IFPA/B TMD5 DUAL LINK

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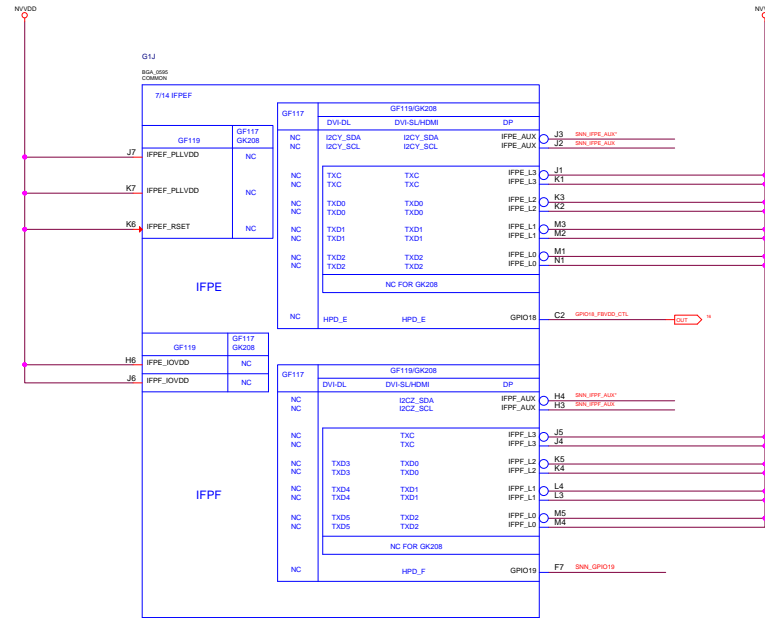
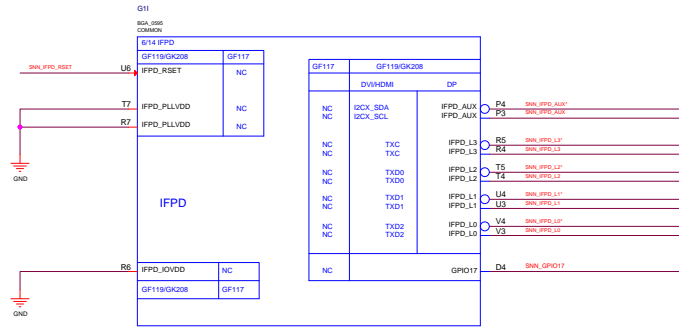


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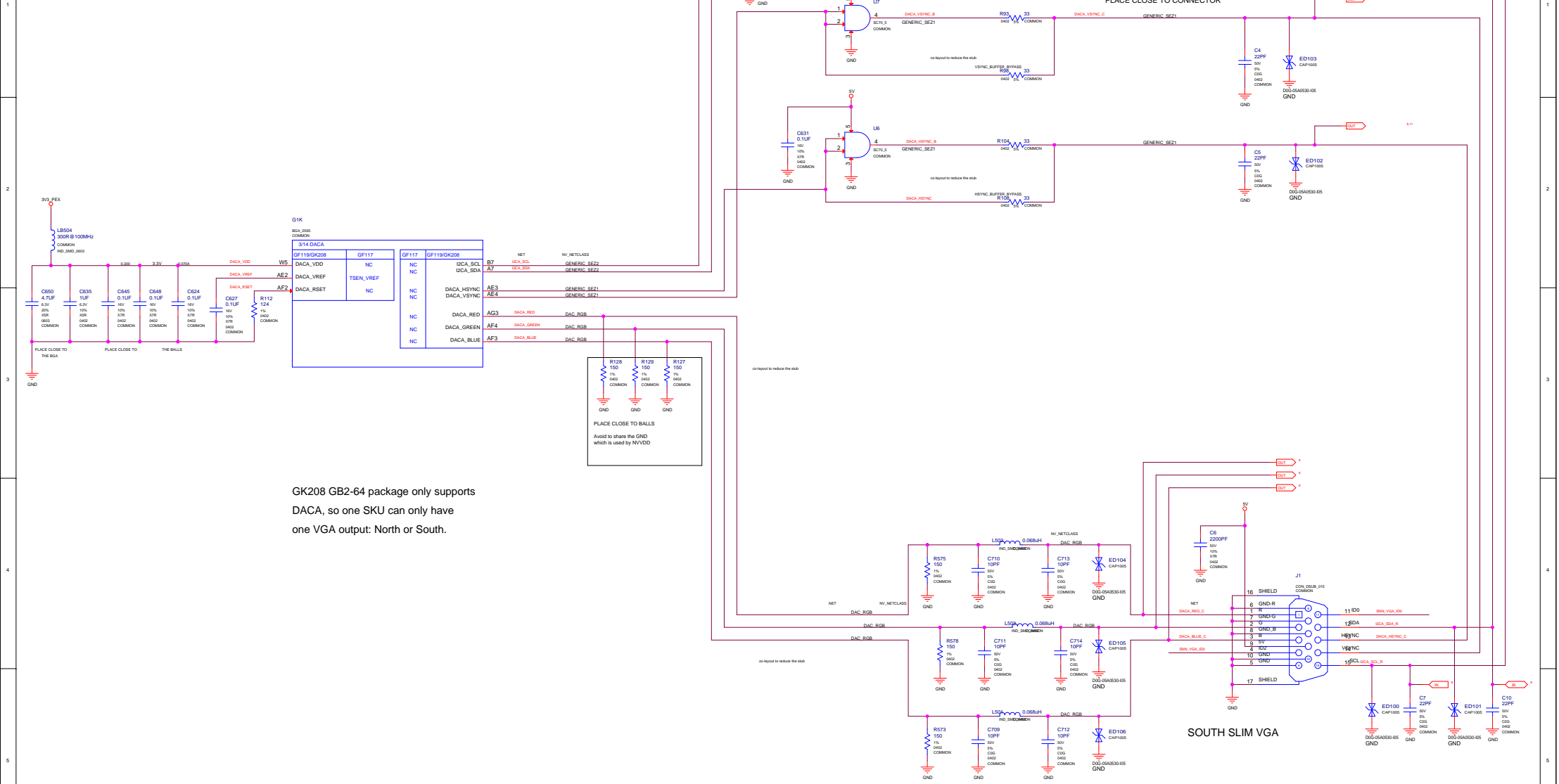
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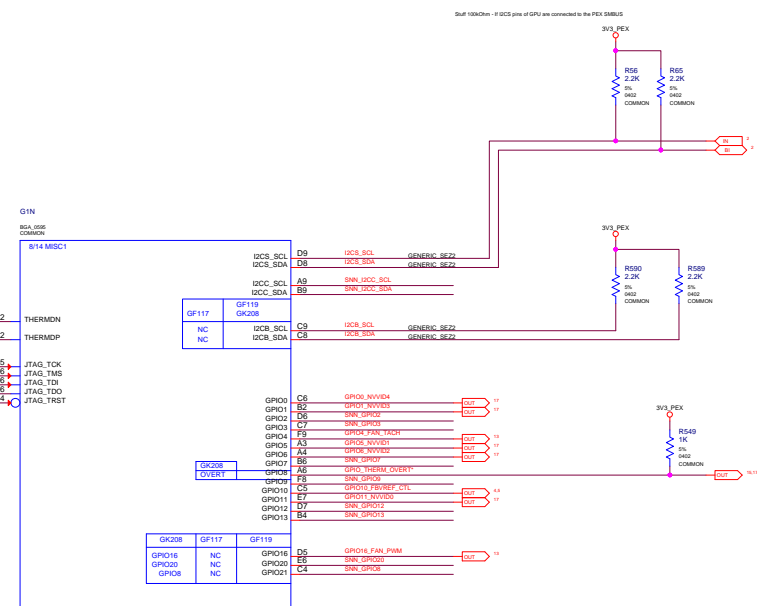
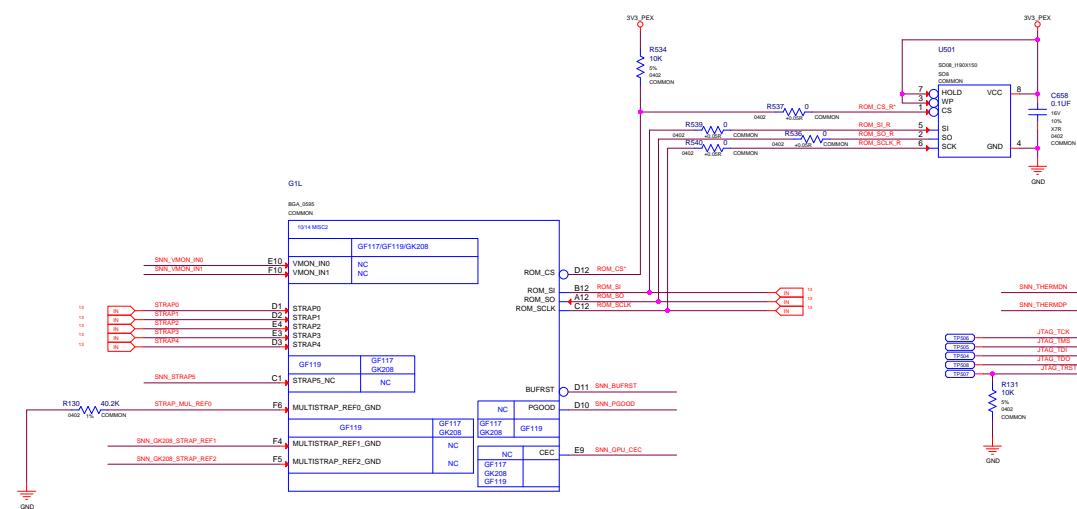


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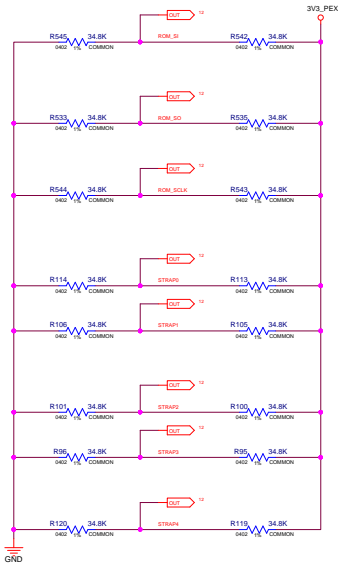
XTALSSIN	XTALOUTBUFF	Inverted PWM %
PU	PU	66 (33% HIGH)
PU	PD	50 (50% HIGH)
PD	PU	33 (66% HIGH)
PD	PD	0 (100% HIGH)



Multilevel Straps

0000	5.1K to GND	1000	5.1K to VCC
0001	10K to GND	1001	10K to VCC
0010	15K to GND	1010	15K to VCC
0011	20K to GND	1011	20K to VCC
0100	24.5K to GND	1100	24.5K to VCC
0101	30.1K to GND	1101	30.1K to VCC
0110	34.8K to GND	1110	34.8K to VCC
0111	45.3K to GND	1111	45.3K to VCC

GK208 Straps

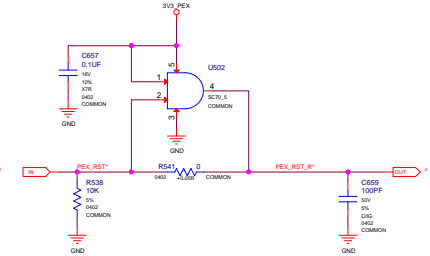


Bit Signal

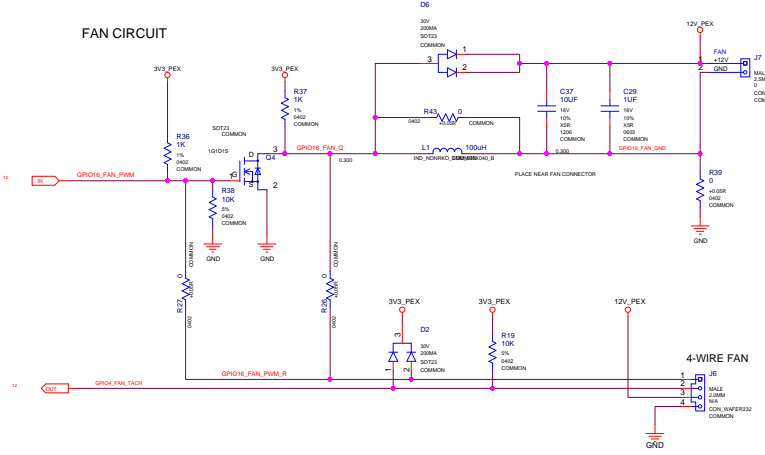
Values

03	RAMCFG[0]	0000	Reserved			ROM_SI
02	RAMCFG[0]	0001	USB-A16 GEOREX X16 64bit Epiphy			
01	RAMCFG[0]	0010	USB-A16 GEOREX X16 64bit Epiphy A-die			
00	RAMCFG[0]	0011	USB-A16 GEOREX X16 64bit Samsung D-die			
02	FB[0]	01	128MB			ROM_SO
01	FB[0]	10	256MB			
01	SMB_ALT_ADDR	0	DISABLED	1	ENABLED	
01	VGA_DEVICE	0	DISABLED	1	ENABLED	
03	PCI_DEV0_EXT	0000	0	SKU1	TBD	ROM_SCLK
02	SUB_VENDOR	0	NO_BIOS	1	BIOS	
01	PCI_DEV0_EXT_BITS	0000	0	SKU1	TBD	
00	PEX_FAL_EN_TERMINO	0	DISABLED	1	ENABLED	
03	USE[RE]	0000	NOT			Strap0
02	USE[RE]					
01	USE[RE]					
00	USE[RE]					
03	3G0_PADCFG_LUT_ADDR[0]	0000	DISKTOP_DEFAULT	1000	DISKTOP_JTHRS	
02	3G0_PADCFG_LUT_ADDR[0]	0001	MOBILE_JTHRS_LAMP	1001	MOBILE_JTHRS_LAMP	
01	3G0_PADCFG_LUT_ADDR[0]	0010	MOBILE_JTHRS_LAMP	1010	MOBILE_JTHRS_LAMP	
00	3G0_PADCFG_LUT_ADDR[0]	0011	MOBILE_JTHRS_LAMP	1011	MOBILE_JTHRS_LAMP	
03	3G0_PADCFG_LUT_ADDR[0]	0100	MOBILE_JTHRS_LAMP	1100	MOBILE_JTHRS_LAMP	
02	3G0_PADCFG_LUT_ADDR[0]	0101	MOBILE_JTHRS_LAMP	1101	MOBILE_JTHRS_LAMP	
01	3G0_PADCFG_LUT_ADDR[0]	0110	MOBILE_JTHRS_LAMP	1110	MOBILE_JTHRS_LAMP	
00	3G0_PADCFG_LUT_ADDR[0]	0111	MOBILE_JTHRS_LAMP	1111	MOBILE_JTHRS_LAMP	
03	PCI_DEV0[0]	0000	0010			Strap1
02	PCI_DEV0[0]					
01	PCI_DEV0[0]					
00	PCI_DEV0[0]					
03	SCORE_EXPOSED	0	DISABLED	1	ENABLED	Strap2
02	SCORE_EXPOSED	1	ENABLED			
01	SCORE_EXPOSED	0	DISABLED	1	ENABLED	
00	SCORE_EXPOSED	0	DISABLED	1	ENABLED	
03	PCI_SPEED_CHANGE_GEN0	0	DISABLED	1	ENABLED	Strap3
02	PCI_SPEED_CHANGE_GEN0					
01	PCI_SPEED_CHANGE_GEN0					
00	PCI_SPEED_CHANGE_GEN0					
03	DP_FAL_VDD_3V3	0	USE V6	1	USE V3V3	Strap4
02	DP_FAL_VDD_3V3					
01	DP_FAL_VDD_3V3					
00	DP_FAL_VDD_3V3					

PCI-E Reset



FAN CIRCUIT



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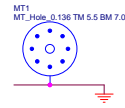
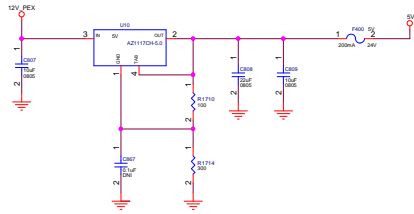
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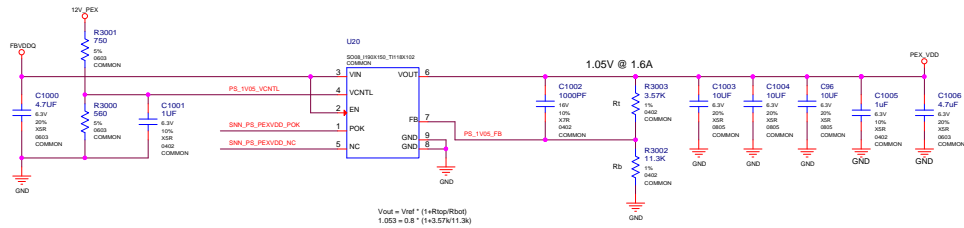
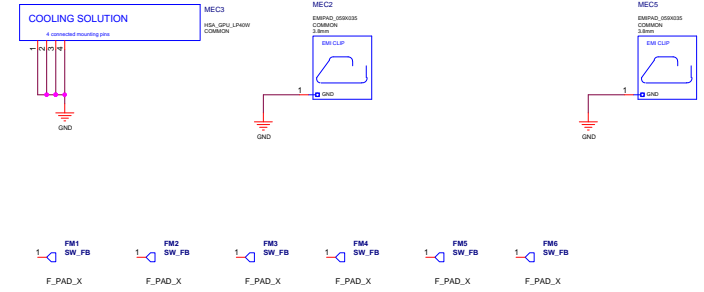
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5V Power

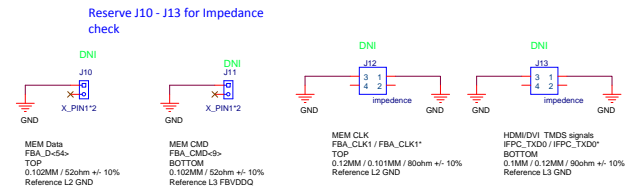
STUFF FOR DVI/HDMI/VGA DISPLAY SKU



Mechanical



PEX_VDD



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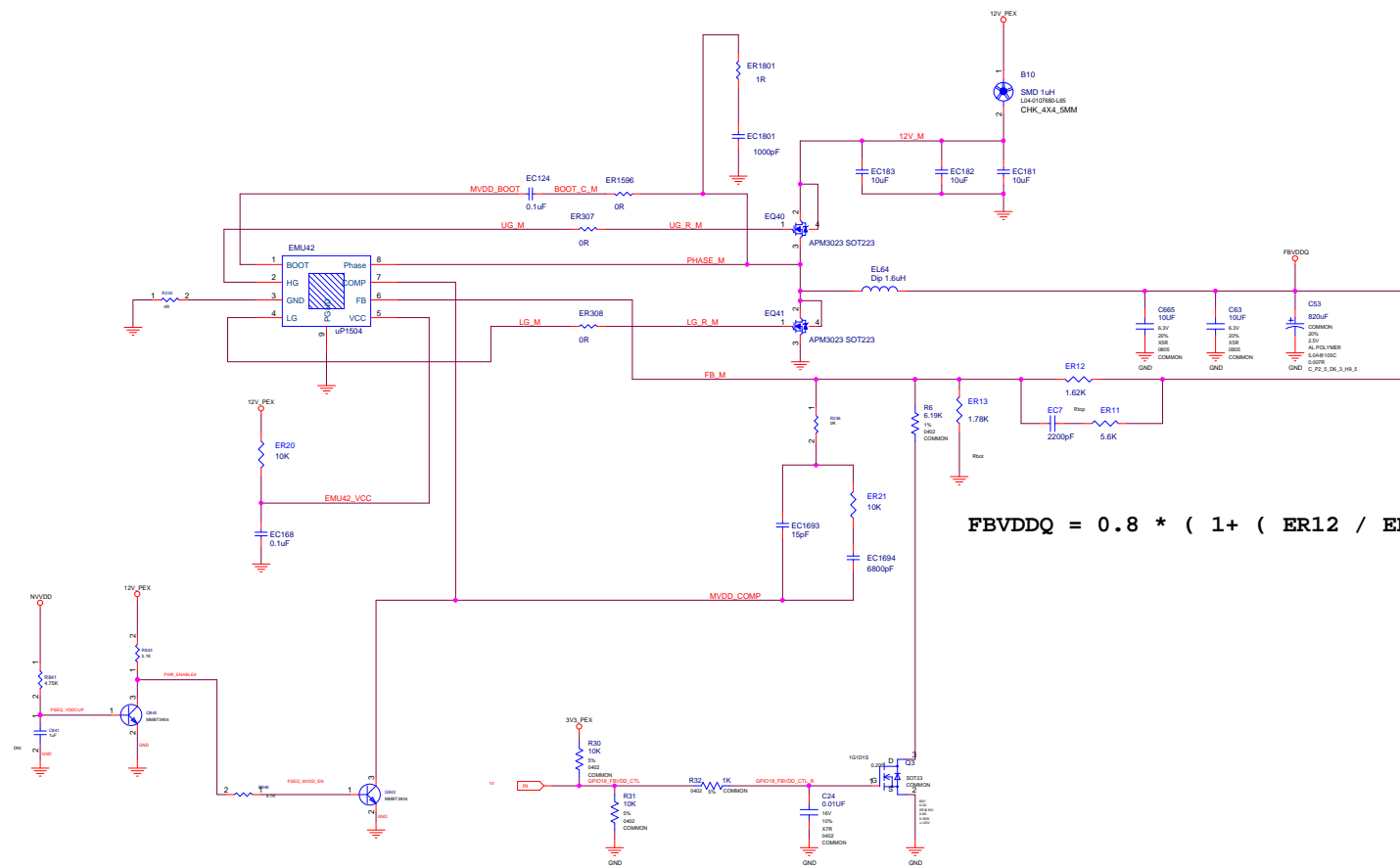


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$$FBVDDQ = 0.8 * (1 + (ER12 / ER13))$$

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