

P2002

2GB GDDR5, 256b, 64Mx32

Stacked DVI-I/DVI-D + HDMI + DP

TABLE OF CONTENTS

Page Description

1 Table of Contents
2 Block Diagram
3 PCI Express
4 MEMORY: GPU Partition A/B
5 MEMORY: FBA[31:0]
6 MEMORY: FBA[63:32]
7 MEMORY: FBB[31:0]
8 MEMORY: FBB[63:32]
9 MEMORY: GPU Partition C/D
10 MEMORY: FBC[31:0]
11 MEMORY: FBC[63:32]
12 MEMORY: FBD[31:0]
13 MEMORY: FBD[63:32]
14 GPU PWR and GND
15 GPU Decoupling
16 DACA Interface
17 IFPAB DVI-DL
18 IFPEF DVI-DL
19 IFPC HDMI
20 IFPD DP
21 MIOA/B Interface
22 MISC1: Fan, Thermal, JTAG, GPIO
23 MISC2: ROM, XTAL, Straps
24 PS: 5V, PEX_VDD
25 PS: FBVDD/IQ

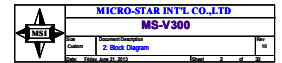
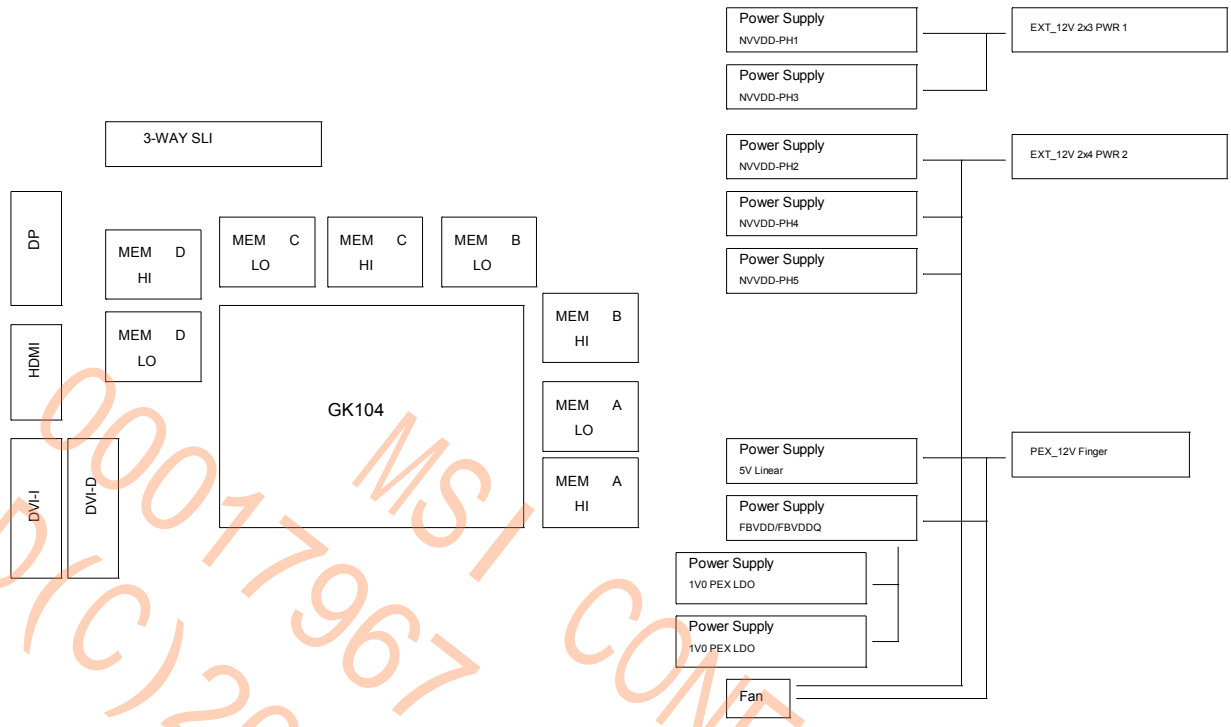
Page Description

26 PS: UP1811 & V check point
27 PS: NVVDD Phase 1,3
28 PS: NVVDD Phase 2,5
29 PS: NVVDD Phase 4
30 PS: Inputs, Filtering, and Monitoring
31 PS: Shutdown
32 PS: 12V Current Steering PSI Control and LED
33 MECH: Bracket/Thermal

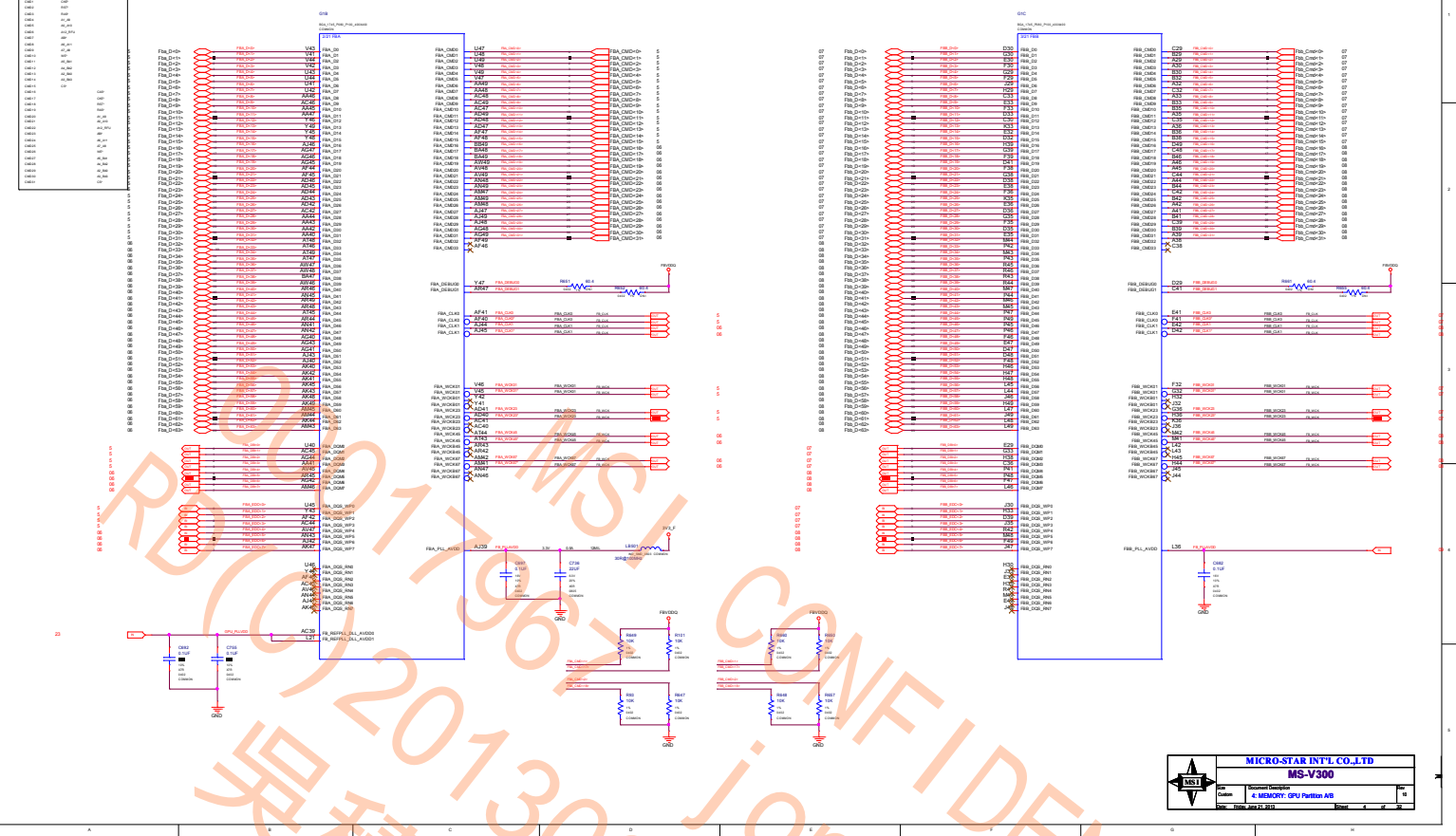
Base on V282-3.1

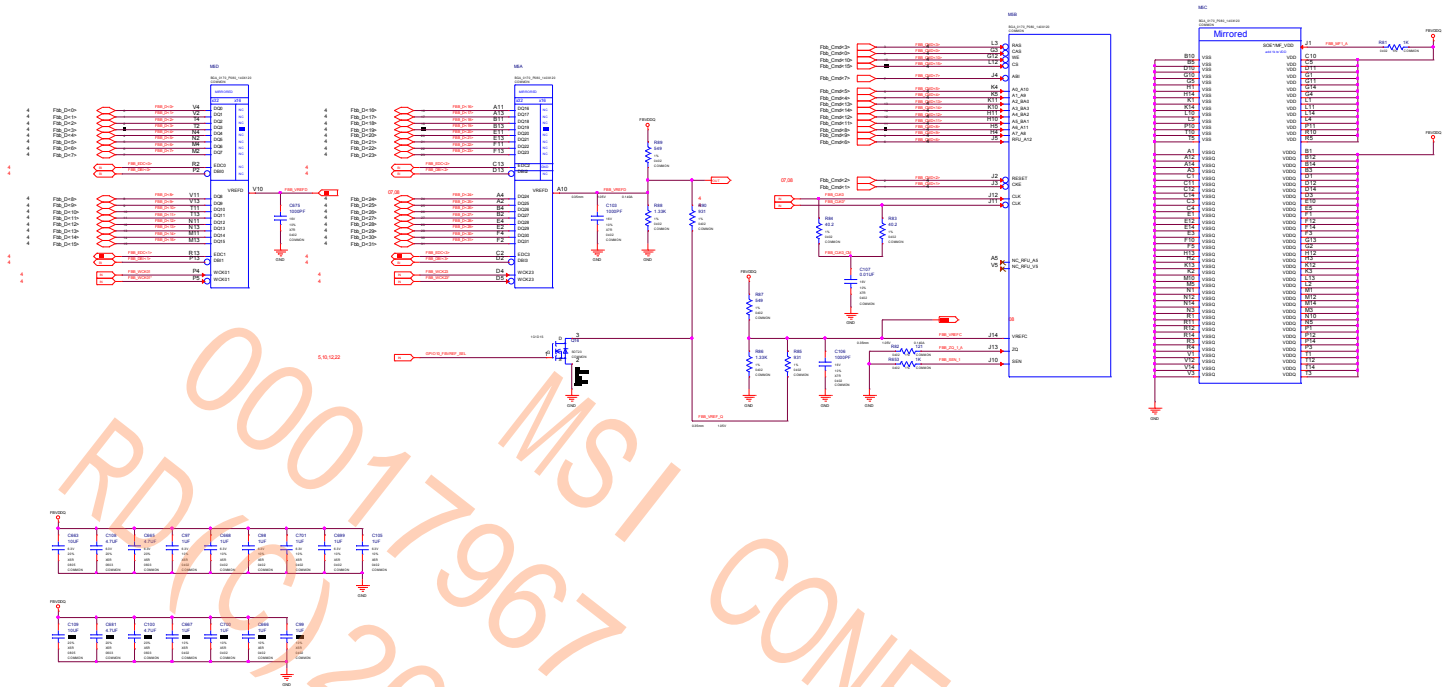
P23 add net: dual bios & bios swich
P24 add net: PS1_1V_FB
P25 add net: PS3_FBVDDQ_FB
P26 add up1811 & V check point
P27 add net: NCP4206_FB

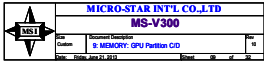


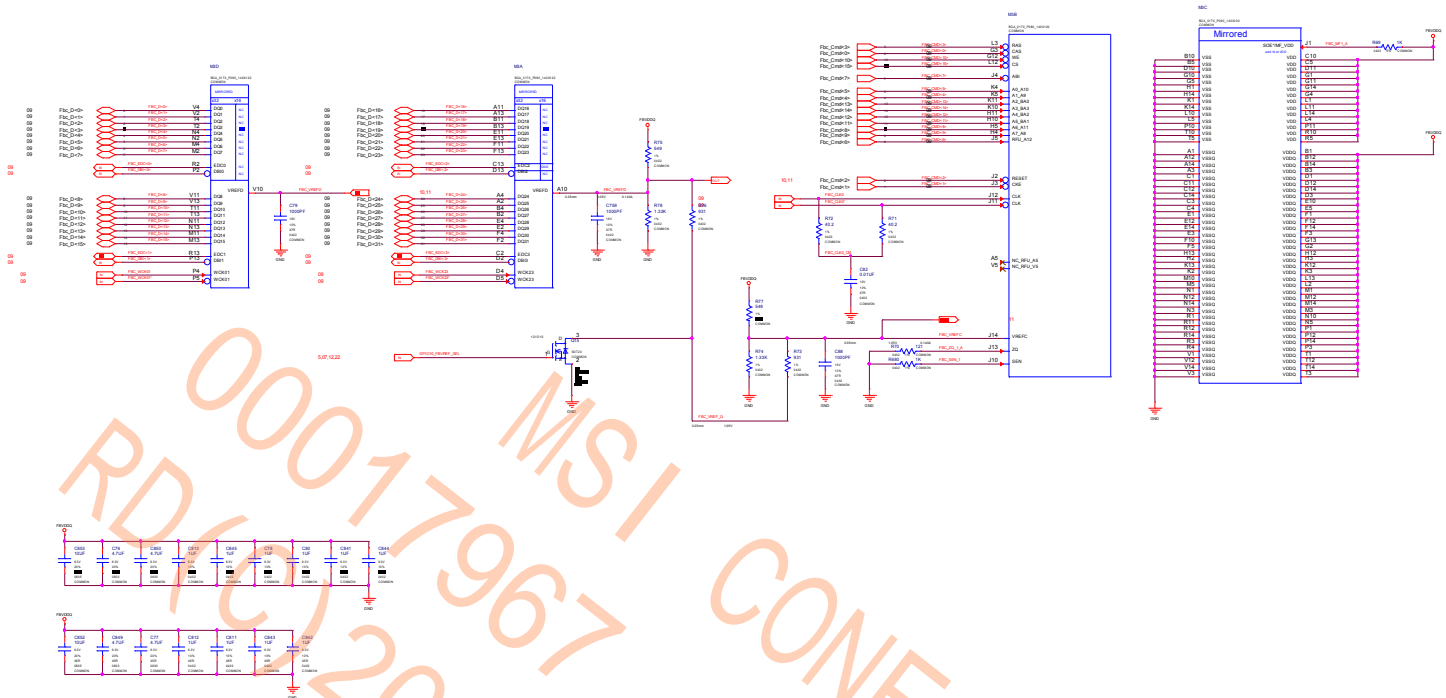


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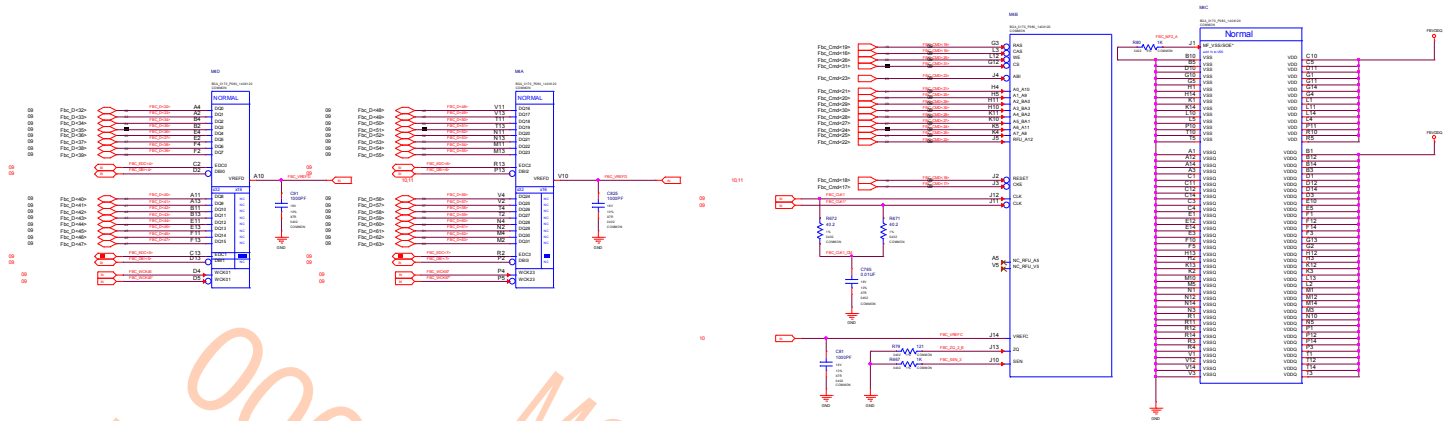




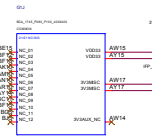


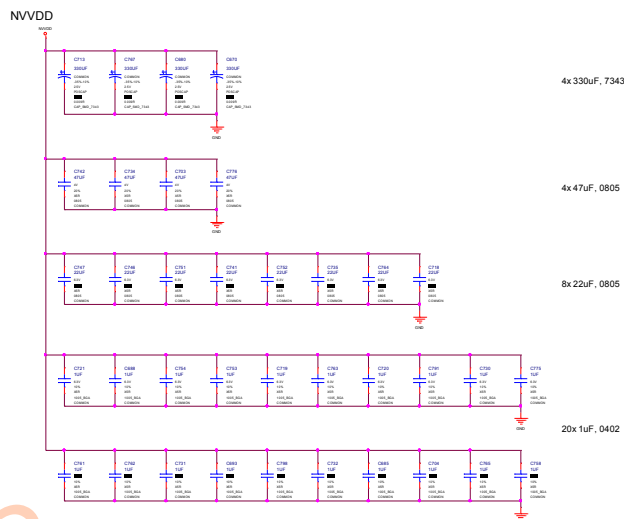
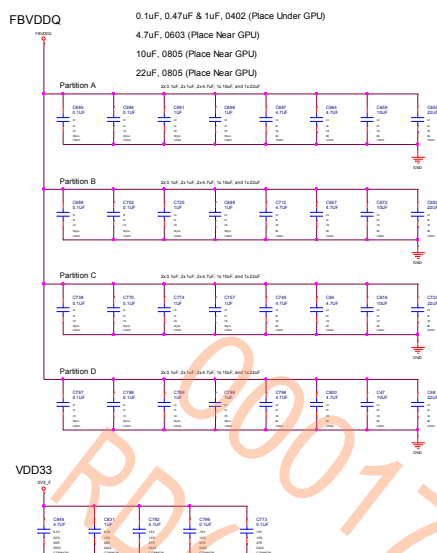
MICRO-STAR INT'L CO., LTD	
MS-V300	
Part No.	MS-V300
Rev.	1.0
Date	2013/09/25
Drawn	MS-V300
Checked	MS-V300
Approved	MS-V300

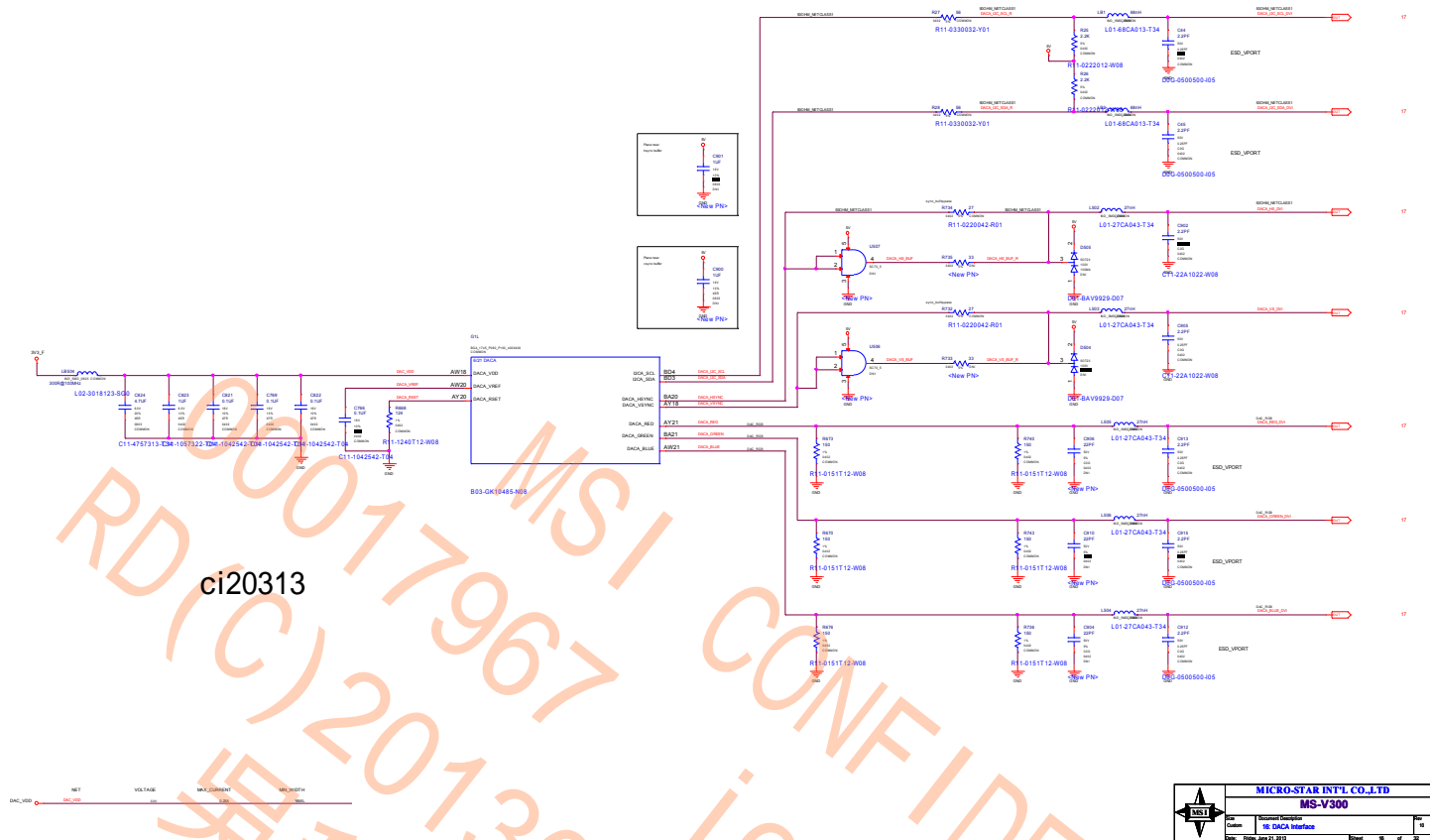
00017967 MSI CONFIDENTIAL
 RD (C) 2013092502 jonepei (裴亮樂)
 吳積源 (00011601) RMA工程課

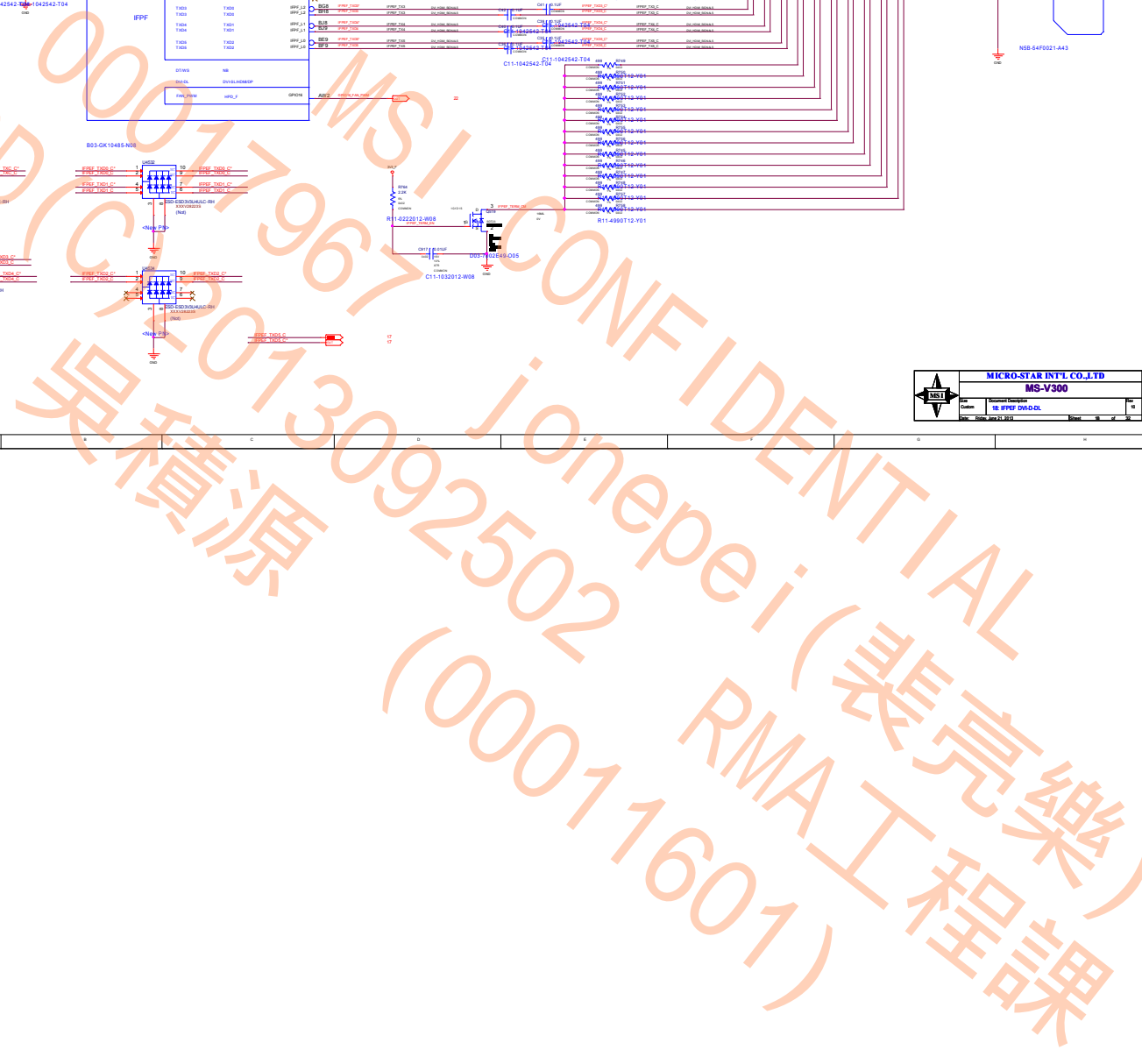


00017967 MS1 CONFIDENTIAL
 RD (C) 2013092502 jonepei (裴亮樂)
 吳積源 (00011601) RMA工程課





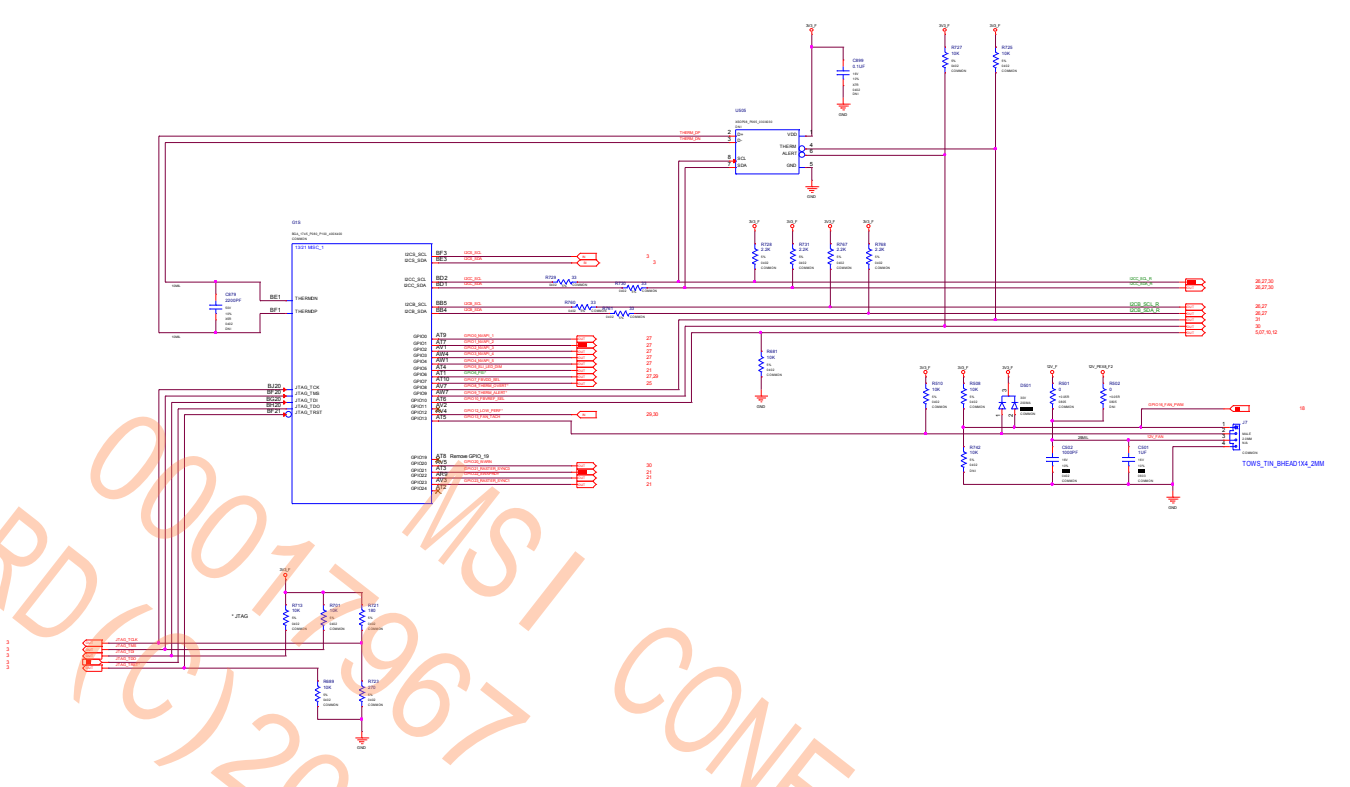






MS-V300

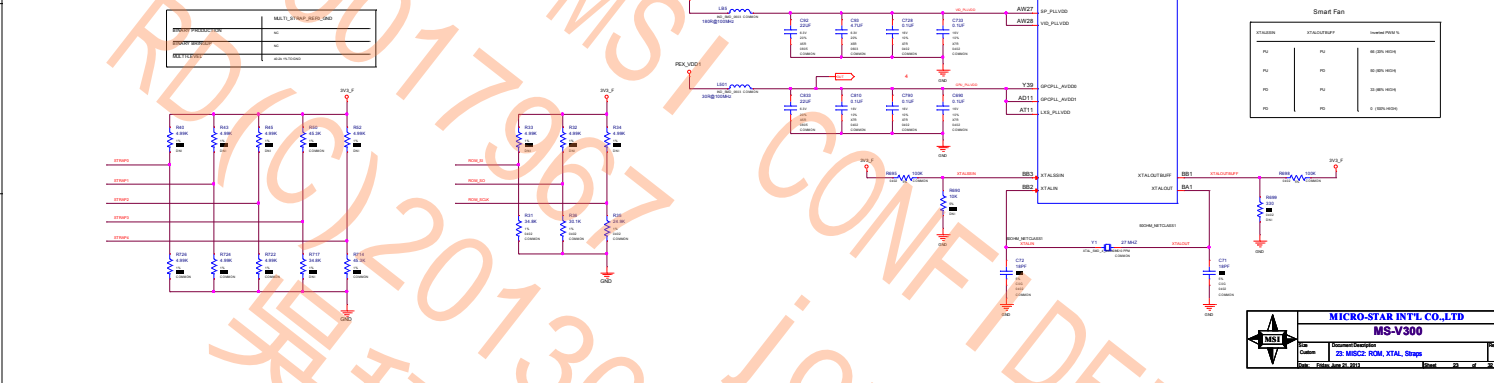
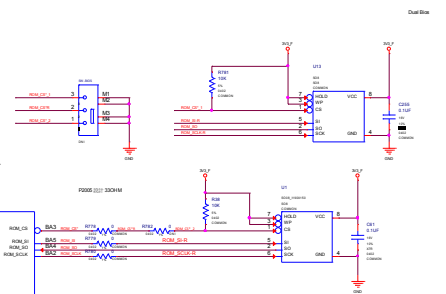
Interface



STRAP0	USER_BT [0:0]	0000	5K PU
STRAP1	3G0_PADCFG_LUT_ADDR	0000	5K PU Desktop
STRAP2	PC3_DEVID [0:0]	0000 - (0x1180)	5K PU -400 GPU
STRAP3	SOR_EXPRESS [0:0]	1111	45K PU
STRAP4	DP_FL_VDD_3V3	1	FOR 3_3V
	PC3_MAX_SPEED	1	FOR GEN2.0
	PC3_SPO_CHANGE_GEN	1	ENABLED
	RAMCFG[0]	0	
ROM_SI	RAMCFG[1]	1	25K PU
	RAMCFG[2]	1	
	RAMCFG[3]	0	
	VGA_DEVICE	1	
ROM_SO	SMB_ALT_ADDR	0	10K PU
	FB[1] APERTURE_SIZE	0	For 256MB
	FB[1] APERTURE_SIZE	1	For 256MB
	PC3_FLT_DR_TERM[00]	0	DISABLED
	PC3_DEVID_EXT[0]	0	For 0x1180
ROM_CLK	SUB_VENDOR	1	Dedicated BIOS
	PC3_DEVID_EXT[4]	0	For 0x1180

QND	3V3
5A	0000 1000
10A	0001 1001
15A	0010 1010
20A	0011 1011
25A	0100 1100
30A	0101 1101
35A	0110 1110
40A	0111 1111

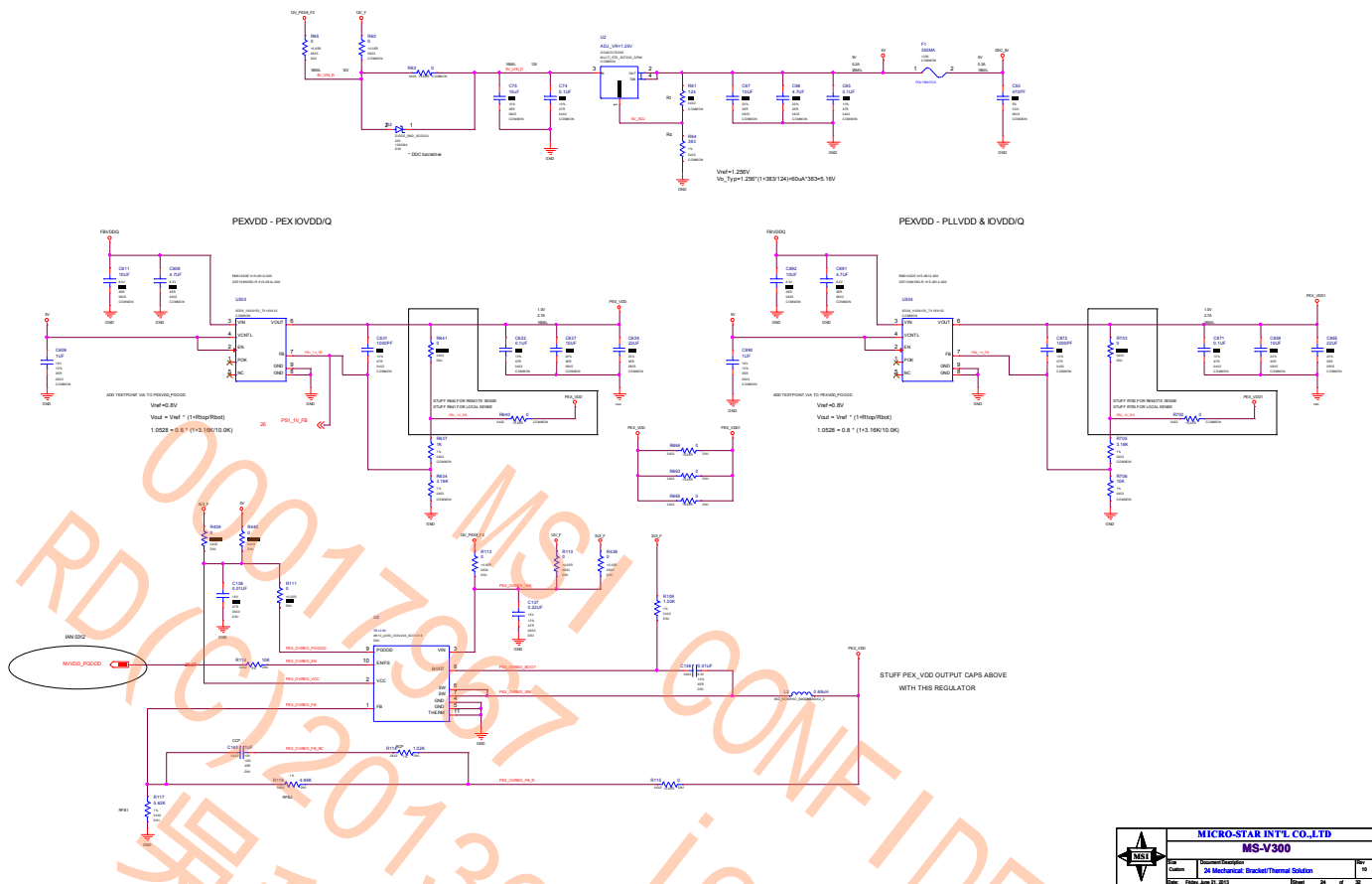
CFQ3 [0] Config With Vendor
0000 Reserved
0001 329A32 256-MB Elysia
0010 329A32 256-MB Hymn
0011 329A32 256-MB Samsung
0100 Reserved
0101 646A32 256-MB Elysia
0110 646A32 256-MB Hymn
0111 646A32 256-MB Samsung
1000 Reserved
1001 329A32 128-MB Elysia
1010 329A32 128-MB Hymn
1011 329A32 128-MB Samsung
1100 Reserved
1101 646A32 128-MB Elysia
1110 646A32 128-MB Hymn
1111 646A32 128-MB Samsung



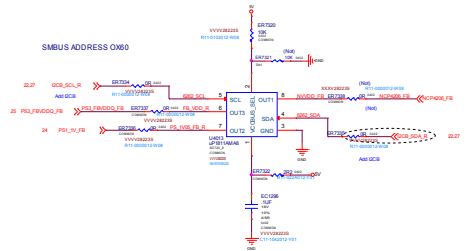
Smart Fan		
XTAL0IN	XTAL0OUT	XTAL0VDD
F0	F0	0.000V-0.000V
F1	F1	0.000V-0.000V
F2	F2	0.000V-0.000V
F3	F3	0.000V-0.000V



ci20313

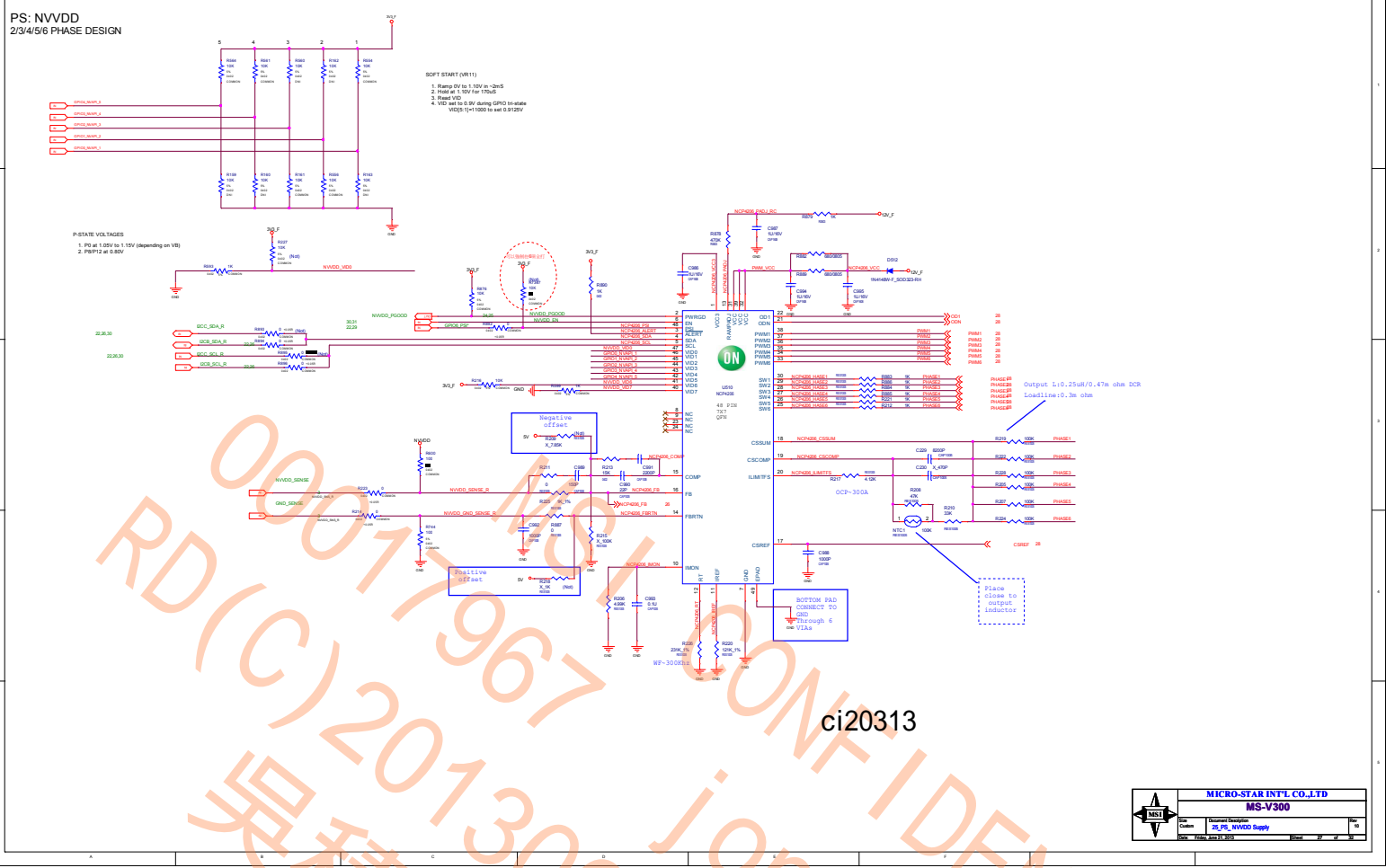


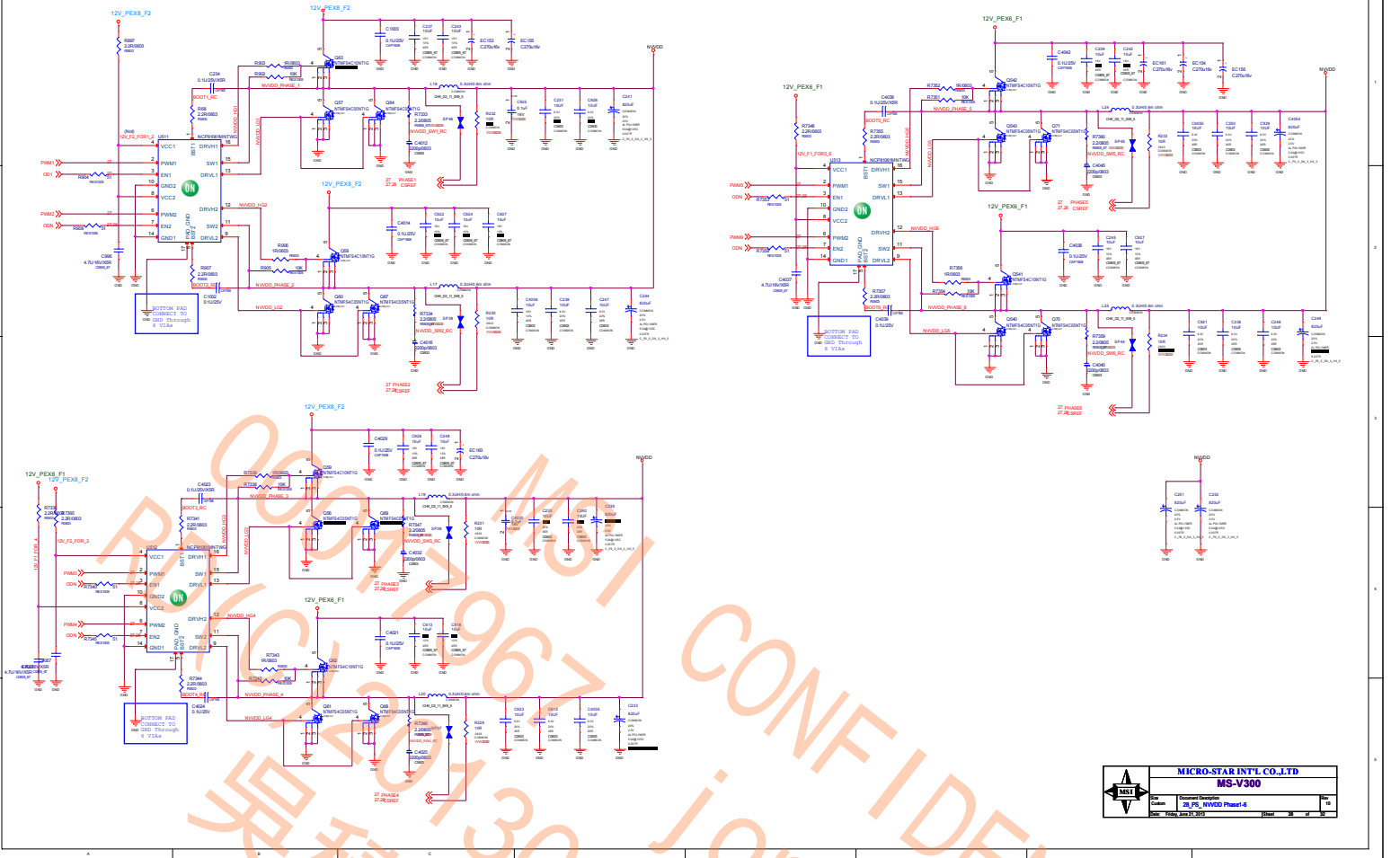




	MICRO-STAR INT'L CO., LTD.	
	MS-V300	
Rev.	1.0	
Drawn by	jonepei	
Checked by	RMA	
Date	2013.12.10	
Sheet	26	27

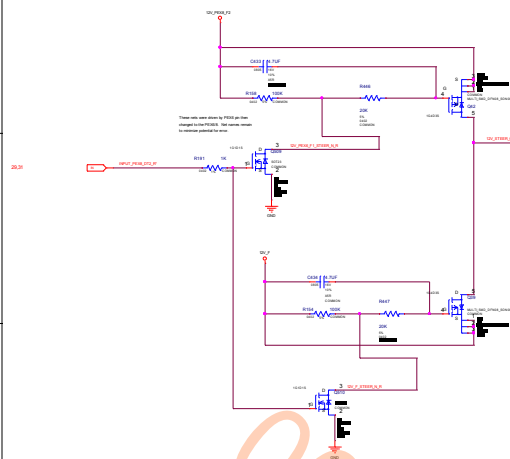
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RD(C)2013092502 jonepei (裴亮樂)
吳積源 RMA工程課
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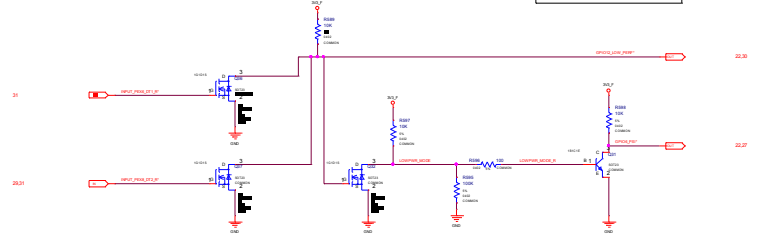


MICRO-STAR INT'L CO., LTD	
MS-V300	
Model	MS-V300
Version	1.0
Rev	1.0
Rev	1.0

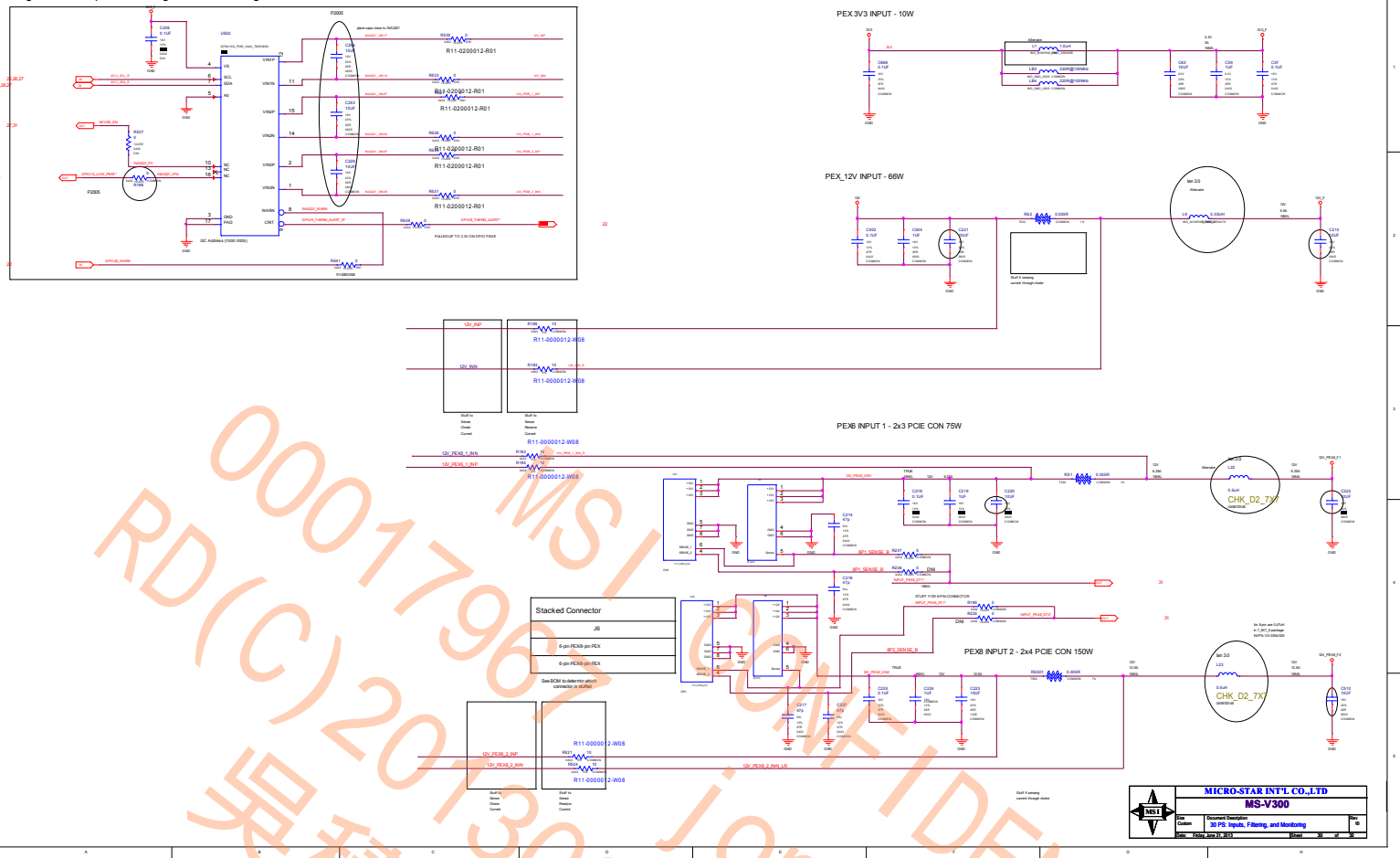
PEX Input - 12V Current Steering FETs

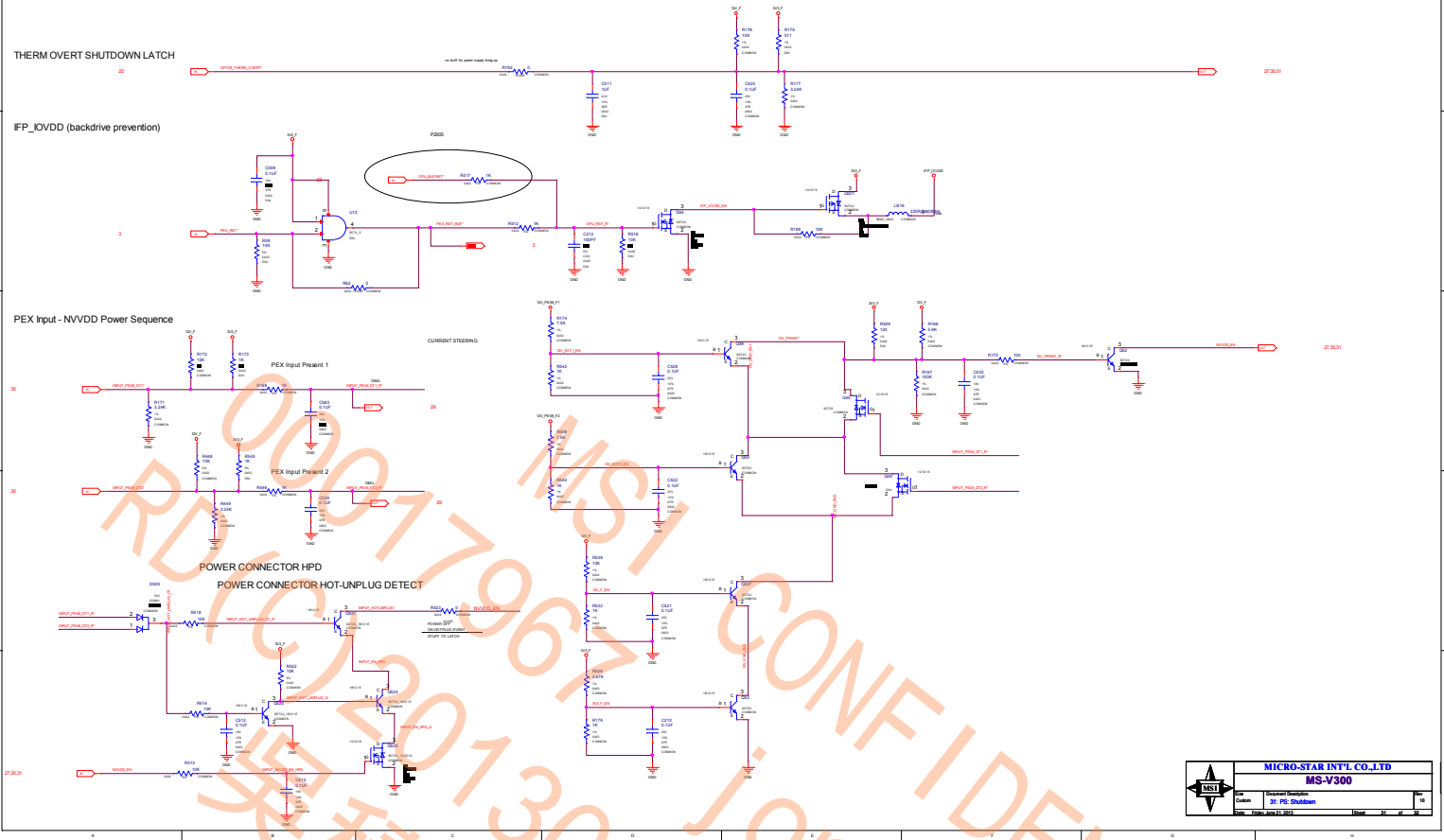


PEX Input - Power Level/PSI Control



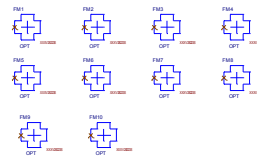
REMOVE GeForce Logo LED





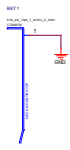
MICRO-STAR INT'L CO., LTD	
MS-V300	
Doc. No.	PS: Shutdown
Rev.	1.0
Date	2008-09-25

optical cross point



Top & bottom each layer set 4 points

Brackets:



Bracket Screw



Hockey Stick



GPU Stiffner



Mechanical Holes Symbol

