

V056: G72, DDR2 MEMORY 16Mx16


base on P381 A02
add PMW circute for FBVDDQ.

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Page13: PowerSupply: NVVDD, FBVDDQ
Page14: PowerSupplyI: 5V, DDC5V, F3V3, TMDS_PLLVDD

REV	VARIANT	NVPN	ASSEMBLY
8	BASE	600-10381-xxxx-000	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU00	600-10381-0000-100	P381: G72Q3-6A, 32 BIT DDR2 MEMORY, VGA+SDTV
2	SKU01	600-10381-0001-100	P381: G72Q3-6A, 64 BIT DDR2 MEMORY, VGA+SDTV
3	SKU02	600-10381-0002-100	P381: G72Q3-6A, 32 BIT DDR2 MEMORY, DVI+SDTV
4	SKU03	600-10381-0003-100	P381: G72Q3-6A, 64 BIT DDR2 MEMORY, DVI+SDTV
5	SKU04	600-10381-0004-100	P381: G72Q3-6A, 64 BIT DDR2 MEMORY, VGA+DVI+SDTV
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	P381 Overview



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Document Number

Rev
00A

Date

Wednesday, January 18, 2006

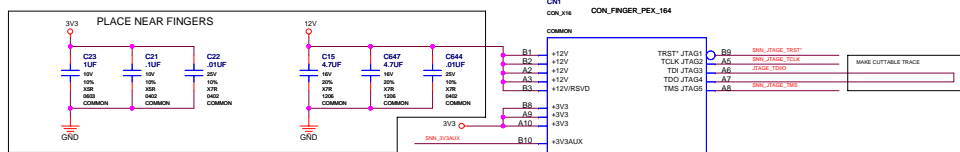
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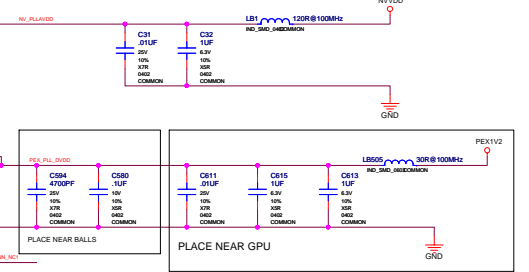
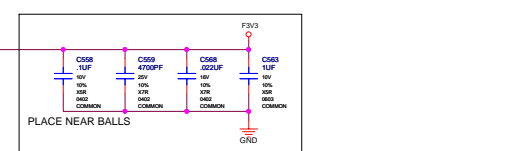
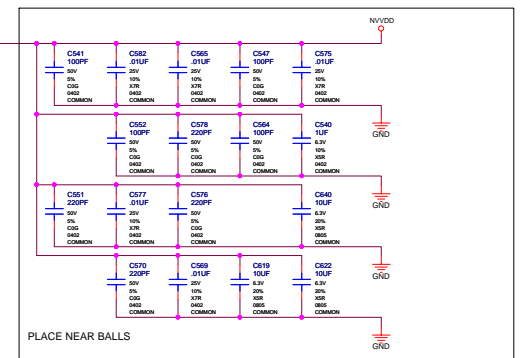
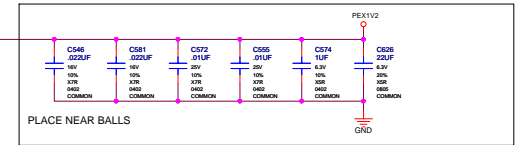
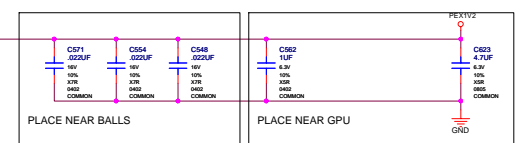
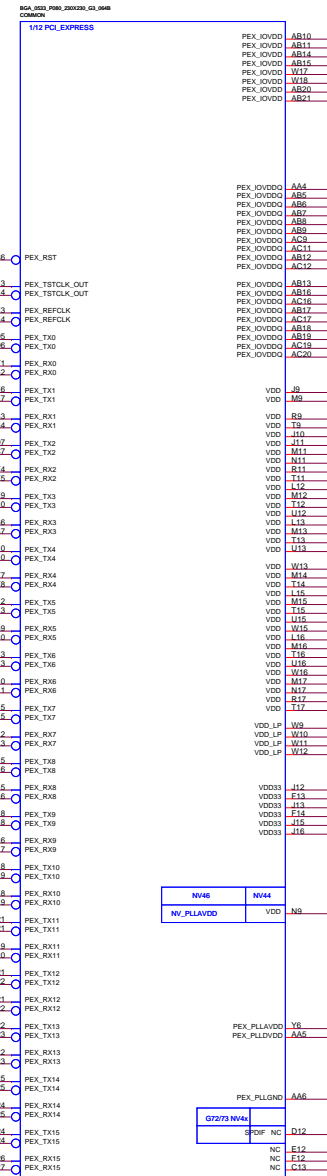
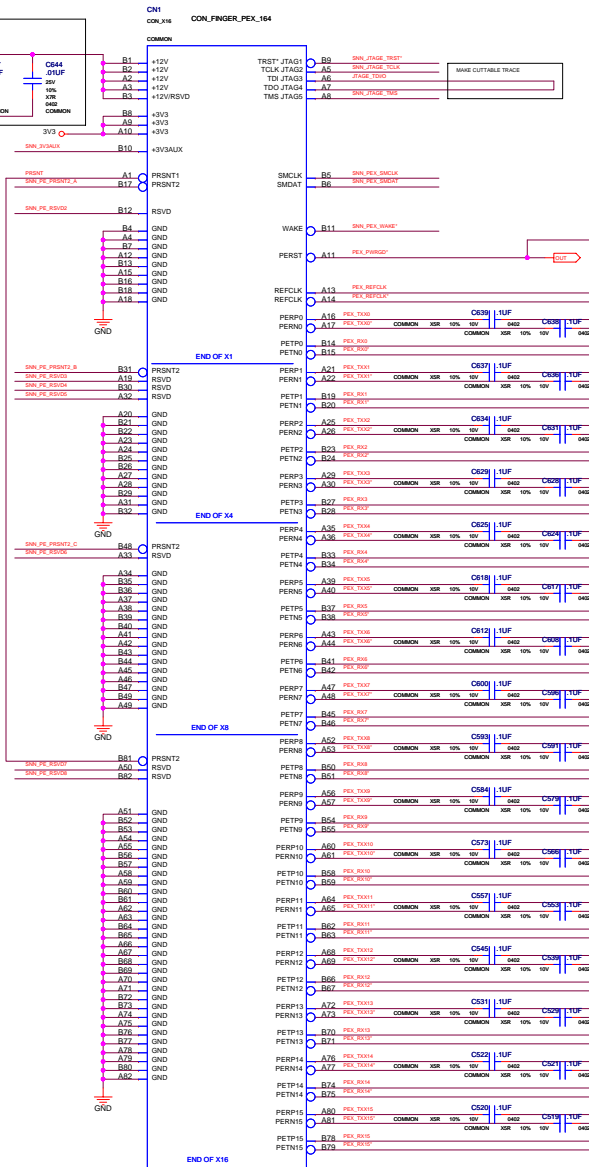
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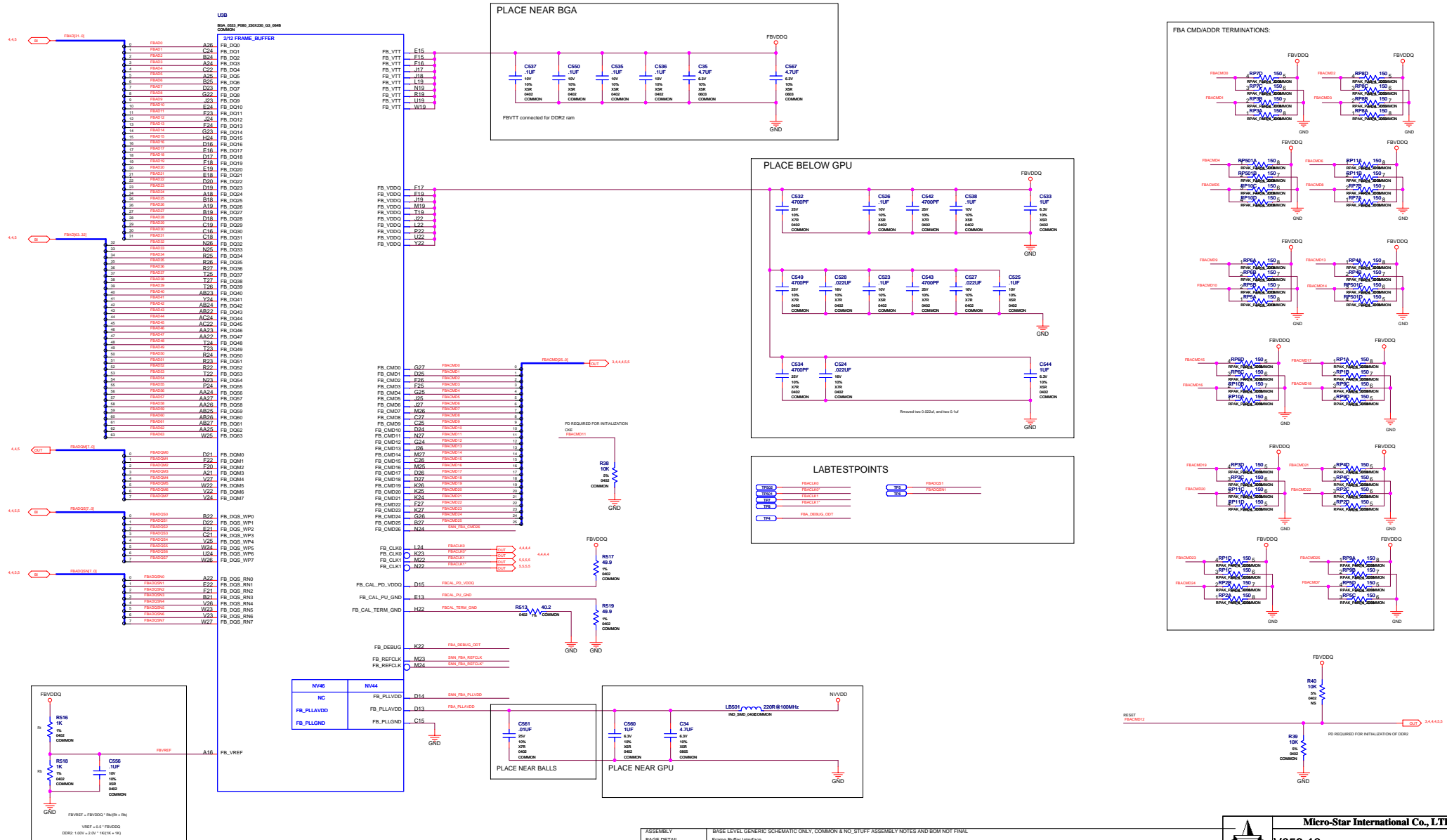
PEX-Interface



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24	PEX_10157	PEX_10159	+	1000FF
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27	PEX_10160	PEX_10162	+	1000FF
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30	PEX_10163	PEX_10165	+	1000FF
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37	PEX_10170	PEX_10172	+	1000FF
38	PEX_10171	PEX_10173	+	1000FF
39	PEX_10172	PEX_10174	+	1000FF
40	PEX_10173	PEX_10175	+	1000FF
41	PEX_10174	PEX_10176	+	1000FF
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44	PEX_10177	PEX_10179	+	1000FF
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GPU: FB-Interface

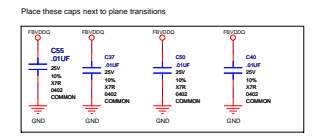
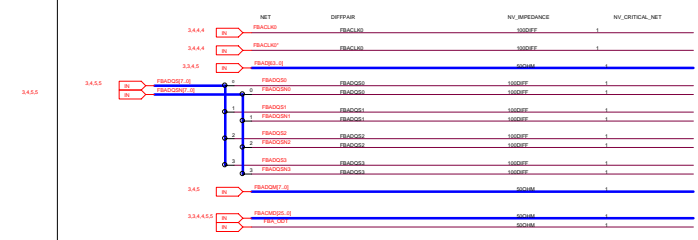


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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Frame Buffer Interface

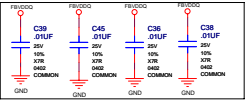
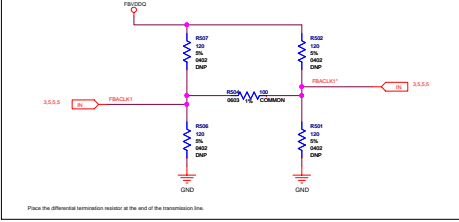
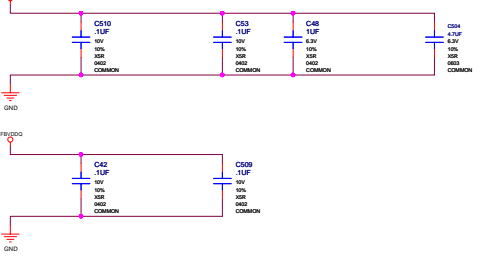
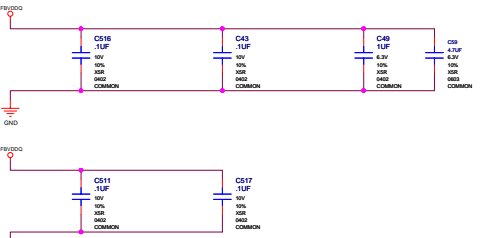
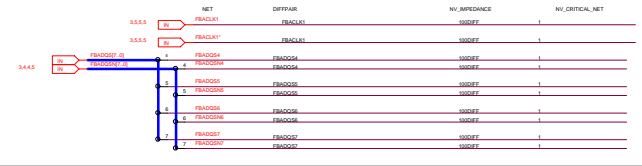
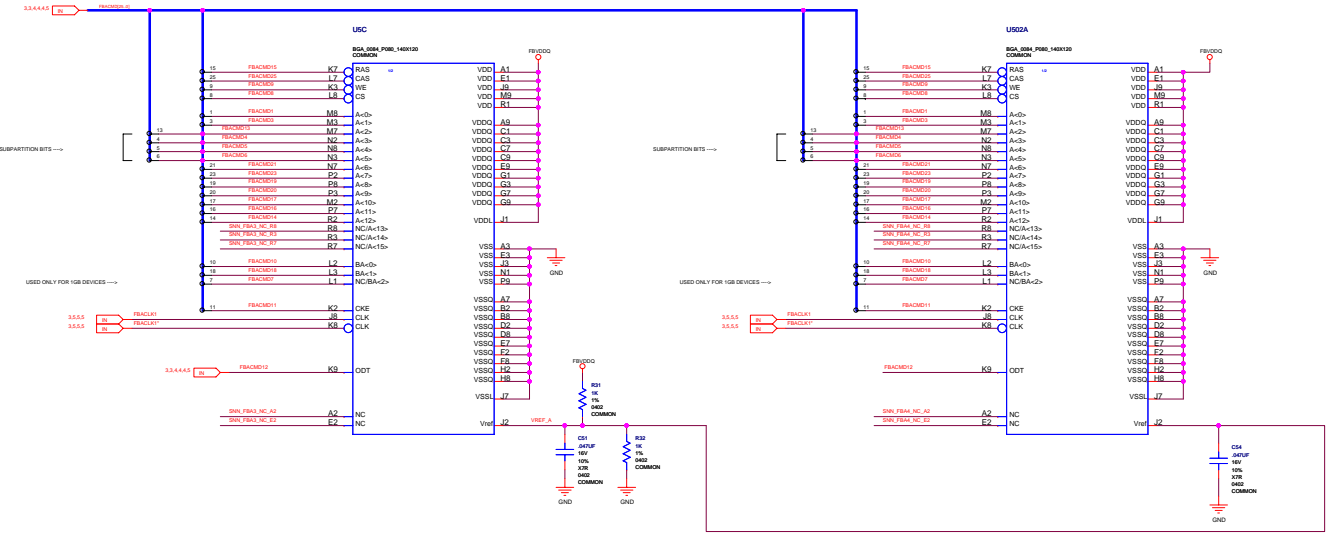
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	V056-10		
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Date: Wednesday, January 15, 2006		Sheet 3 of 14	

PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY



Memory Bit 32..63

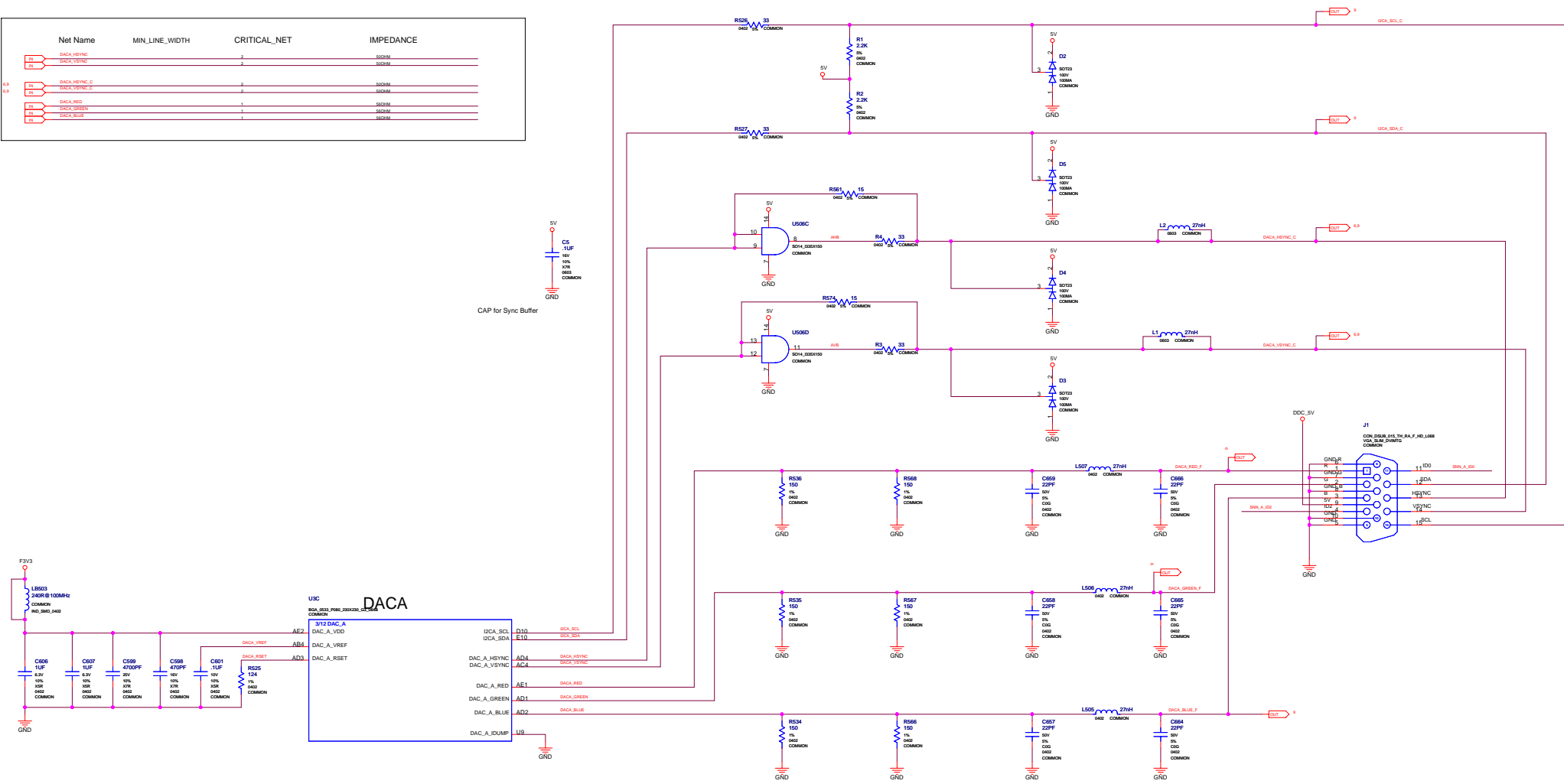
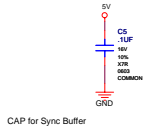
PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY



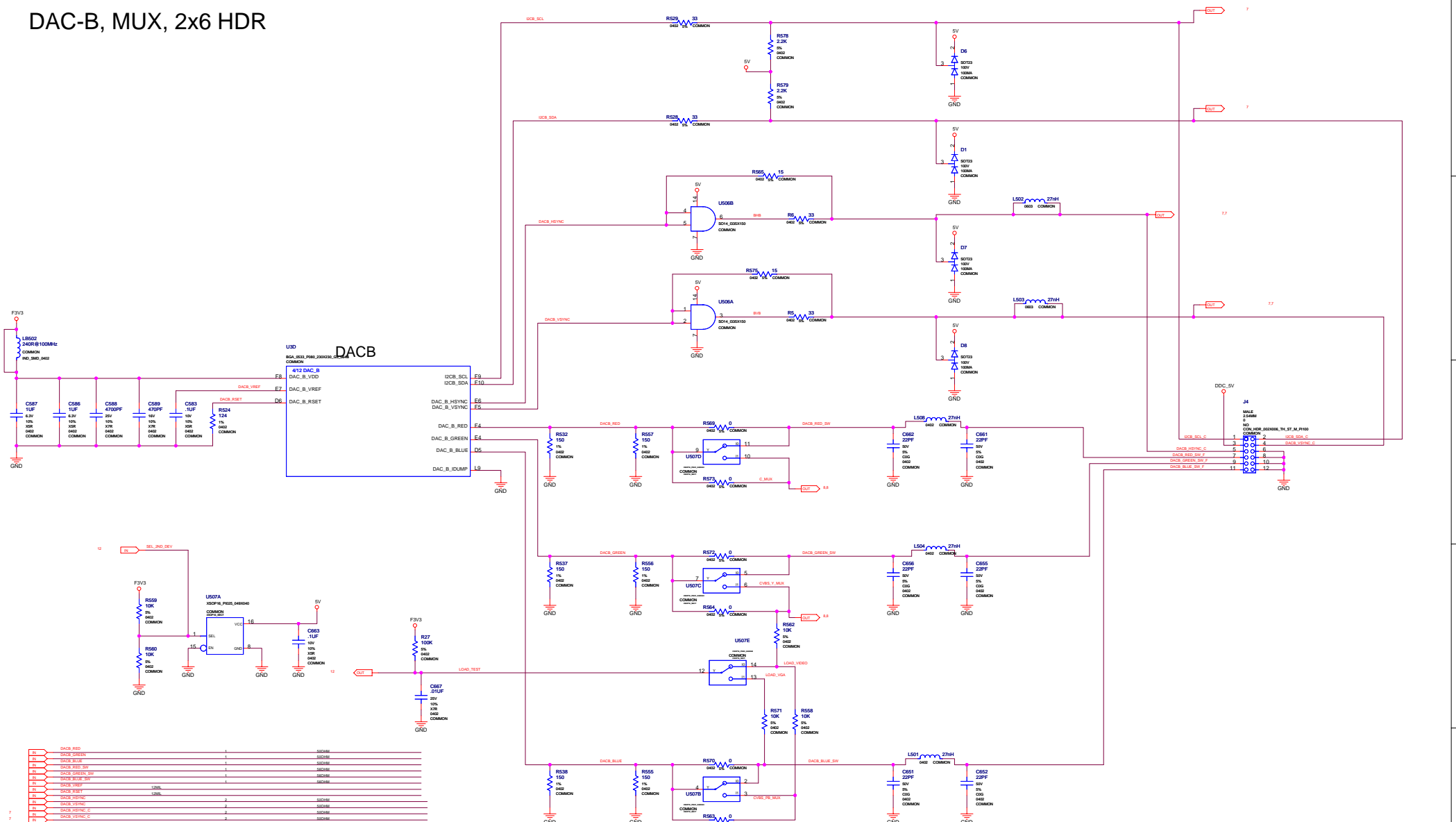
ASSEMBLY PAGE DETAIL BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO. STUFF ASSEMBLY NOTES AND BOM NOT FINAL Memory 1st bank 32..63

DAC-A, DB15 Connector

Net Name	MIN_LINE_WIDTH	CRITICAL_NET	IMPEDANCE
DACA_HSYNC	2	1	100OHM
DACA_VSYNC	2	1	100OHM
DACA_VSYNC_C	2	1	100OHM
DACA_VSYNC_F	2	1	100OHM
DACA_RED	1	1	100OHM
DACA_GREEN	1	1	100OHM
DACA_BLUE	1	1	100OHM



DAC-B, MUX, 2x6 HDR



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACB, VGA Output, TV Out



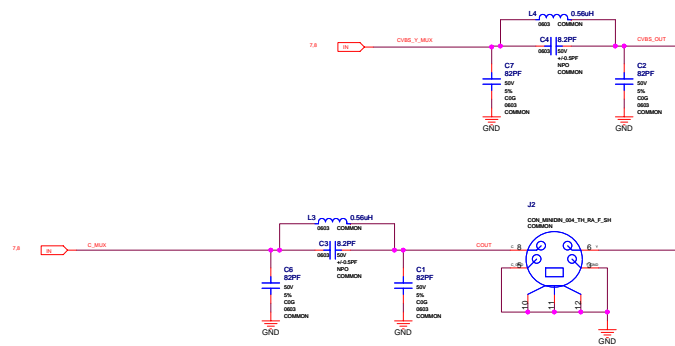
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Custom			00A
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VIDEO CONNECTORS: MiniDIN

	Name	LINE_WIDTH	CRITICAL	IMPEDANCE
7A	C_MUX	2		50OHM
7A	CWAVE_C_MUX	2		50OHM
	DDUT	2		50OHM
	CWAVE_DDUT	2		50OHM



Replace MiniDin with Compupack Part

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	VIDEO CONNECTORS: MiniDIN, 2x6 HDR

Micro-Star International Co., LTD.

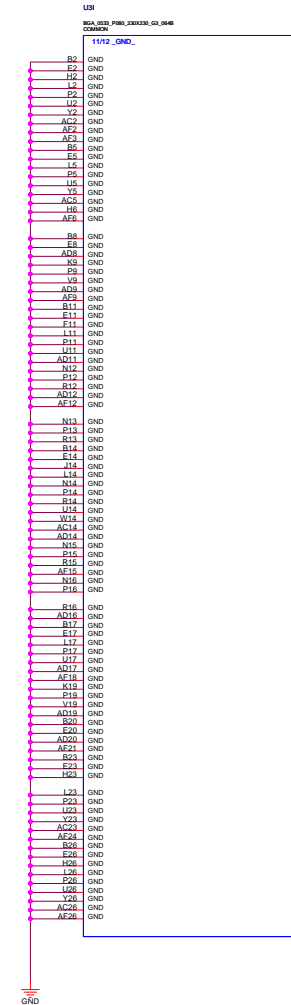
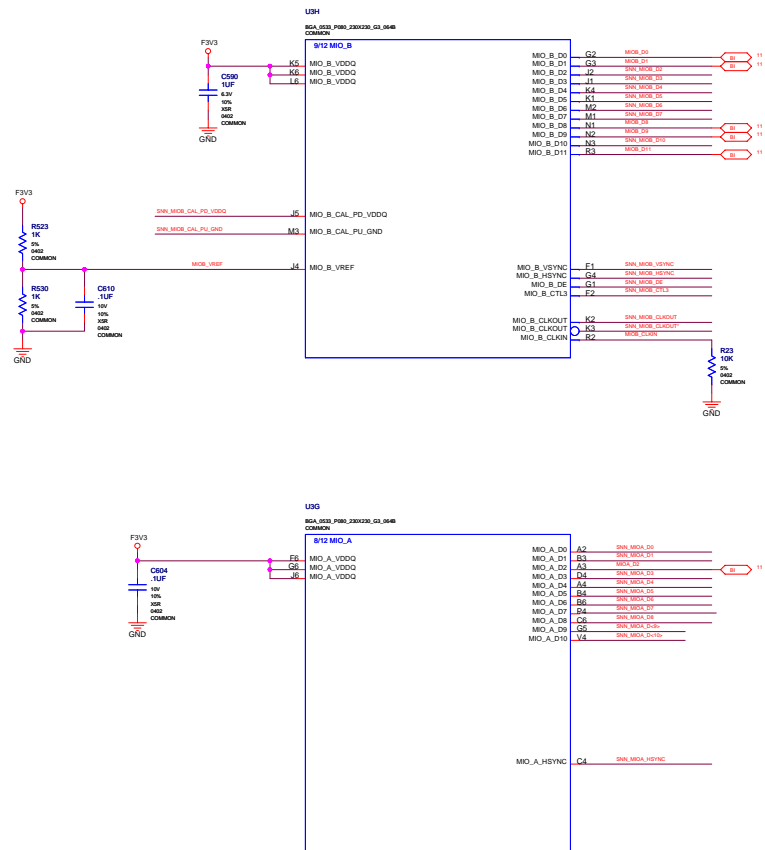


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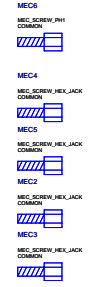
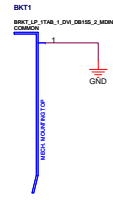
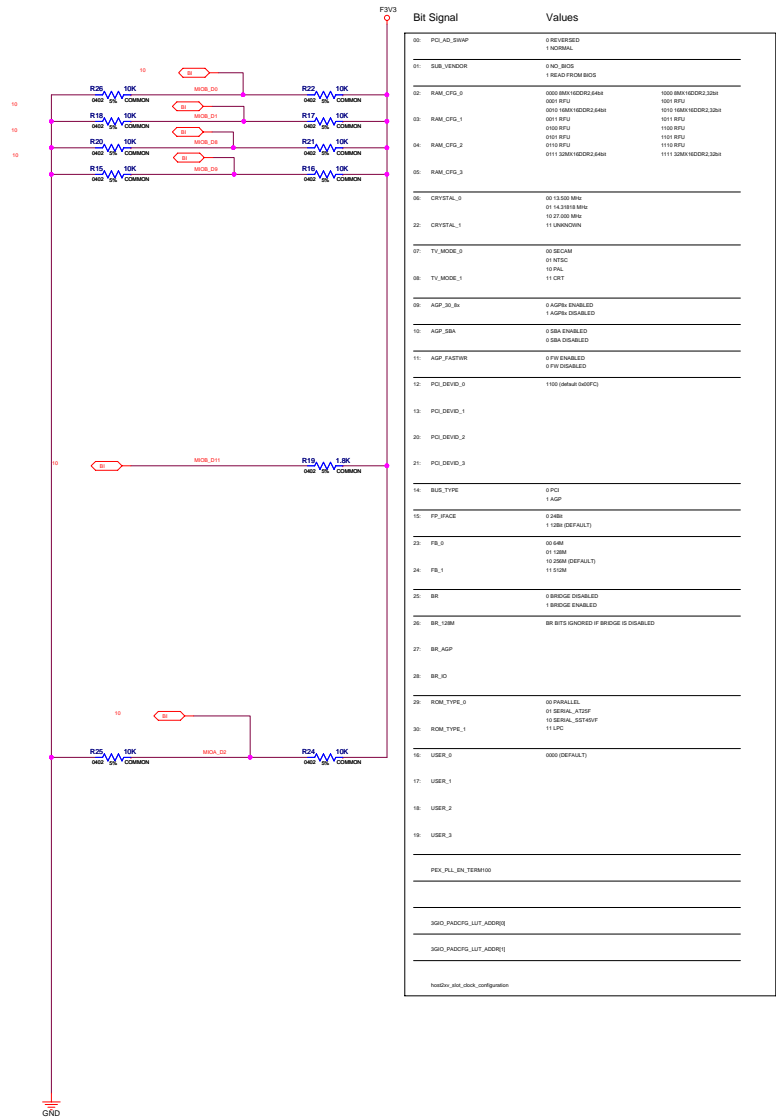
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MIOA, MIOB Interface

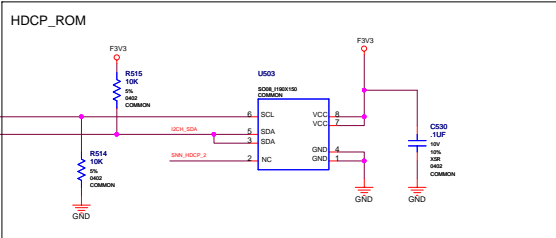
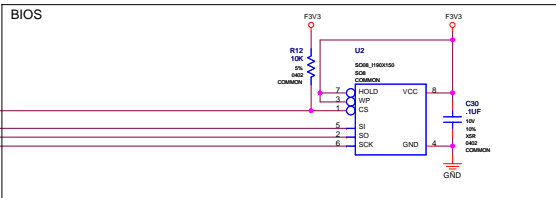
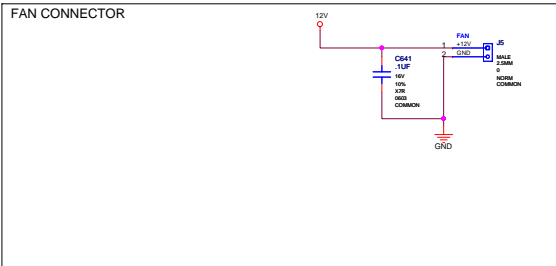
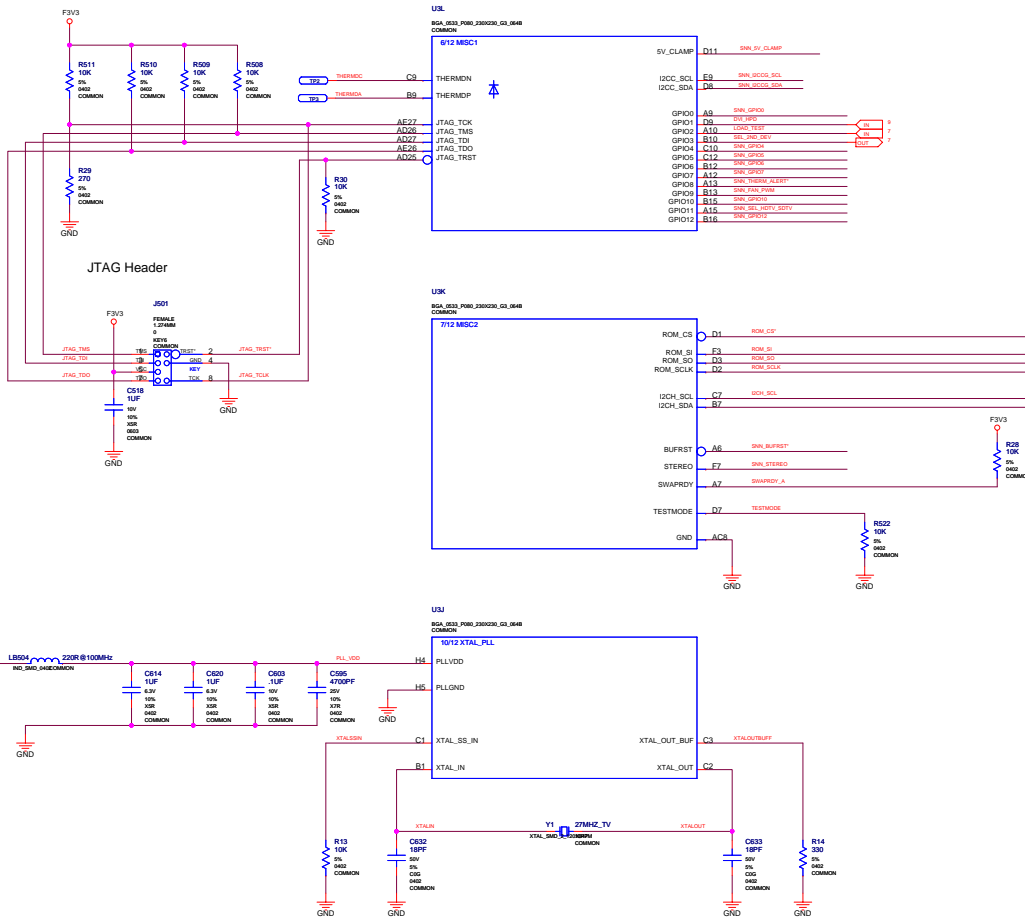


STRAPS, Mechanical Parts



XTAL, GPIO, BIOS, FAN, JTAG, THERMAL

Net Name	LINE_WIDTH	Voltage
PR1	P1L_VDD	3.3V
PR2	XTALP1	3.3V
PR3	XTALOUT	3.3V



PowerSupplyI: NVVDD, PLLVDD

Net Name	LINE_WIDTH	Current	Voltage
12V_P	30MIL	4	12V
12V_F	30MIL	3	12V
NVVDD	30MIL	16	1.2V
PEX1V2	30MIL	1.4	1.2V
GND	10MIL		0V

