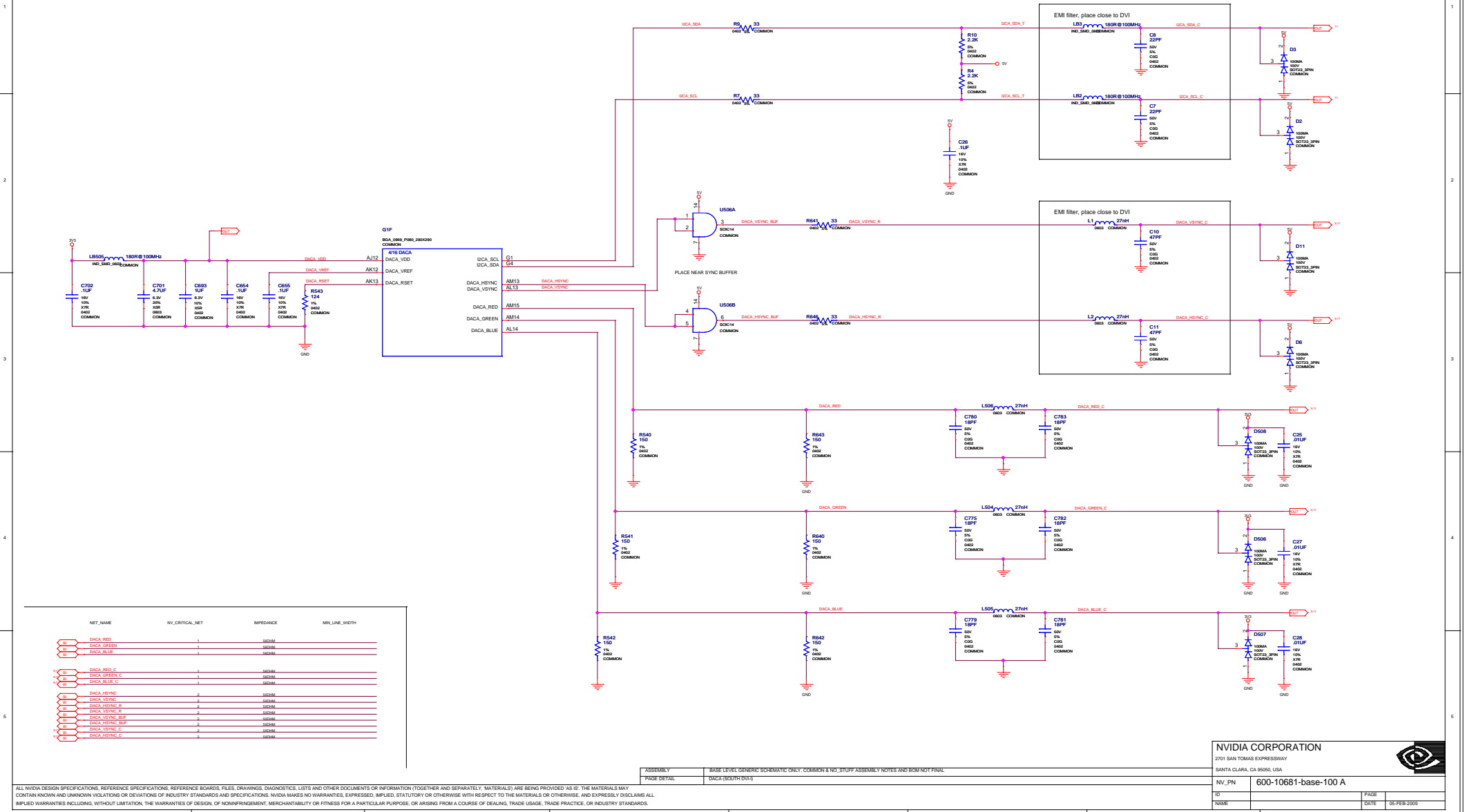
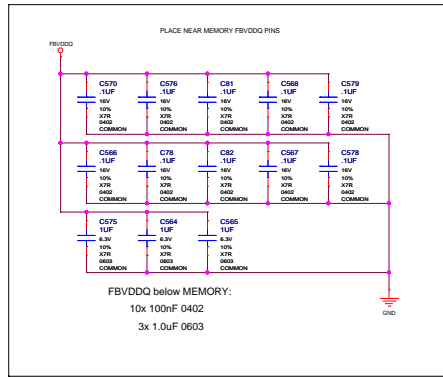


DACA (SOUTH DVI-I)

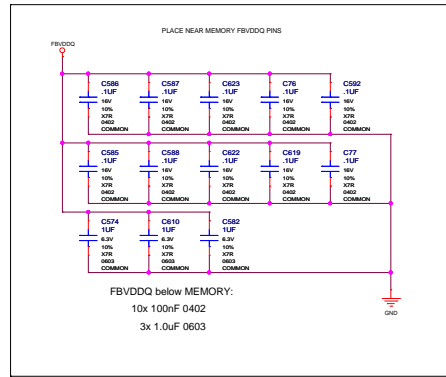


FBC DECOUPLING CAPS & NVVDD DECOUPLING CAPS

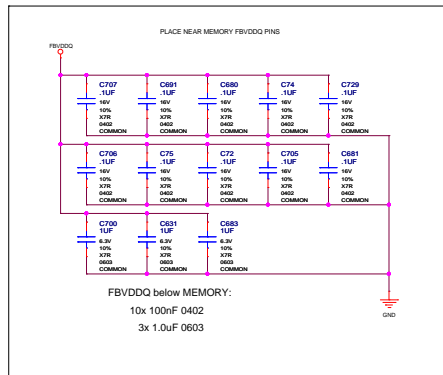
Decoupling for FBC 0..15



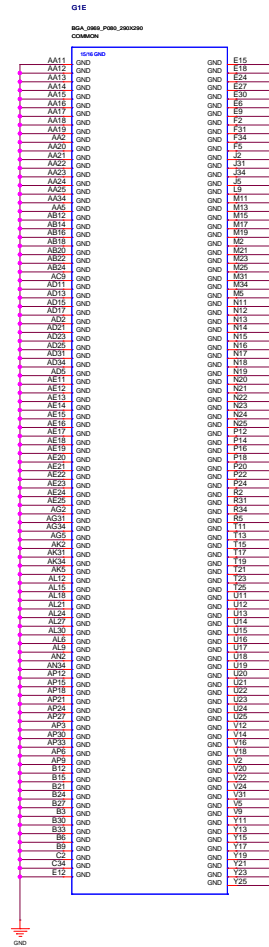
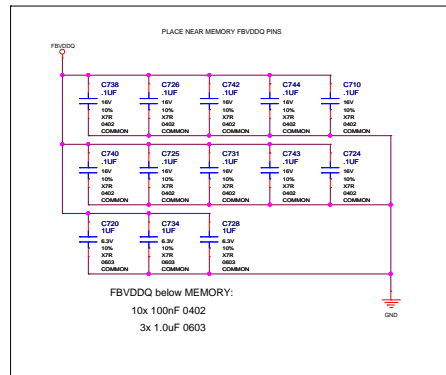
Decoupling for FBC 16..31



Decoupling for FBC 32..47



Decoupling for FBC 48..63



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PAGE DETAIL	FBC DECOUPLING CAPS

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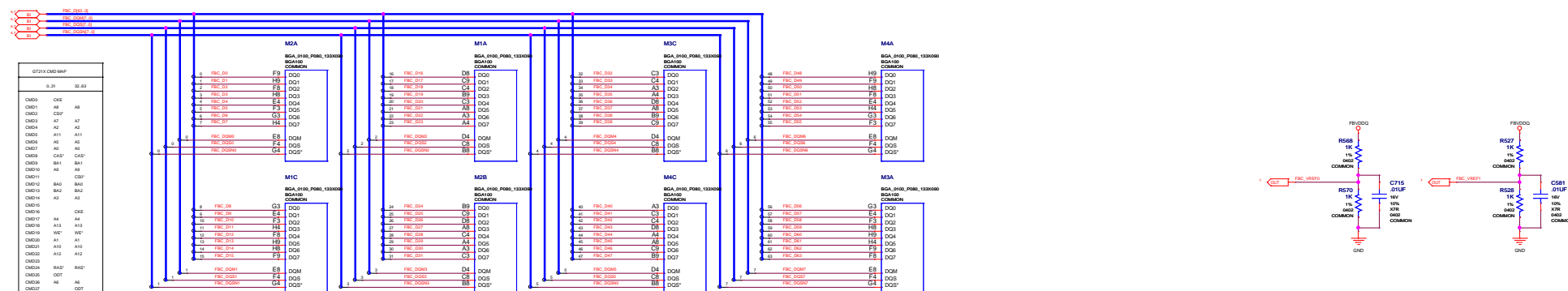
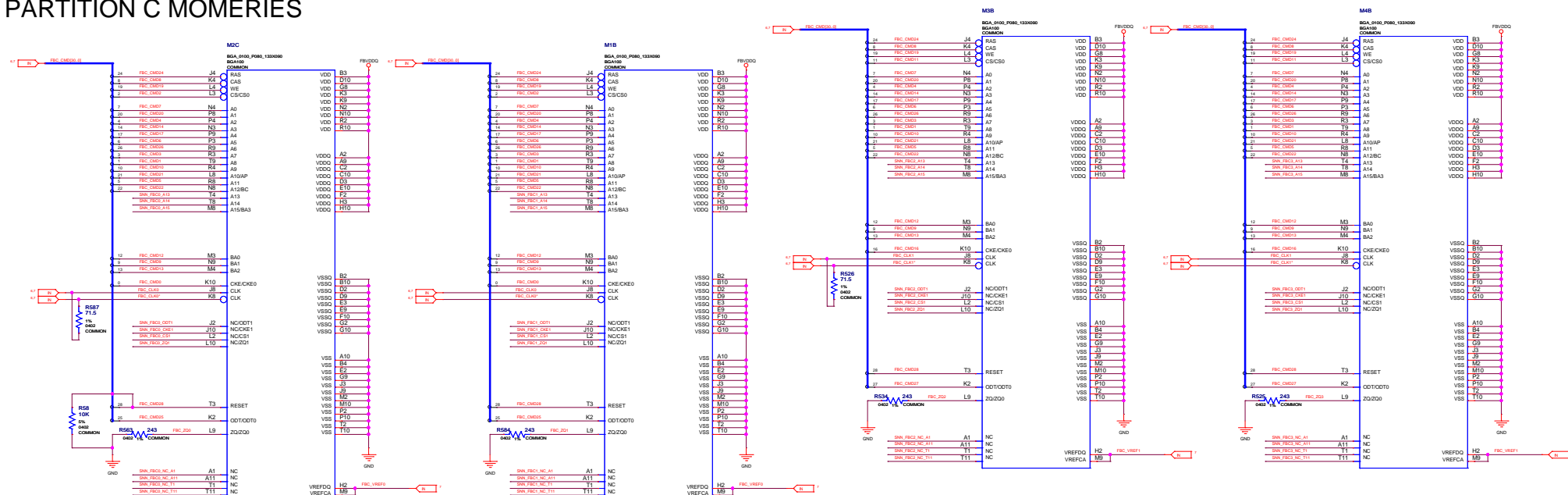
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ID		PAGE	
NAME		DATE	05-FEB-2009



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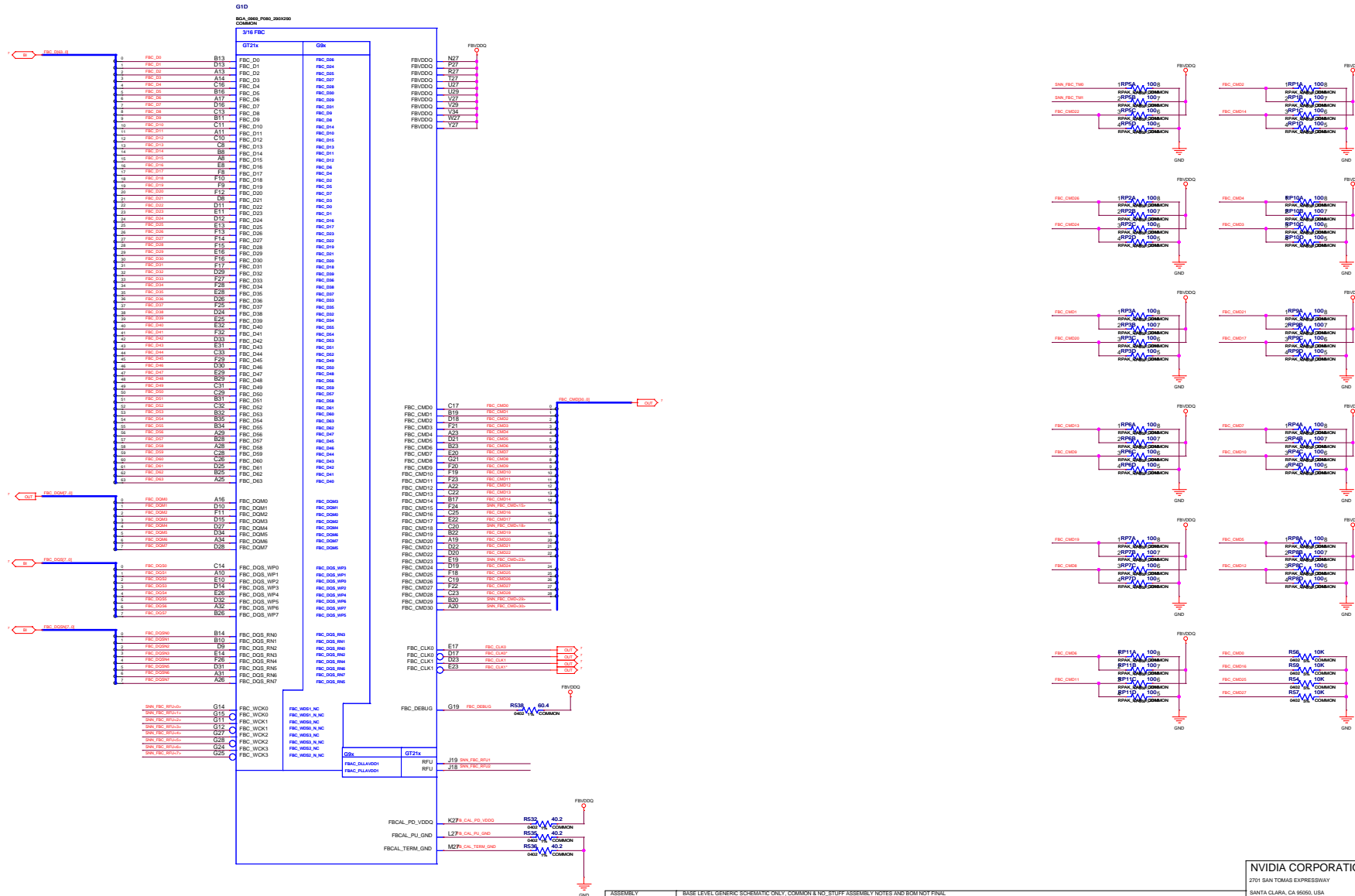
NV PN	600-10681-base-100 A
-------	----------------------

ID	
----	--

NAME	
------	--

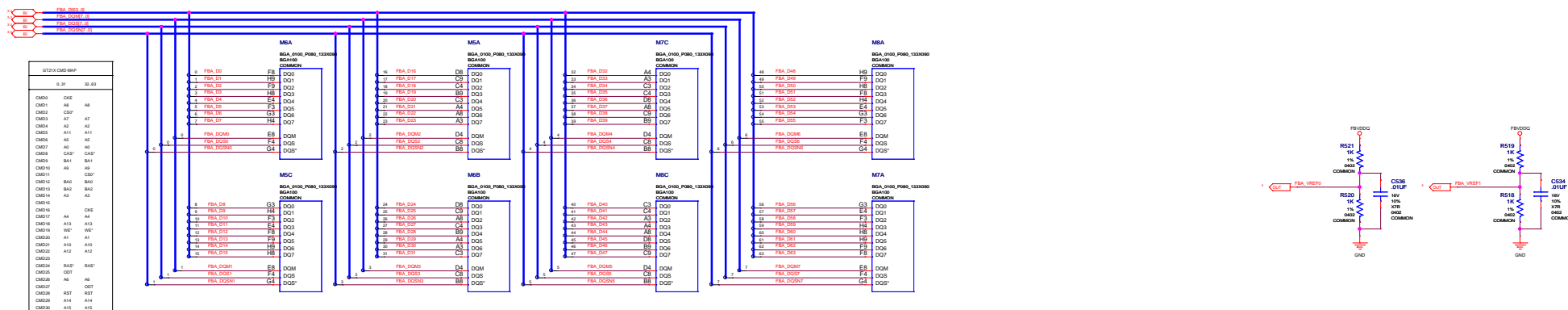
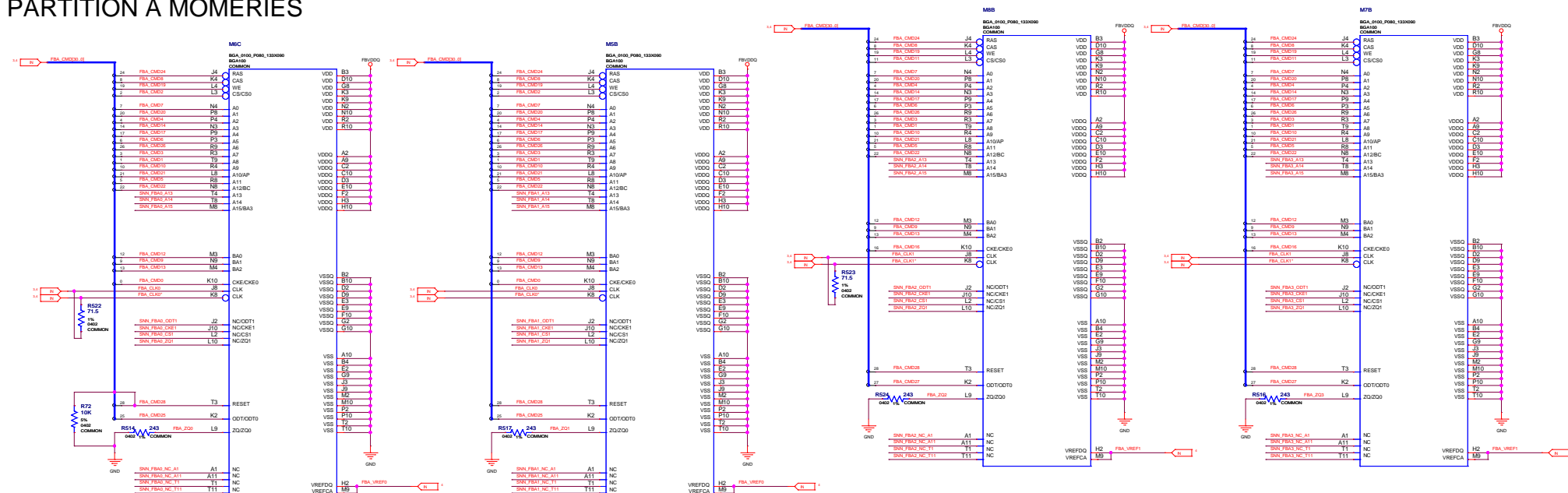
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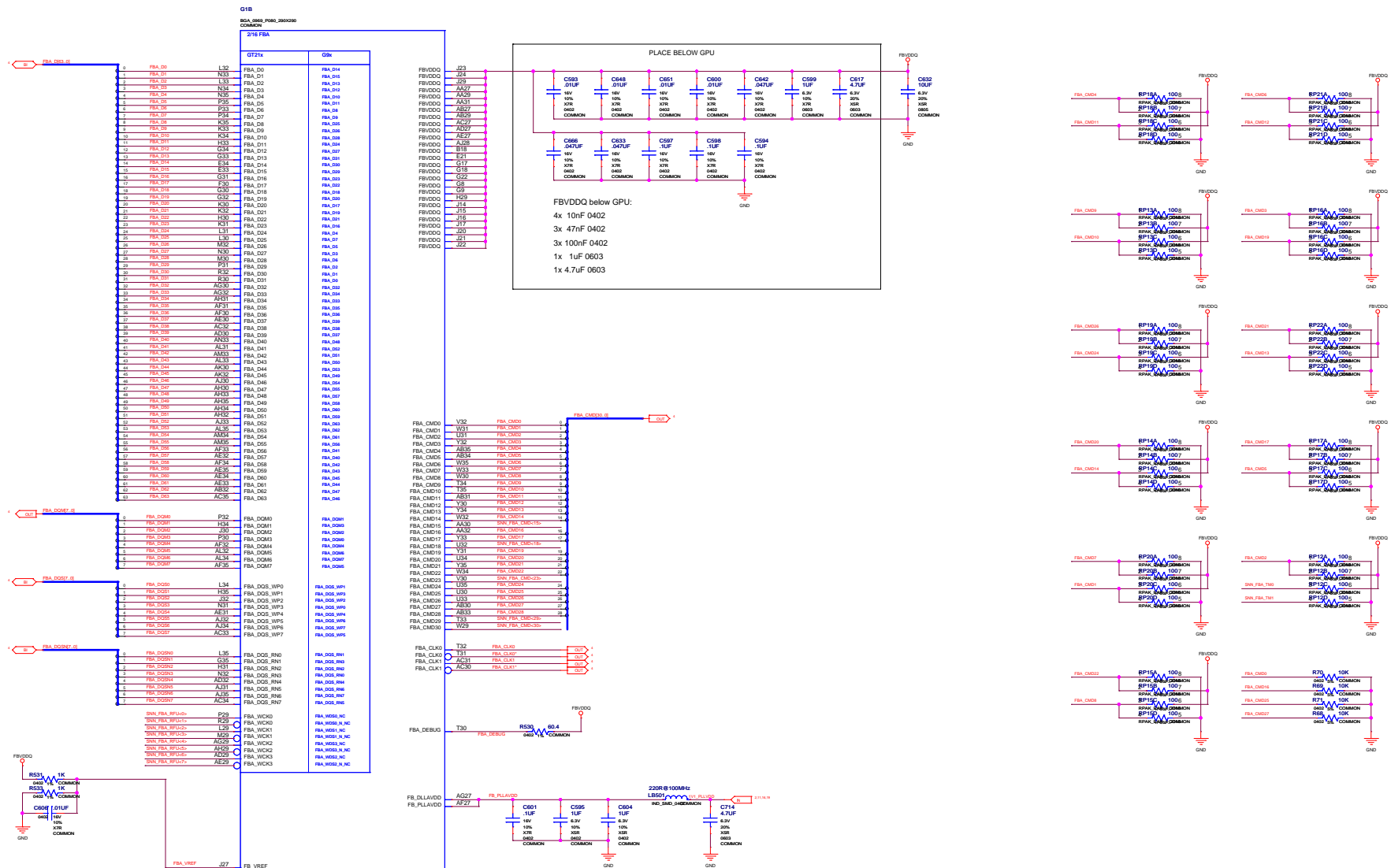
NET	DIFFPAIR	NV_IMPEDANCE	NV_CRITICAL_NET
FRA_DQS0	FRA_DQS0#	400Ω	1
FRA_DQS1	FRA_DQS1#	400Ω	1
FRA_DQS2	FRA_DQS2#	400Ω	1
FRA_DQS3	FRA_DQS3#	400Ω	1
FRA_DQS4	FRA_DQS4#	400Ω	1
FRA_DQS5	FRA_DQS5#	400Ω	1
FRA_DQS6	FRA_DQS6#	400Ω	1
FRA_DQS7	FRA_DQS7#	400Ω	1
FRA_DQS8	FRA_DQS8#	400Ω	1
FRA_DQS9	FRA_DQS9#	400Ω	1
FRA_DQS10	FRA_DQS10#	400Ω	1
FRA_DQS11	FRA_DQS11#	400Ω	1
FRA_DQS12	FRA_DQS12#	400Ω	1
FRA_DQS13	FRA_DQS13#	400Ω	1
FRA_DQS14	FRA_DQS14#	400Ω	1
FRA_DQS15	FRA_DQS15#	400Ω	1
FRA_DQS16	FRA_DQS16#	400Ω	1
FRA_DQS17	FRA_DQS17#	400Ω	1
FRA_DQS18	FRA_DQS18#	400Ω	1
FRA_DQS19	FRA_DQS19#	400Ω	1
FRA_DQS20	FRA_DQS20#	400Ω	1
FRA_DQS21	FRA_DQS21#	400Ω	1
FRA_DQS22	FRA_DQS22#	400Ω	1
FRA_DQS23	FRA_DQS23#	400Ω	1
FRA_DQS24	FRA_DQS24#	400Ω	1
FRA_DQS25	FRA_DQS25#	400Ω	1
FRA_DQS26	FRA_DQS26#	400Ω	1
FRA_DQS27	FRA_DQS27#	400Ω	1
FRA_DQS28	FRA_DQS28#	400Ω	1
FRA_DQS29	FRA_DQS29#	400Ω	1
FRA_DQS30	FRA_DQS30#	400Ω	1
FRA_DQS31	FRA_DQS31#	400Ω	1
FRA_DQS32	FRA_DQS32#	400Ω	1
FRA_DQS33	FRA_DQS33#	400Ω	1
FRA_DQS34	FRA_DQS34#	400Ω	1
FRA_DQS35	FRA_DQS35#	400Ω	1
FRA_DQS36	FRA_DQS36#	400Ω	1
FRA_DQS37	FRA_DQS37#	400Ω	1
FRA_DQS38	FRA_DQS38#	400Ω	1
FRA_DQS39	FRA_DQS39#	400Ω	1
FRA_DQS40	FRA_DQS40#	400Ω	1
FRA_DQS41	FRA_DQS41#	400Ω	1
FRA_DQS42	FRA_DQS42#	400Ω	1
FRA_DQS43	FRA_DQS43#	400Ω	1
FRA_DQS44	FRA_DQS44#	400Ω	1
FRA_DQS45	FRA_DQS45#	400Ω	1
FRA_DQS46	FRA_DQS46#	400Ω	1
FRA_DQS47	FRA_DQS47#	400Ω	1
FRA_DQS48	FRA_DQS48#	400Ω	1
FRA_DQS49	FRA_DQS49#	400Ω	1
FRA_DQS50	FRA_DQS50#	400Ω	1
FRA_DQS51	FRA_DQS51#	400Ω	1
FRA_DQS52	FRA_DQS52#	400Ω	1
FRA_DQS53	FRA_DQS53#	400Ω	1
FRA_DQS54	FRA_DQS54#	400Ω	1
FRA_DQS55	FRA_DQS55#	400Ω	1
FRA_DQS56	FRA_DQS56#	400Ω	1
FRA_DQS57	FRA_DQS57#	400Ω	1
FRA_DQS58	FRA_DQS58#	400Ω	1
FRA_DQS59	FRA_DQS59#	400Ω	1
FRA_DQS60	FRA_DQS60#	400Ω	1
FRA_DQS61	FRA_DQS61#	400Ω	1
FRA_DQS62	FRA_DQS62#	400Ω	1
FRA_DQS63	FRA_DQS63#	400Ω	1
FRA_DQS64	FRA_DQS64#	400Ω	1
FRA_DQS65	FRA_DQS65#	400Ω	1
FRA_DQS66	FRA_DQS66#	400Ω	1
FRA_DQS67	FRA_DQS67#	400Ω	1
FRA_DQS68	FRA_DQS68#	400Ω	1
FRA_DQS69	FRA_DQS69#	400Ω	1
FRA_DQS70	FRA_DQS70#	400Ω	1
FRA_DQS71	FRA_DQS71#	400Ω	1
FRA_DQS72	FRA_DQS72#	400Ω	1
FRA_DQS73	FRA_DQS73#	400Ω	1
FRA_DQS74	FRA_DQS74#	400Ω	1
FRA_DQS75	FRA_DQS75#	400Ω	1
FRA_DQS76	FRA_DQS76#	400Ω	1
FRA_DQS77	FRA_DQS77#	400Ω	1
FRA_DQS78	FRA_DQS78#	400Ω	1
FRA_DQS79	FRA_DQS79#	400Ω	1
FRA_DQS80	FRA_DQS80#	400Ω	1
FRA_DQS81	FRA_DQS81#	400Ω	1
FRA_DQS82	FRA_DQS82#	400Ω	1
FRA_DQS83	FRA_DQS83#	400Ω	1
FRA_DQS84	FRA_DQS84#	400Ω	1

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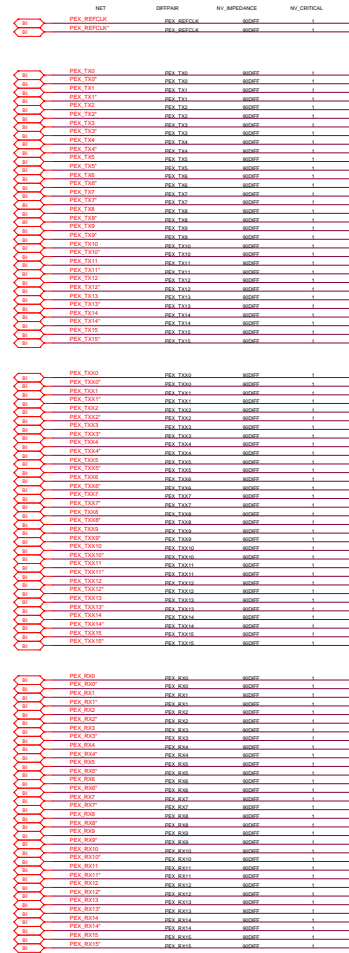
[illegible]

	A	B	C	D	E	F	G	H
1	<div> <div>SNV_FB62_NC_A1 4.3E</div> <div>SNV_FB62_NC_A11 4.3E</div> <div>SNV_FB62_NC_T1 4.3E</div> <div>SNV_FB62_NC_T11 4.3E</div> <div>SNV_FB62_O0T1 4.3E</div> <div>SNV_FB62_2J01 4.3E</div> <div>SNV_FB62_O0E1 4.3D</div> <div>SNV_FB63_C01 4.3D</div> <div>SNV_FB62_NC_A1 4.3E</div> <div>SNV_FB62_NC_A11 4.3E</div> <div>SNV_FB62_NC_T1 4.3E</div> <div>SNV_FB62_NC_T11 4.3E</div> <div>SNV_FB63_O0T1 4.3E</div> <div>SNV_FB63_2J01 4.3D</div> <div>SNV_FPA_C0M0C15 3.3C</div> <div>SNV_FPA_C0M0C23 3.4C</div> <div>SNV_FPA_C0M0C29 3.4C</div> <div>SNV_FPA_C0M0C20 3.4C</div> <div>SNV_FPA_RFU00 3.4E</div> <div>SNV_FPA_RFU01 3.4E</div> <div>SNV_FPA_RFU02 3.4E</div> <div>SNV_FPA_RFU03 3.4E</div> <div>SNV_FPA_RFU04 3.4E</div> <div>SNV_FPA_RFU05 3.4E</div> <div>SNV_FPA_RFU06 3.4E</div> <div>SNV_FPA_RFU07 3.4E</div> <div>SNV_FPA_RFU08 3.4E</div> <div>SNV_FPA_RFU09 3.4E</div> <div>SNV_FPA_RFU10 3.4E</div> <div>SNV_FPA_RFU11 3.4E</div> <div>SNV_FPA_RFU12 3.4E</div> <div>SNV_FPA_RFU13 3.4E</div> <div>SNV_FPA_RFU14 3.4E</div> <div>SNV_FPA_RFU15 3.4E</div> <div>SNV_FPA_RFU16 3.4E</div> <div>SNV_FPA_RFU17 3.4E</div> <div>SNV_FPA_RFU18 3.4E</div> <div>SNV_FPA_RFU19 3.4E</div> <div>SNV_FPA_RFU20 3.4E</div> <div>SNV_FPA_RFU21 3.4E</div> <div>SNV_FPA_RFU22 3.4E</div> <div>SNV_FPA_RFU23 3.4E</div> <div>SNV_FPA_RFU24 3.4E</div> <div>SNV_FPA_RFU25 3.4E</div> <div>SNV_FPA_RFU26 3.4E</div> <div>SNV_FPA_RFU27 3.4E</div> <div>SNV_FPA_RFU28 3.4E</div> <div>SNV_FPA_RFU29 3.4E</div> <div>SNV_FPA_RFU30 3.4E</div> <div>SNV_FPA_RFU31 3.4E</div> <div>SNV_FPA_RFU32 3.4E</div> <div>SNV_FPA_RFU33 3.4E</div> <div>SNV_FPA_RFU34 3.4E</div> <div>SNV_FPA_RFU35 3.4E</div> <div>SNV_FPA_RFU36 3.4E</div> <div>SNV_FPA_RFU37 3.4E</div> <div>SNV_FPA_RFU38 3.4E</div> <div>SNV_FPA_RFU39 3.4E</div> <div>SNV_FPA_RFU40 3.4E</div> <div>SNV_FPA_RFU41 3.4E</div> <div>SNV_FPA_RFU42 3.4E</div> <div>SNV_FPA_RFU43 3.4E</div> <div>SNV_FPA_RFU44 3.4E</div> <div>SNV_FPA_RFU45 3.4E</div> <div>SNV_FPA_RFU46 3.4E</div> <div>SNV_FPA_RFU47 3.4E</div> <div>SNV_FPA_RFU48 3.4E</div> <div>SNV_FPA_RFU49 3.4E</div> <div>SNV_FPA_RFU50 3.4E</div> <div>SNV_FPA_RFU51 3.4E</div> <div>SNV_FPA_RFU52 3.4E</div> <div>SNV_FPA_RFU53 3.4E</div> <div>SNV_FPA_RFU54 3.4E</div> <div>SNV_FPA_RFU55 3.4E</div> <div>SNV_FPA_RFU56 3.4E</div> <div>SNV_FPA_RFU57 3.4E</div> <div>SNV_FPA_RFU58 3.4E</div> <div>SNV_FPA_RFU59 3.4E</div> <div>SNV_FPA_RFU60 3.4E</div> <div>SNV_FPA_RFU61 3.4E</div> <div>SNV_FPA_RFU62 3.4E</div> <div>SNV_FPA_RFU63 3.4E</div> <div>SNV_FPA_RFU64 3.4E</div> <div>SNV_FPA_RFU65 3.4E</div> <div>SNV_FPA_RFU66 3.4E</div> <div>SNV_FPA_RFU67 3.4E</div> <div>SNV_FPA_RFU68 3.4E</div> <div>SNV_FPA_RFU69 3.4E</div> <div>SNV_FPA_RFU70 3.4E</div> <div>SNV_FPA_RFU71 3.4E</div> <div>SNV_FPA_RFU72 3.4E</div> <div>SNV_FPA_RFU73 3.4E</div> <div>SNV_FPA_RFU74 3.4E</div> <div>SNV_FPA_RFU75 3.4E</div> <div>SNV_FPA_RFU76 3.4E</div> <div>SNV_FPA_RFU77 3.4E</div> <div>SNV_FPA_RFU78 3.4E</div> <div>SNV_FPA_RFU79 3.4E</div> <div>SNV_FPA_RFU80 3.4E</div> <div>SNV_FPA_RFU81 3.4E</div> <div>SNV_FPA_RFU82 3.4E</div> <div>SNV_FPA_RFU83 3.4E</div> <div>SNV_FPA_RFU84 3.4E</div> <div>SNV_FPA_RFU85 3.4E</div> <div>SNV_FPA_RFU86 3.4E</div> <div>SNV_FPA_RFU87 3.4E</div> <div>SNV_FPA_RFU88 3.4E</div> <div>SNV_FPA_RFU89 3.4E</div> <div>SNV_FPA_RFU90 3.4E</div> <div>SNV_FPA_RFU91 3.4E</div> <div>SNV_FPA_RFU92 3.4E</div> <div>SNV_FPA_RFU93 3.4E</div> <div>SNV_FPA_RFU94 3.4E</div> <div>SNV_FPA_RFU95 3.4E</div> <div>SNV_FPA_RFU96 3.4E</div> <div>SNV_FPA_RFU97 3.4E</div> <div>SNV_FPA_RFU98 3.4E</div> <div>SNV_FPA_RFU99 3.4E</div> <div>SNV_FPA_RFU100 3.4E</div> <div>SNV_FPA_RFU101 3.4E</div> <div>SNV_FPA_RFU102 3.4E</div> <div>SNV_FPA_RFU103 3.4E</div> <div>SNV_FPA_RFU104 3.4E</div> <div>SNV_FPA_RFU105 3.4E</div> <div>SNV_FPA_RFU106 3.4E</div> <div>SNV_FPA_RFU107 3.4E</div> <div>SNV_FPA_RFU108 3.4E</div> <div>SNV_FPA_RFU109 3.4E</div> <div>SNV_FPA_RFU110 3.4E</div> <div>SNV_FPA_RFU111 3.4E</div> <div>SNV_FPA_RFU112 3.4E</div> <div>SNV_FPA_RFU113 3.4E</div> <div>SNV_FPA_RFU114 3.4E</div> <div>SNV_FPA_RFU115 3.4E</div> <div>SNV_FPA_RFU116 3.4E</div> <div>SNV_FPA_RFU117 3.4E</div> <div>SNV_FPA_RFU118 3.4E</div> <div>SNV_FPA_RFU119 3.4E</div> <div>SNV_FPA_RFU120 3.4E</div> <div>SNV_FPA_RFU121 3.4E</div> <div>SNV_FPA_RFU122 3.4E</div> <div>SNV_FPA_RFU123 3.4E</div> <div>SNV_FPA_RFU124 3.4E</div> <div>SNV_FPA_RFU125 3.4E</div> <div>SNV_FPA_RFU126 3.4E</div> <div>SNV_FPA_RFU127 3.4E</div> <div>SNV_FPA_RFU128 3.4E</div> <div>SNV_FPA_RFU129 3.4E</div> <div>SNV_FPA_RFU130 3.4E</div> <div>SNV_FPA_RFU131 3.4E</div> <div>SNV_FPA_RFU132 3.4E</div> <div>SNV_FPA_RFU133 3.4E</div> <div>SNV_FPA_RFU134 3.4E</div> <div>SNV_FPA_RFU135 3.4E</div> <div>SNV_FPA_RFU136 3.4E</div> <div>SNV_FPA_RFU137 3.4E</div> <div>SNV_FPA_RFU138 3.4E</div> <div>SNV_FPA_RFU139 3.4E</div> <div>SNV_FPA_RFU140 3.4E</div> <div>SNV_FPA_RFU141 3.4E</div> <div>SNV_FPA_RFU142 3.4E</div> <div>SNV_FPA_RFU143 3.4E</div> <div>SNV_FPA_RFU144 3.4E</div> <div>SNV_FPA_RFU145 3.4E</div> <div>SNV_FPA_RFU146 3.4E</div> <div>SNV_FPA_RFU147 3.4E</div> <div>SNV_FPA_RFU148 3.4E</div> <div>SNV_FPA_RFU149 3.4E</div> <div>SNV_FPA_RFU150 3.4E</div> <div>SNV_FPA_RFU151 3.4E</div> <div>SNV_FPA_RFU152 3.4E</div> <div>SNV_FPA_RFU153 3.4E</div> <div>SNV_FPA_RFU154 3.4E</div> <div>SNV_FPA_RFU155 3.4E</div> <div>SNV_FPA_RFU156 3.4E</div> <div>SNV_FPA_RFU157 3.4E</div> <div>SNV_FPA_RFU158 3.4E</div> <div>SNV_FPA_RFU159 3.4E</div> <div>SNV_FPA_RFU160 3.4E</div> <div>SNV_FPA_RFU161 3.4E</div> <div>SNV_FPA_RFU162 3.4E</div> <div>SNV_FPA_RFU163 3.4E</div> <div>SNV_FPA_RFU164 3.4E</div> <div>SNV_FPA_RFU165 3.4E</div> <div>SNV_FPA_RFU166 3.4E</div> <div>SNV_FPA_RFU167 3.4E</div> <div>SNV_FPA_RFU168 3.4E</div> <div>SNV_FPA_RFU169 3.4E</div> <div>SNV_FPA_RFU170 3.4E</div> <div>SNV_FPA_RFU171 3.4E</div> <div>SNV_FPA_RFU172 3.4E</div> <div>SNV_FPA_RFU173 3.4E</div> <div>SNV_FPA_RFU174 3.4E</div> <div>SNV_FPA_RFU175 3.4E</div> <div>SNV_FPA_RFU176 3.4E</div> <div>SNV_FPA_RFU177 3.4E</div> <div>SNV_FPA_RFU178 3.4E</div> <div>SNV_FPA_RFU179 3.4E</div> <div>SNV_FPA_RFU180 3.4E</div> <div>SNV_FPA_RFU181 3.4E</div> <div>SNV_FPA_RFU182 3.4E</div> <div>SNV_FPA_RFU183 3.4E</div> <div>SNV_FPA_RFU184 3.4E</div> <div>SNV_FPA_RFU185 3.4E</div> <div>SNV_FPA_RFU186 3.4E</div> <div>SNV_FPA_RFU187 3.4E</div> <div>SNV_FPA_RFU188 3.4E</div> <div>SNV_FPA_RFU189 3.4E</div> <div>SNV_FPA_RFU190 3.4E</div> <div>SNV_FPA_RFU191 3.4E</div> <div>SNV_FPA_RFU192 3.4E</div> <div>SNV_FPA_RFU193 3.4E</div> <div>SNV_FPA_RFU194 3.4E</div> <div>SNV_FPA_RFU195 3.4E</div> <div>SNV_FPA_RFU196 3.4E</div> <div>SNV_FPA_RFU197 3.4E</div> <div>SNV_FPA_RFU198 3.4E</div> <div>SNV_FPA_RFU199 3.4E</div> <div>SNV_FPA_RFU200 3.4E</div> <div>SNV_FPA_RFU201 3.4E</div> <div>SNV_FPA_RFU202 3.4E</div> <div>SNV_FPA_RFU203 3.4E</div> <div>SNV_FPA_RFU204 3.4E</div> <div>SNV_FPA_RFU205 3.4E</div> <div>SNV_FPA_RFU206 3.4E</div> <div>SNV_FPA_RFU207 3.4E</div> <div>SNV_FPA_RFU208 3.4E</div> <div>SNV_FPA_RFU209 3.4E</div> <div>SNV_FPA_RFU210 3.4E</div> <div>SNV_FPA_RFU211 3.4E</div> <div>SNV_FPA_RFU212 3.4E</div> <div>SNV_FPA_RFU213 3.4E</div> <div>SNV_FPA_RFU214 3.4E</div> <div>SNV_FPA_RFU215 3.4E</div> <div>SNV_FPA_RFU216 3.4E</div> <div>SNV_FPA_RFU217 3.4E</div> <div>SNV_FPA_RFU218 3.4E</div> <div>SNV_FPA_RFU219 3.4E</div> <div>SNV_FPA_RFU220 3.4E</div> <div>SNV_FPA_RFU221 3.4E</div> <div>SNV_FPA_RFU222 3.4E</div> <div>SNV_FPA_RFU223 3.4E</div> <div>SNV_FPA_RFU224 3.4E</div> <div>SNV_FPA_RFU225 3.4E</div> <div>SNV_FPA_RFU226 3.4E</div> <div>SNV_FPA_RFU227 3.4E</div> <div>SNV_FPA_RFU228 3.4E</div> <div>SNV_FPA_RFU229 3.4E</div> <div>SNV_FPA_RFU230 3.4E</div> <div>SNV_FPA_RFU231 3.4E</div> <div>SNV_FPA_RFU232 3.4E</div> <div>SNV_FPA_RFU233 3.4E</div> <div>SNV_FPA_RFU234 3.4E</div> <div>SNV_FPA_RFU235 3.4E</div> <div>SNV_FPA_RFU236 3.4E</div> <div>SNV_FPA_RFU237 3.4E</div> <div>SNV_FPA_RFU238 3.4E</div> <div>SNV_FPA_RFU239 3.4E</div> <div>SNV_FPA_RFU240 3.4E</div> <div>SNV_FPA_RFU241 3.4E</div> <div>SNV_FPA_RFU242 3.4E</div> <div>SNV_FPA_RFU243 3.4E</div> <div>SNV_FPA_RFU244 3.4E</div> <div>SNV_FPA_RFU245 3.4E</div> <div>SNV_FPA_RFU246 3.4E</div> <div>SNV_FPA_RFU247 3.4E</div> <div>SNV_FPA_RFU248 3.4E</div> <div>SNV_FPA_RFU249 3.4E</div> <div>SNV_FPA_RFU250 3.4E</div> <div>SNV_FPA_RFU251 3.4E</div> <div>SNV_FPA_RFU252 3.4E</div> <div>SNV_FPA_RFU253 3.4E</div> <div>SNV_FPA_RFU254 3.4E</div> <div>SNV_FPA_RFU255 3.4E</div> <div>SNV_FPA_RFU256 3.4E</div> <div>SNV_FPA_RFU257 3.4E</div> <div>SNV_FPA_RFU258 3.4E</div> <div>SNV_FPA_RFU259 3.4E</div> <div>SNV_FPA_RFU260 3.4E</div> <div>SNV_FPA_RFU261 3.4E</div> <div>SNV_FPA_RFU262 3.4E</div> <div>SNV_FPA_RFU263 3.4E</div> <div>SNV_FPA_RFU264 3.4E</div> <div>SNV_FPA_RFU265 3.4E</div> <div>SNV_FPA_RFU266 3.4E</div> <div>SNV_FPA_RFU267 3.4E</div> <div>SNV_FPA_RFU268 3.4E</div> <div>SNV_FPA_RFU269 3.4E</div> <div>SNV_FPA_RFU270 3.4E</div> <div>SNV_FPA_RFU271 3.4E</div> <div>SNV_FPA_RFU272 3.4E</div> <div>SNV_FPA_RFU273 3.4E</div> <div>SNV_FPA_RFU274 3.4E</div> <div>SNV_FPA_RFU275 3.4E</div> <div>SNV_FPA_RFU276 3.4E</div> <div>SNV_FPA_RFU277 3.4E</div> <div>SNV_FPA_RFU278 3.4E</div> <div>SNV_FPA_RFU279 3.4E</div> <div>SNV_FPA_RFU280 3.4E</div> <div>SNV_FPA_RFU281 3.4E</div> <div>SNV_FPA_RFU282 3.4E</div> <div>SNV_FPA_RFU283 3.4E</div> <div>SNV_FPA_RFU284 3.4E</div> <div>SNV_FPA_RFU285 3.4E</div> <div>SNV_FPA_RFU286 3.4E</div> <div>SNV_FPA_RFU287 3.4E</div> <div>SNV_FPA_RFU288 3.4E</div> <div>SNV_FPA_RFU289 3.4E</div> <div>SNV_FPA_RFU290 3.4E</div> <div>SNV_FPA_RFU291 3.4E</div> <div>SNV_FPA_RFU292 3.4E</div> <div>SNV_FPA_RFU293 3.4E</div> <div>SNV_FPA_RFU294 3.4E</div> <div>SNV_FPA_RFU295 3.4E</div> <div>SNV_FPA_RFU296 3.4E</div> <div>SNV_FPA_RFU297 3.4E</div> <div>SNV_FPA_RFU298 3.4E</div> <div>SNV_FPA_RFU299 3.4E</div> <div>SNV_FPA_RFU300 3.4E</div> <div>SNV_FPA_RFU301 3.4E</div> <div>SNV_FPA_RFU302 3.4E</div> <div>SNV_FPA_RFU303 3.4E</div> <div>SNV_FPA_RFU304 3.4E</div> <div>SNV_FPA_RFU305 3.4E</div> <div>SNV_FPA_RFU306 3.4E</div> <div>SNV_FPA_RFU307 3.4E</div> <div>SNV_FPA_RFU308 3.4E</div> <div>SNV_FPA_RFU309 3.4E</div> <div>SNV_FPA_RFU310 3.4E</div> <div>SNV_FPA_RFU311 3.4E</div> <div>SNV_FPA_RFU312 3.4E</div> <div>SNV_FPA_RFU313 3.4E</div> <div>SNV_FPA_RFU314 3.4E</div> <div>SNV_FPA_RFU315 3.4E</div> <div>SNV_FPA_RFU316 3.4E</div> <div>SNV_FPA_RFU317 3.4E</div> <div>SNV_FPA_RFU318 3.4E</div> <div>SNV_FPA_RFU319 3.4E</div> <div>SNV_FPA_RFU320 3.4E</div> <div>SNV_FPA_RFU321 3.4E</div> <div>SNV_FPA_RFU322 3.4E</div> <div>SNV_FPA_RFU323 3.4E</div> <div>SNV_FPA_RFU324 3.4E</div> <div>SNV_FPA_RFU325 3.4E</div> <div>SNV_FPA_RFU326 3.4E</div> <div>SNV_FPA_RFU327 3.4E</div> <div>SNV_FPA_RFU328 3.4E</div> <div>SNV_FPA_RFU329 3.4E</div> <div>SNV_FPA_RFU330 3.4E</div> <div>SNV_FPA_RFU331 3.4E</div> <div>SNV_FPA_RFU332 3.4E</div> <div>SNV_FPA_RFU333 3.4E</div> <div>SNV_FPA_RFU334 3.4E</div> <div>SNV_FPA_RFU335 3.4E</div> <div>SNV_FPA_RFU336 3.4E</div> <div>SNV_FPA_RFU337 3.4E</div> <div>SNV_FPA_RFU338 3.4E</div> <div>SNV_FPA_RFU339 3.4E</div> <div>SNV_FPA_RFU340 3.4E</div> <div>SNV_FPA_RFU341 3.4E</div> <div>SNV_FPA_RFU342 3.4E</div> <div>SNV_FPA_RFU343 3.4E</div> <div>SNV_FPA_RFU344 3.4E</div> <div>SNV_FPA_RFU345 3.4E</div> <div>SNV_FPA_RFU346 3.4E</div> <div>SNV_FPA_RFU347 3.4E</div> <div>SNV_FPA_RFU348 3.4E</div> <div>SNV_FPA_RFU349 3.4E</div> <div>SNV_FPA_RFU350 3.4E</div> <div>SNV_FPA_RFU351 3.4E</div> <div>SNV_FPA_RFU352 3.4E</div> <div>SNV_FPA_RFU353 3.4E</div> <div>SNV_FPA_RFU354 3.4E</div> <div>SNV_FPA_RFU355 3.4E</div> <div>SNV_FPA_RFU356 3.4E</div> <div>SNV_FPA_RFU357 3.4E</div> <div>SNV_FPA_RFU358 3.4E</div> <div>SNV_FPA_RFU359 3.4E</div> <div>SNV_FPA_RFU360 3.4E</div> <div>SNV_FPA_RFU361 3.4E</div> <div>SNV_FPA_RFU362 3.4E</div> <div>SNV_FPA_RFU363 3.4E</div> <div>SNV_FPA_RFU364 3.4E</div> <div>SNV_FPA_RFU365 3.4E</div> <div>SNV_FPA_RFU366 3.4E</div> <div>SNV_FPA_RFU367 3.4E</div> <div>SNV_FPA_RFU368 3.4E</div> <div>SNV_FPA_RFU369 3.4E</div> <div>SNV_FPA_RFU370 3.4E</div> <div>SNV_FPA_RFU371 3.4E</div> <div>SNV_FPA_RFU372 3.4E</div> <div>SNV_FPA_RFU373 3.4E</div> <div>SNV_FPA_RFU374 3.4E</div> <div>SNV_FPA_RFU375 3.4E</div> <div>SNV_FPA_RFU376 3.4E</div> <div>SNV_FPA_RFU377 3.4E</div> <div>SNV_FPA_RFU378 3.4E</div> <div>SNV_FPA_RFU379 3.4E</div> <div>SNV_FPA_RFU380 3.4E</div> <div>SNV_FPA_RFU381 3.4E</div> <div>SNV_FPA_RFU382 3.4E</div> <div>SNV_FPA_RFU383 3.4E</div> <div>SNV_FPA_RFU384 3.4E</div> <div>SNV_FPA_RFU385 3.4E</div> <div>SNV_FPA_RFU386 3.4E</div> <div>SNV_FPA_RFU387 3.4E</div> <div>SNV_FPA_RFU388 3.4E</div> <div>SNV_FPA_RFU389 3.4E</div> <div>SNV_FPA_RFU390 3.4E</div> <div>SNV_FPA_RFU391 3.4E</div> <div>SNV_FPA_RFU392 3.4E</div> <div>SNV_FPA_RFU393 3.4E</div> <div>SNV_FPA_RFU394 3.4E</div> <div>SNV_FPA_RFU395 3.4E</div> <div>SNV_FPA_RFU396 3.4E</div> <div>SNV_FPA_RFU397 3.4E</div> <div>SNV_FPA_RFU398 3.4E</div> <div>SNV_FPA_RFU399 3.4E</div> <div>SNV_FPA_RFU400 3.4E</div> <div>SNV_FPA_RFU401 3.4E</div> <div>SNV_FPA_RFU402 3.4E</div> <div>SNV_FPA_RFU403 3.4E</div> <div>SNV_FPA_RFU404 3.4E</div> <div>SNV_FPA_RFU405 3.4E</div> <div>SNV_FPA_RFU406 3.4E</div> <div>SNV_FPA_RFU407 3.4E</div> <div>SNV_FPA_RFU408 3.4E</div> <div>SNV_FPA_RFU409 3.4E</div> <div>SNV_FPA_RFU410 3.4E</div> <div>SNV_FPA_RFU411 3.4E</div> <div>SNV_FPA_RFU412 3.4E</div> <div>SNV_FPA_RFU413 3.4E</div> <div>SNV_FPA_RFU414 3.4E</div> <div>SNV_FPA_RFU415 3.4E</div> <div>SNV_FPA_RFU416 3.4E</div> <div>SNV_FPA_RFU417 3.4E</div> <div>SNV_FPA_RFU418 3.4E</div> <div>SNV_FPA_RFU419 3.4E</div> <div>SNV_FPA_RFU420 3.4E</div> <div>SNV_FPA_RFU421 3.4E</div> <div>SNV_FPA_RFU422 3.4E</div> <div>SNV_FPA_RFU423 3.4E</div> <div>SNV_FPA_RFU424 3.4E</div> <div>SNV_FPA_RFU425 3.4E</div> <div>SNV_FPA_RFU426 3.4E</div> <div>SNV_FPA_RFU427 3.4E</div> <div>SNV_FPA_RFU428 3.4E</div> <div>SNV_FPA_RFU429 3.4E</div> <div>SNV_FPA_RFU430 3.4E</div> <div>SNV_FPA_RFU431 3.4E</div> <div>SNV_FPA_RFU432 3.4E</div> <div>SNV_FPA_RFU433 3.4E</div> <div>SNV_FPA_RFU434 3.4E</div> <div>SNV_FPA_RFU435 3.4E</div> <div>SNV_FPA_RFU436 3.4E</div> <div>SNV_FPA_RFU437 3.4E</div> <div>SNV_FPA_RFU438 3.4E</div> <div>SNV_FPA_RFU439 3.4E</div> <div>SNV_FPA_RFU440 3.4E</div> <div>SNV_FPA_RFU441 3.4E</div> <div>SNV_FPA_RFU442 3.4E</div> <div>SNV_FPA_RFU443 3.4E</div> <div>SNV_FPA_RFU444 3.4E</div> <div>SNV_FPA_RFU445 3.4E</div> <div>SNV_FPA_RFU446 3.4E</div> <div>SNV_FPA_RFU447 3.4E</div> <div>SNV_FPA_RFU448 3.4E</div> <div>SNV_FPA_RFU449 3.4E</div> <div>SNV_FPA_RFU450 3.4E</div> <div>SNV_FPA_RFU451 3.4E</div> <div>SNV_FPA_RFU452 3.4E</div> <div>SNV_FPA_RFU453 3.4E</div> <div>SNV_FPA_RFU454 3.4E</div> <div>SNV_FPA_RFU455 3.4E</div> <div>SNV_FPA_RFU456 3.4E</div> <div>SNV_FPA_RFU457 3.4E</div> <div>SNV_FPA_RFU458 3.4E</div> <div>SNV_FPA_RFU459 3.4E</div> <div>SNV_FPA_RFU460 3.4E</div> <div>SNV_FPA_RFU461 3.4E</div> <div>SNV_FPA_RFU462 3.4E</div> <div>SNV_FPA_RFU463 3.4E</div> <div>SNV_FPA_RFU464 3.4E</div> <div>SNV_FPA_RFU465 3.4E</div> <div>SNV_FPA_RFU466 3.4E</div> <div>SNV_FPA_RFU467 3.4E</div> <div>SNV_FPA_RFU468 3.4E</div> <div>SNV_FPA_RFU469 3.4E</div> <div>SNV_FPA_RFU470 3.4E</div> <div>SNV_FPA_RFU471 3.4E</div> <div>SNV_FPA_RFU472 3.4E</div> <div>SNV_FPA_RFU473 3.4E</div> <div>SNV_FPA_RFU474 3.4E</div> <div>SNV_FPA_RFU475 3.4E</div> <div>SNV_FPA_RFU476 3.4E</div> <div>SNV_FPA_RFU477 3.4E</div> <div>SNV_FPA_RFU478 3.4E</div> <div>SNV_FPA_RFU479 3.4E</div> <div>SNV_FPA_RFU480 3.4E</div> <div>SNV_FPA_RFU481 3.4E</div> <div>SNV_FPA_RFU482 3.4E</div> <div>SNV_FPA_RFU483 3.4E</div> <div>SNV_FPA_RFU484 3.4E</div> <div>SNV_FPA_RFU485 3.4E</div> <div>SNV_FPA_RFU486 3.4E</div> <div>SNV_FPA_RFU487 3.4E</div> <div>SNV_FPA_RFU488 3.4E</div> <div>SNV_FPA_RFU489 3.4E</div> <div>SNV_FPA_RFU490 3.4E</div> <div>SNV_FPA_RFU491 3.4E</div> <div>SNV_FPA_RFU492 3.4E</div> <div>SNV_FPA_RFU493 3.4E</div> <div>SNV_FPA_RFU494 3.4E</div> <div>SNV_FPA_RFU495 3.4E</div> <div>SNV_FPA_RFU496 3.4E</div> <div>SNV_FPA_RFU497 3.4E</div> <div>SNV_FPA_RFU498 3.4E</div> <div>SNV_FPA_RFU499 3.4E</div> <div>SNV_FPA_RFU500 3.4E</div> <div>SNV_FPA_RFU501 3.4E</div> <div>SNV_FPA_RFU502 3.4E</div> <div>SNV_FPA_RFU503 3.4E</div> <div>SNV_FPA_RFU504 3.4E</div> <div>SNV_FPA_RFU505 3.4E</div> <div>SNV_FPA_RFU506 3.4E</div> <div>SNV_FPA_RFU507 3.4E</div> <div>SNV_FPA_RFU508 3.4E</div> <div>SNV_FPA_RFU509 3.4E</div> <div>SNV_FPA_RFU510 3.4E</div> <div>SNV_FPA_RFU511 3.4E</div> <div>SNV_FPA_RFU512 3.4E</div> <div>SNV_FPA_RFU513 3.4E</div> <div>SNV_FPA_RFU514 3.4E</div> <div>SNV_FPA_RFU515 3.4E</div> <div>SNV_FPA_RFU516 3.4E</div> <div>SNV_FPA_RFU517 3.4E</div> <div>SNV_FPA_RFU518 3.4E</div> <div>SNV_FPA_RFU519 3.4E</div> <div>SNV_FPA_RFU520 3.4E</div> <div>SNV_FPA_RFU521 3.4E</div> <div>SNV_FPA_RFU522 3.4E</div> <div>SNV_FPA_RFU523 3.4E</div> <div>SNV_FPA_RFU524 3.4E</div> <div>SNV_FPA_RFU525 3.4E</div> <div>SNV_FPA_RFU526 3.4E</div> <div>SNV_FPA_RFU527 3.4E</div> <div>SNV_FPA_RFU528 3.4E</div> <div>SNV_FPA_RFU529 3.4E</div> <div>SNV_FPA_RFU530 3.4E</div> <div>SNV_FPA_RFU531 3.4E</div> <div>SNV_FPA_RFU532 3.4E</div> <div>SNV_FPA_RFU533 3.4E</div> <div>SNV_FPA_RFU534 3.4E</div> <div>SNV_FPA_RFU535 3.4E</div> <div>SNV_FPA_RFU536 3.4E</div> <div>SNV_FPA_RFU537 3.4E</div> <div>SNV_FPA_RFU538 3.4E</div> <div>SNV_FPA_RFU539 3.4E</div> <div>SNV_FPA_RFU540 3.4E</div> <div>SNV_FPA_RFU541 3.4E</div> <div>SNV_FPA_RFU542 3.4E</div> <div>SNV_FPA_RFU543 3.4E</div> <div>SNV_FPA_RFU544 3.4E</div> <div>SNV_FPA_RFU545 3.4E</div> <div>SNV_FPA_RFU546 3.4E</div> <div>SNV_FPA_RFU547 3.4E</div> <div>SNV_FPA_RFU548 3.4E</div> <div>SNV_FPA_RFU549 3.4E</div> <div>SNV_FPA_RFU550 3.4E</div> <div>SNV_FPA_RFU551 3.4E</div> <div>SNV_FPA_RFU552 3.4E</div> <div>SNV_FPA_RFU553 3.4E</div> <div>SNV_FPA_RFU554 3.4E</div> <div>SNV_FPA_RFU555 3.4E</div> <div>SNV_FPA_RFU556 3.4E</div> <div>SNV_FPA_RFU557 3.4E</div> <div>SNV_FPA_RFU558 3.4E</div> <div>SNV_FPA_RFU559 3.4E</div> <div>SNV_FPA_RFU560 3.4E</div> <div>SNV_FPA_RFU561 3.4E</div> <div>SNV_FPA_RFU562 3.4E</div> <div>SNV_FPA_RFU563 3.4E</div> <div>SNV_FPA_RFU564 3.4E</div> <div>SNV_FPA_RFU565 3.4E</div> <div>SNV_FPA_RFU566 3.4E</div> <div>SNV_FPA_RFU567 3.4E</div> <div>SNV_FPA_RFU568 3.4E</div> <div>SNV_FPA_RFU569 3.4E</div> <div>SNV_FPA_RFU570 3.4E</div> <div>SNV_FPA_RFU571 3.4E</div> <div>SNV_FPA_RFU572 3.4E</div> <div>SNV_FPA_RFU573 3.4E</div> <div>SNV_FPA_RFU574 3.4E</div> <div>SNV_FPA_RFU575 3.4E</div> <div>SNV_FPA_RFU576 3.4E</div> <div>SNV_FPA_RFU577 3.4E</div> <div>SNV_FPA_RFU578 3.4E</div> <div>SNV_FPA_RFU579 3.4E</div> <div>SNV_FPA_RFU580 3.4E</div> <div>SNV_FPA_RFU581 3.4E</div> <div>SNV_FPA_RFU582 3.4E</div> <div>SNV_FPA_RFU583 3.4E</div> <div>SNV_FPA_RFU584 3.4E</div> <div>SNV_FPA_RFU585 3.4E</div> <div>SNV_FPA_RFU586 3.4E</div> <div>SNV_FPA_RFU587 3.4E</div> <div>SNV_FPA_RFU588 3.4E</div> <div>SNV_FPA_RFU589 3.4E</div> <div>SNV_FPA_RFU590 3.4E</div> <div>SNV_FPA_RFU591 3.4E</div> <div>SNV_FPA_RFU592 3.4E</div> <div>SNV_FPA_RFU593 3.4E</div> <div>SNV_FPA_RFU594 3.4E</div> <div>SNV_FPA_RFU595 3.4E</div> <div>SNV_FPA_RFU596 3.4E</div> <div>SNV_FPA_RFU597 3.4E</div> <div>SNV_FPA_RFU598 3.4E</div> <div>SNV_FPA_RFU599 3.4E</div> <div>SNV_FPA_RFU600 3.4E</div> <div>SNV_FPA_RFU</div></div>							

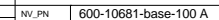
[illegible]

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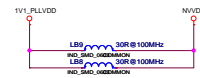


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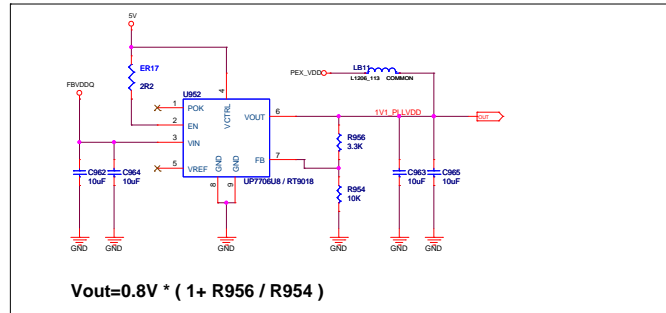


ID	PAGE
NAME	DATE 05-FEB-2009

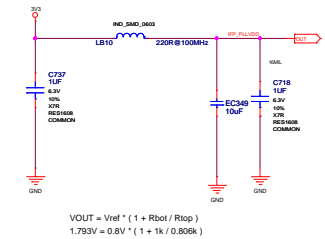
LINEAR POWER SUPPLIES



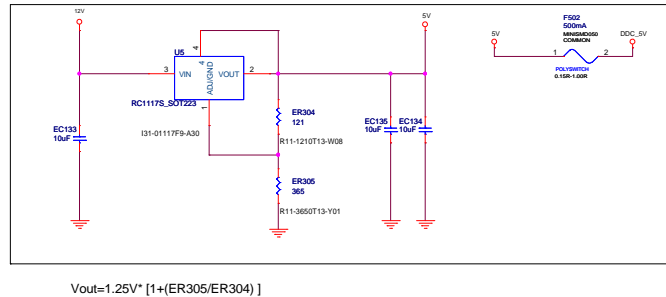
PEX_PLLVDD SUPPLY (OPTIAN)



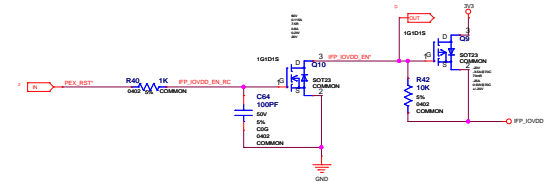
IFP_PLLVDD SUPPLY



5V & DDC_5V REGULATOR



IFP_IOVDD BACKDRIVE PREVENTION



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PAGE DETAIL	LINEAR POWER SUPPLIES

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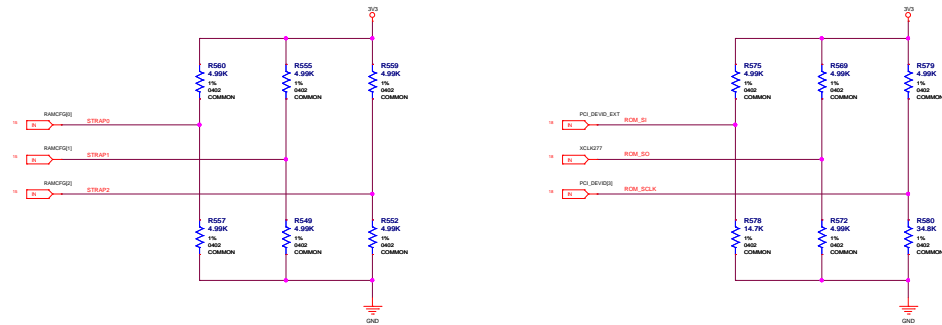


NV_PN	600-10681-base-100 A
-------	----------------------

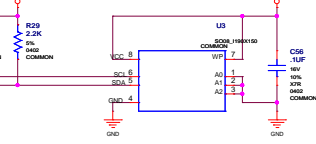
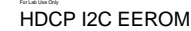
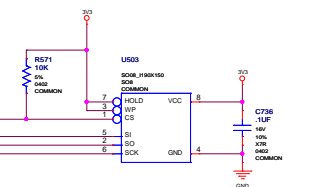
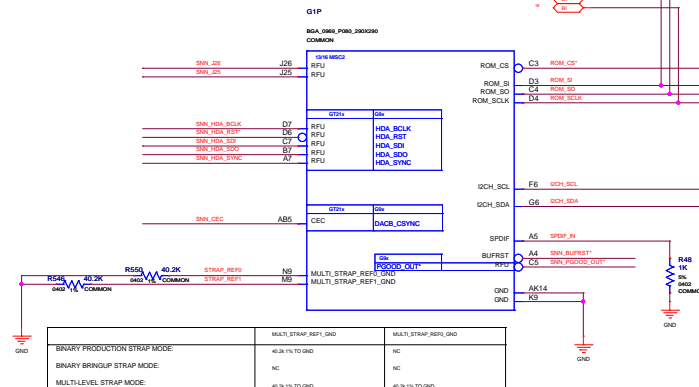
ID		PAGE	
NAME		DATE	05-FEB-2009

BIOS ROM, HDCP ROM, STRAPPING OPTIONS

STRAPPING OPTIONS



Step0	USEDC[0]	ENDC 1110
Step1	JGO_PANDQ[0]	DESKTOP_0000
Step2	PCI_DEVNO[0]	07210-200 (0A2) 0001
ROM_01	RANCFCS[0]	0001 1288 648A16 D0D0 Grounds 0010 1288 648A16 D0D0 Inps 0011 1288 648A16 D0D0 Samsung 0101 1288 1288A16 D0D0 Grounds 0110 1288 1288A16 D0D0 Inps 0111 1288 1288A16 D0D0 Samsung 1001 648 648A16 D0D0 Grounds 1010 648 1288A16 D0D0 Inps 1011 648 648A16 D0D0 Samsung 1101 648 1288A16 D0D0 Grounds 1110 648 1288A16 D0D0 Inps 1111 648 1288A16 D0D0 Samsung
ROM_SCLK	3 PCI_DEVNO_0[0] 2 SCL_VENDOR[0] - 0000 (not present) 1 SCL_CLK_0[0] - 0000 (not present) 0 PEX_PLS_EN_TMR0 (0 - disable p0 tmr)	0110
ROM_ID	3 KX_0[0] (0 - P0R 27F000) 2 PLS_0[0] (0 - P0R 27F000) 1 SCL_0[0] (0 - P0R 27F000) 0 SCL_0[0] (0 - P0R 27F000) 0 VISA_0[0] (0 - P0R 27F000)	0001
		0 P0 0a 1 P0 1a 2 P0 1a 3 P0 2a 4 P0 2a 5 P0 3a 6 P0 3a 7 P0 4a



	MULTI_STRAP_REF1_GND	MULTI_STRAP_REF0_GND
BINARY PRODUCTION STRAP MODE:	40.2k 1% TO GND	NC
BINARY BRINGUP STRAP MODE:	NC	NC
MULTI-LEVEL STRAP MODE:	40.2k 1% TO GND	40.2k 1% TO GND

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PAGE DETAIL	BIOS ROM, HDCP ROM, STRAPPING OPTIONS

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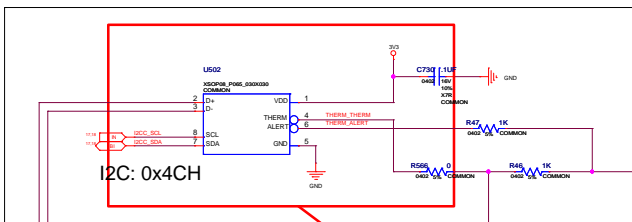


NV_PN	600-10681-base-100 A
-------	----------------------

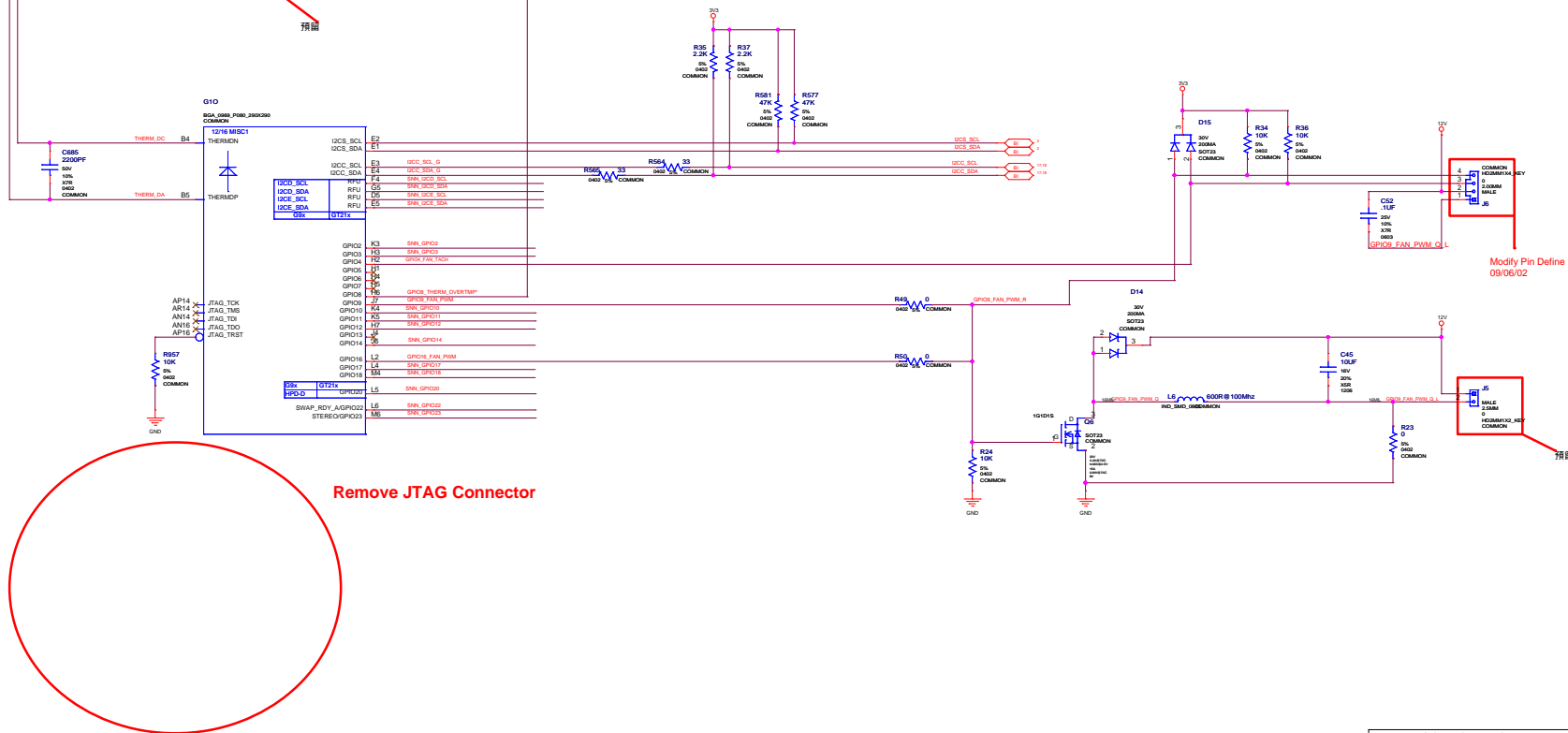
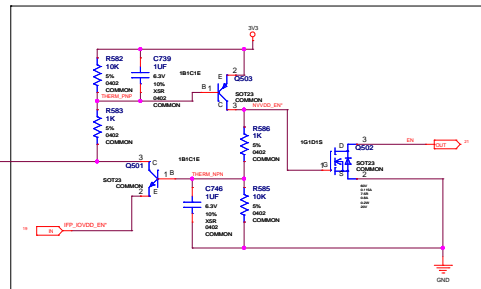
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EXTERNAL THERMAL SENSOR, FAN CONTROL, GPIO, JTAG

THERMAL SENSOR



OVERTEMP LATCH



Remove JTAG Connector

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	EXTERNAL THERMAL SENSOR, FAN CONTROL, GPIO, JTAG

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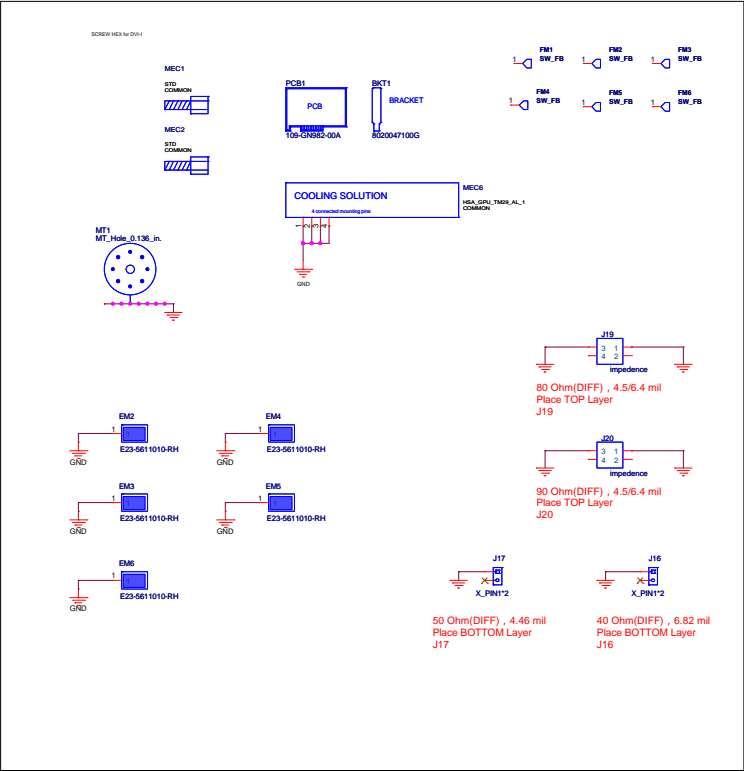
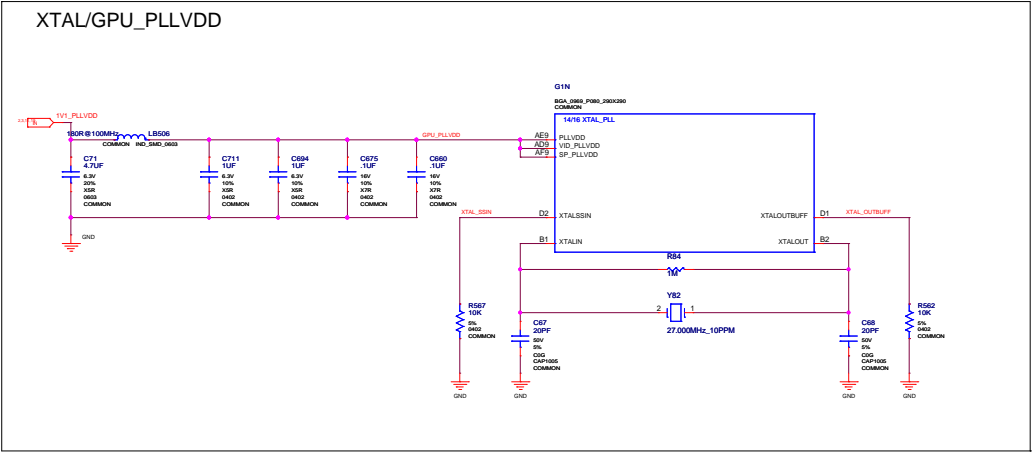
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XTAL, MECHANICALS, THERMALS

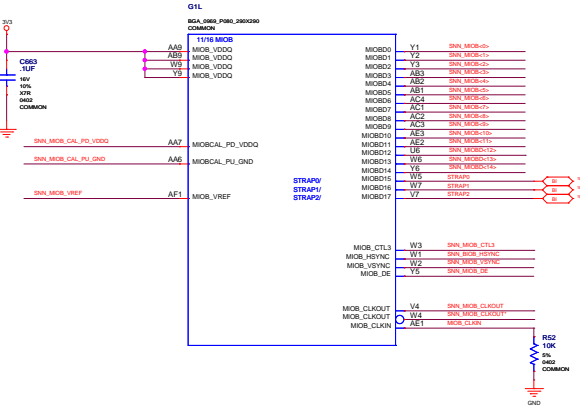
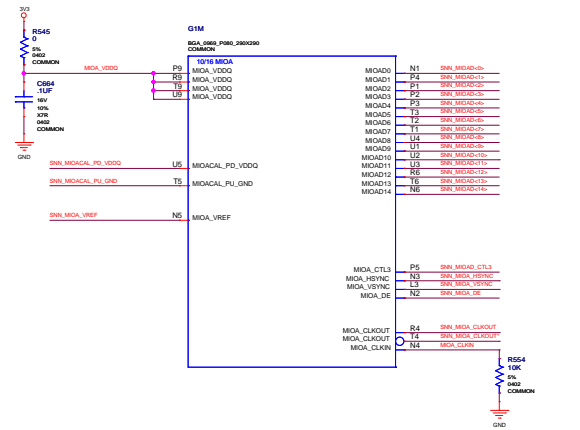


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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON'S NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	XTAL, MECHANICALS, THERMALS

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NV_PN	600-10681-base-100 A	PAGE	
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MIOA & MIOB



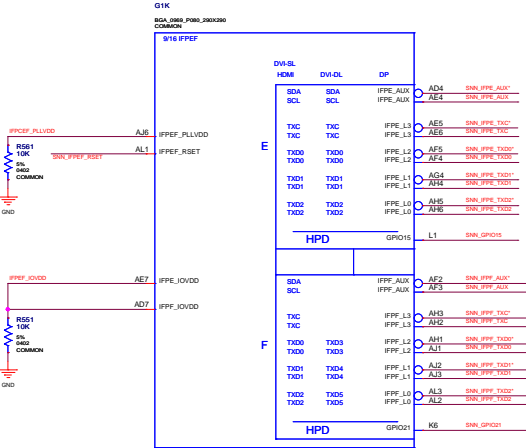
ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE BOARD SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY "MATERIALS") ARE BEING PROVIDED "AS IS". THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE, WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

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PAGE DETAIL	MIOA & MIOB

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IFP EF (UNUSED)



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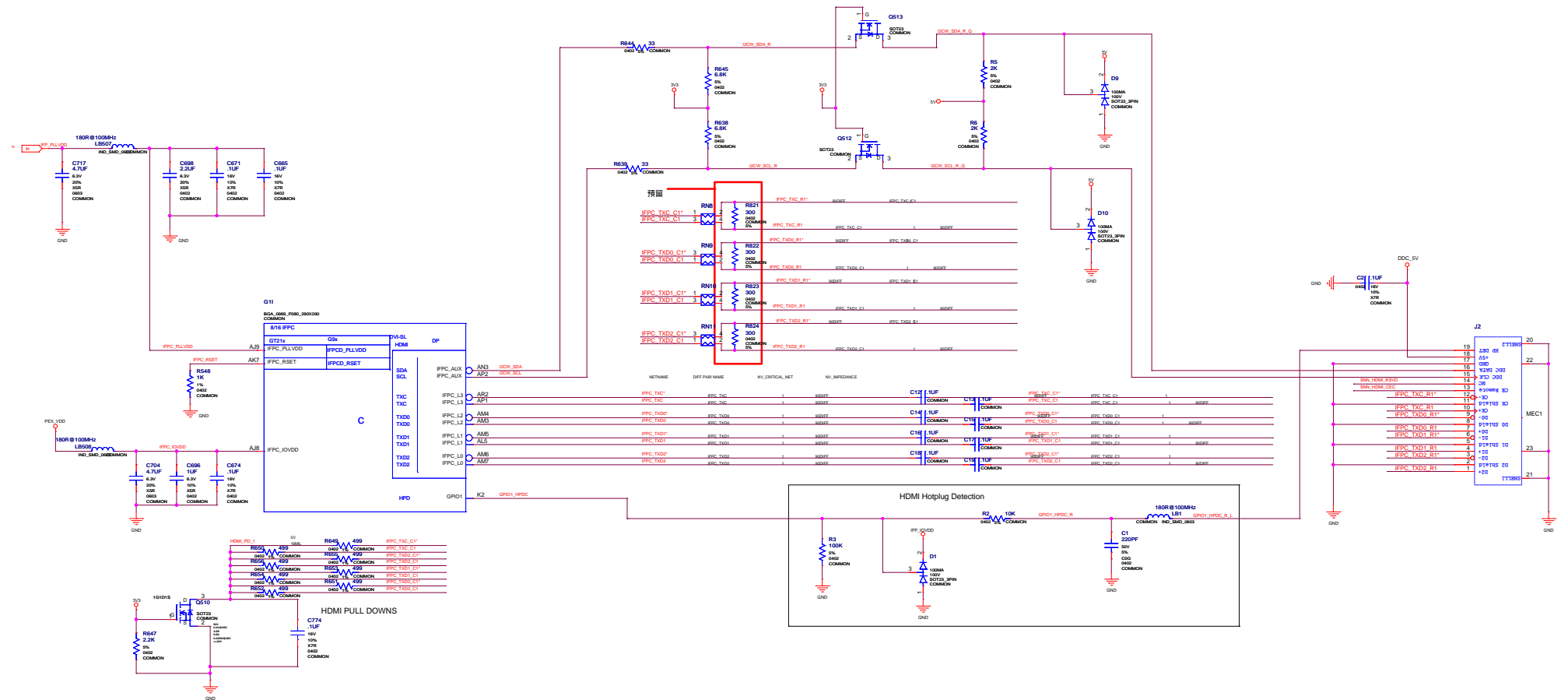
ID		PAGE
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IFP C (NORTH HDMI)



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PAGE DETAIL	IFPC (NORTH HDMI)

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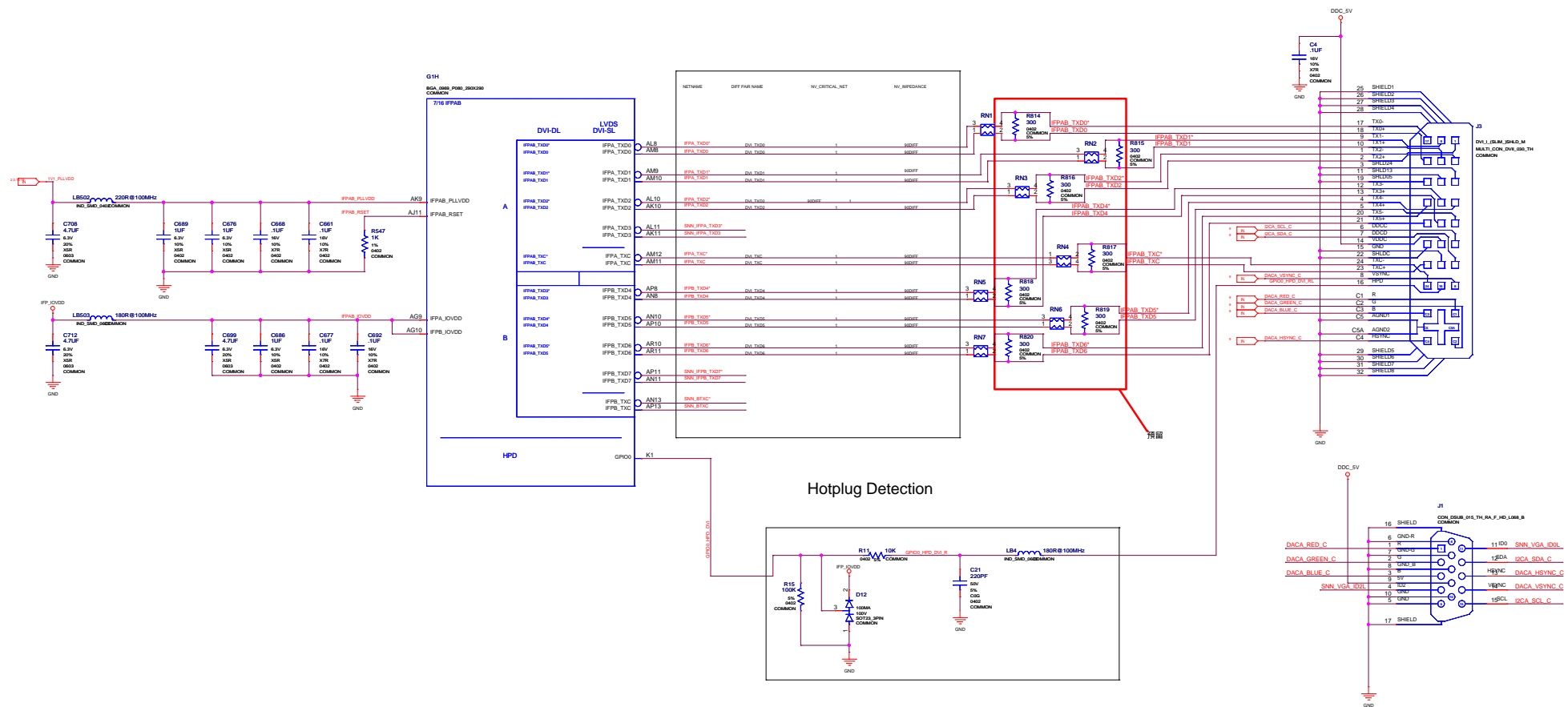
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NAME		DATE	05-FEB-2009

IFP AB (SOUTH DVI-I)



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	IFP AB (SOUTH DVI-I)

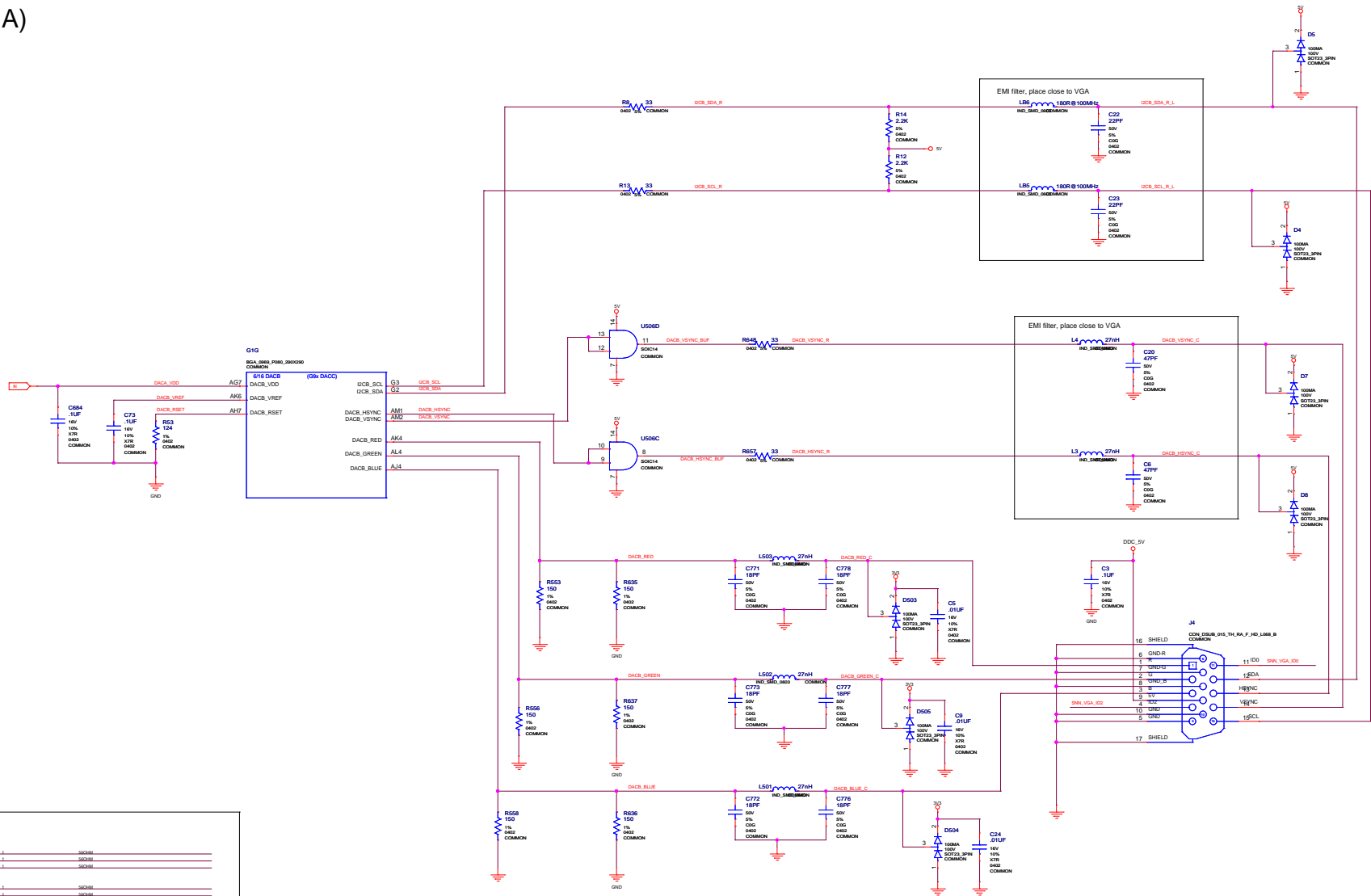
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DACB (MID VGA)



91	DACH RED	1	ISO/IR1
92	DACH GREEN	1	ISO/IR1
93	DACH BLUE	1	ISO/IR1
94		1	
95	DACH RED C	1	ISO/IR1
96	DACH GREEN C	1	ISO/IR1
97	DACH BLUE C	1	ISO/IR1
98			
99	DACH YELLOW	2	ISO/IR1
100	DACH RED	2	ISO/IR1
101	DACH GREEN	2	ISO/IR1
102	DACH YELLOW-R	2	ISO/IR1
103	DACH RED-R	2	ISO/IR1
104	DACH GREEN-R	2	ISO/IR1
105	DACH RED-YELLOW-R	2	ISO/IR1
106	DACH GREEN-YELLOW-R	2	ISO/IR1
107	DACH RED	2	ISO/IR1
108	DACH GREEN	2	ISO/IR1
109	DACH YELLOW-C	2	ISO/IR1

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACB (MD VGA)

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P681-A01 GT215/216 DESKTOP GB1-128 DDR3
PCI-EXPRESSx16 DL-DVI VGA HDMI

Page 1: P681-A01 OVERVIEW

Page 2: PCI-EXPRESS INTERFACE

Page 3: PARTITION A FRAME BUFFER INTERFACE

Page 4: PARTITION A MEMORIES

Page 5: FBA DECOUPLING CAPS & NVVDD DECOUPLING CAPS

Page 6: PARTITION C FRAME BUFFER INTERFACE

Page 7: PARTITION C MEMORIES

Page 8: FBC DECOUPLING CAPS

Page 9: DACA (SOUTH DVI-I)

Page 10: DACB (MID VGA)

Page 11: IFP AB (SOUTH DVI-I)

Page 12: IFP C (NORTH HDMI)

Page 13: IFP D (UNUSED)

Page 14: IFP EF (UNUSED)

Page 15: MIOA & MIOB

Page 16: XTAL, MECHANICALS, THERMALS

Page 17: EXTERNAL THERMAL SENSOR, FAN CONTROL, GPIO, JTAG

Page 18: BIOS ROM, HDCP ROM, STRAPPING OPTIONS

Page 19: LINEAR POWER SUPPLIES

Page 20: FBVDDQ/PEXVDD POWER SUPPLY

Page 21: NVVDD POWER SUPPLY

V199 For Lenovo Schematic Change List 2009/03/31 by STEVEN CHANG

- Page 2: Remove JTAG Component
- Page 10: Move J1 D-SUB Connector to Page 11
- Page 11: Add Slim Type D-SUB Connector
- Page 17: Remove JTAG Connector
- Page 19: Remove IFP_PLLVDD SUPPLY LDO IC
- Page 19: Add UP7706 LDO to Change PEX_VDD Power Supply
- Page 19: Change AP1117 LDO External Schematic Design
- Page 20: Change UP6161 PWM IC to use
UP6101 PWM IC Solution For FBVDDQ Power Supply
- Page 20: Change UP6210 PWM IC to use
RT9232 PWM IC Solution For NVVDD Power Supply
- Page 20: Remove NVVDD SENSE Net

REV	VARIANT	NVPN	ASSEMBLY
B	BASE	600-10681-000-100	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU001	600-10681-0001-100	GT216-300 600/1500MHz 1024MB 64Mx16 BGA100 900MHz DDR3 DVI-I/VGA/HDMI
2	SKU002	600-10681-0002-100	GT216-300 600/1500MHz 1024MB 64Mx16 BGA100 1000MHz DDR3 DVI-I/VGA/HDMI
3	SKU0011	600-10681-0011-100	GT216-300 600/1500MHz 1024MB 64Mx16 BGA100 900MHz DDR3 DVI-I/VGA/HDMI
4	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
5	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
6	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
7	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
8	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
9	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
10	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
11	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
12	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
13	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
14	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
15	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>

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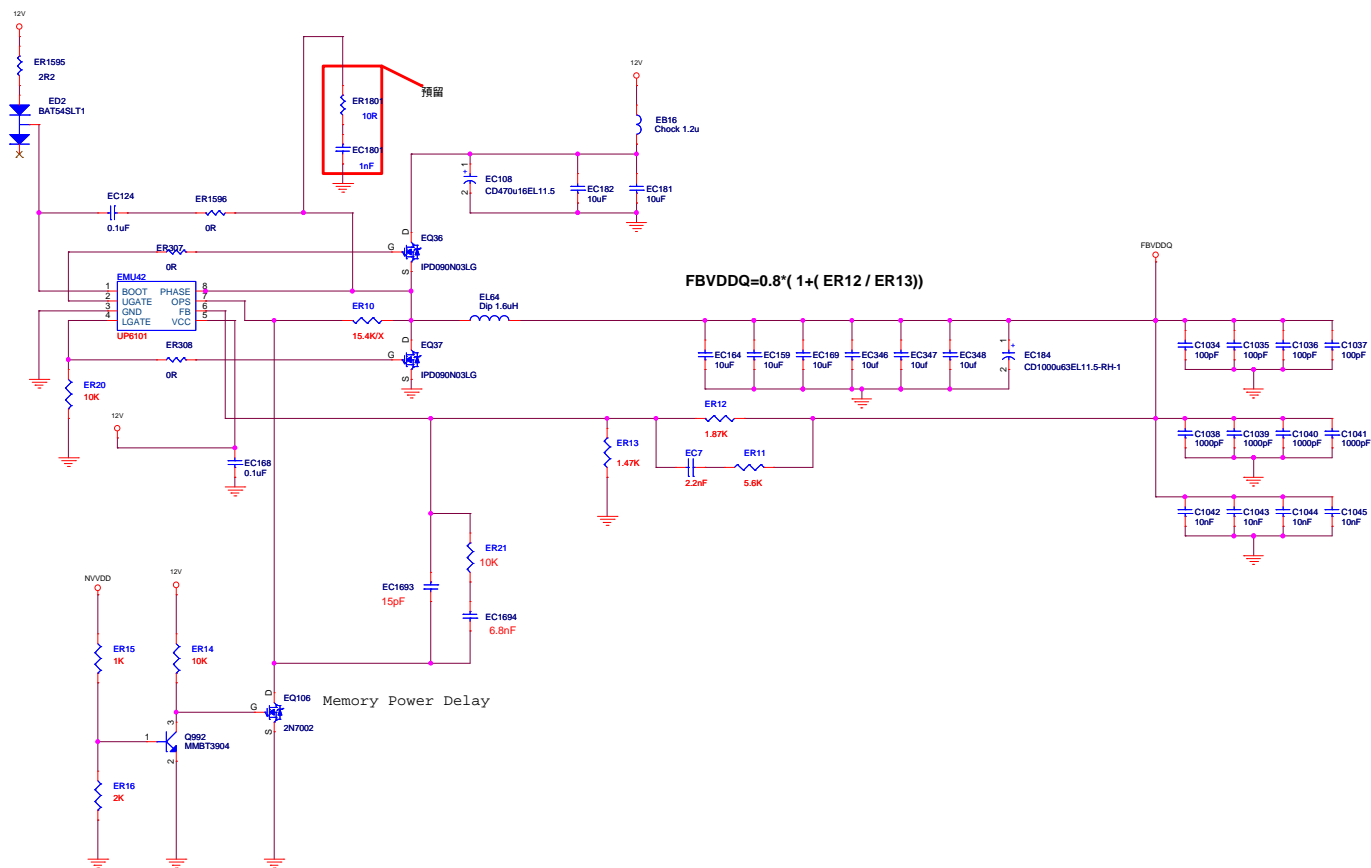
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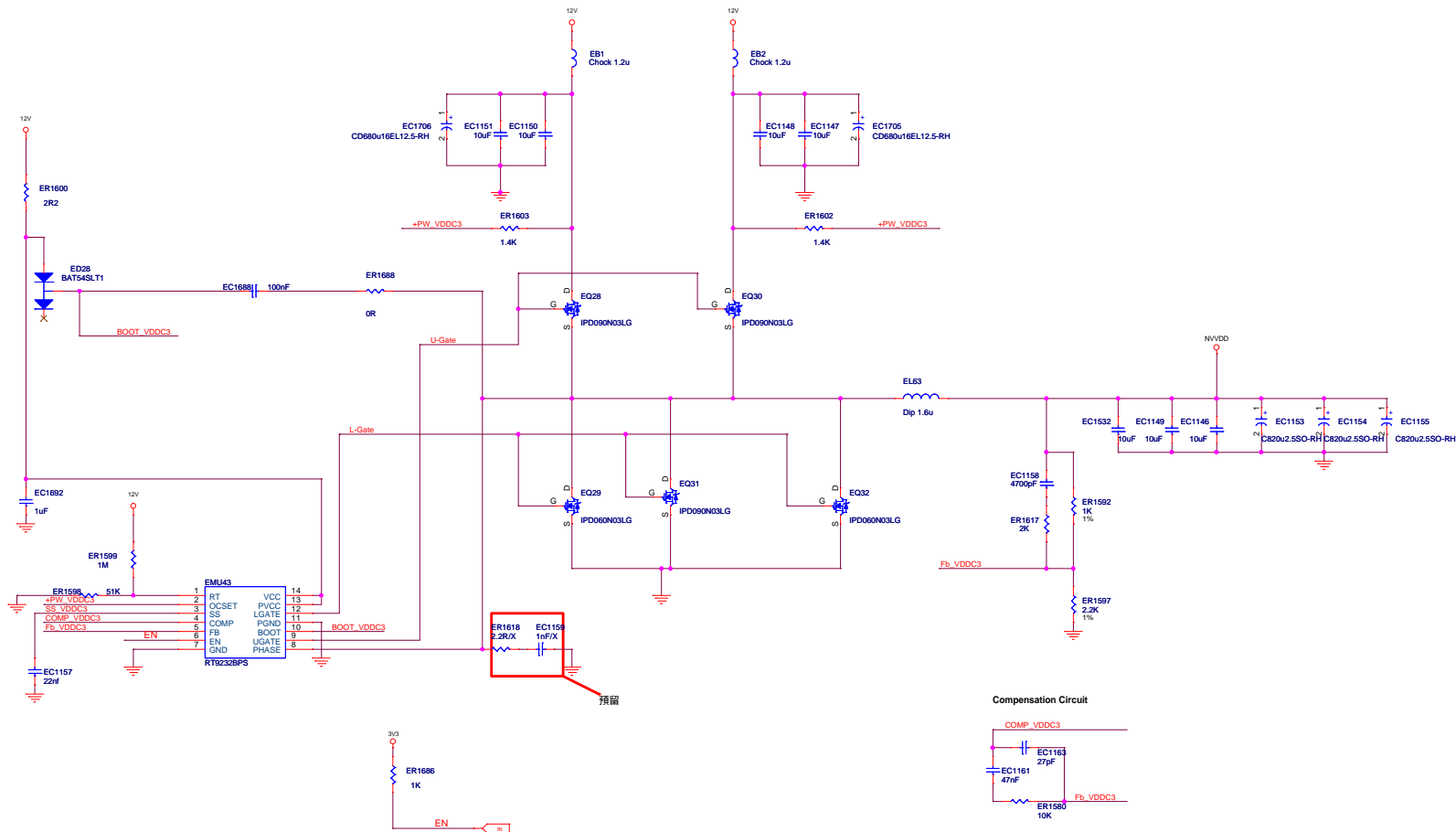
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FBVDDQ POWER SUPPLY



NVVDD POWER

CORE REGULATOR NVVDD



$$NVVDD = 0.8 * (1 + (ER1592 / ER1597))$$