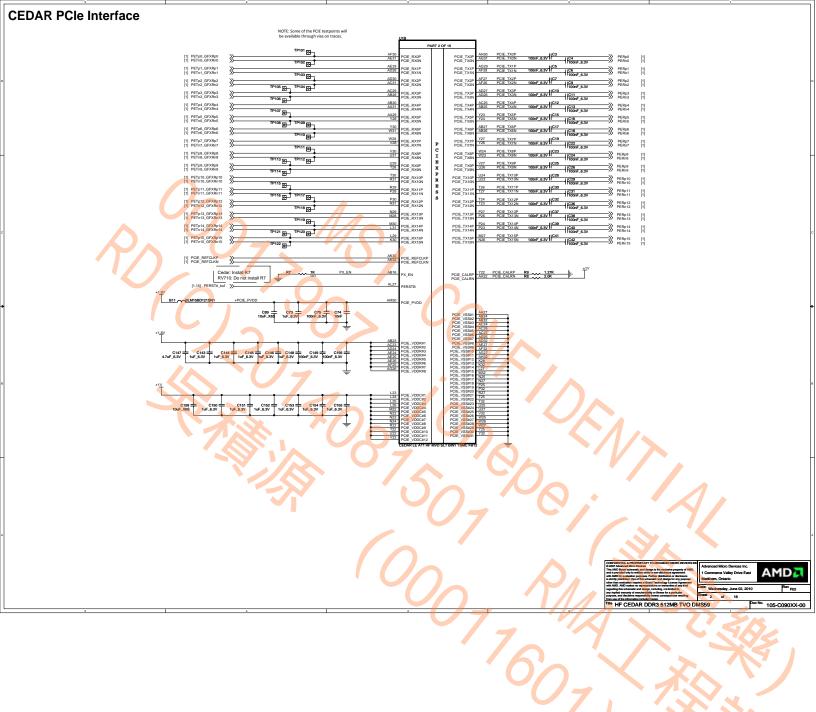
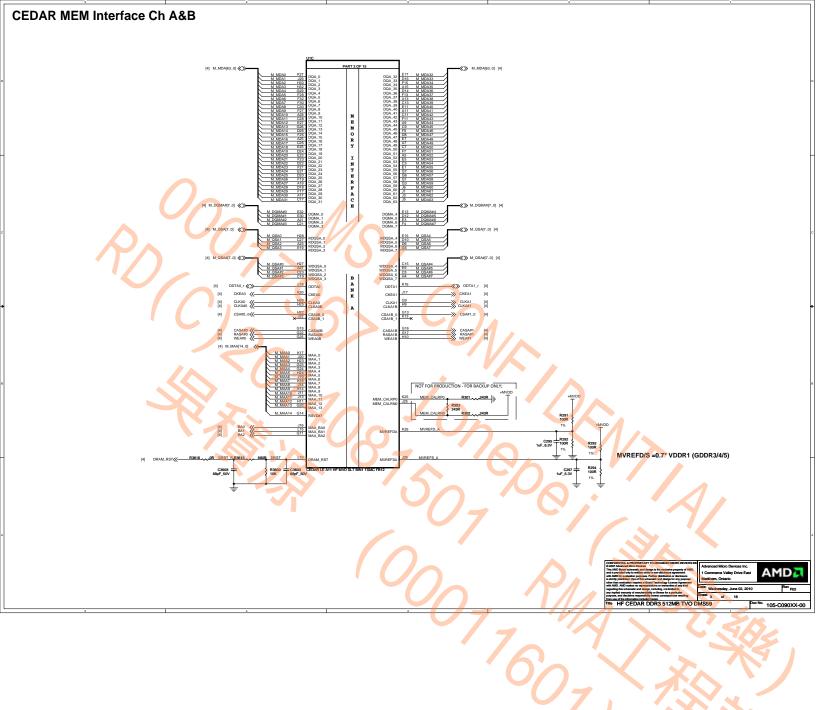
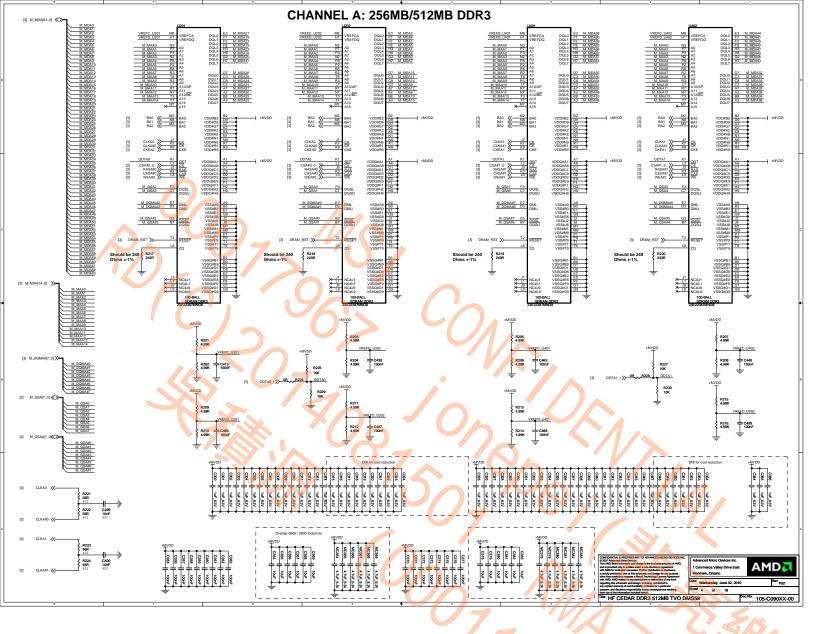
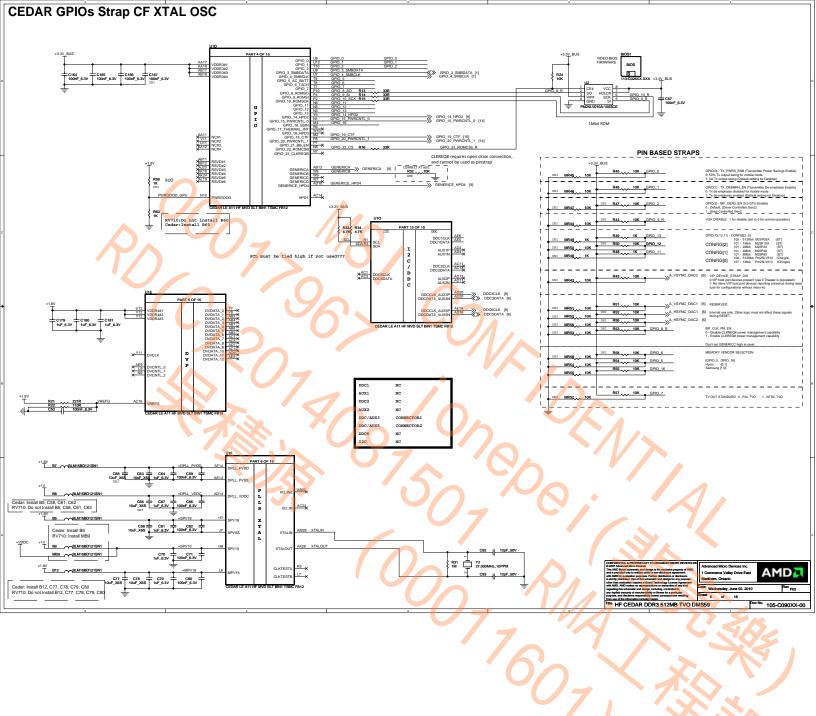
the information included harmin.

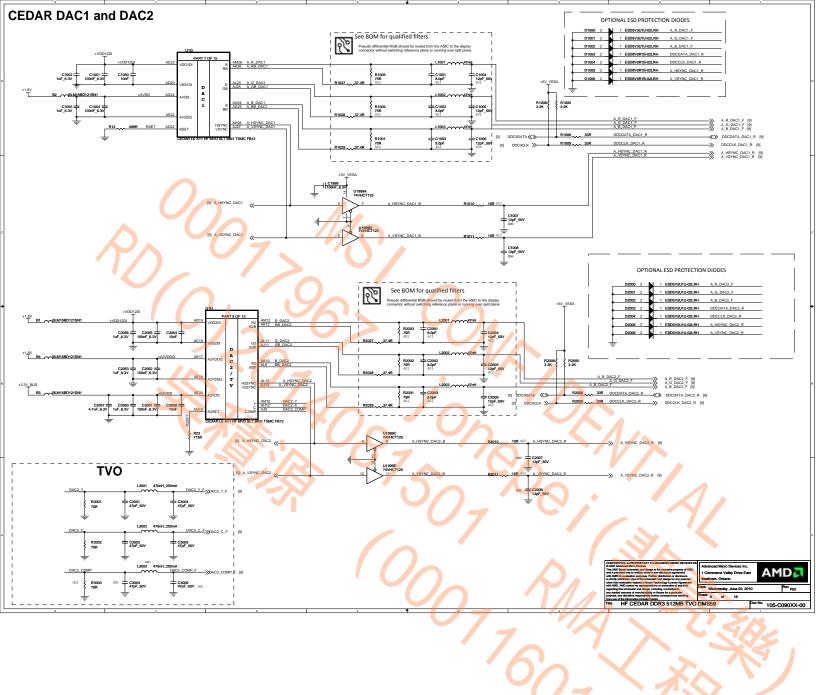
HF CEDAR DDR3 512MB TVC

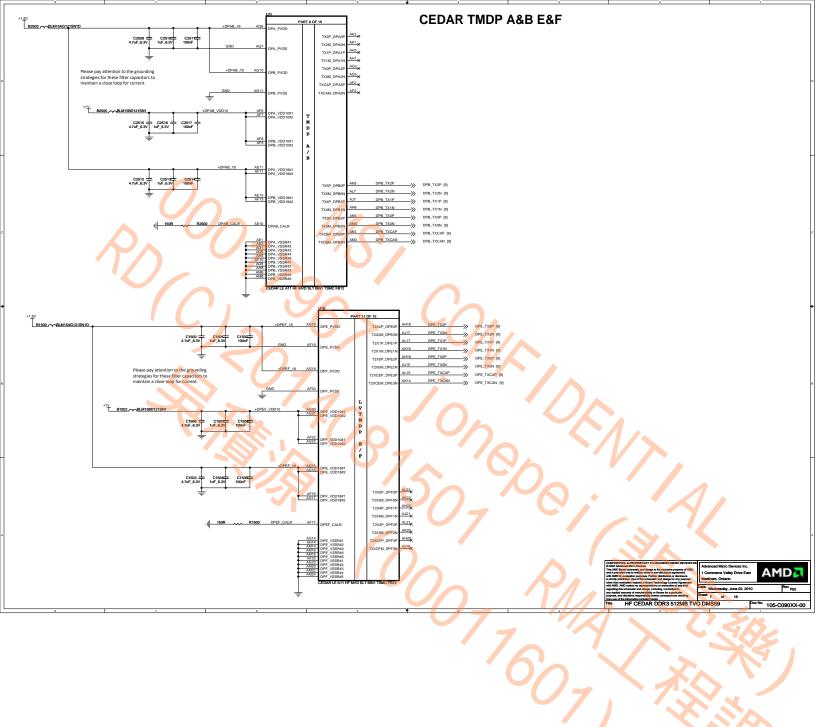


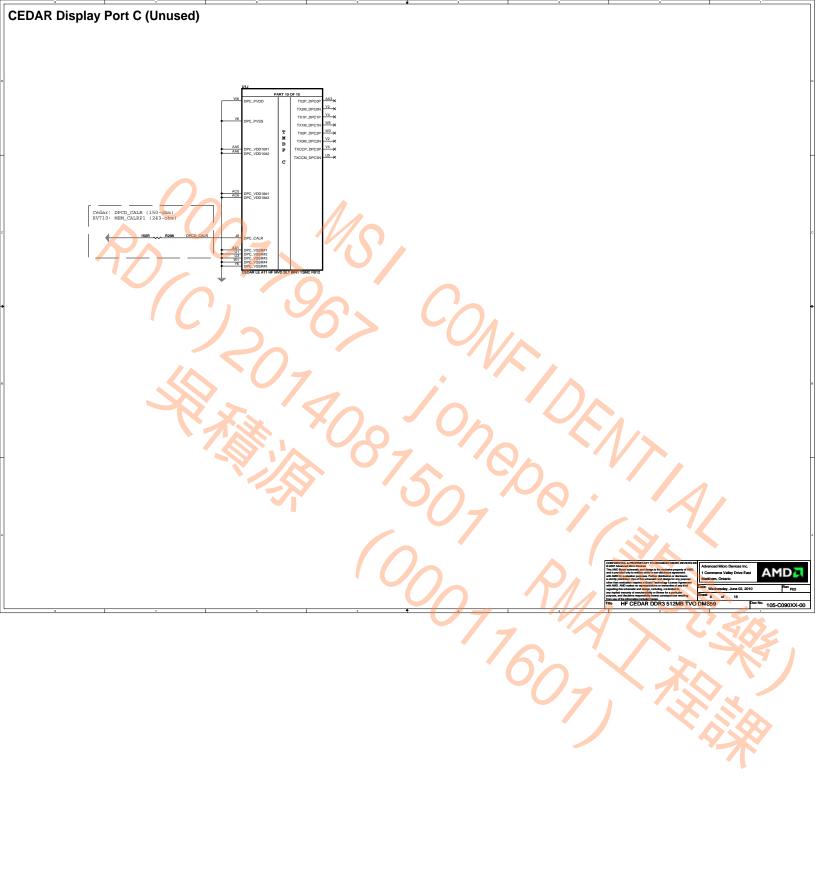


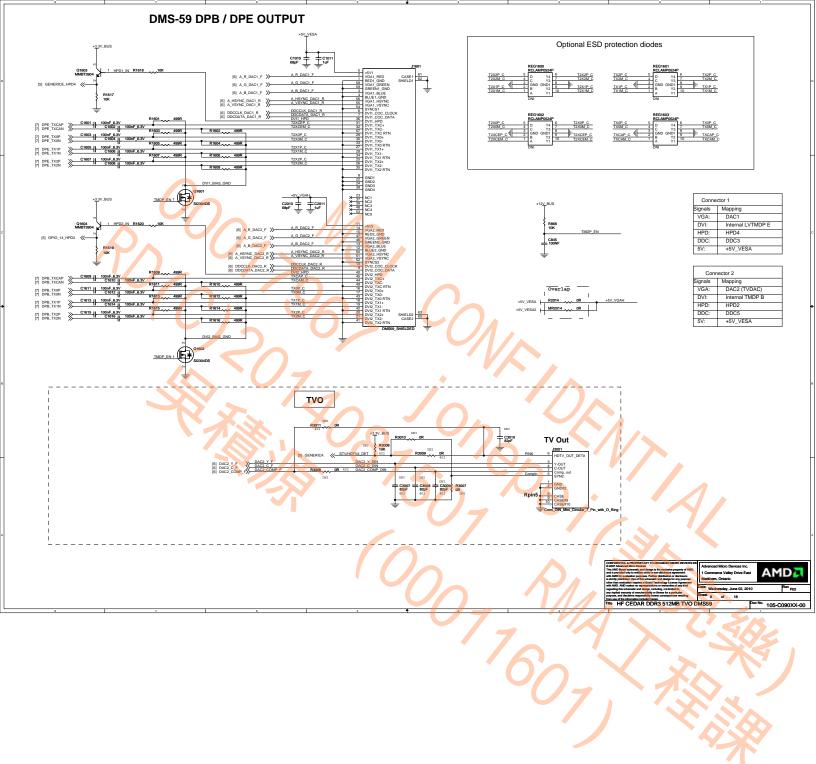


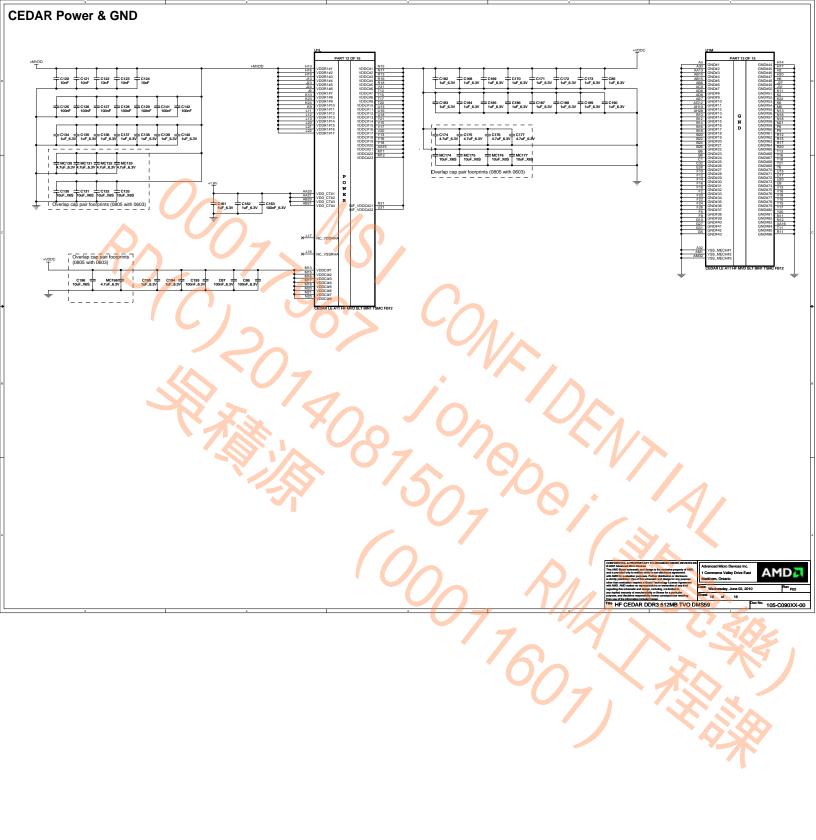


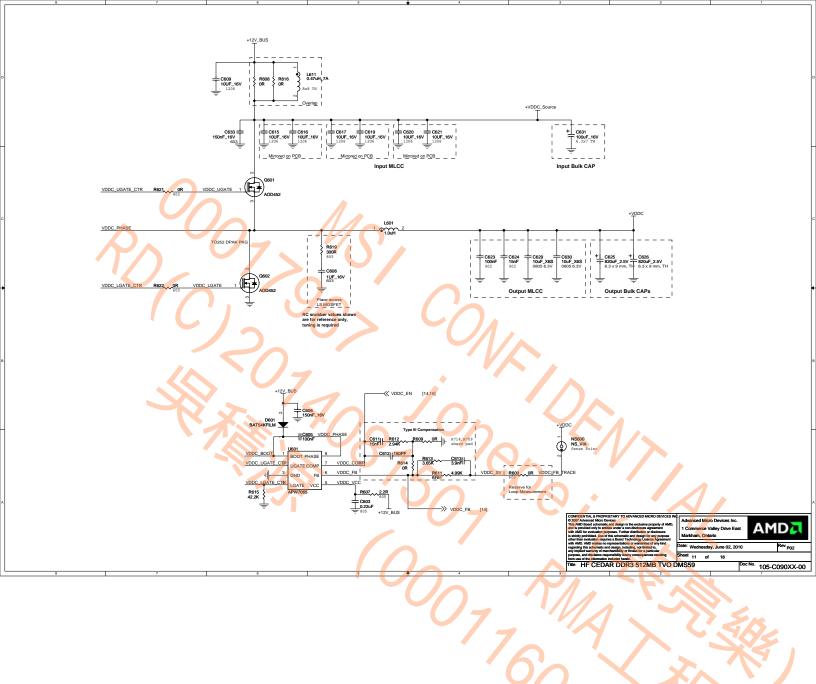


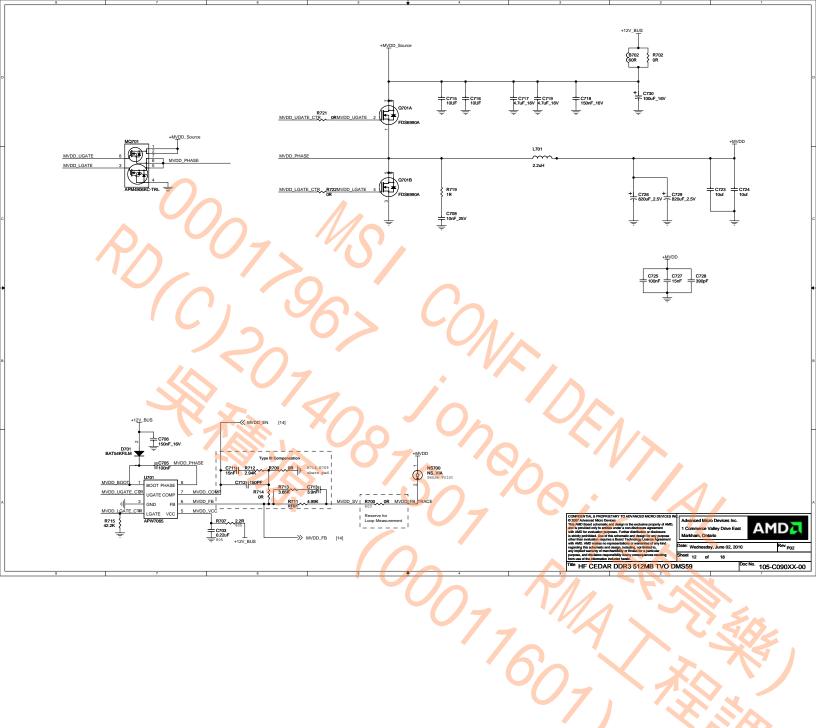


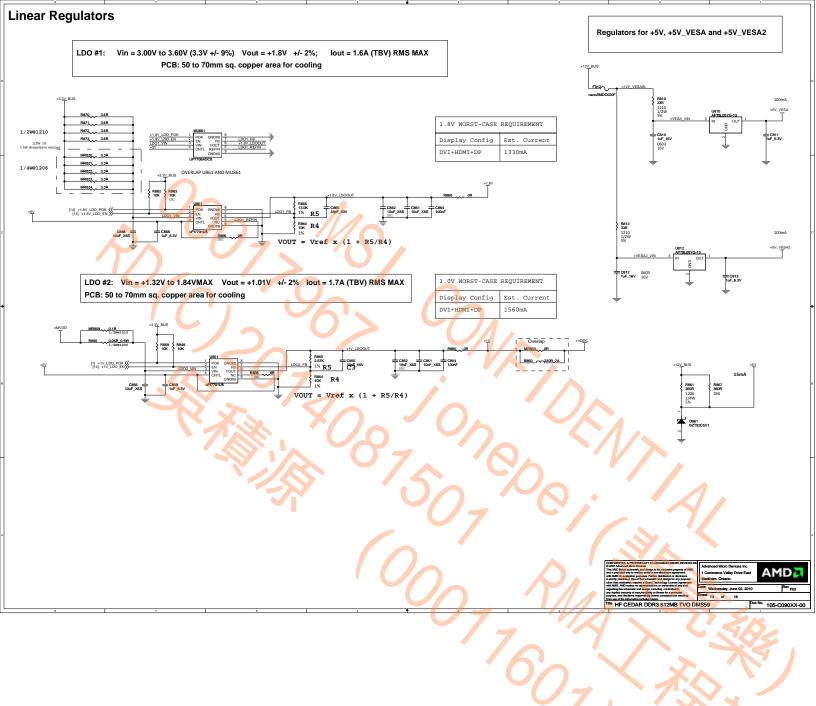


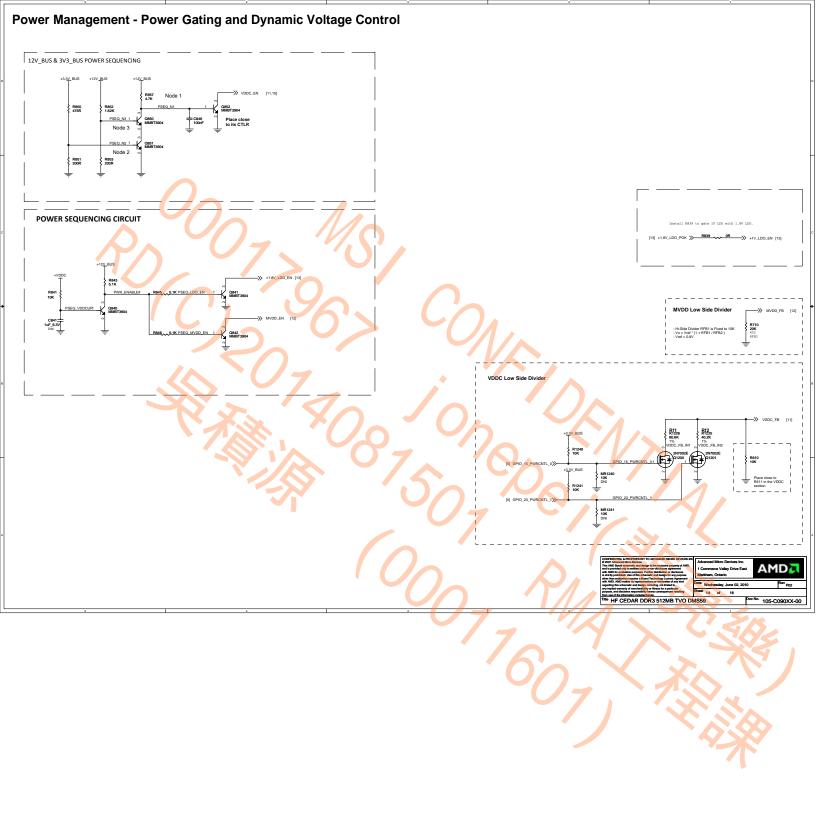


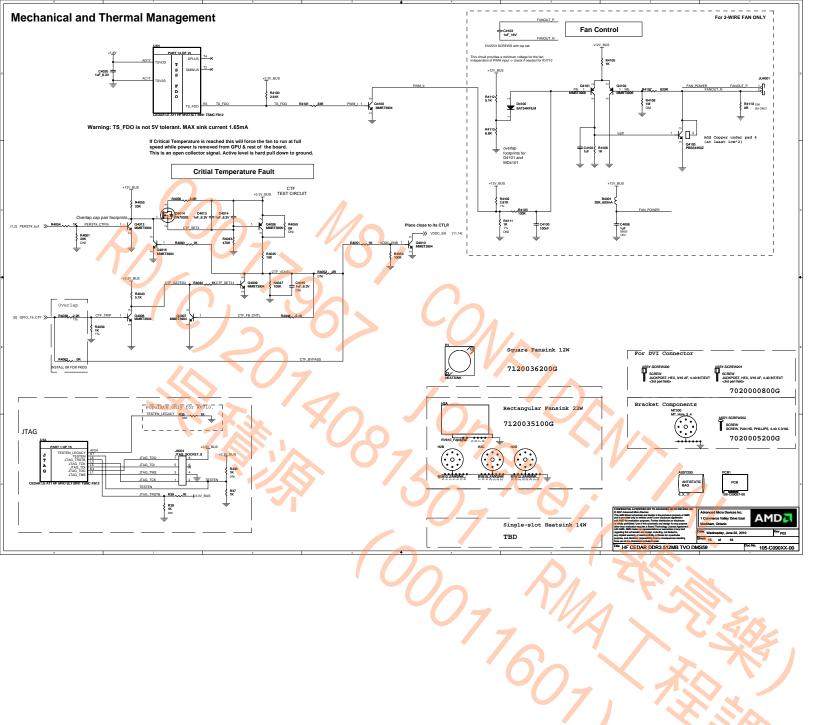


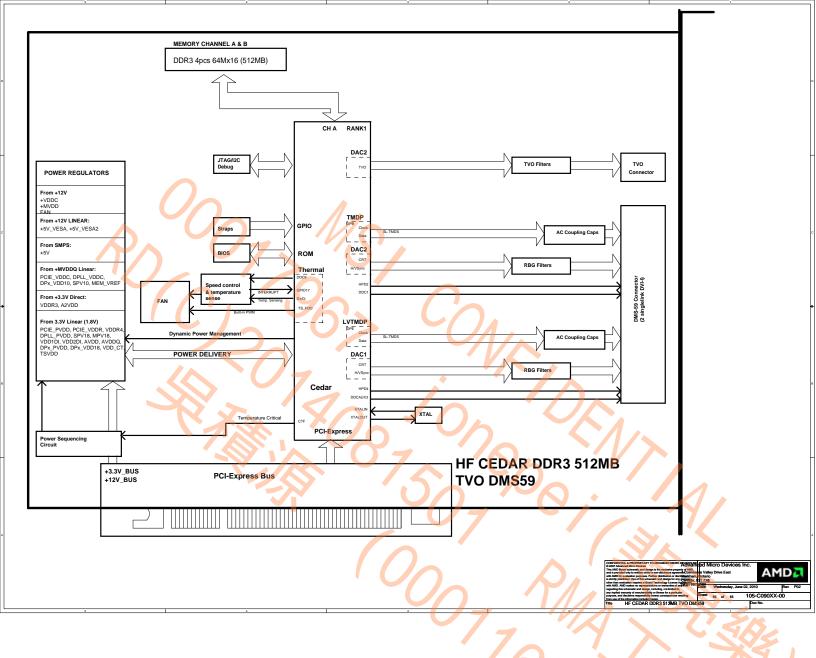












	ΑΙ	MD	5	Title HF CEDAR DDR3 512MB TVO DMS59	3	Schematic No. 105-C090XX-00	Date: Wednesday, June 02	
				REVISION HISTORY	For Stuffing options (com Please contact AMD repre	s the PCB, it does not represent any specific S ponent values, DNI's,) please consult the pr sentative to obtain latest BOM closest to the a	KU. oduct specific BOM. pplication desired.	Rev P02
S R	ch ev	PCB Rev	Date	REVISION DESCRIPTION				
	00	00A	2010/03/2	5 Initial Cedar schematic, based on C026xx-10				
+	01	00B		2 Added TVO feature				
	02	00 2	2010/06/02	Release 105-C090XX-00 schematic;				
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