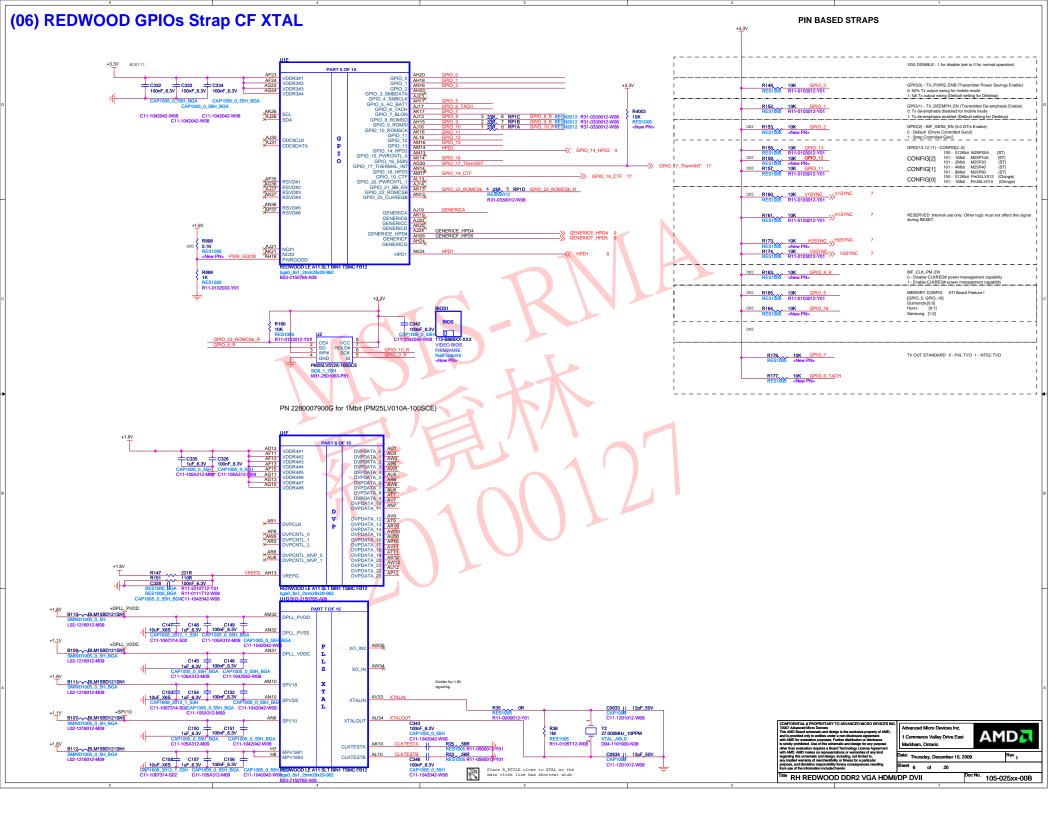
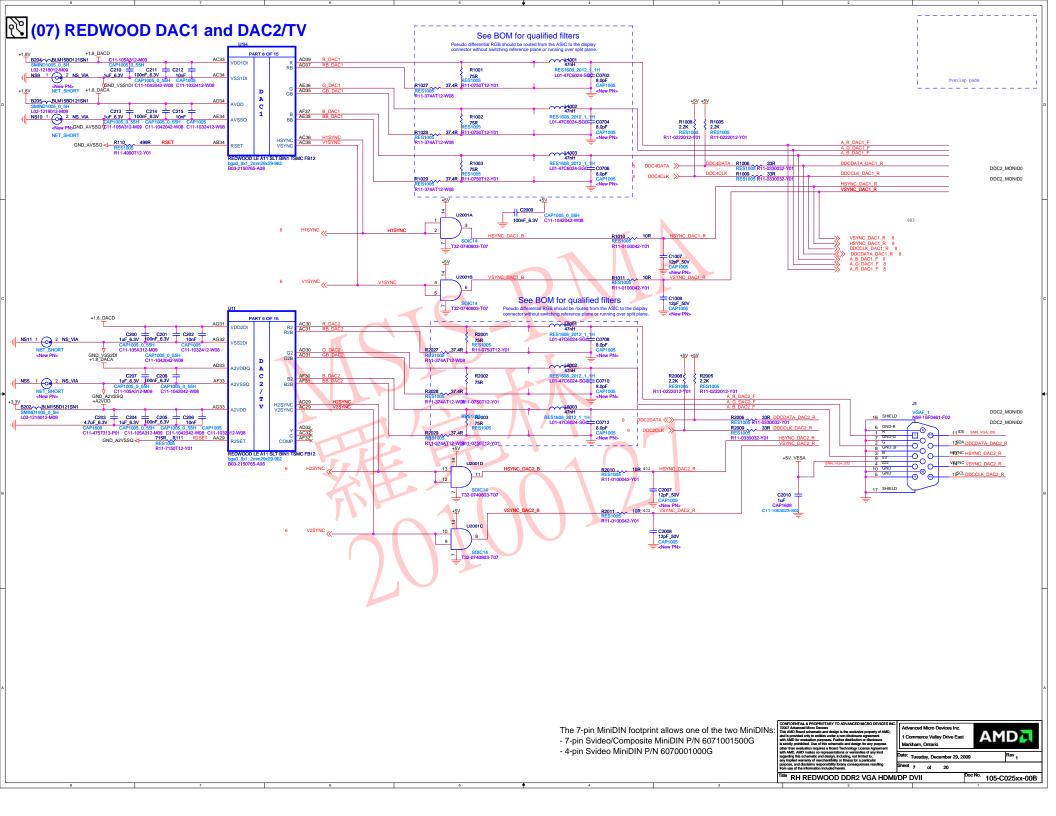


(4) DDR2 Ch A **CHANNEL A: 64MX16/128MX16 DDR2** 3 DQA0 [31..0] 《X 3 MAA_BA_[2..0] >> DQ1: DQ1: DQ1: DQ1: DQ1: DQ1: DQ1: DQ9 DQ8 DQ7 DQ6 DQ5 DQ4 DQ3 DQ2 DQ1 DQ1 DQ15 DQ14 DQ13 DQ12 DQ11 DQ10 DQ9 DQ8 DQ7 DQ6 DQ5 DQ4 DQ3 DQ2 DQ1 DQ1 3 MAA_[13..0] > VDDQ1 VDDQ2 VDDQ3 VDDQ4 VDDQ5 VDDQ6 VDDQ7 VDDQ8 VDDQ1 VDDQ: VDDQ: VDDQ: VDDQ: VDDQ: VDDQ: VDDQ: VDDQ: VDDQ: VDDQ9 VDD1 VDD2 VDD3 VDD4 VDD5 Overlap R2201 30R R2202 30R R2203 0R VDDL VSSDL VDDL VSSDL VDDI IND1005_0_RES1005 = C2200 = C2201 100nF_6.3V 1uF_6.3V ew PN> R11-0000012-1 C2206 — C2207 100nF_6.3V 1uF_6.3V C2203 100nF_6.3V VSSQ1 VSSQ2 VSSQ3 VSSQ4 VSSQ5 VSSQ6 VSSQ7 VSSQ8 VSSQ9 VSSQ10 VSSQ1 VSSQ2 VSSQ3 VSSQ4 VSSQ5 VSSQ6 VSSQ7 VSSQ8 VSSQ9 VSSQ10 VSSQ1 VSSQ2 VSSQ3 VSSQ4 VSSQ5 VSSQ6 VSSQ7 VSSQ8 VSSQ9 VSSQ10 R201 4.99K R203 4.99K R205 4.99K R207 4.99K S1005 R208 4.99K C2202 100nF_6.3V C2205 100nF_6.3V VSS1 VSS2 VSS3 VSS4 NC#A2 NC#E2 NC#R3 NC#R7 NC#R8 VSS1 VSS2 VSS3 VSS4 VSS5 C2211 100nF_6.3V VSS: VSS: VSS: VSS: VSS: C11-1032412-W08 R220 4.99K 4-4991T1 R210 4.99K 4.991T12-Y01 C245 10nF R11-0560012-Y01 RES1005 C131) 1uf 6.3V C1310 1uf 6.3V C1300 1uf 6.3V C1530 1uf 6.3V C1537 1uf 6.3V C1537 1uf 6.3V C1316 1UF_6.3V C1347 1uF_6.3V C1326 1uF_6.3V C1346 1uF_6.3V C1347 1uF_6.3V C1349 1UF_6.3V C1349, 1uF 6.3V C1329, uuF 6.3V C1329, uuF 6.3V C1349, uuF 6.3V C1549, uuF 6.3V C1549, uuF 6.3V C1366 1UF_6.3V C1367 1UF_6.3V C1368 1UF_6.3V C1369 1uF_6.3V C1344 1uF_6.3V C1364 1uF_6.3V C1359 10F_6.3V C1351 10F_6.3V C1362 10F_6.3V C1366 10F_6.3V C1324 10F_6.3V C1373 | 1uF_6.3V C1372 | 1uF_6.3V C1371 | 1uF_6.3V C1376 | 1uF_6.3V C1377 | 1uF_6.3V tMYPRos o +MVDD C1370 | 10F_63V C1369 | 10F_63V C1364 | 10F_63V C1364 | 10F_63V C1360 | 10F_63V C1360 | 10F_63V C1339 C1336 C1334 Overlap cap pair foorprints (0805 with 0603) DNI **AMD** foorprints (0805 with 0603) arkham Ontario See BOM for Qualified Option Overlap_cap pair_ I foorprints (0805 with 0603) Monday, December 28, 2009 RH REDWOOD DDR2 VGA HDMI/DP DVII 105-025xx-00B

(5) DDR2 Ch B **CHANNEL B: 64MX16/128MX16 DDR2** DQ15 DQ14 DQ13 DQ12 DQ11 DQ10 DQ9 DQ8 DQ7 DQ6 DQ5 DQ4 DQ3 DQ2 DQ1 DQ0 VDDQ1 VDDQ2 VDDQ3 VDDQ4 VDDQ5 VDDQ6 VDDQ7 VDDQ8 VDDQ9 VDDQ10 VDDQ1 VDDQ2 VDDQ3 VDDQ4 VDDQ5 VDDQ7 VDDQ8 VDDQ8 VDDQ1 VDDQ1 VDDQ2 VDDQ3 VDDQ4 VDDQ5 VDDQ6 VDDQ7 VDDQ8 VDDQ9 VDDQ10 VDDQ1 VDDQ2 VDDQ3 VDDQ4 VDDQ5 VDDQ6 VDDQ7 VDDQ8 VDDQ9 VDDQ1(RAS VDD1 VDD2 VDD3 VDD4 VDD5 VDDL VSSDL SMIND1005_0_5HRE\$1005 — C2303 — C2304 100nF_6.3V 1uF_6.3V New PN> R11-0000 VSSQ1 VSSQ2 VSSQ3 VSSQ4 VSSQ5 VSSQ6 VSSQ7 VSSQ8 VSSQ9 VSSQ10 +MVDD R301 4.99K VREF_U3 RES1005 R306 4.99K R11-4991T12-Y01 RES1005 R302 4.99K C2305 100nF_6.3V CAP100F_0 R304 4.99K R308 4.99K VSS1 VSS2 VSS3 VSS4 VSS5 VSS1 VSS2 VSS3 VSS4 VSS5 C2587 100nF_6.3V VSS: VSS: VSS: VSS: VSS: NC#A2 NC#E2 NC#R3 NC#R7 NC#R8 NC#A2 NC#E2 NC#R3 NC#R7 NC#R8 C344 10nF R309 4.99K R319 4.99K R323 1-6660012-Y0 RBS1005 R310 4.99K R320 4.99K R324 R5S 56R R11-0560012-1 RES1005 C150) | IUF.6.3V C1505, IUF.6.3V C1529, IUF.6.3V C1519, IUF.6.3V C1519, IUF.6.3V C1559, IUF.6.3V C1559, IUF.6.3V C1559, IUF.6.3V C1622 tuF 6.3V C1492 tuF 6.3V C1492 tuF 6.3V C1609 tuF 6.3V C1507 tuF 6.3V C1507 tuF 6.3V C1509 tuF 6.3V C1499 1UF_6.3V C1511 1UF_6.3V C1510 1UF_6.3V C1509 1UF_6.3V C1523 1UF_6.3V C1495 1uF_6.3V C1494, 1uF.6.3V C1509, 1uF.6.3V C1504, 1uF.6.3V C1504, 1uF.6.3V C1504, 1uF.6.3V C1504, 1uF.6.3V C1505, 1uF.6.3V C1505, 1uF.6.3V C1536 1uF_6.3V Overlap cap pair foorprints (0805 **AMD** Overlap cap pair See BOM for Qualified Option foorprints (0805 with 0603) foorprints (0805 with 0603) _ _ RH REDWOOD DDR2 VGA HDMI/DP DVII 105-025xx-00B

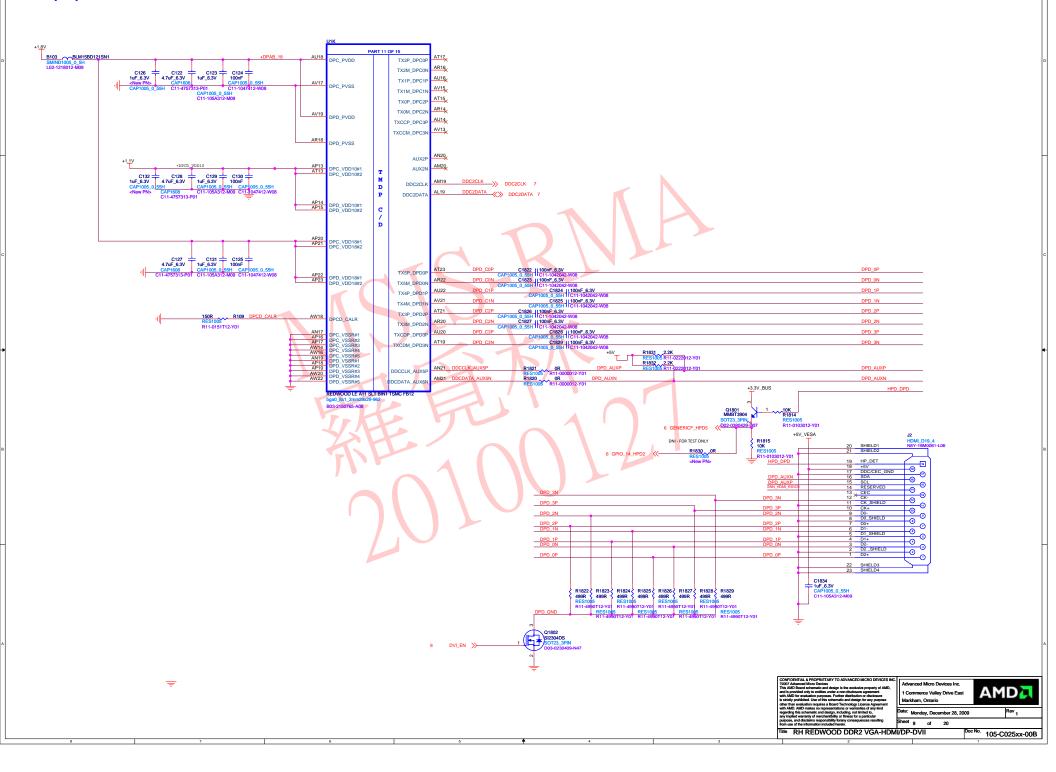


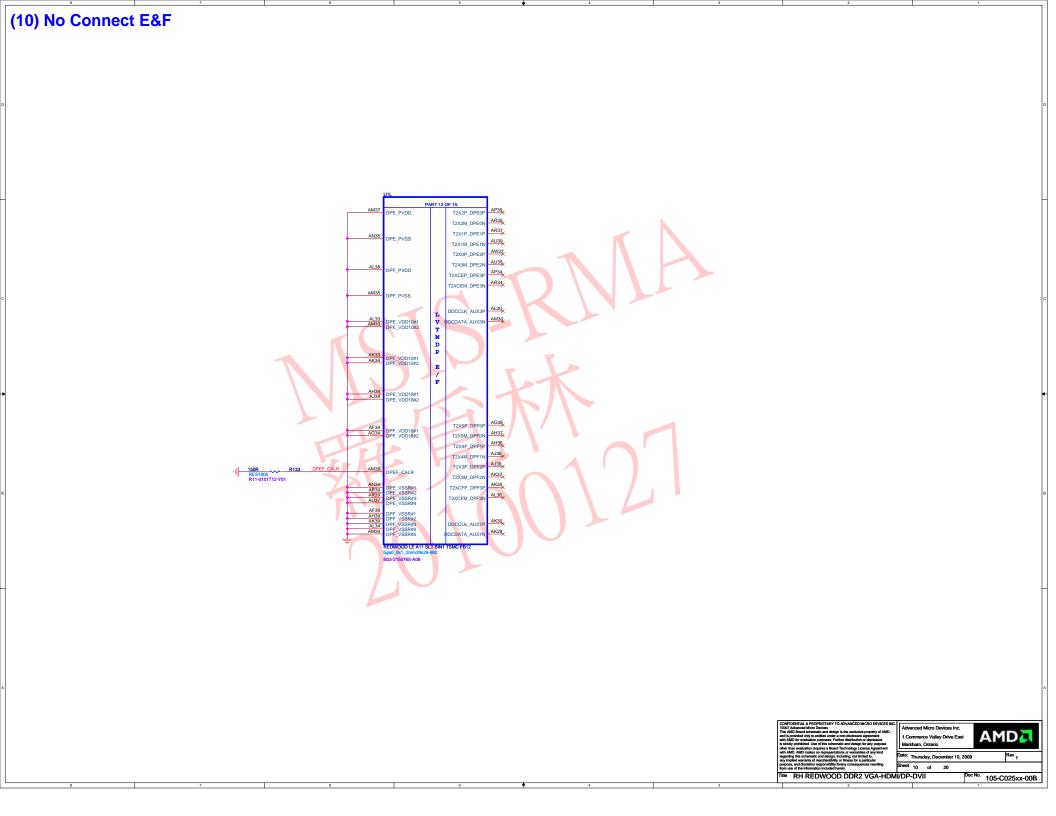


(08) REDWOOD TMDS A&B +DP_PVDD PART 10 OF 15 R1120 499R RES1005 R11-4990T12-Y01 B102 BLM15BD121SN1 DPA_TX2P C1120 | 100nF_6.3V CAP1005 0 55H | C11-1042042 DPA PVDD C1121 100nF_6.3V CAP1005 0 55H C11-1042042-W0 ABTX2M L02-1218012-M09 DPA TX2N TX2M DPA01 C103 = 1uF_6.3V DPA_TX1P_C1122 | 100nF_6.3V CAP1005_0_55H | C11-1042042 AV27 DPA_TX1N C1123 | 100nF_6.3V DPA_TX0P C1124 | 100nF_6.3V CAP1005_0_55H | C11-1042042 DPA_TX0N C1125 100nF_6.3V TX0M_DPA21 AV29 DPB_PVDD CAP1005_0_55H IIC1 DPA_TXCARC1126 ||100nF_6.3V CAP1005_0_55H ||C11-1042042-W08 DPA_TXCAN ||C1127 ||10 12-YABTXCP TXCAP DPA3F ____ C1127 | 100nF_6.3V CAP1005_0_55H | C11-1042042 ABTXCM TXCAM_DPA3N R1127 499R R11-4990T12-Y01 AR28 DPB_PVSS <u>~</u>~6 AUX1P AL27 AUX1N C112 4.70E 8 C109 C110 100F CAP1005_0 55H CAP105_0 C1010 = 1uF CAP1608 C11-1053923-S02 AM26 DDC1CLE ~ 19 AN26 R1022 ~~ 10K 10K HSYNC_DAC1_R C105 — 1uF_6.3V AT33 DPB_TX5P C1132 | 100nF_6.3V | CAP1005_0_55H | C11-1042042 TX5P_DPB0F M09 CAP1005_0_55H AP25 C11-1047412-W08 AP26 DPB_VDD18#1 DPB_VDD18#2 C1133 100nF_6.3V TX5M DPB0 TX4P DPB1 C1135 | 100nF_6.3V TX4M_DPB1N DPB_TX3P C1136 | 100nF_6.3V | CAP1005 0 55H | C11-1042042-W 150R R130 DPAB_CALR C1137 | 100nF_6.3V R11-0151T12-Y01 TXCBP_DPB3P TXCBM_DPB3N AT29 +12V_BUS R849 10K DPA_VSSR#4 DPA_VSSR#5 DPB_VSSR#1 DPB_VSSR#2 DPB_VSSR#3 DPB_VSSR#4 DPB_VSSR#5 Q1110 SI2304DS SOT23_3PIN D03-0220400 DDC4CLK 7 DDC4DATA 《》 DDC4DATA 7 AM29 C843 100NF REDWOOD LE A11 SLT BIN1 TSMC FB1

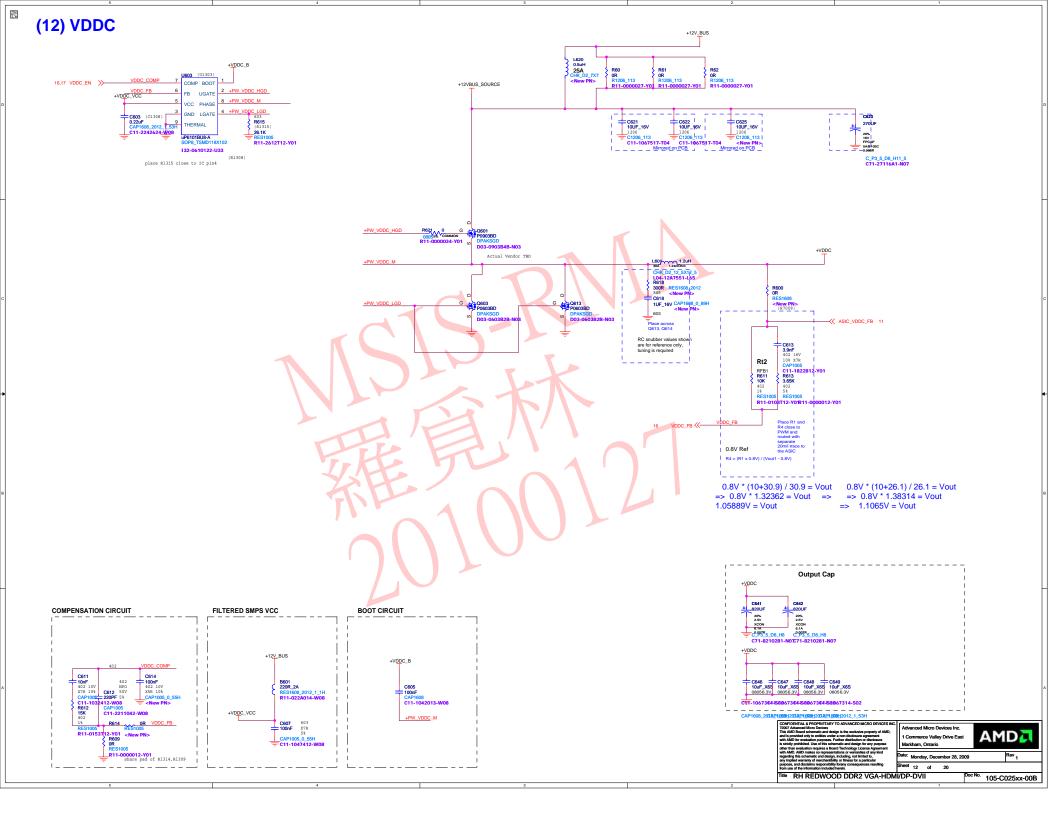


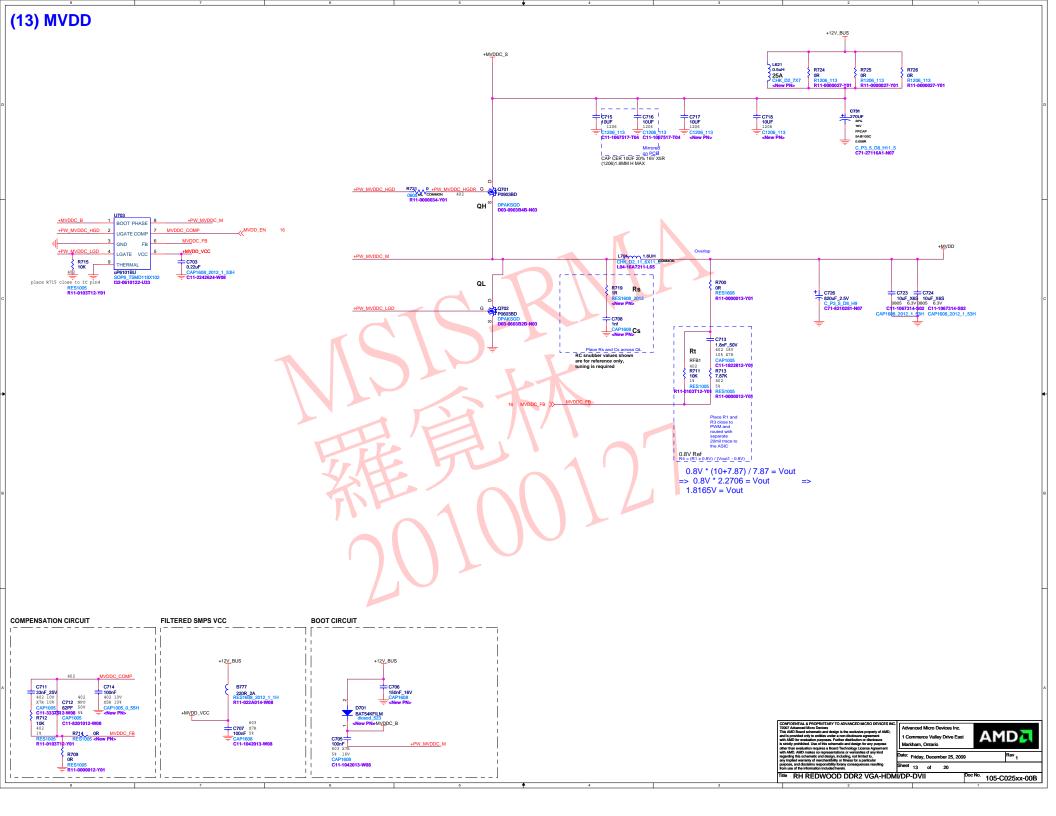
(09) REDWOOD TMDS C&D



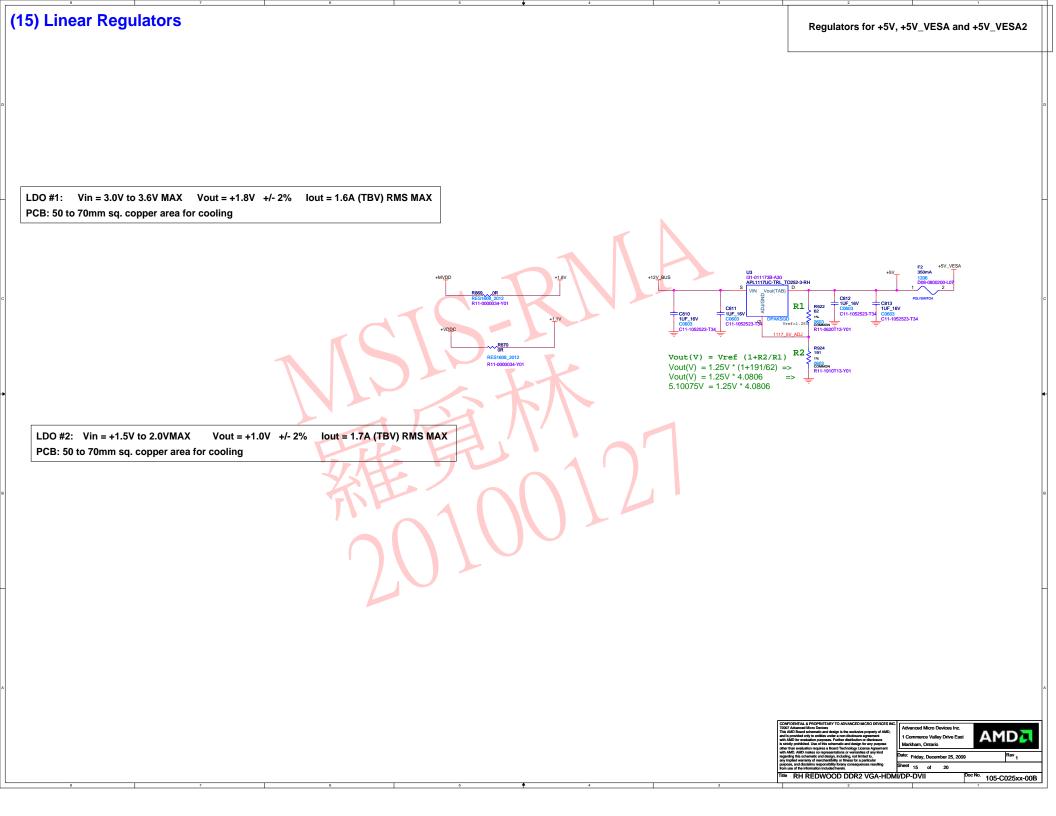


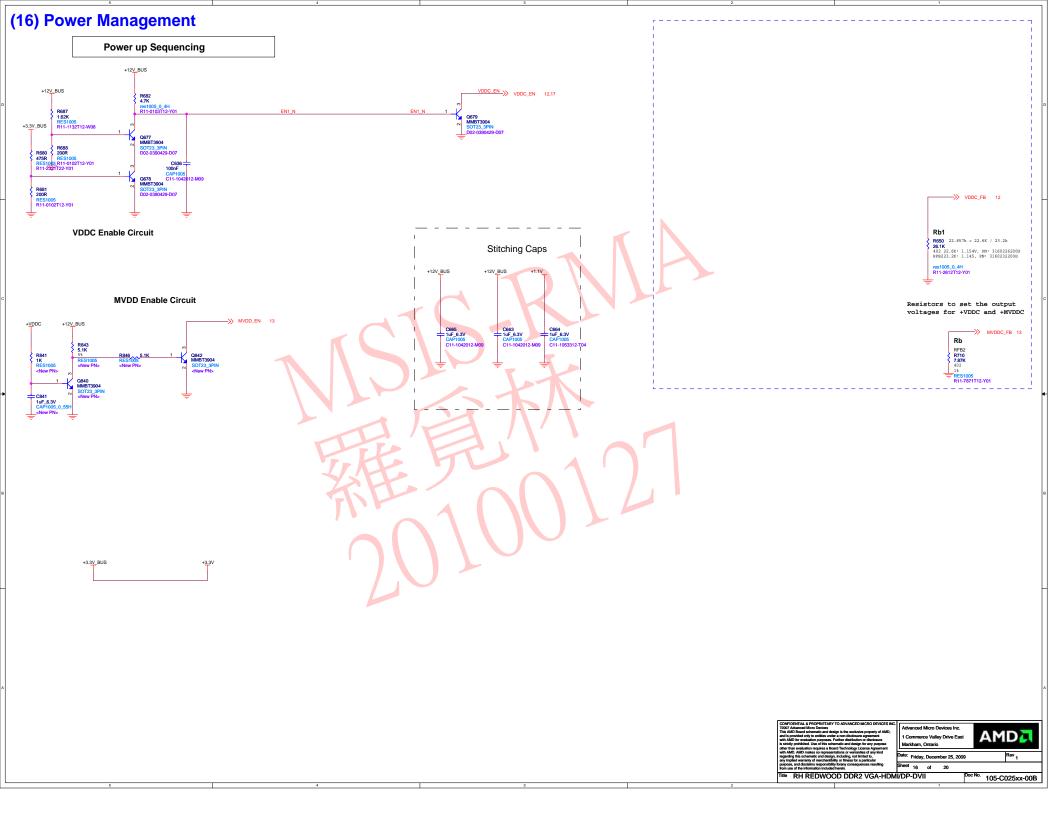
(11) REDWOOD Power & GND Swap With C1225, C1226, C1227, C1228, C1229, C1230, C1231, C1232, C1233, C1272, C1273, C1274 GND#2 GND#3 GND#4 GND#5 GND#6 GND#7 GND#1 GND#12 GND#14 GND#15 GND#16 GND#16 GND#16 GND#16 GND#16 GND#19 GND#22 GND#22 GND#22 GND#22 C280 C281 1uF_6.3V 1uF_6.3V CAPTIONS (0. SET BG) CAPTIONS C282 1uF_6.3V VDDR1#12 VDDR1#13 VDDR1#14 C01-106A912-M09 C11-106A912-M09 C11-106A912-M0 VDDR1#15 VDDR1#16 VDDR1#17 VDDR1#18 VDDR1#19 VDDR1#20 VDDR1#21 VDDR1#21 C11-105A312-M09 C2413 1uF_6.3V C2267 1uF_6.3V CAP1005_0_55H_BGA C11-105A312-M09 Overlap cap pair foorprints (0805 with 0603) GND#23 GND#24 GND#25 GND#26 GND#27 GND#28 GND#29 GND#30 GND#31 GND#32 GND#33 GND#34 VDDR1#23 VDDR1#24 VDDR1#25 VDDR1#25 VDDR1#26 VDDR1#27 VDDR1#28 VDDR1#29 VDDR1#30 VDDR1#31 VDDR1#32 VDDR1#33 CAPTIOS 0.584 BGA CAPTIOS 0.585 BGA CAPTIOS 0.58 VDDR1#34 VDDC#3 GND#35 GND#36 GND#37 GND#38 GND#39 GND#40 GND#42 GND#45 GND#46 GND#46 GND#46 GND#47 GND#47 GND#47 GND#48 GND#53 GND#53 GND#53 GND#53 GND#54 GND#55 GND#54 VDDC#3: VDDC#3: VDDC#3: VDDC#3: VDDC#4: VDDC#4: VDDC#4: VDDC#4: VDDC#4: VDDC#4: VDDC#4: VDDC#5: VDDC#5 R4021__OR M20 RESIDOS ROA NC VSSRH SND#132 R4022_OR RES1005_BGA NC VSSRH GND#57 GND#58 GND#60 GND#60 GND#60 GND#63 GND#63 GND#66 GND#66 GND#66 GND#69 GND#70 GND#71 GND#72 GND#72 GND#73 GND#75 GND#75 GND#76 +VDD_CT +VDDCI +1.8V 1 C77 C75 C74 10F 6.3V 100nF 6.3V CAP1608_2012_1_53H C11-1067314-S02 GND#77 GND#78 GND#79 GND#80 GND#81 GND#82 GND#83 GND#84 GND#85 GND#86 GND#87 /DDCI#10 /DDCI#11 /DDCI#12 /DDC#12 /DDC#13 /DDC#14 /DDC#15 /DDC#16 /DDC#17 /DDC#18 /DDC#19 /DDC#20 NC_TS_A VSS MECH# DDCI#21 OR Under GPU RES1005_BGA <New PN> hna0 8x1 2mm29x29-962 **AMD** Markham Ontario Monday, December 28, 2009 RH REDWOOD DDR2 VGA-HDMI/DP-DVII No. 105-C025xx-00B

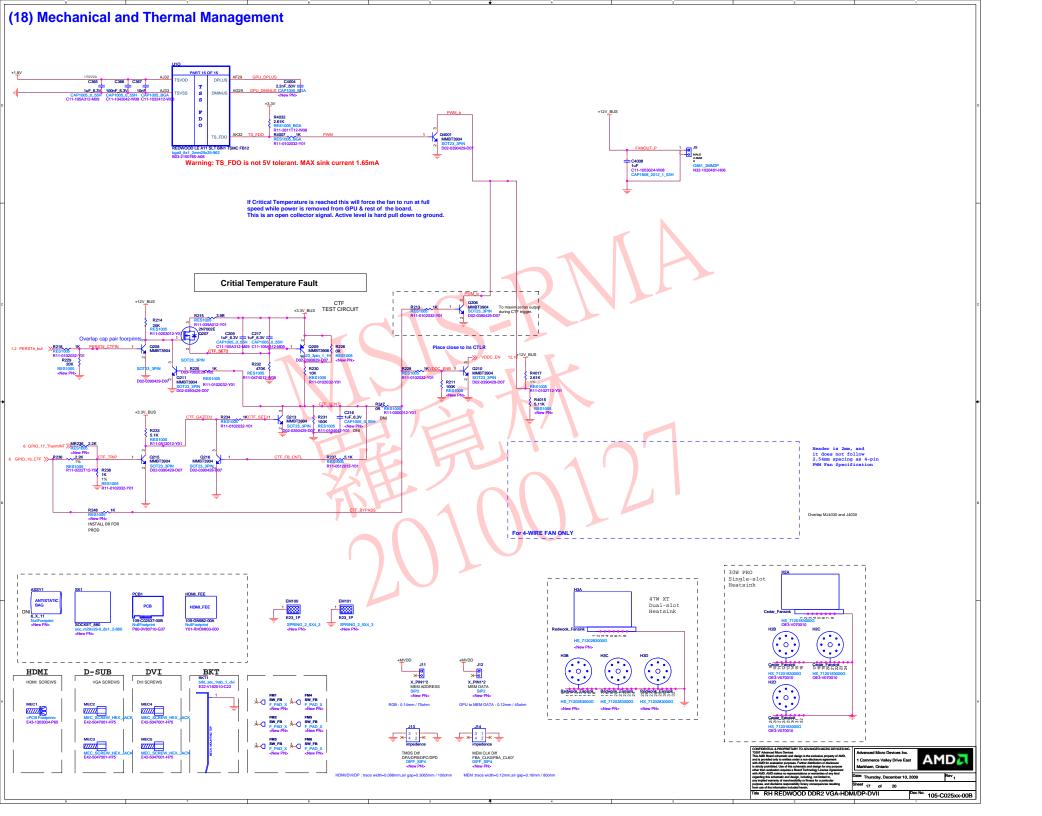


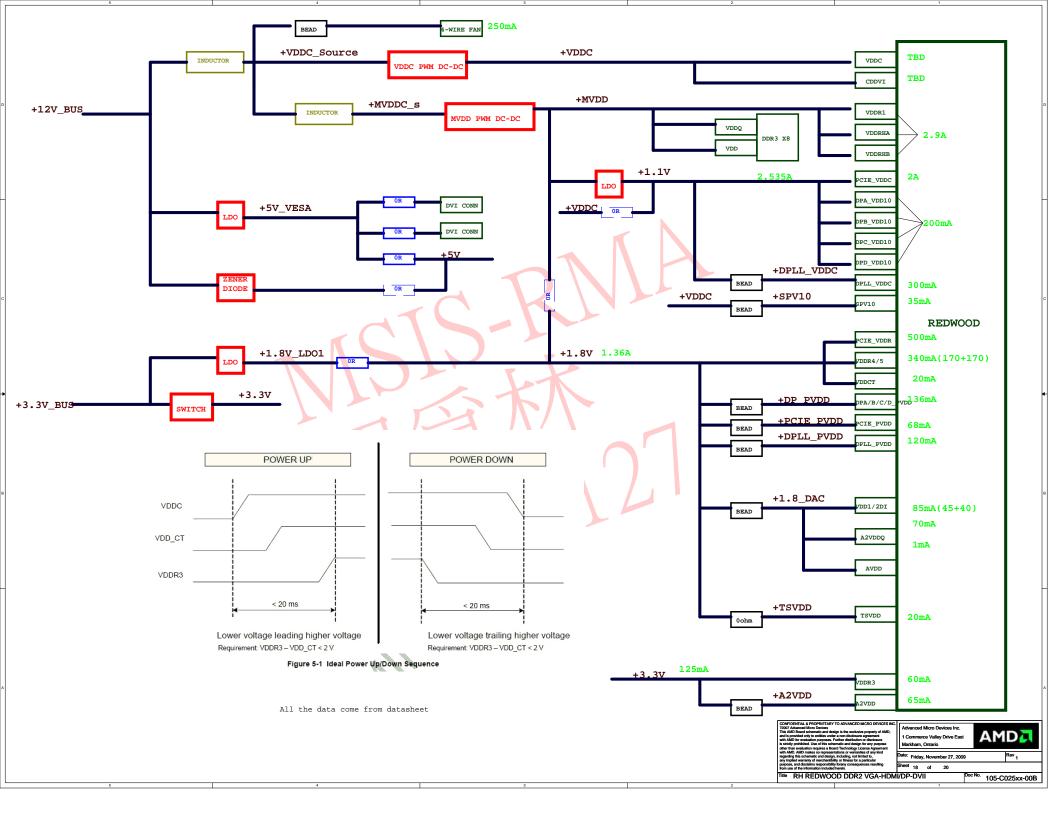


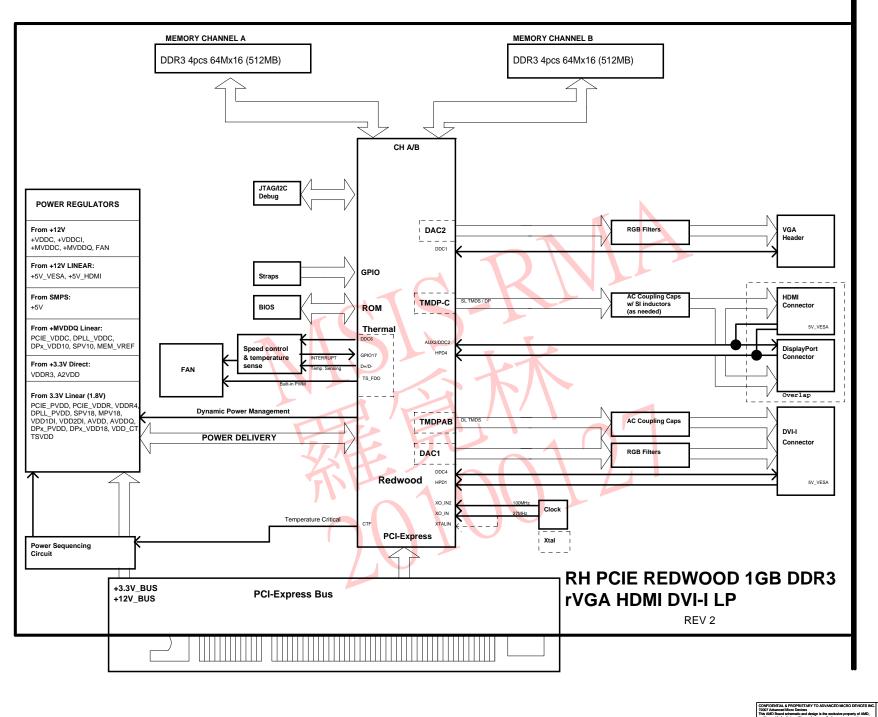














AMD				Title RH REDWOOD DDR2 VGA-HDMI-DVI		Schematic No. 105-025xx-00B	Date: Friday, November 27, 2009		
			REVISION HISTORY NOTE: This schematic represents the PCB, it does not represent any specific SKU. For Stuffing options (component values, DNI , ? please consult the product specific BOM. Please contact AMD representative to obtain latest BOM closest to the application desired.						
	Sch Rev	PCB Rev	Date						
	0	00A	00A 09/07/30						
	0	00B	09/09/25	25 Initial design for Redwood DDR2, VGA/DP HDMI/DP DVI/VGA					
C ►					SV OV			C ←	
4			5	4	3	2		4	