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## H81-M05 u/ ATX 2013.11-13

### CPU:

INTEL - Haswell LGA1150

### System Chipset

INTEL - Lynix Point H81

### OnBoard Chipsets

HD Audio Codec:ALC662  
LAN RTL8111E  
SIO ITE8772E  
FlashROM 64Mb SPI

### MAIN Memory

DDRIII 1066/1333MHz 2-DIMMs

### Expansion Slots

PCI Express x16 x1  
PCI Express x1 X1

### PWM

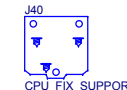
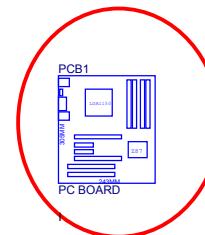
VRD12.5 Controller UPI1651 3 PHASE  
DDR UP1504T  
PCH OP+MOSFET/APL5930

### ACPI

UPI

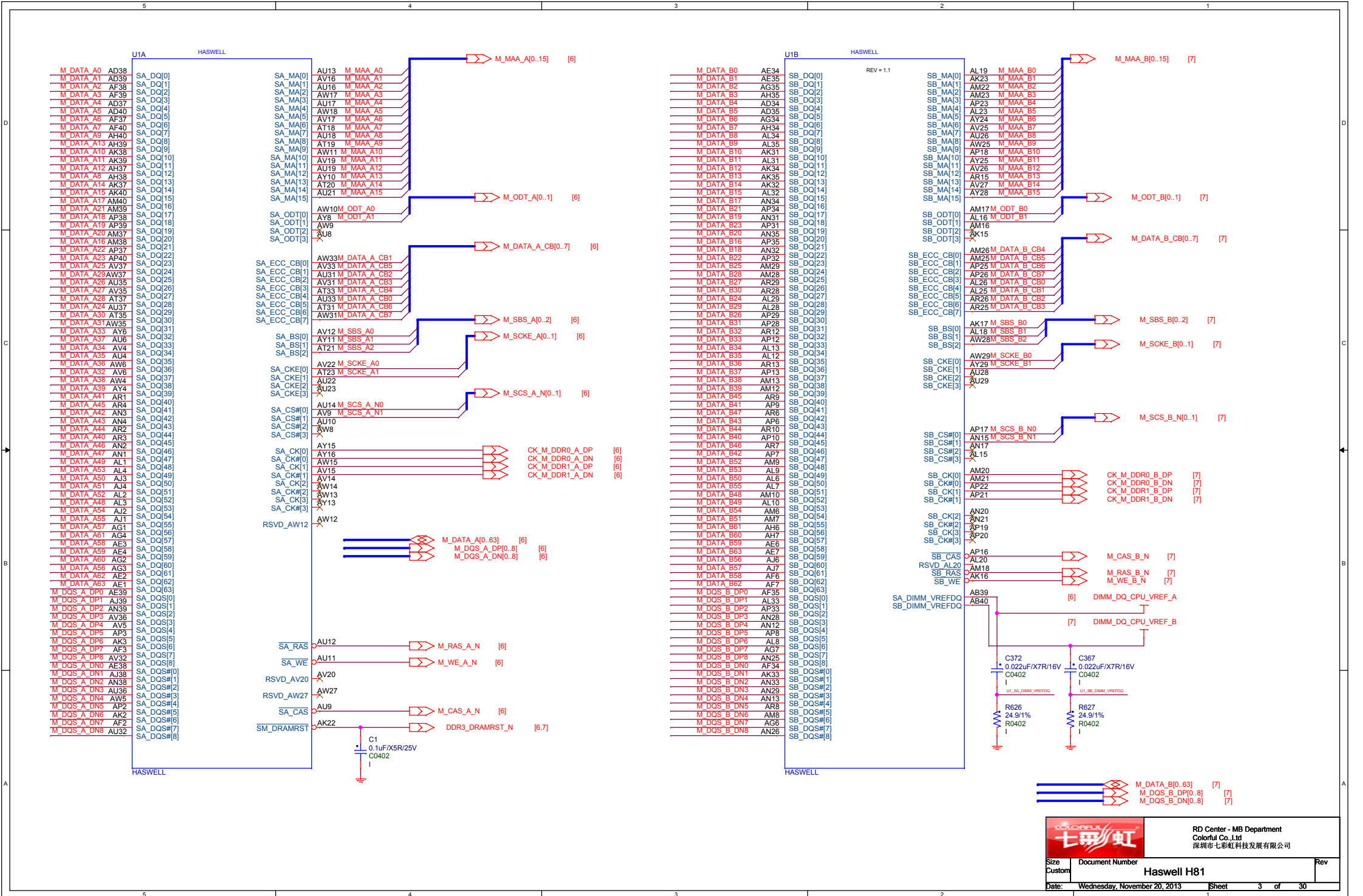
### Other

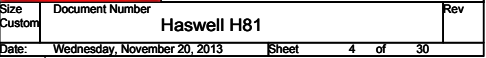
SATA3.0X4  
USB2.0 FX4+RX4  
USB3.0 RX2  
HDMIX1  
D-SUB  
COM HEADERX1

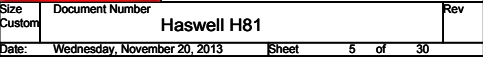


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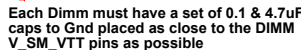




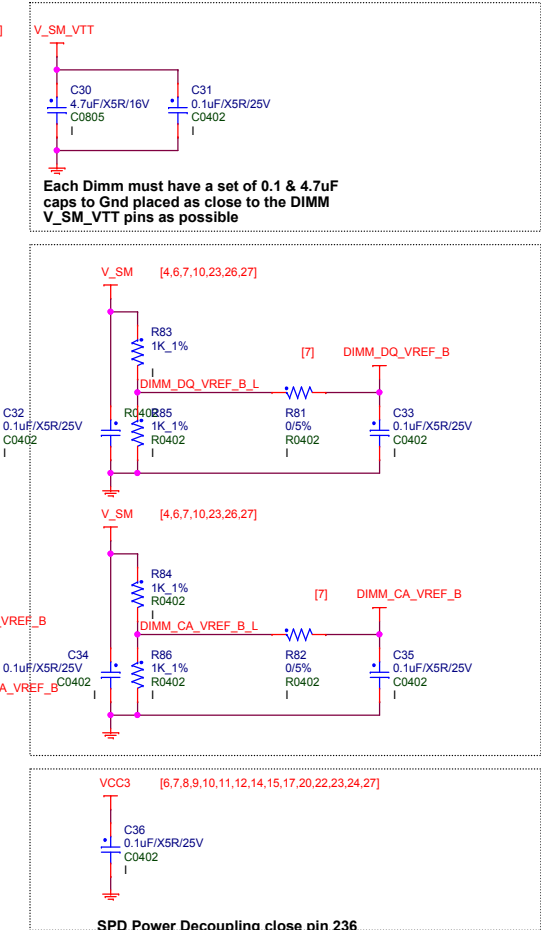
### DDR3 CHA DIMM1 Socket

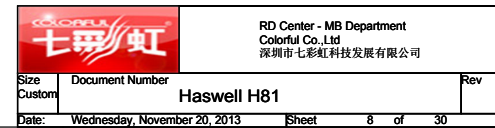


SMBUS ADDRESS: A0



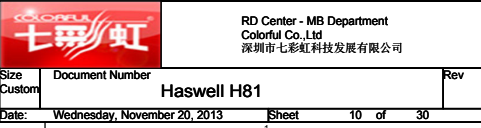
SPD ADDRESS: 010  
SMBUS ADDRESS: A4





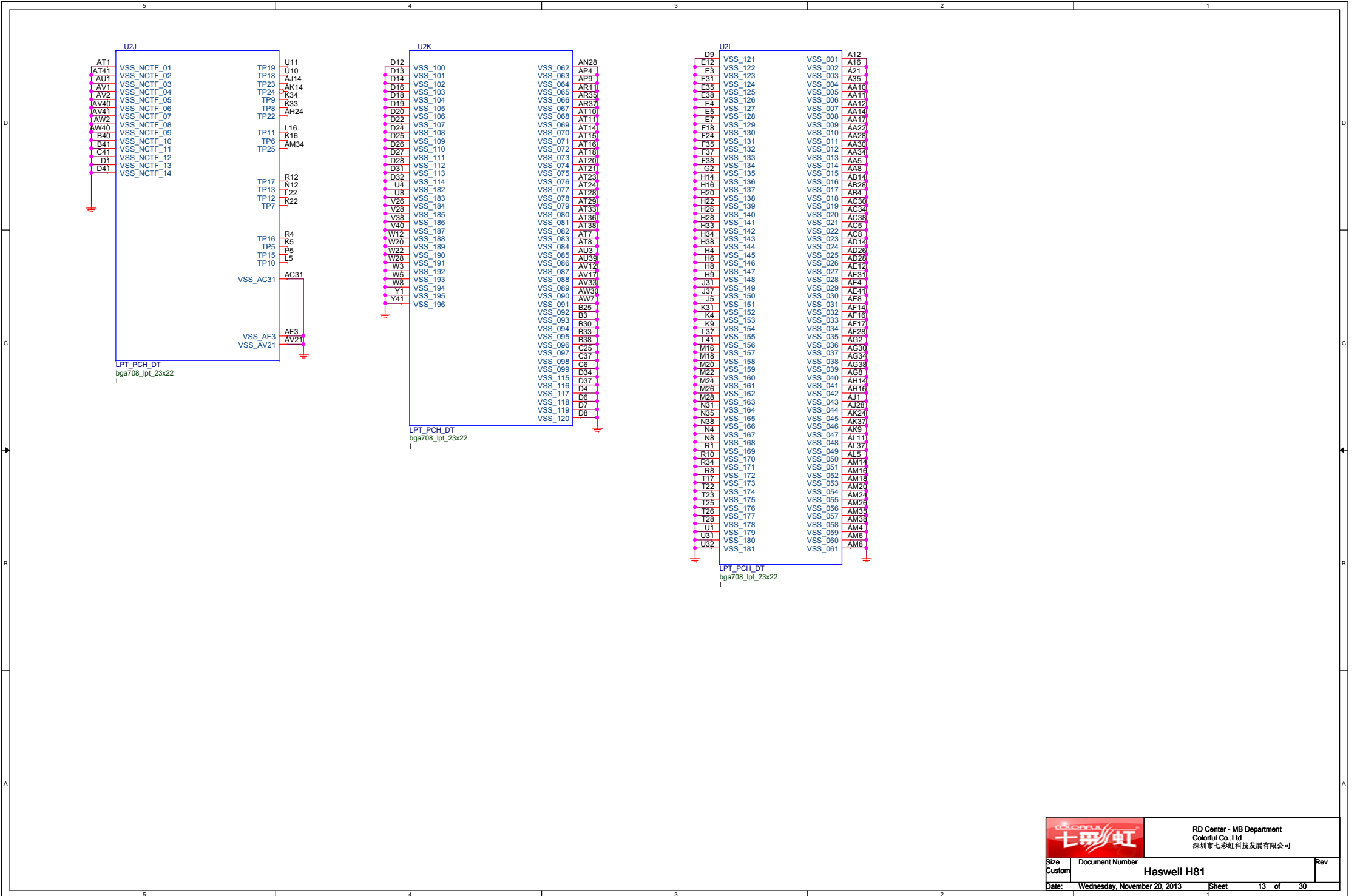




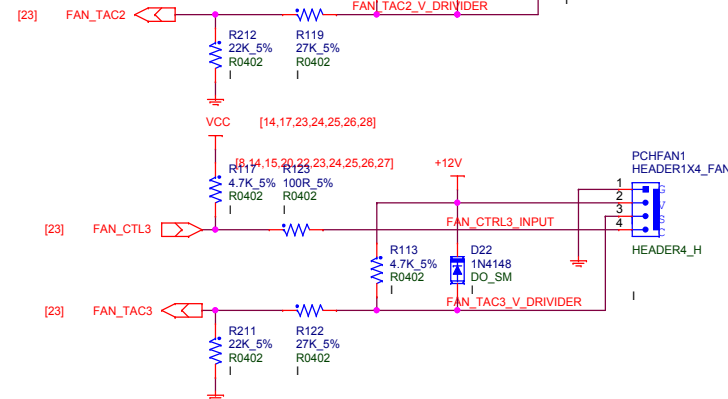
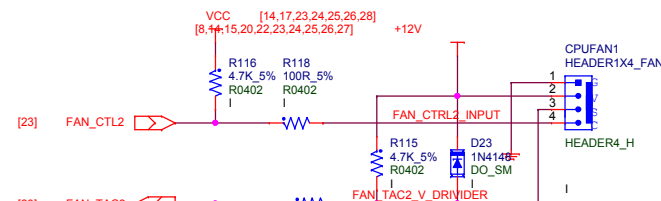
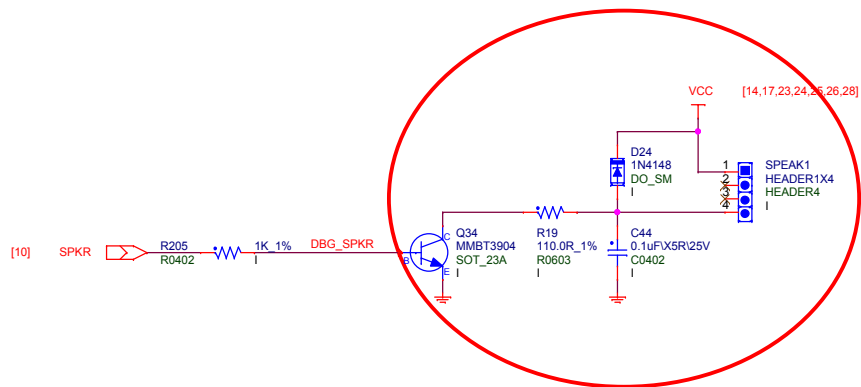
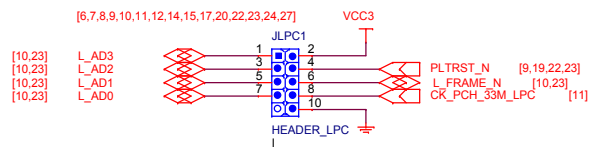
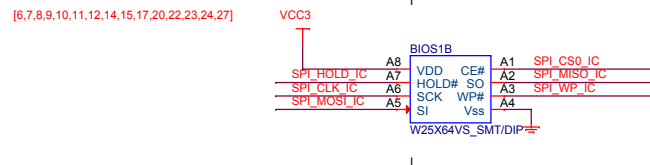
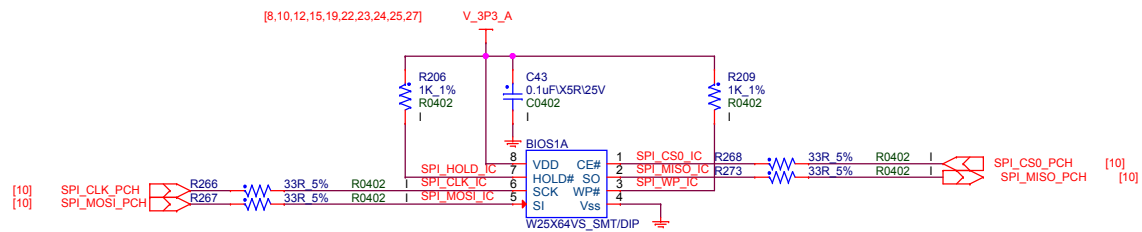






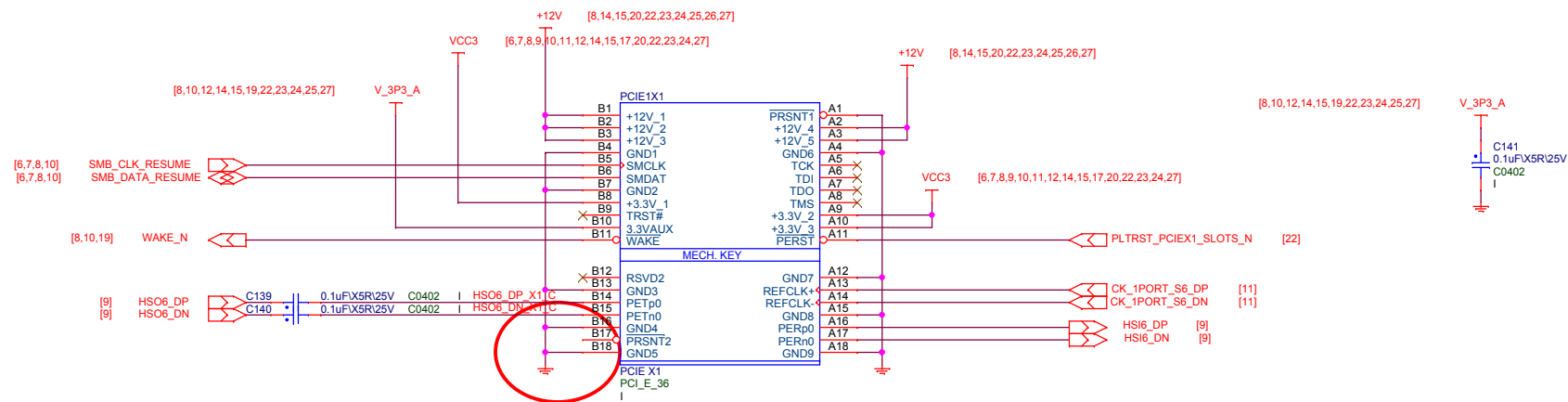




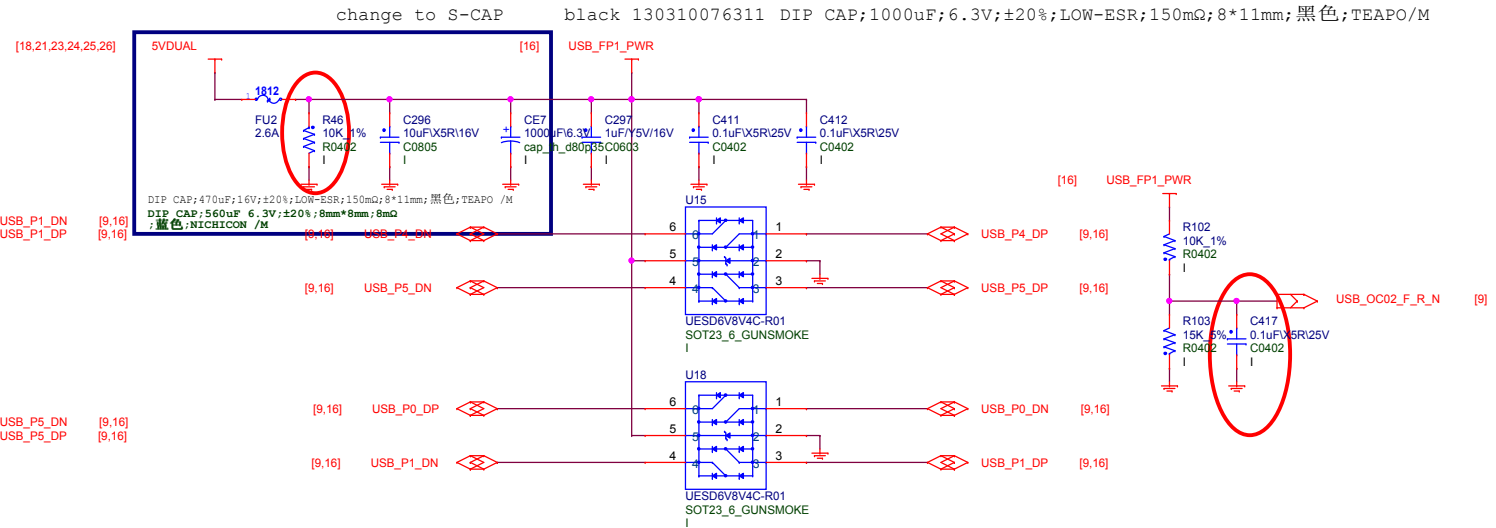
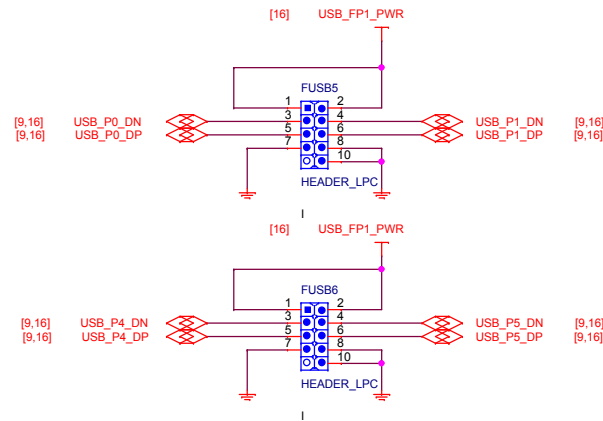


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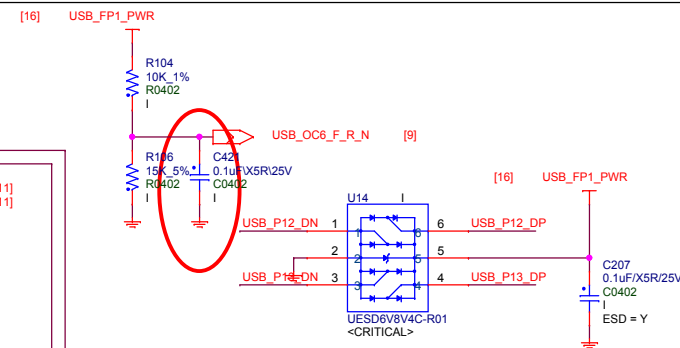
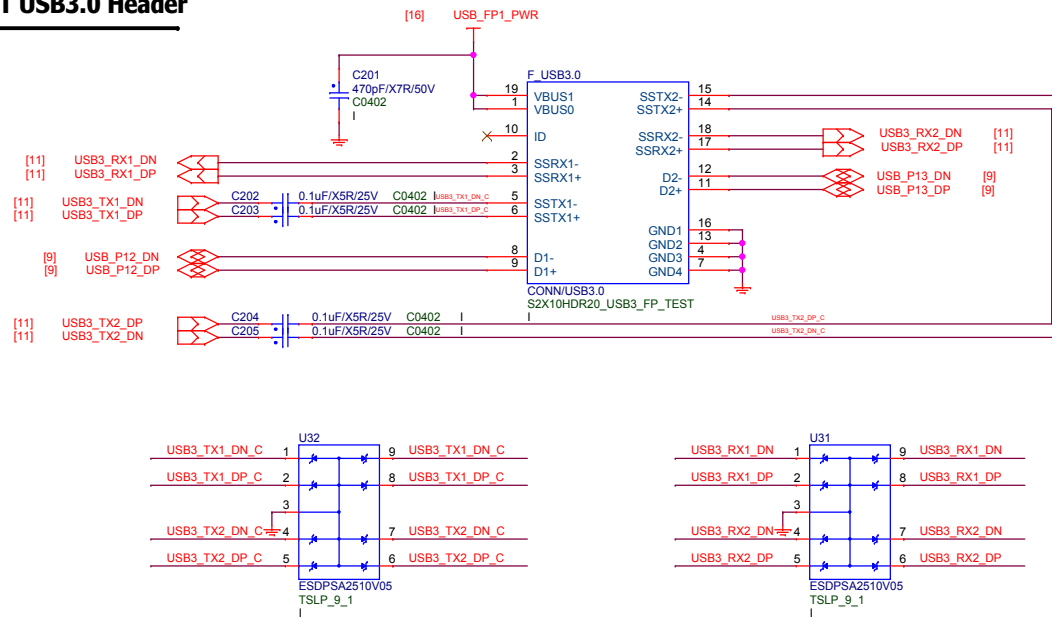
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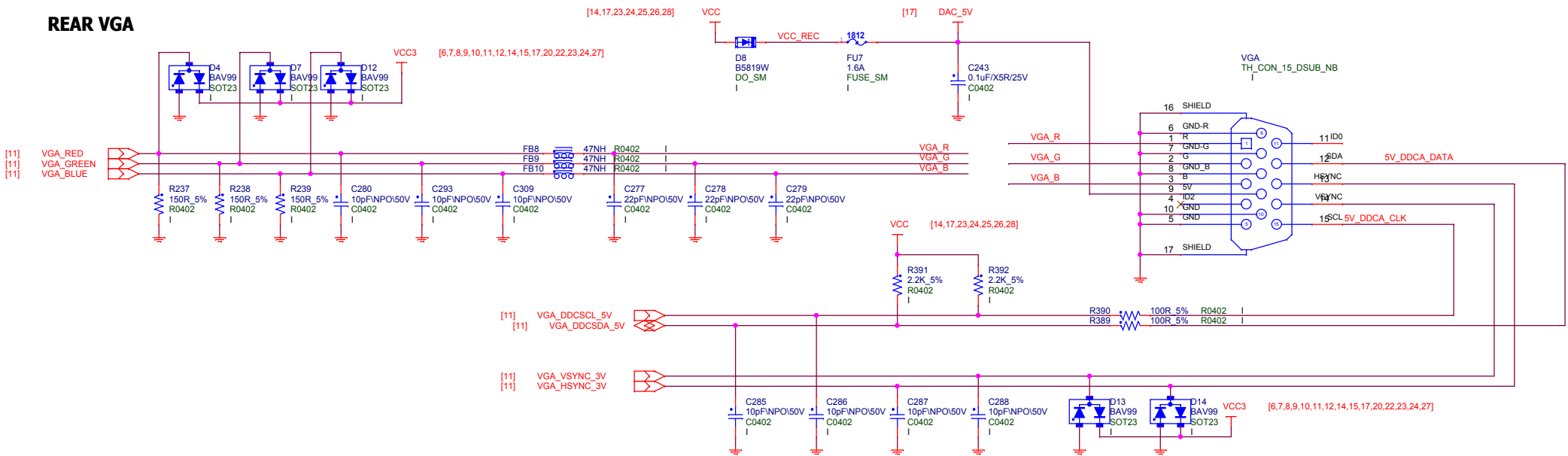
## FRONT USB2.0



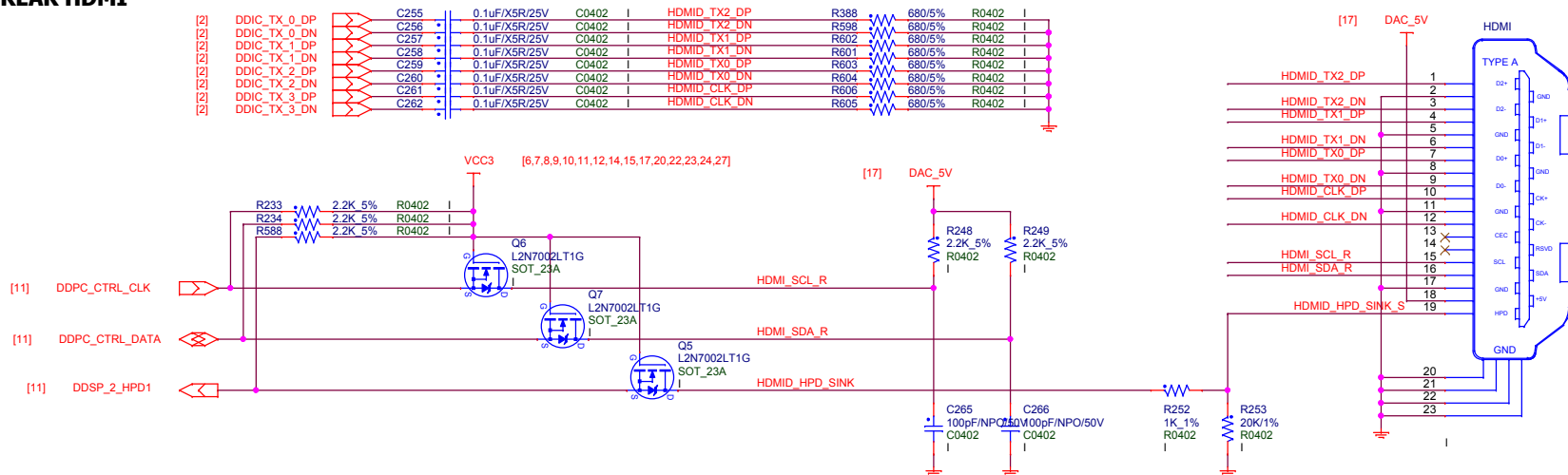
### FRONT USB3.0 Header



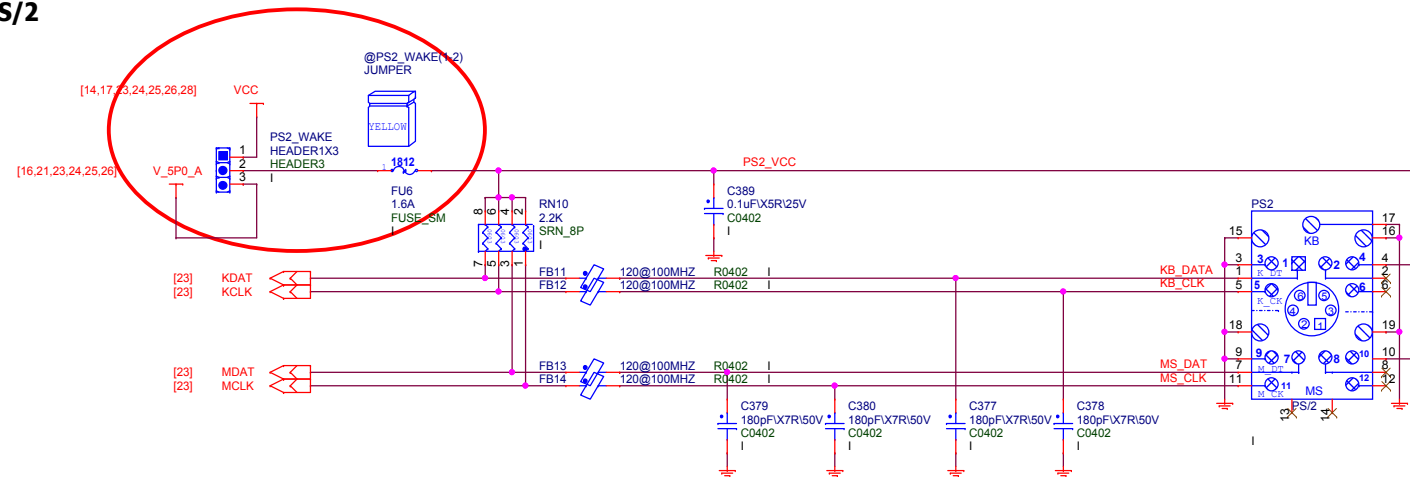
# REAR VGA



# REAR HDMI

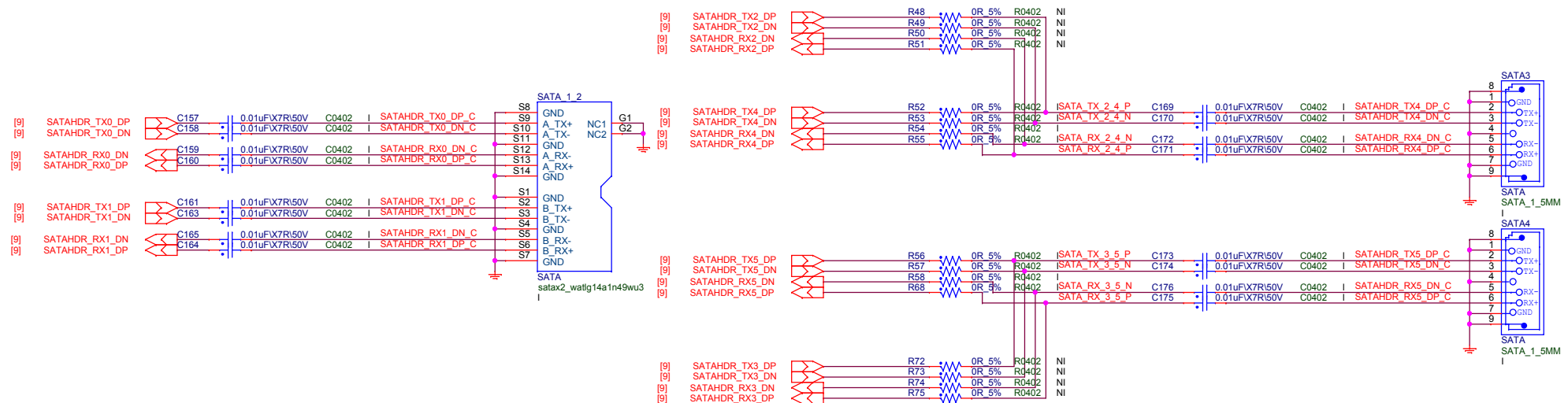


## REAR IO PS/2



**SATA3.0 1-4**

**BOM NOTE: FOR H81 using port 4/5  
FOR B85,using port 2/3**

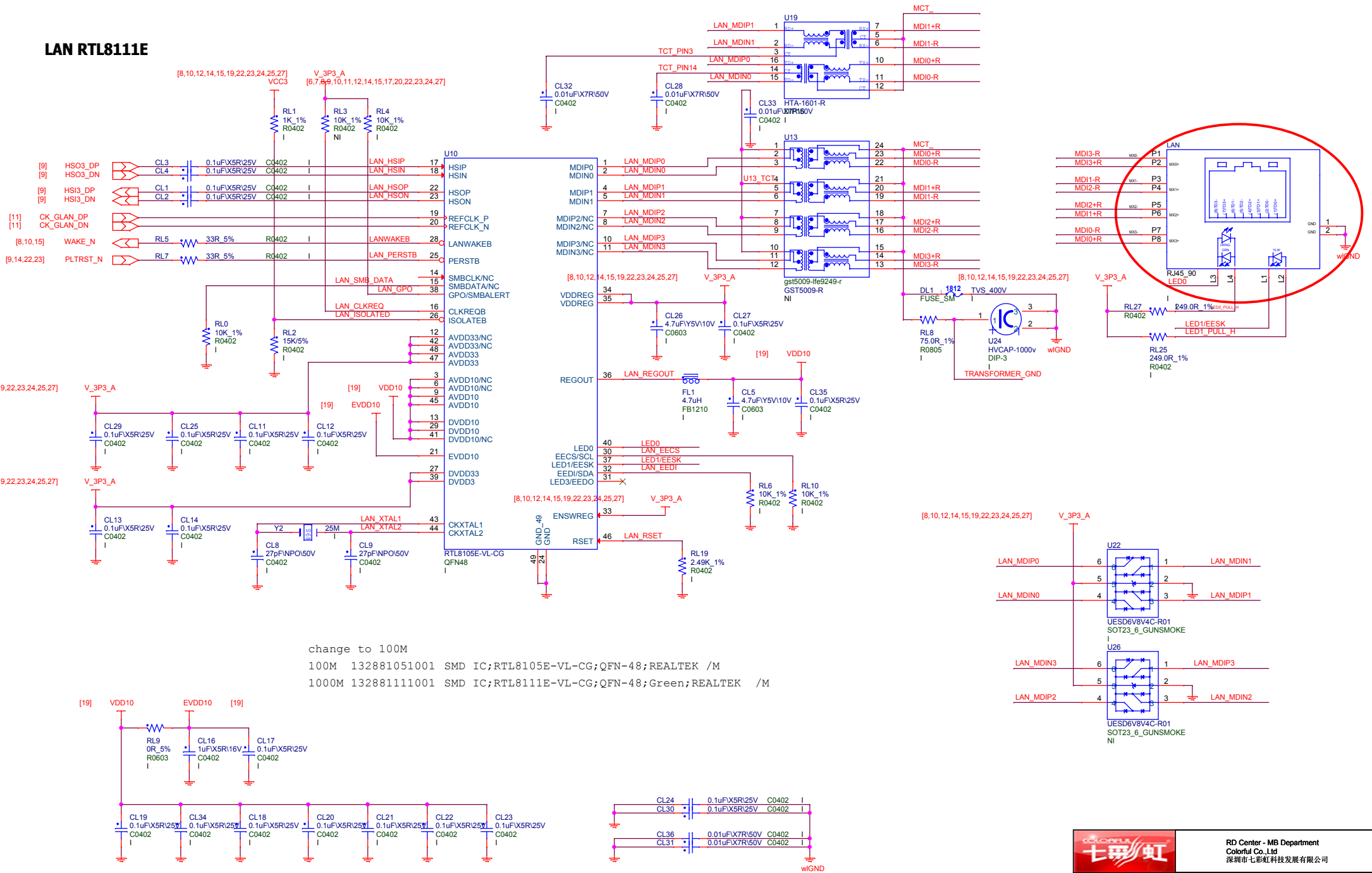


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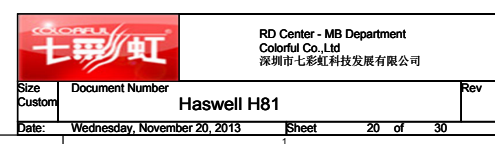
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## LAN RTL8111E



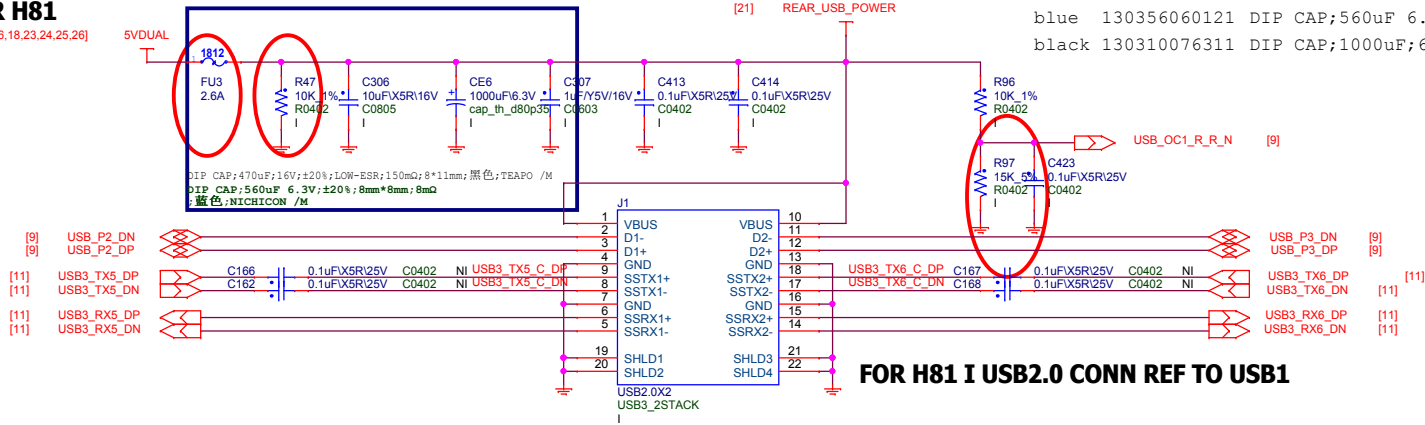
```
[10] AUD_LINK_BCLK
[10] AUD_LINK_SYNC
[10] AUD_LINK_RST
[10] AUD_LINK_SDO
[10] AUD_LINK_SDI0
```



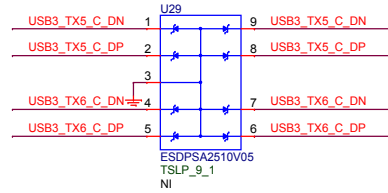
REAR USB 3.0 FOR B85

REAR USB 2.0 FOR H81

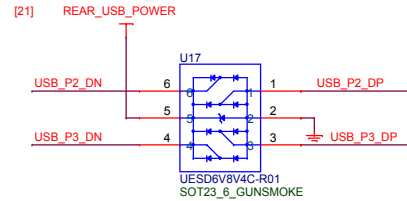
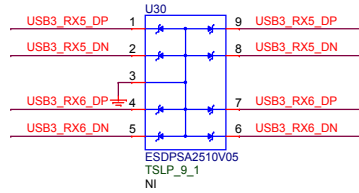
[16,18,23,24,25,26]



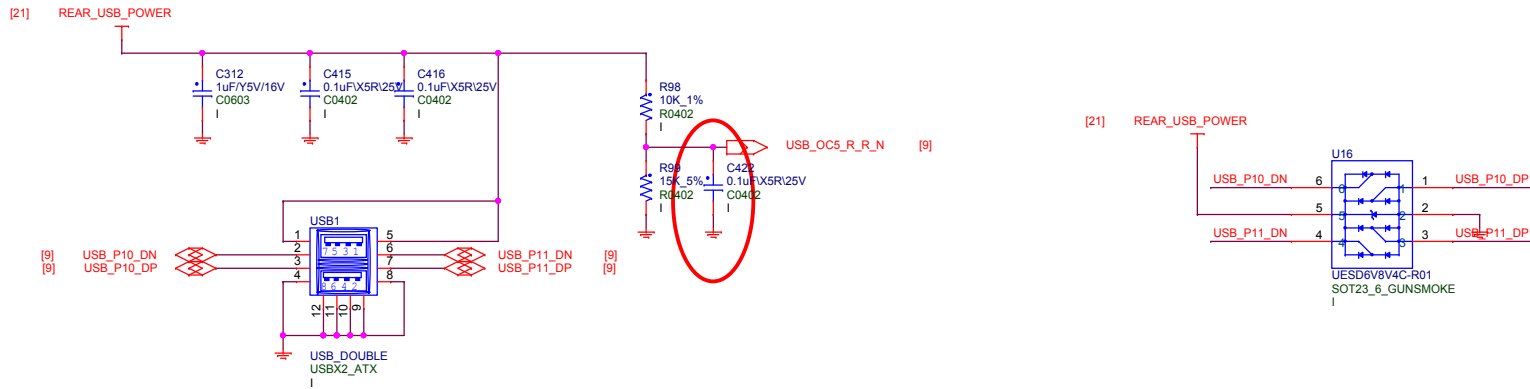
FOR H81 NI



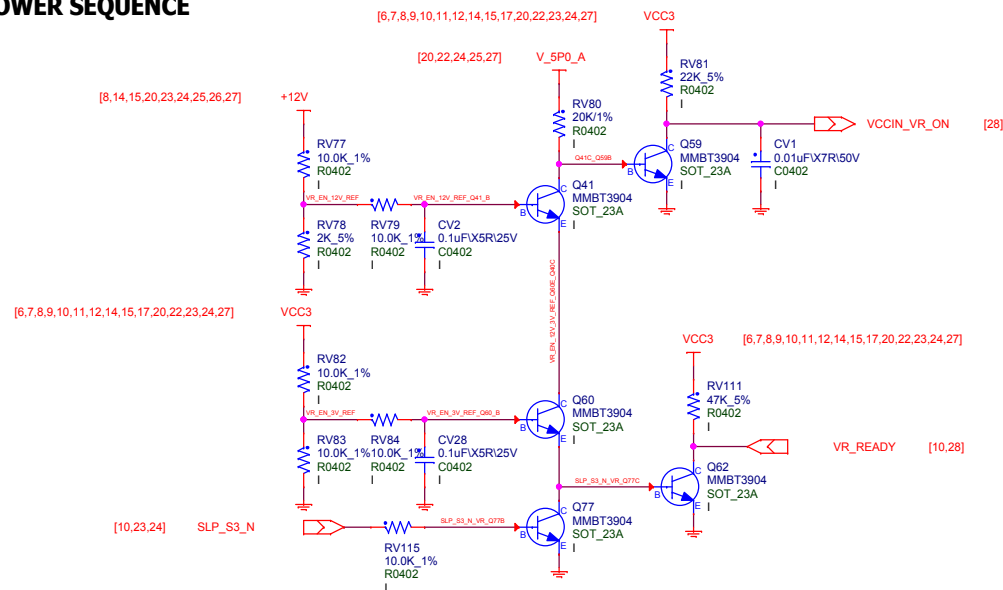
FOR H81 NI



REAR USB 2.0



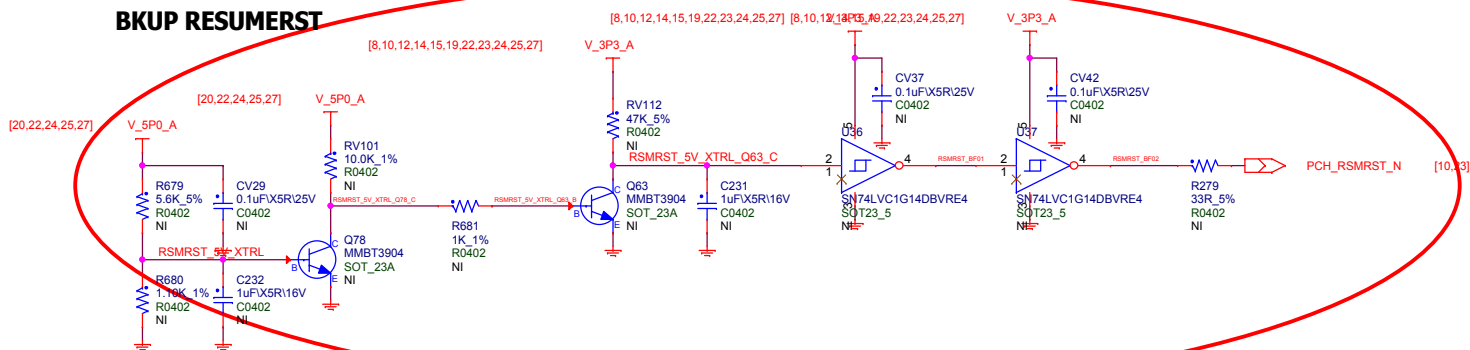
## VR POWER SEQUENCE



## PCIE SLOT RESET



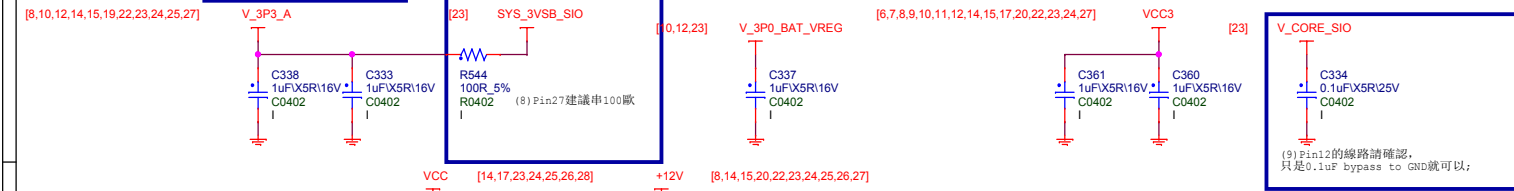
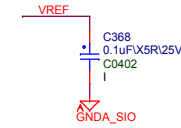
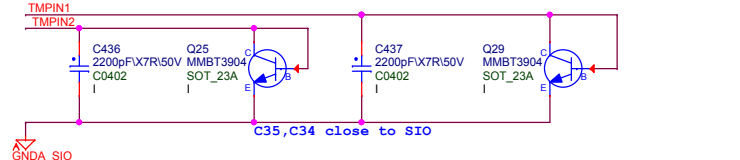
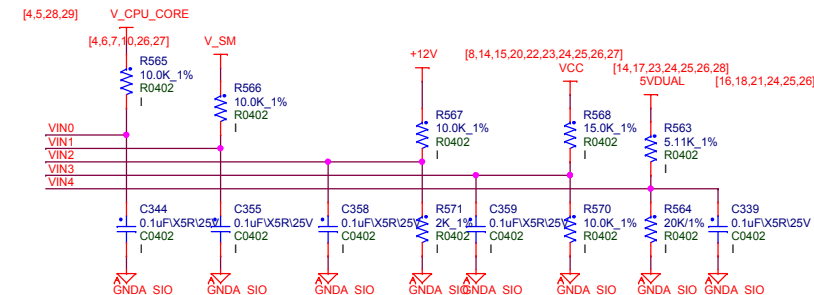
## BKUP RESUMERST



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[8,10,12,14,15,19,22,23,24,25,27]



**VCC** [14,17,23,24,25,26,28]

**+12V** [8,14,15,20,22,23,24,25,26,27]

**-12V**

**U3** GD75232

**C281** 0.1uFX5R125V  
C0402

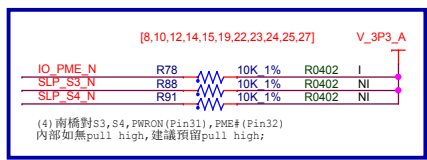
**C2** 0.1uFX5R125V  
C0402

**C81** 0.1uFX5R125V  
C0402

**JCOM1** 2X5N10PIN  
COM

**Pin Connections:**

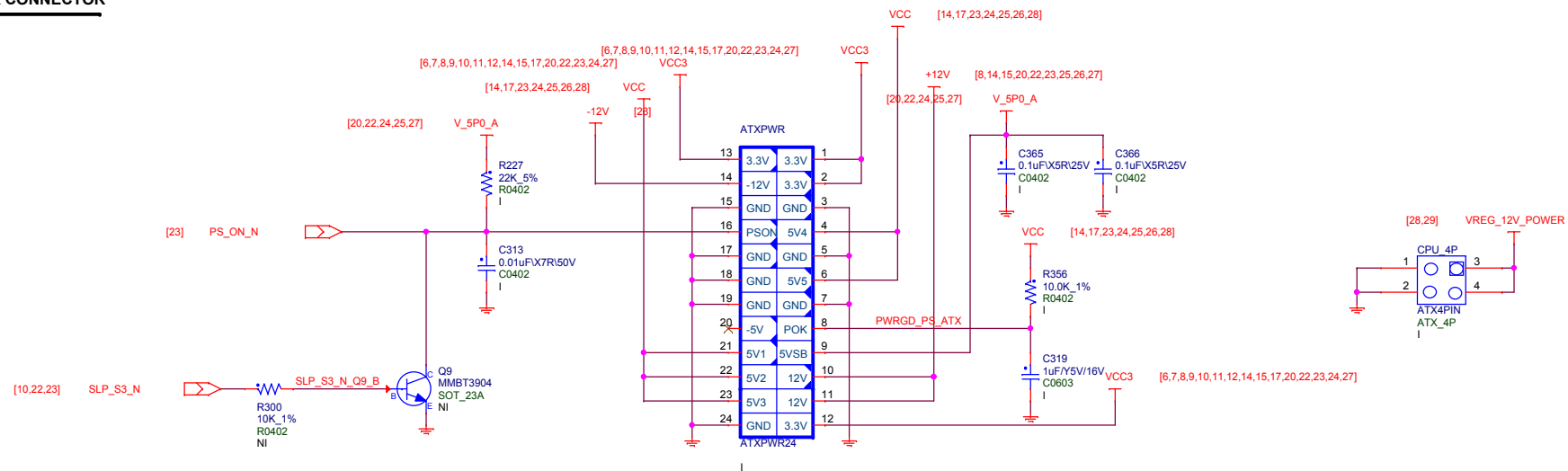
Pin	Signal
1	NDCDA
2	NDSRA
3	NSINA
4	NSOUTA
5	NRTSA
6	NDTRA
7	NCTSA
8	NRTSA
9	NRIA



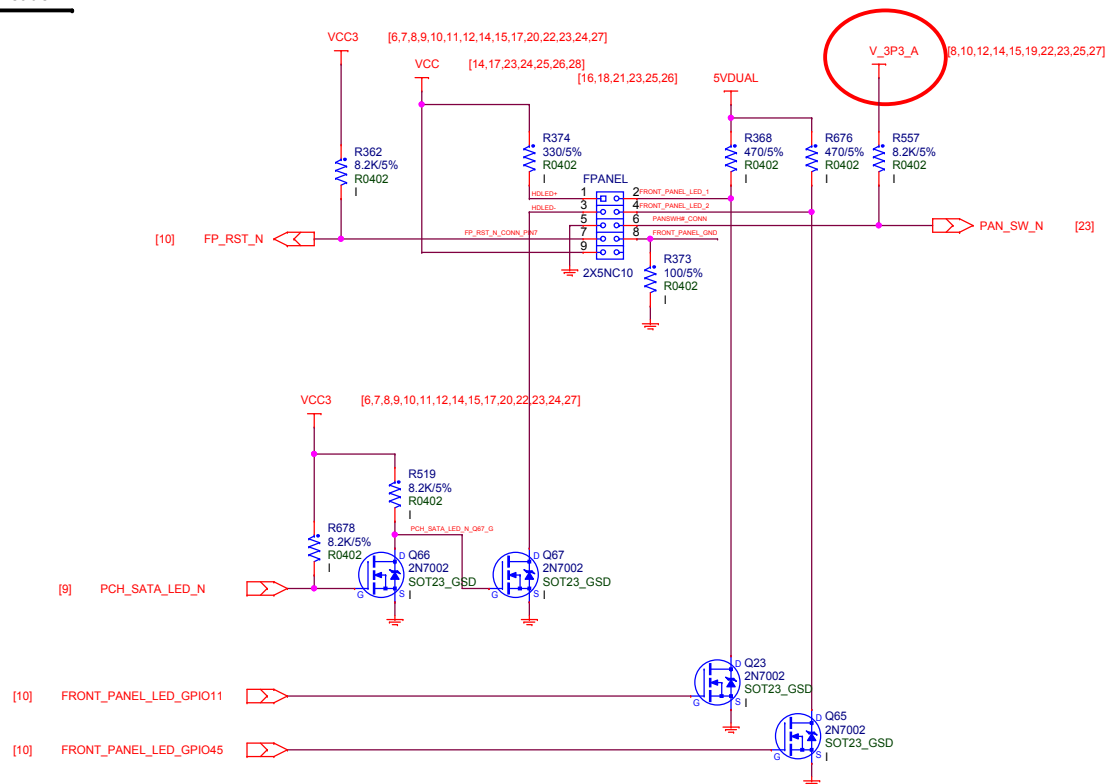
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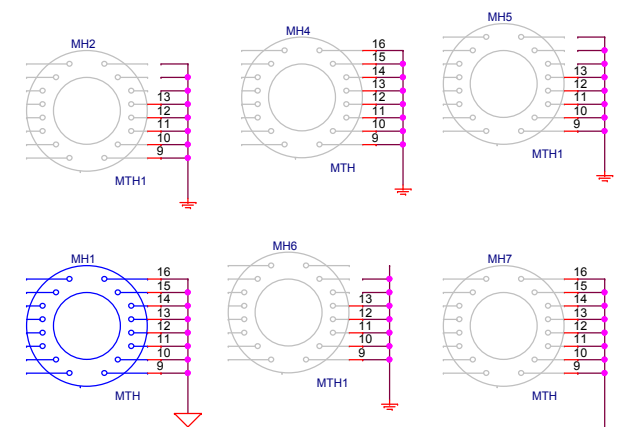
### ATX POWER CONNECTOR

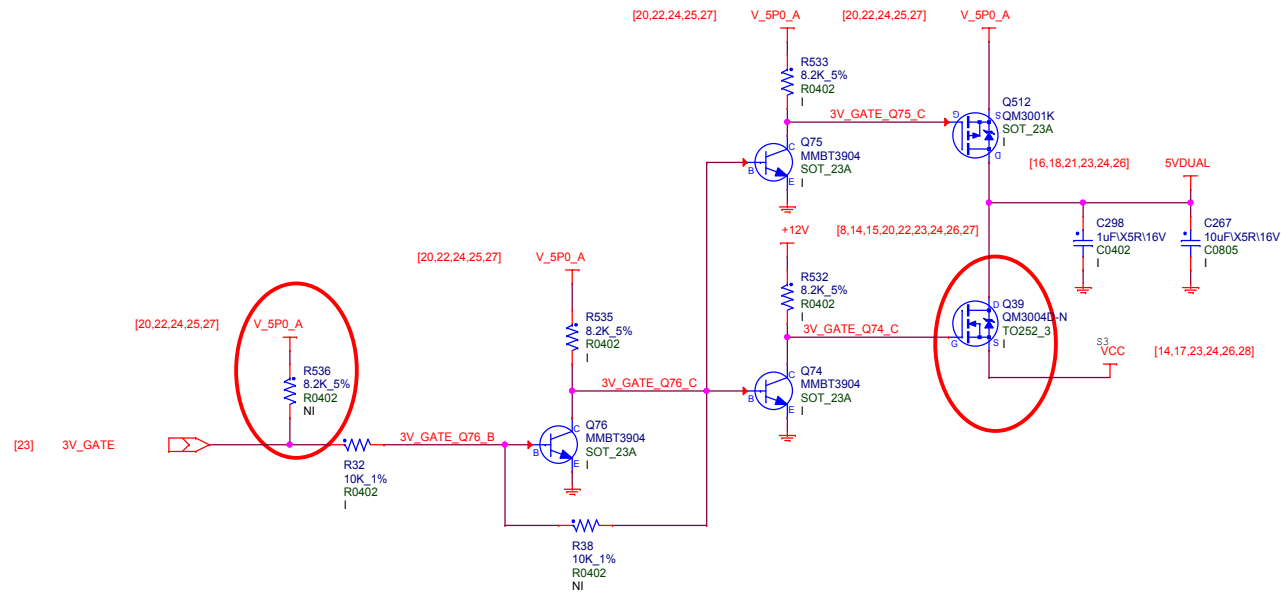


### Front Panel Header



## MOUNTING HOLES



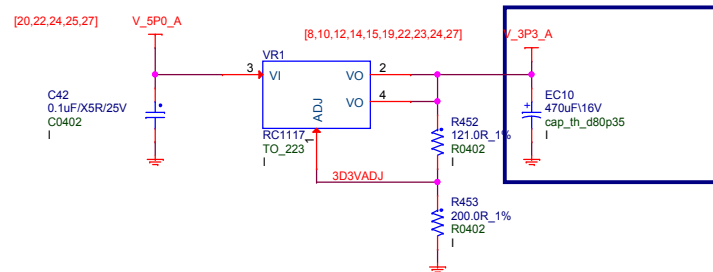


change to Black

blue 130356060121 DIP CAP;560uF 6.3V;±20%;8mm\*8mm;8mΩ;蓝色;NICHICO

black 130310076311 DIP CAP;1000uF;6.3V;±20%;LOW-ESR;150mΩ;8\*11mm;黑色;TEAPO/M

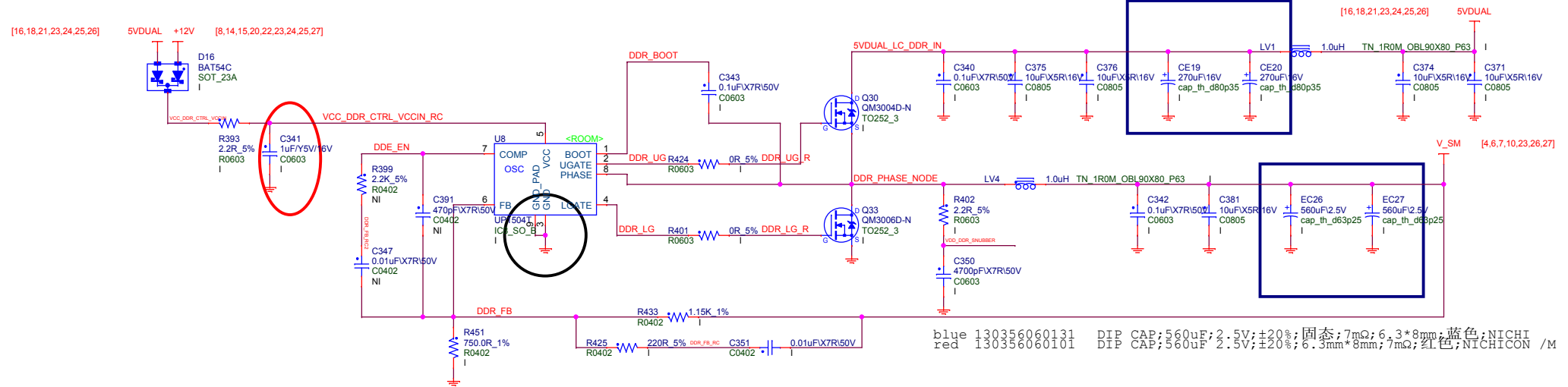
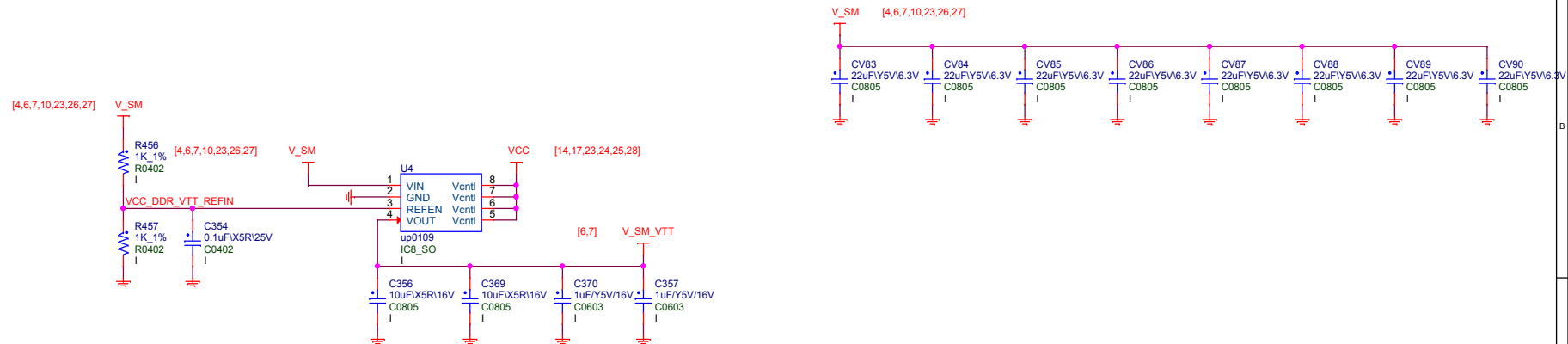
black 130347066311 DIP CAP;470uF;16V;±20%;LOW-ESR;150mΩ;8\*11mm;黑色;TEAPO /M



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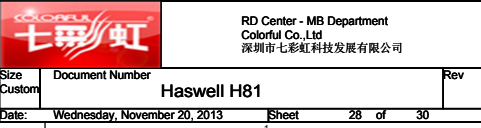
blue	130356060121	DIP	CAP;560uF	6.3V;±20%;8mm*8mm;8mΩ;	蓝色;NICHICON	/M
red	130327060101	DIP	CAP;270uF	16V;±20%;8mm*8mm;10mΩ;	红色;NICHICON	/M


$$\underline{V_{SM}} \quad \underline{V_{TT}}$$


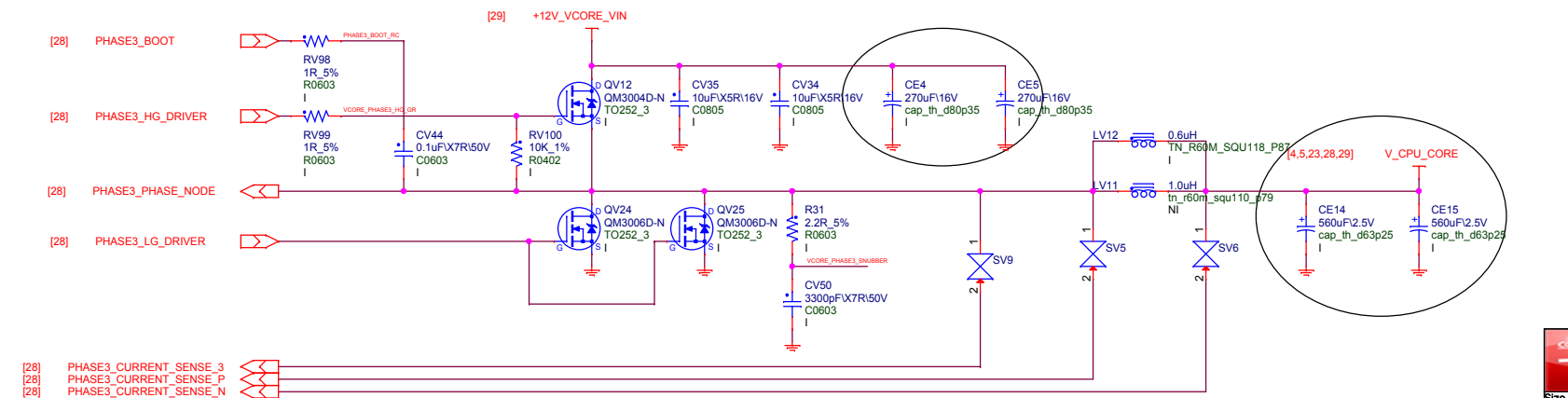
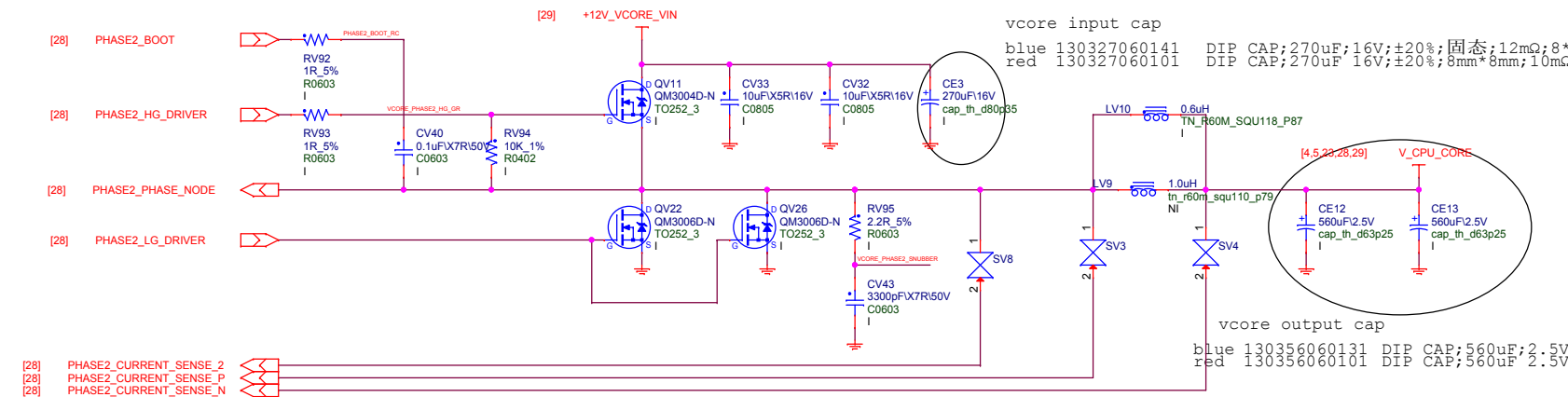
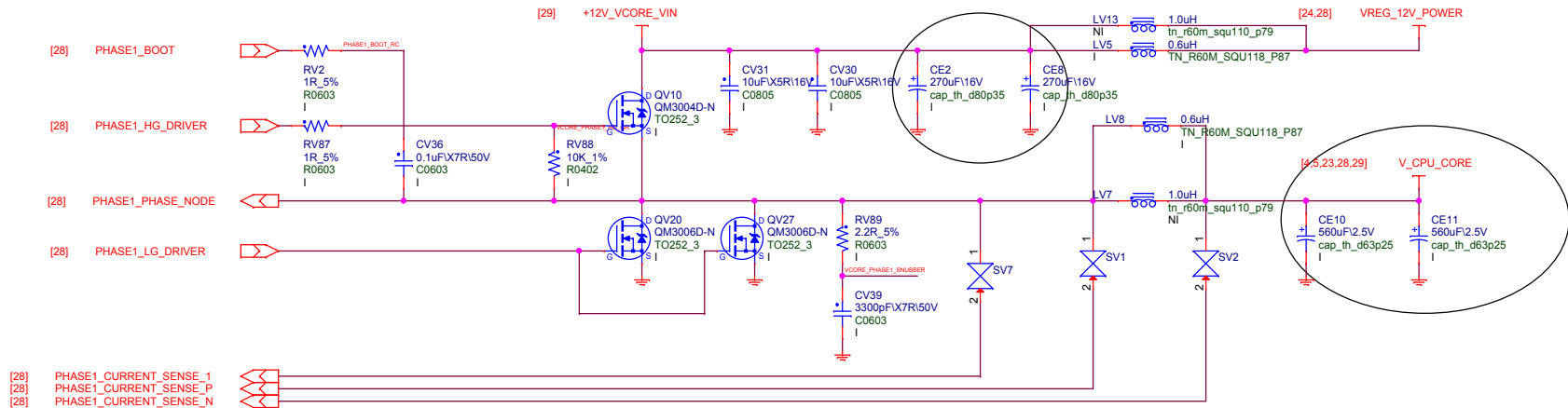
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## H81M02-M03 change list

001 PCB Change to H81-M03 [page 1]

002 R544 change position [page 23]

003 up1651 update symbol, UG/LG Swap[page 28]

004 RV51,RV27 change to 0 OHM[page 28]

005 DDR DIMM DM pins short to GND[page 06,07]

006 SMBUS/Alert add pull up resistor[page 10]

007 PWBTV pull up change to STBY Resistor[page 24]

008 NI Resumrst reserved parts[page 22]

009 NI FRONt USB 3.0 part for H81 Sku[page 16]

010 D22/D23 I[page 14]

011 R536 NI, Duplicated pull up for 3V\_gate[page 25]

012 Update Buzzer Circuit[PAGE 14]

013 Add 0.1uF 0402 on All USB OC pins[Page 16/21]

014 FU3 Change to 2.6A Fuse[page 21]

015 Silkscreen Issue from DYD

001 R391/C285

002 R25/R634

003 C341 update PCB footprint to C0603[page 26]

004 C312/C416

016 Add Colorful silkscreen ,ref to luoyongxin

017 Add 4 port SATA3.0 support

017.1 Add Rn12/13 for H81 Sku

017.2 Add Rn14/15 for b85 Sku

018 SIO Review chnage[page 23]

018.1 R380/R377/R378/R379 NI

018.2 C331 CHANGE TO 1uF

018.3 SYS\_3VSB\_SIO add 100 OHM

018.4 R540 change from GND to VCC3 VSB

018.5 Add R88/R91 and reserved

019 Add Ps/2 wake Enable/Disable Header[page 18]

020 Add pull down 1k 0402 on usb power[page 16/21]

021 PRNSNT PIN NC ON pcie x1[page 15]

2013-09-04

022 电解电容更换为固态电容 c3 change to NI

PAGE 20 AUDIO PART

DIP CAP;100uF;16V;±20%;铝电解;6.3\*7mm;240mΩ;黑色;TEAPO /M

DIP CAP;100uF 16V;±20%;6.3mm\*10mm;25mΩ;蓝色;NICHICON /M

SCH

PCB

BOM

## H81M02-M03 change list

SCH

PCB

BOM

022 电解电容更换为固态电容 c3 change to 270uF/16v

PAGE 08/16/21/25/26/27

DIP CAP;470uF;16V;±20%;LOW-ESR;150mΩ;8\*11mm;黑色;TEAPO /M

DIP CAP;560uF 6.3V;±20%;8mm\*8mm;8mΩ;蓝色;NICHICON /M

023 SPI ROM Add Part Number

024 Thermister Update Description

025 5v Dual Q39 change from QM3010D to QM3004D

026 RV36 CHANGE FROM 560 TO 1.34K  
RV51 CHANGE TO 8.2K

027 STAT conn: H81 Red B85 Black\

## H81M03-M04 change list

LAN Isolated Pin Connected to VCC3 Main Power  
usb3.0 signal swap

## H81M03-M04 change list

2013-11-15

LAN Add RT8105E support

update USB 3.0 ESD Part Footprint

update DDR Power Controller Symbol and PCB Footprint

2013-11-18

update TRANSFORMER PIN CONNECTION

2013-11-19

电容颜色变更

CPU/memory power IO ECAP change to red color

other cap change to black

front usb header change to RED color

pcb change to Version 4

Front Audio Header change to Black

rtl ni

2013-11-20

PCB Change name to M05

1.5V 1.05V MOSFET change Part Number to QM3010D

usb 2.0 ESD Part Number Update

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