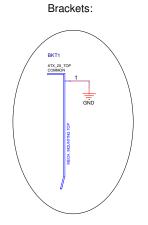
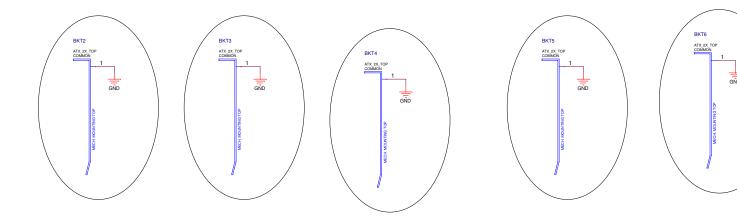
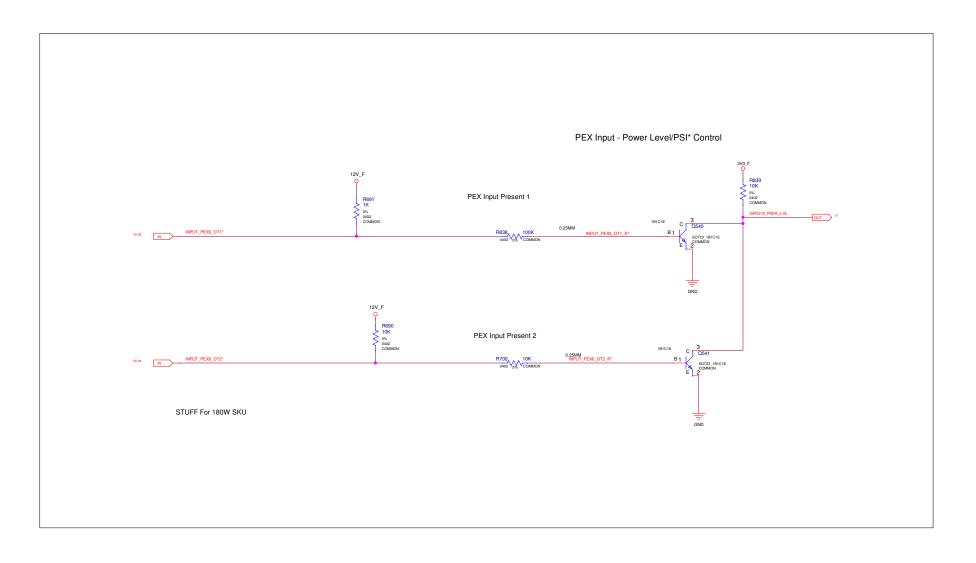


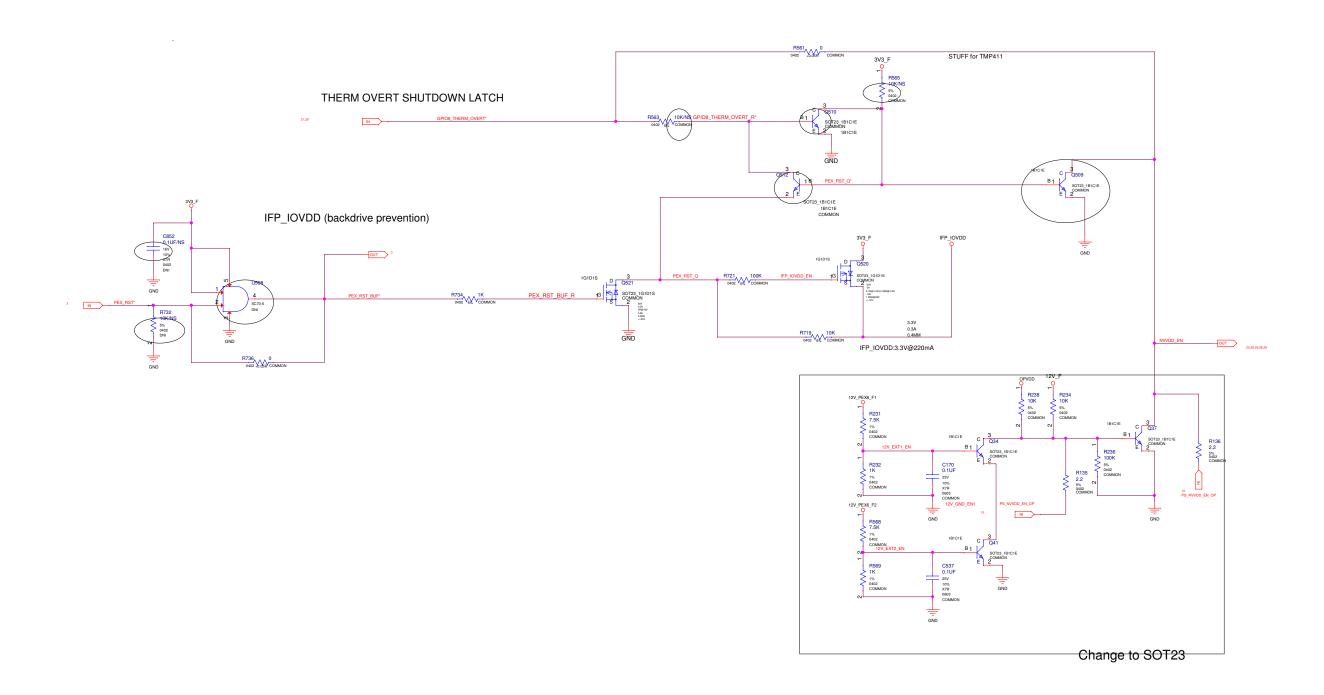
MOUNTING HOLES FOR BASEPLATE:

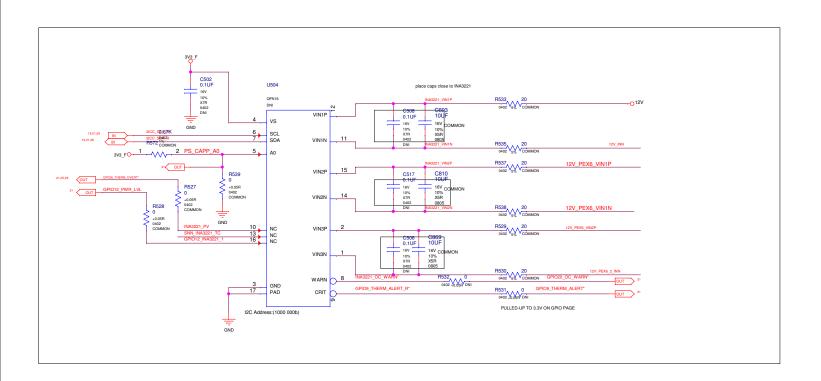


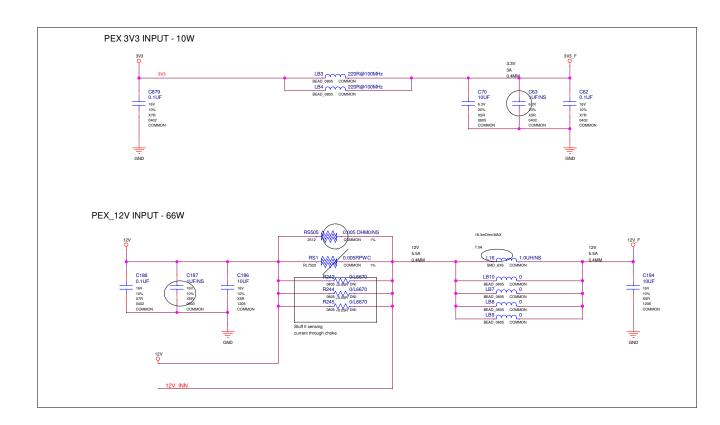


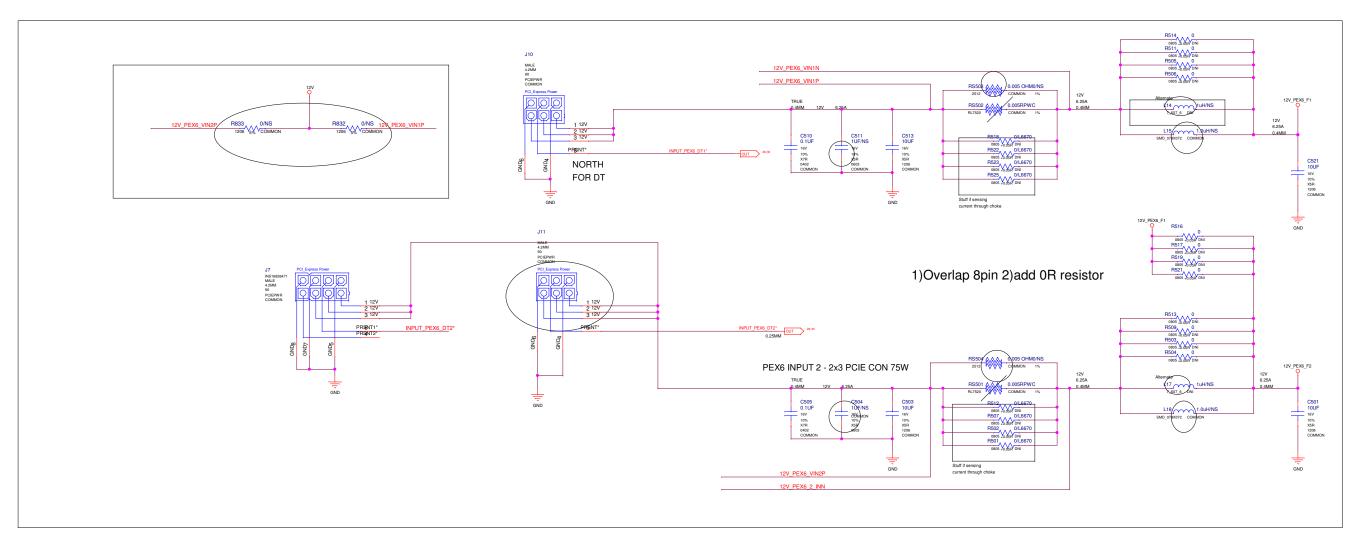


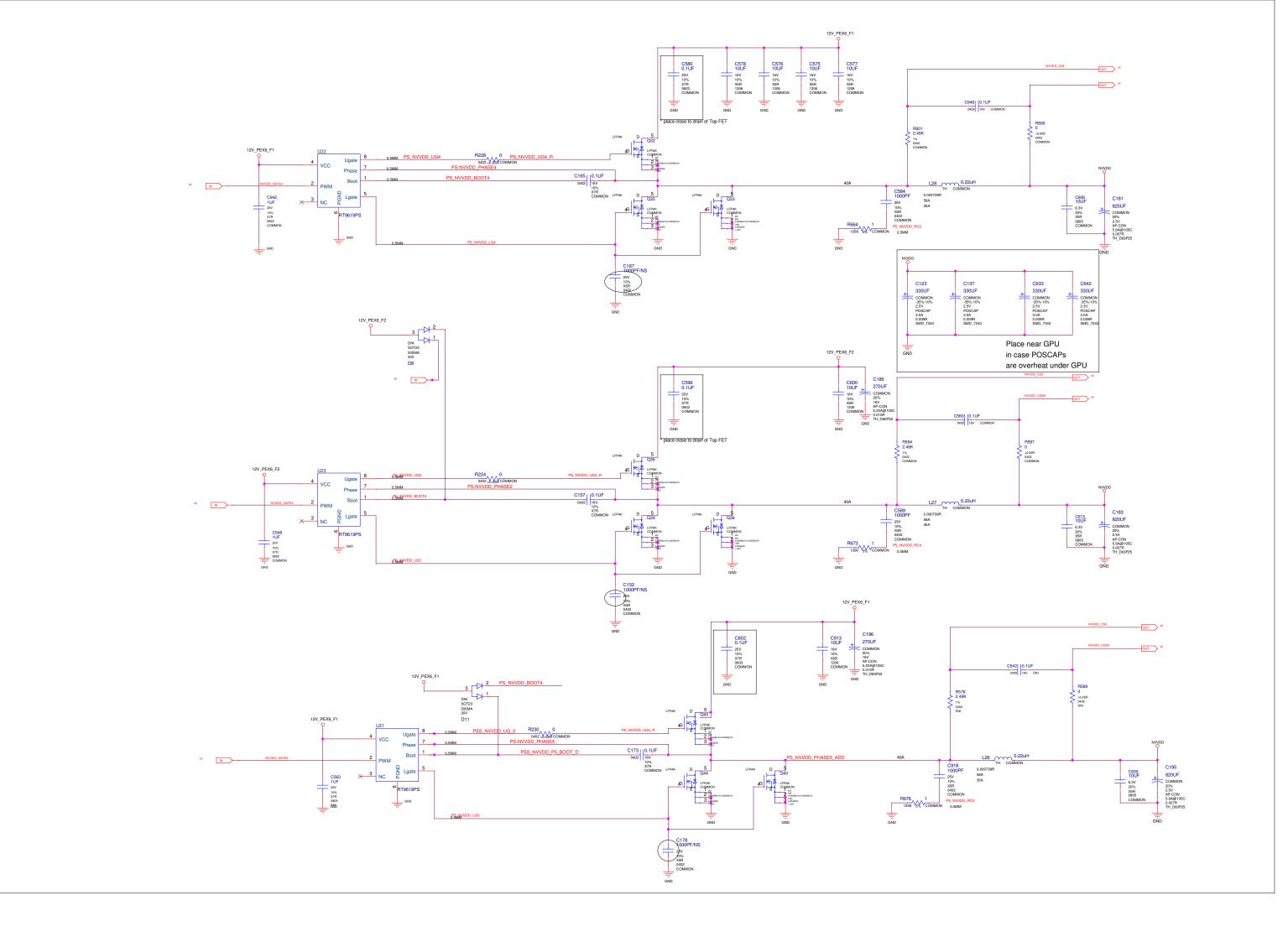
Н

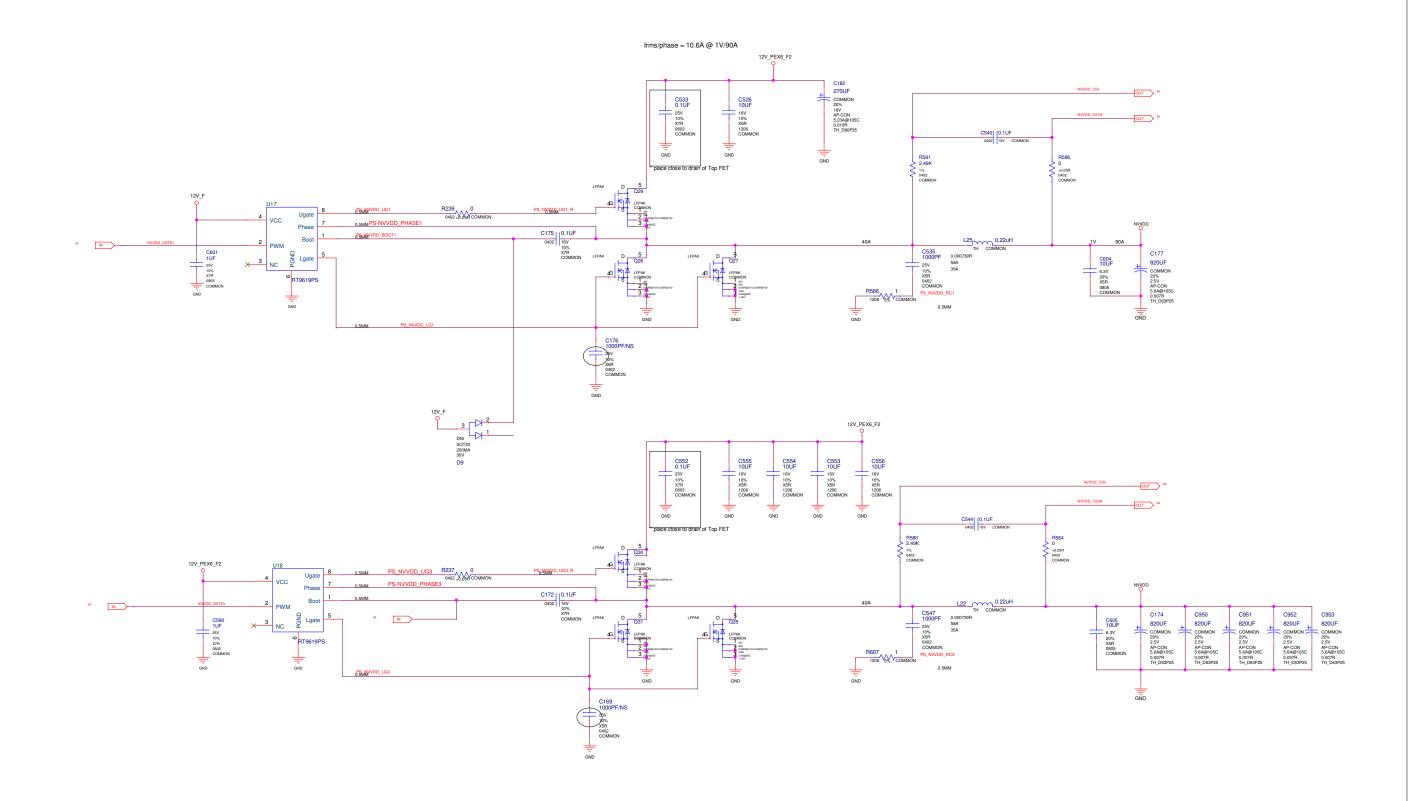




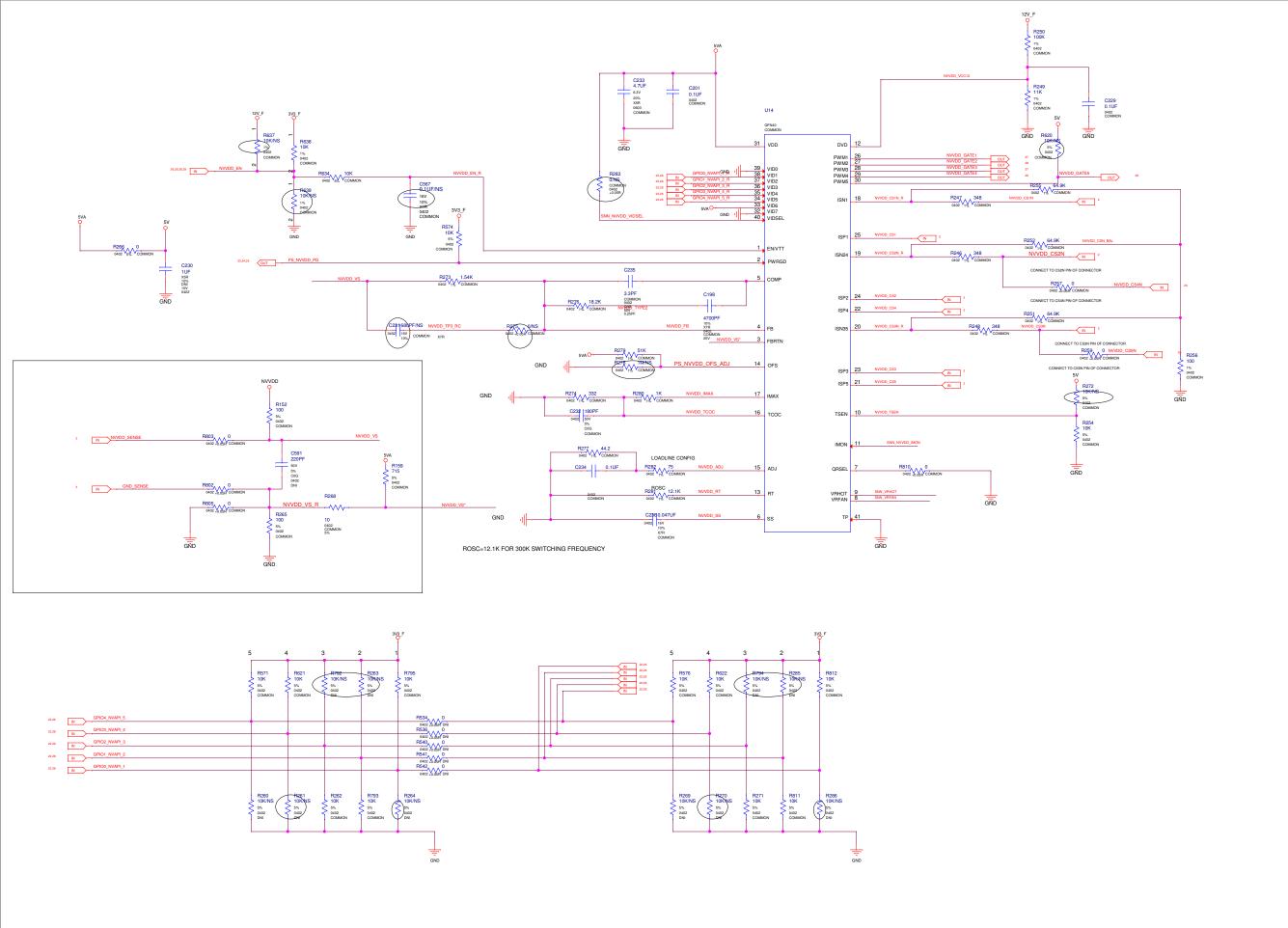


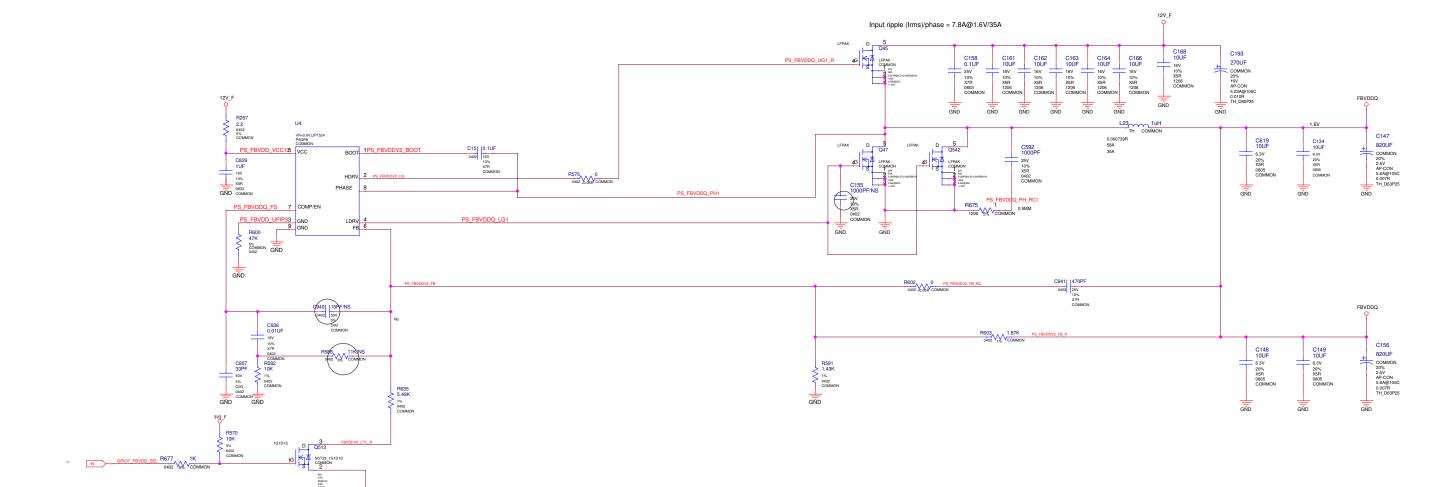


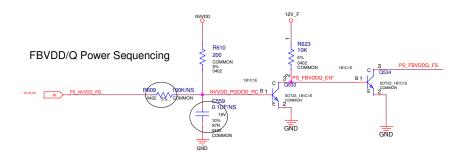




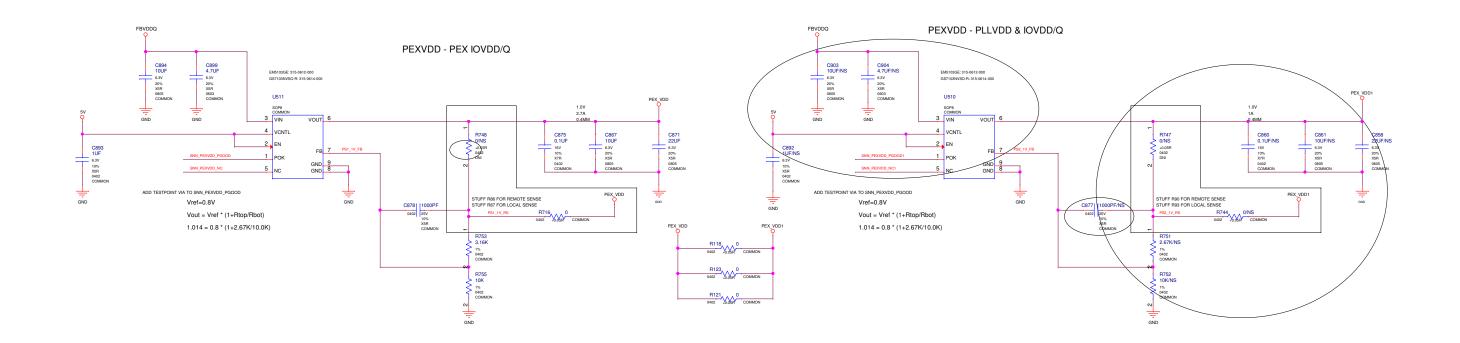
Α

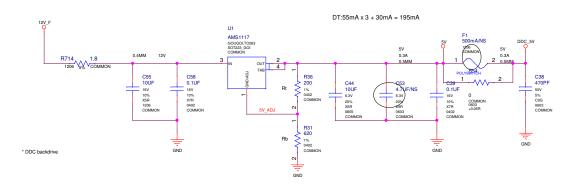


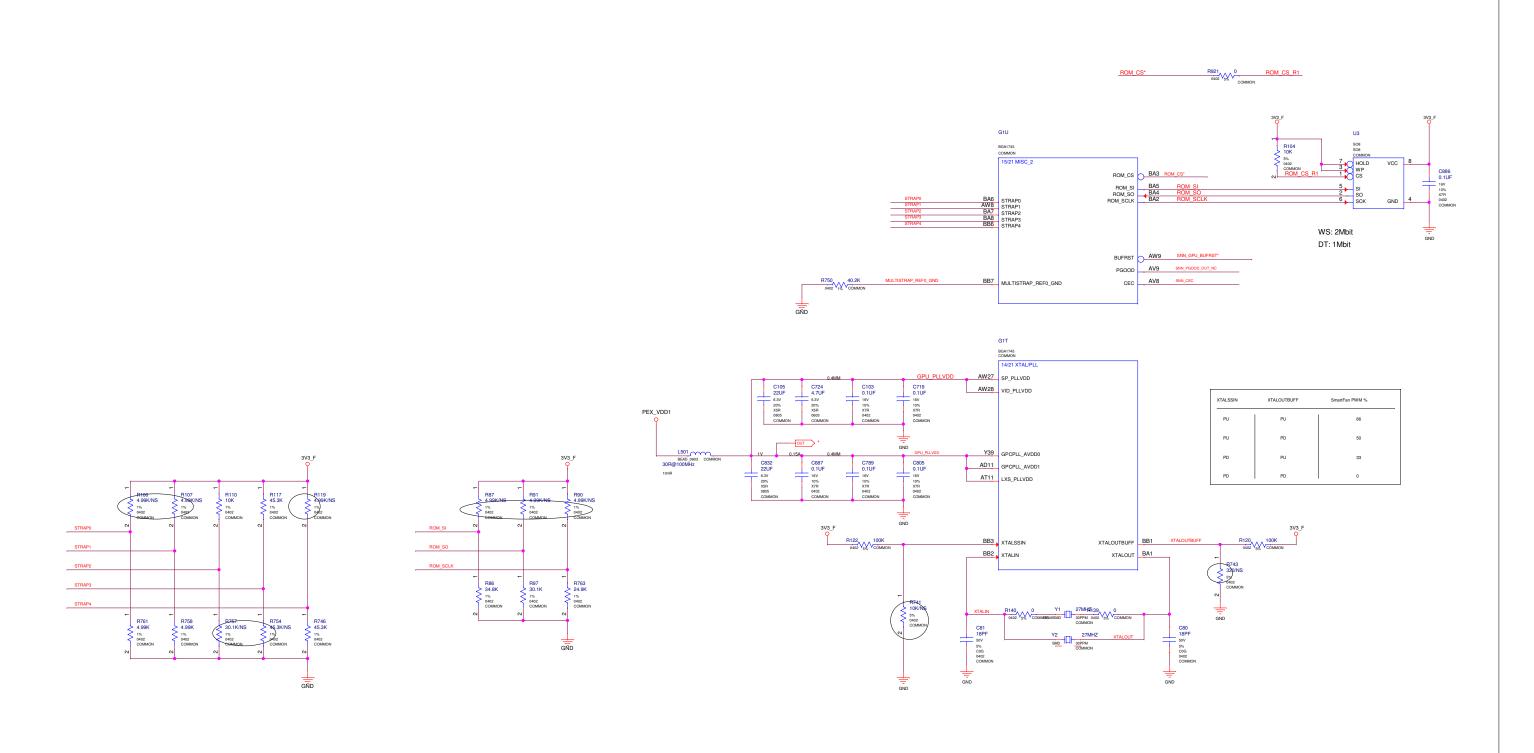


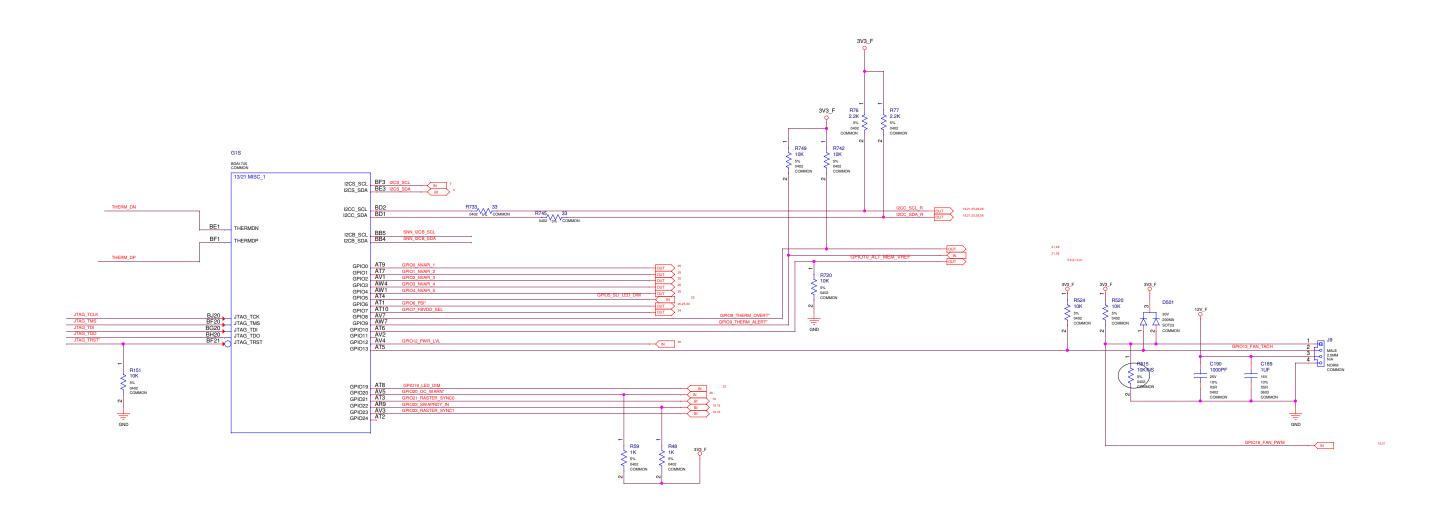


GPIO7 = 1 FBVDD/Q = 1.80V GPIO7 = 0 FBVDD/Q = 1.38V

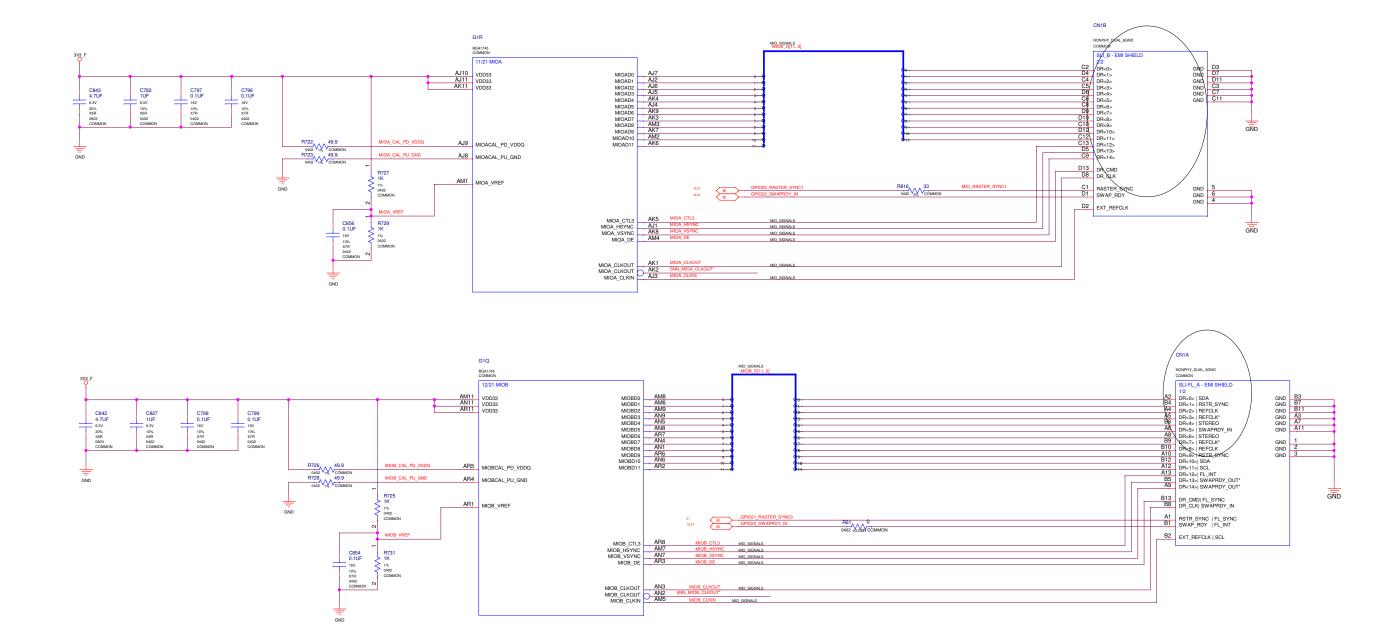




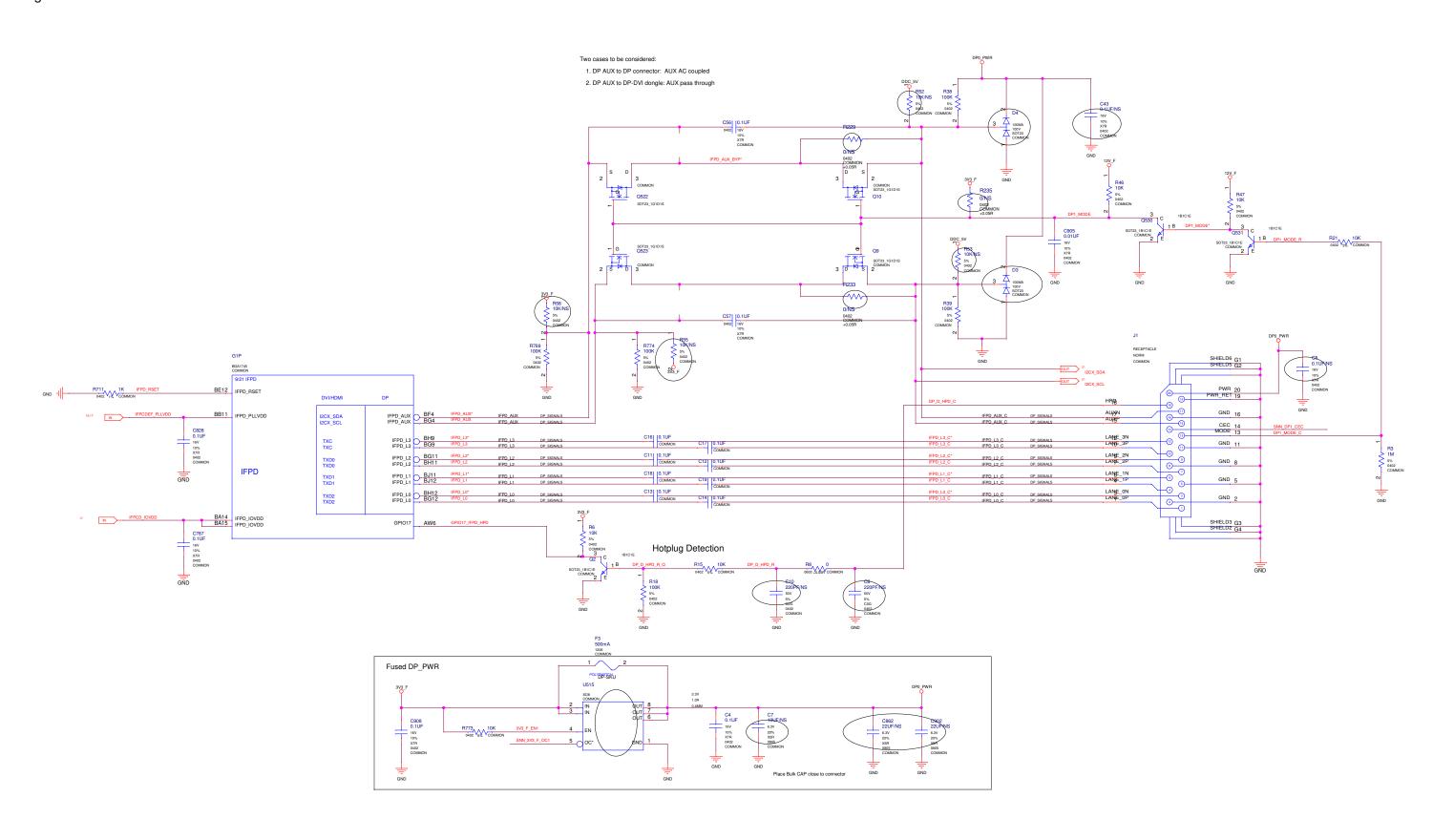


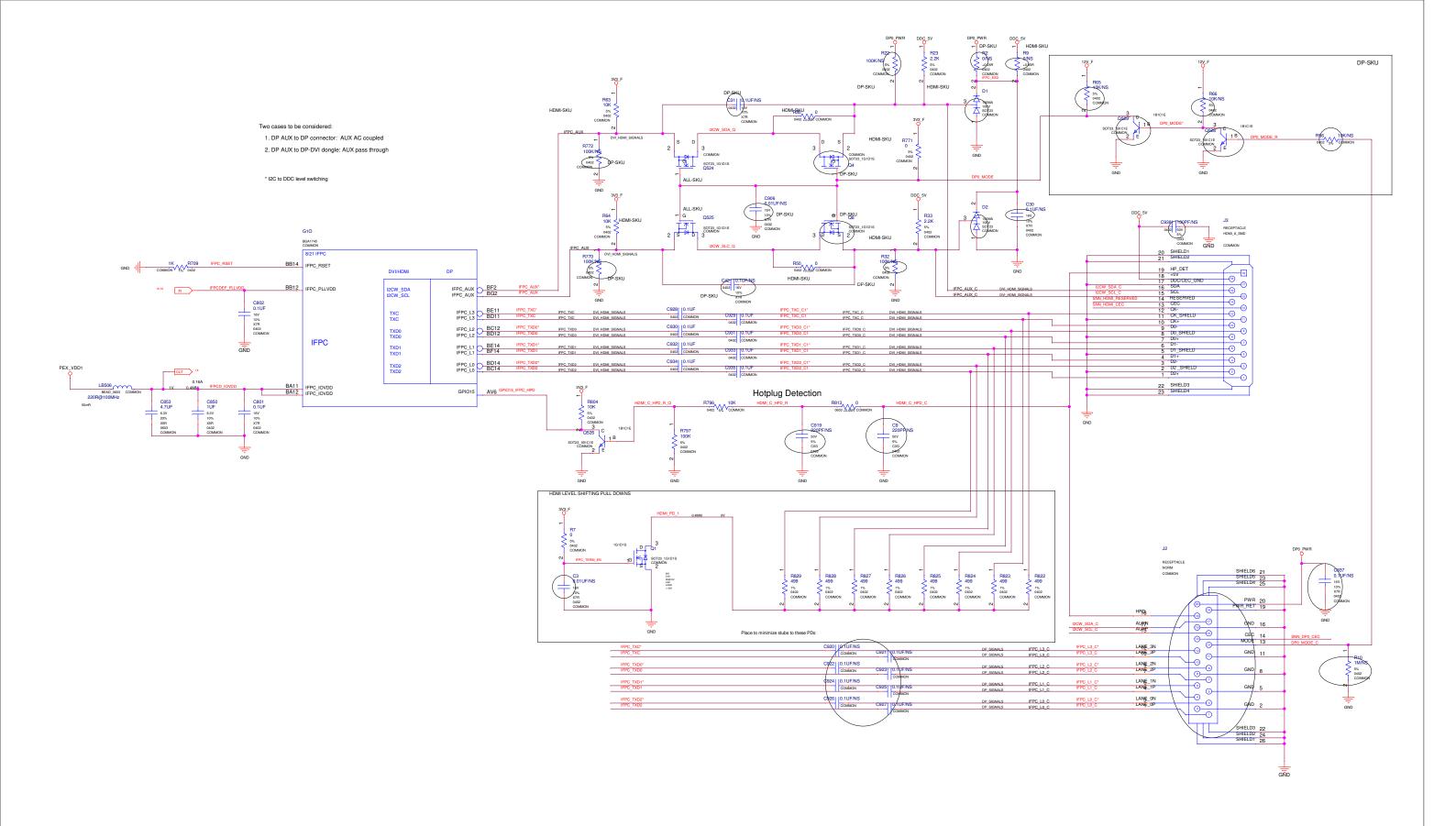


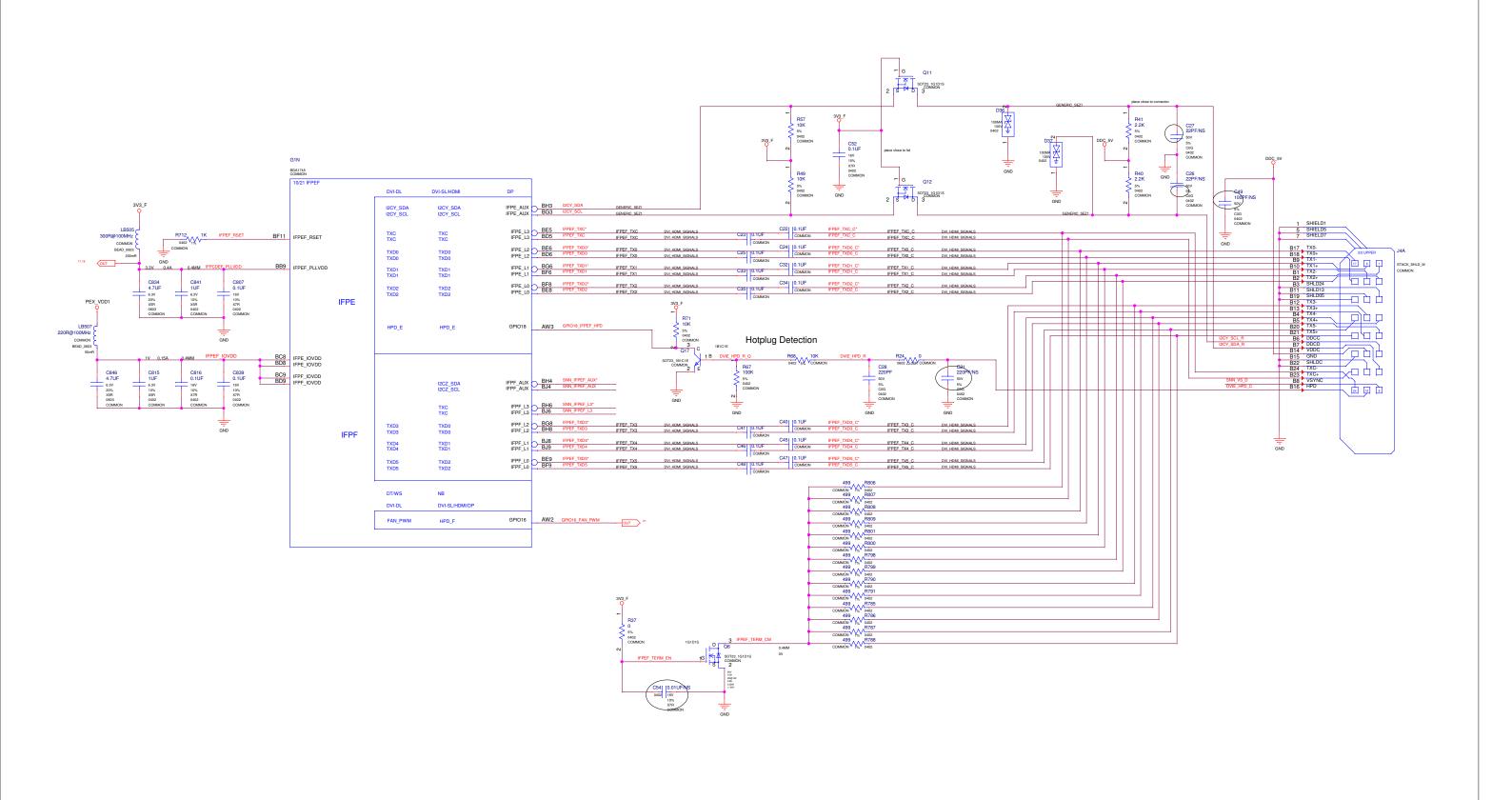
А

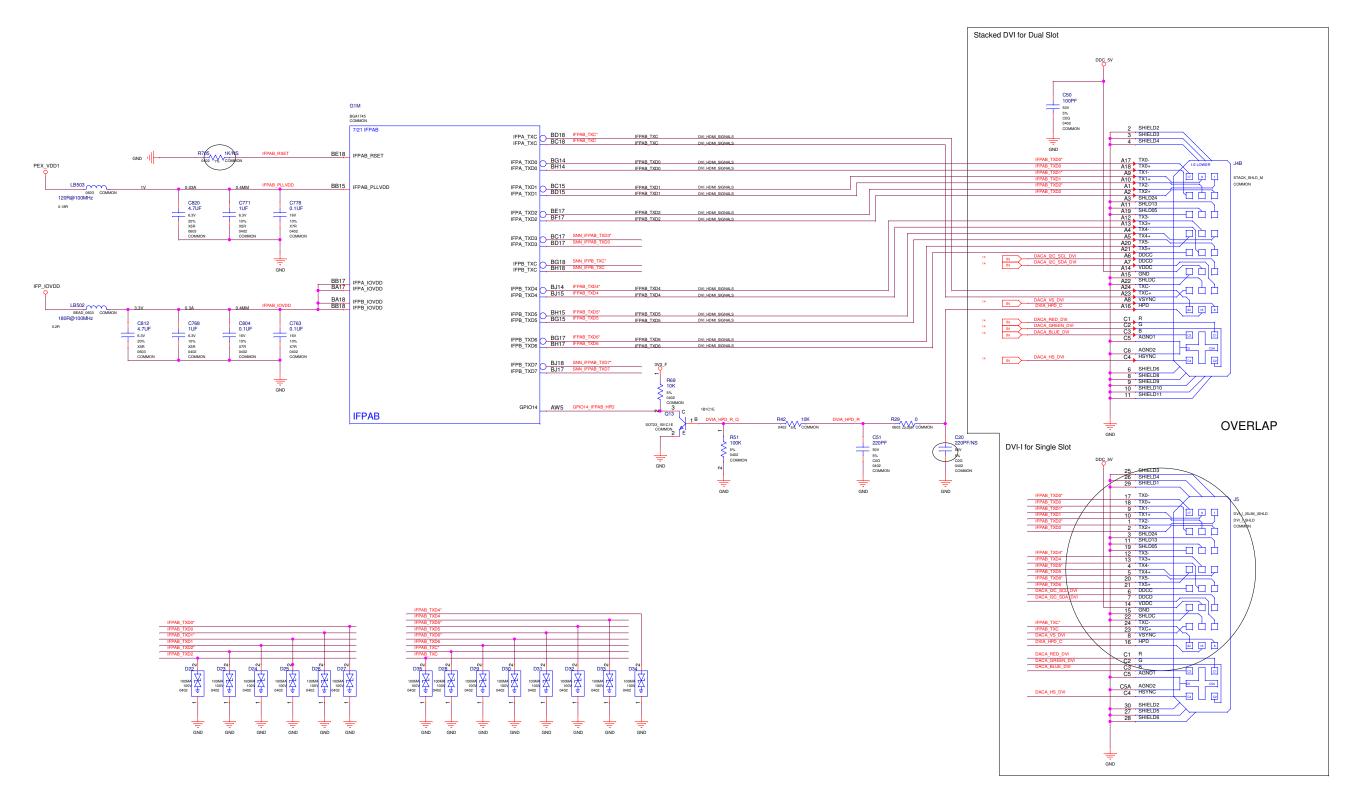


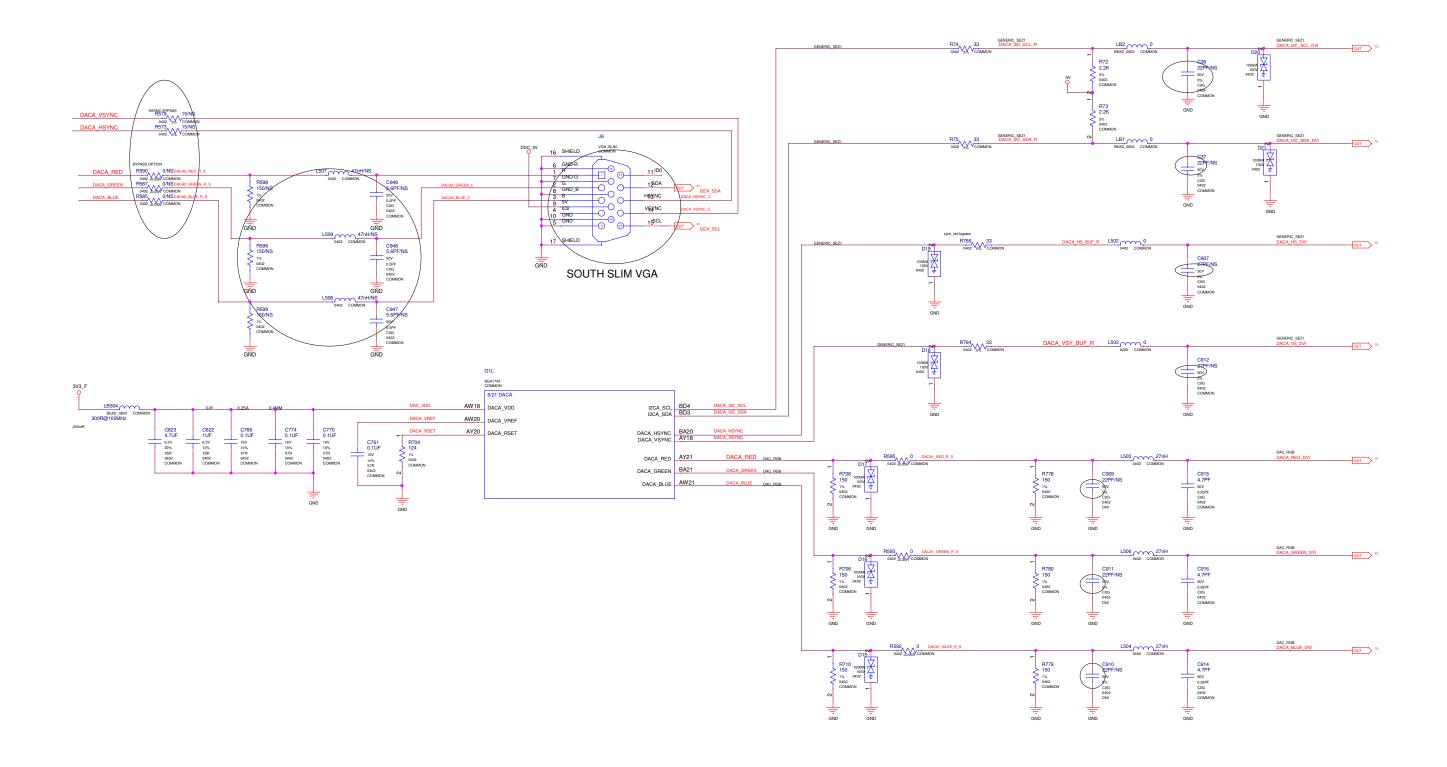
Page18: IFPD DP





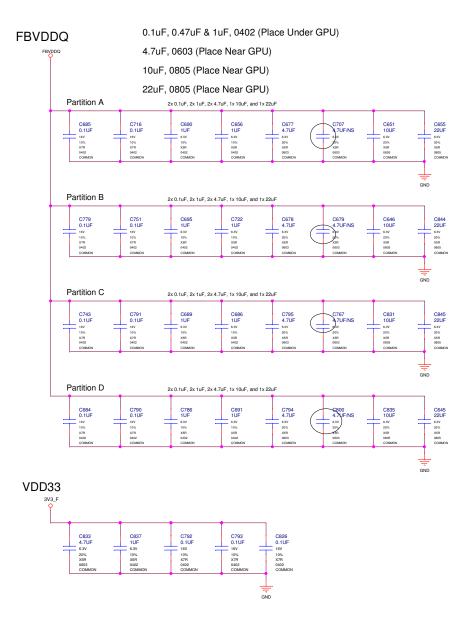






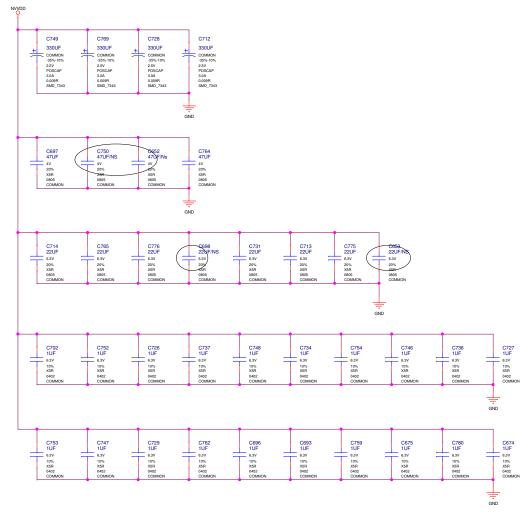
## Page13: GPU Decoupling

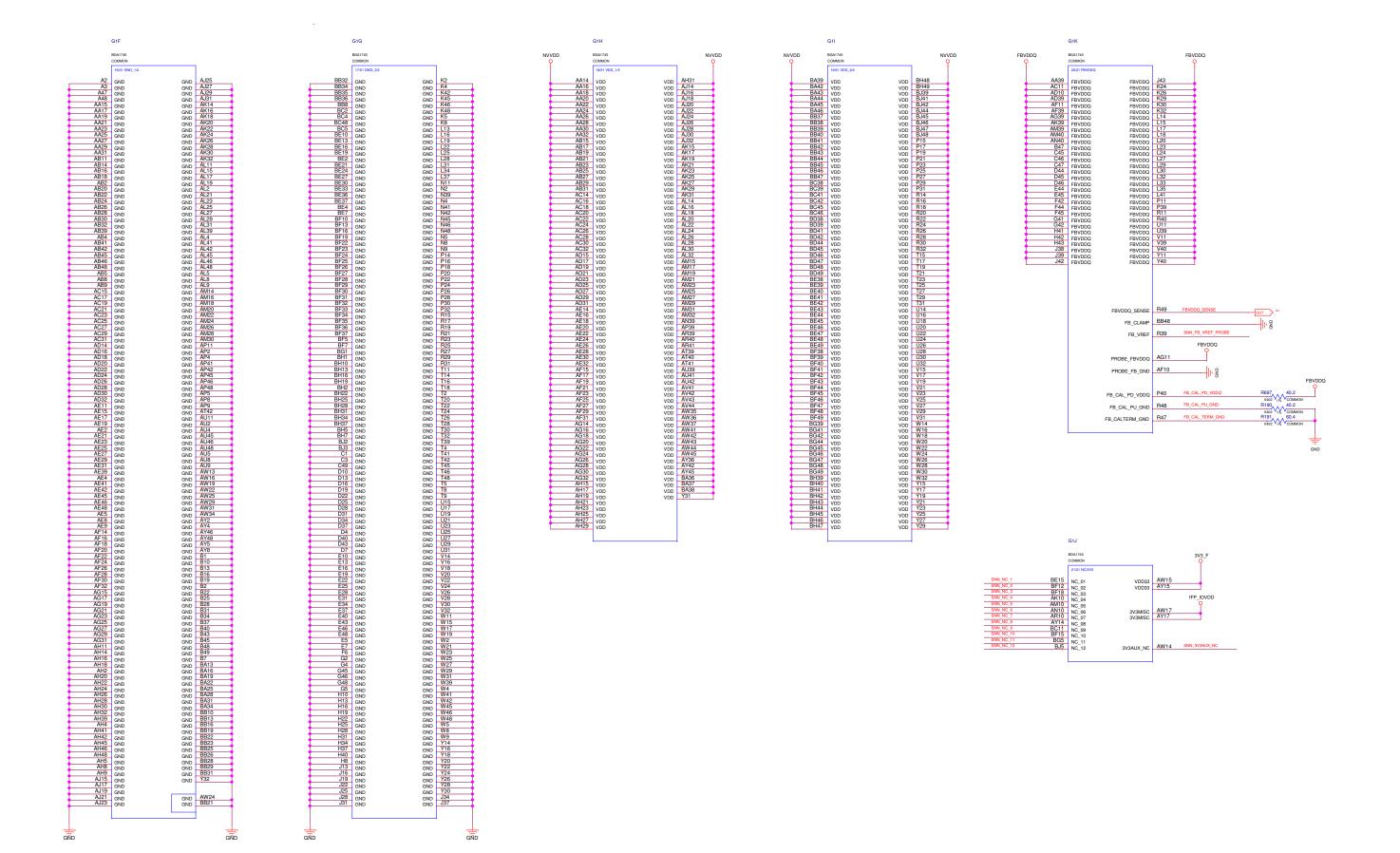
### Based on GB2-X GDDR5 FBVDDQ Decap Guideline

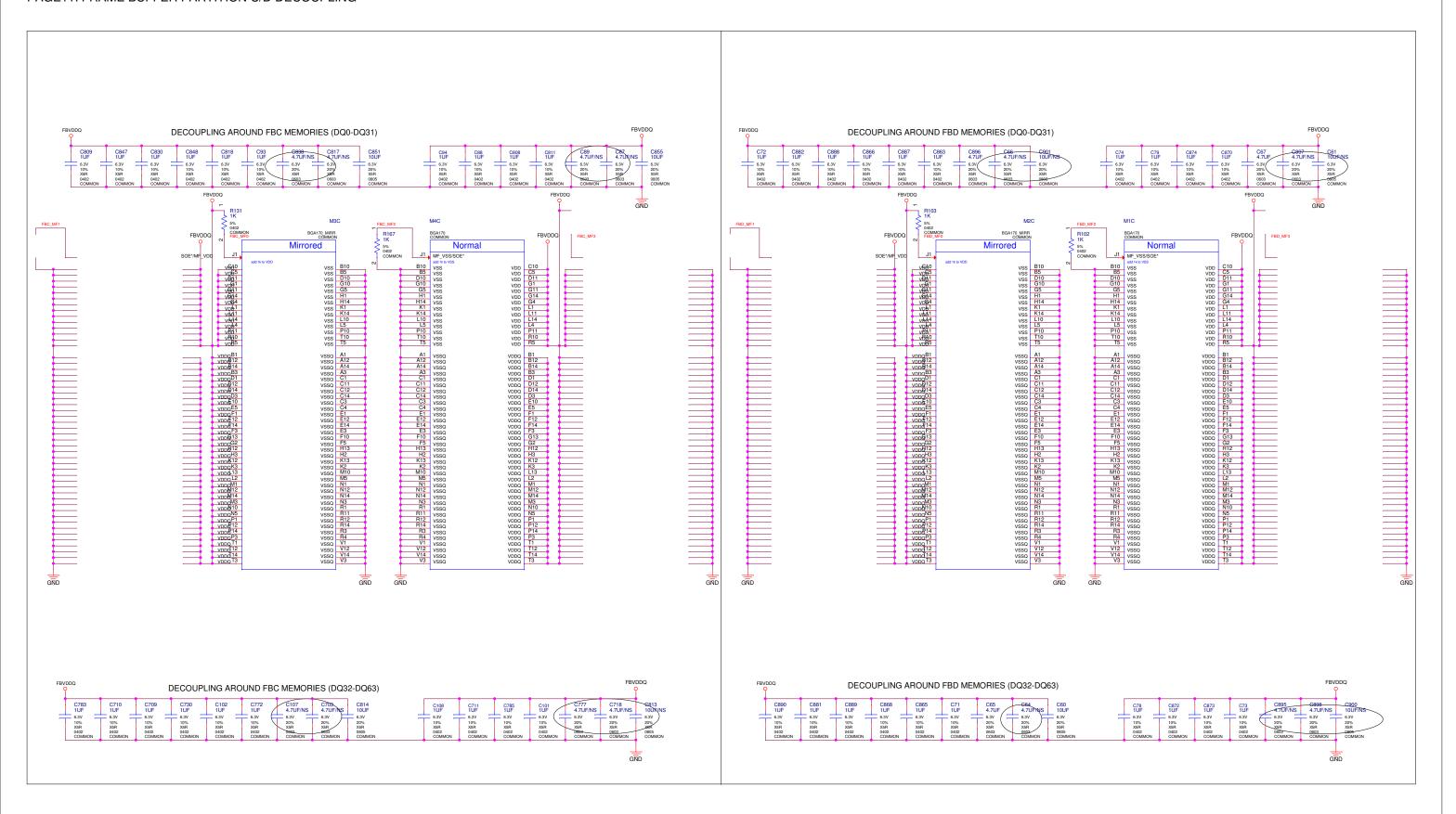


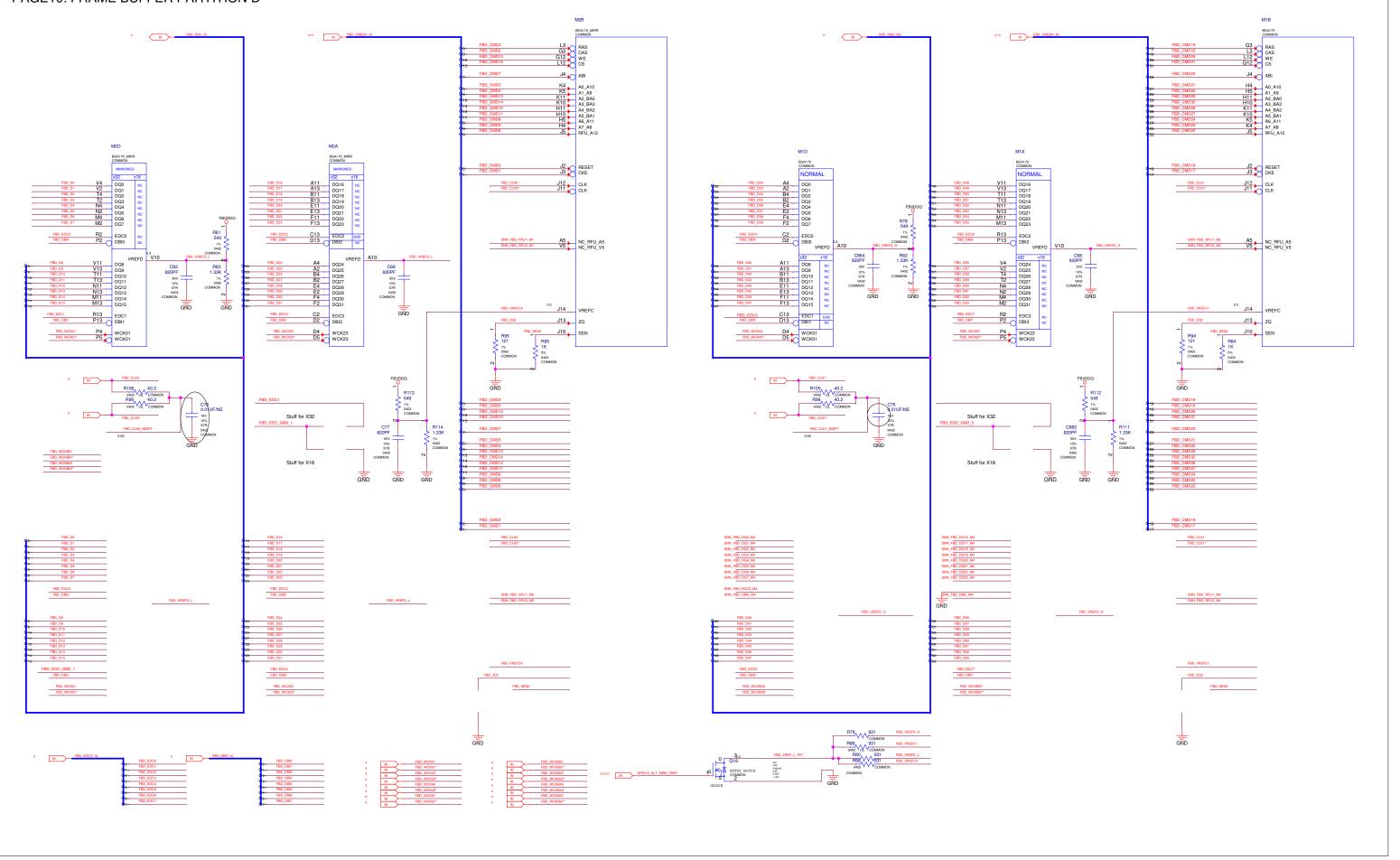
NVVDD Decoupling caps. Place under GPU.

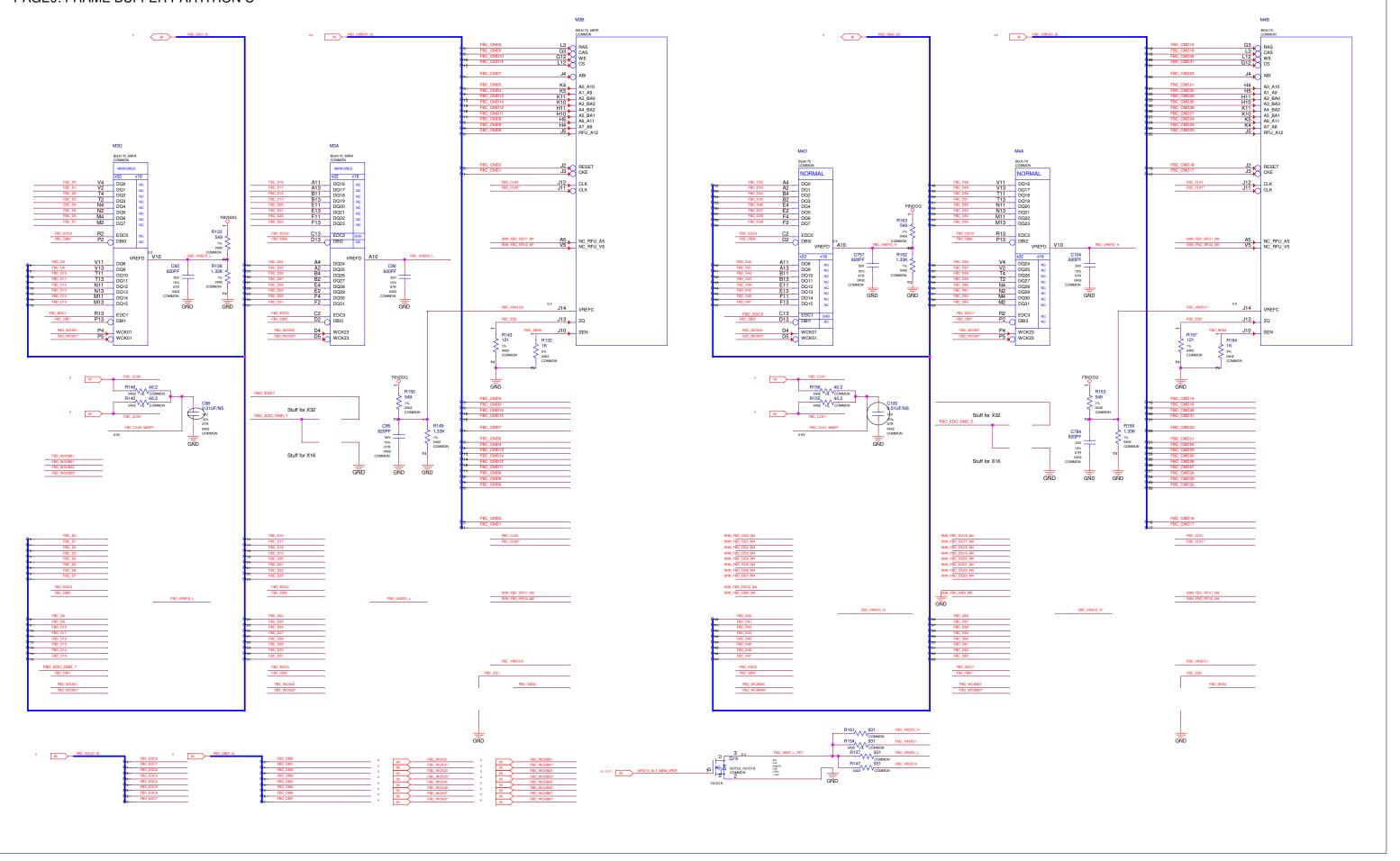
## NVVDD

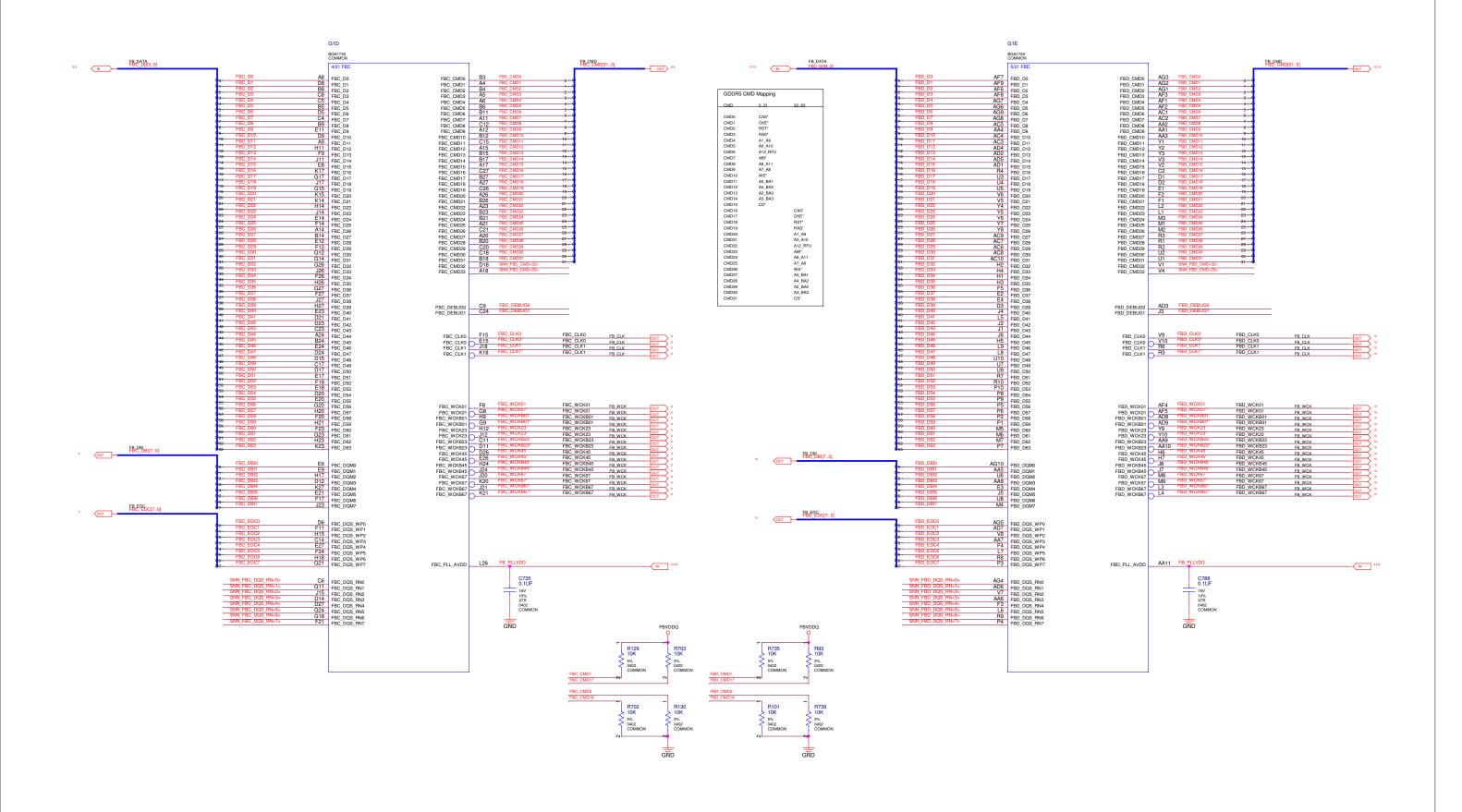


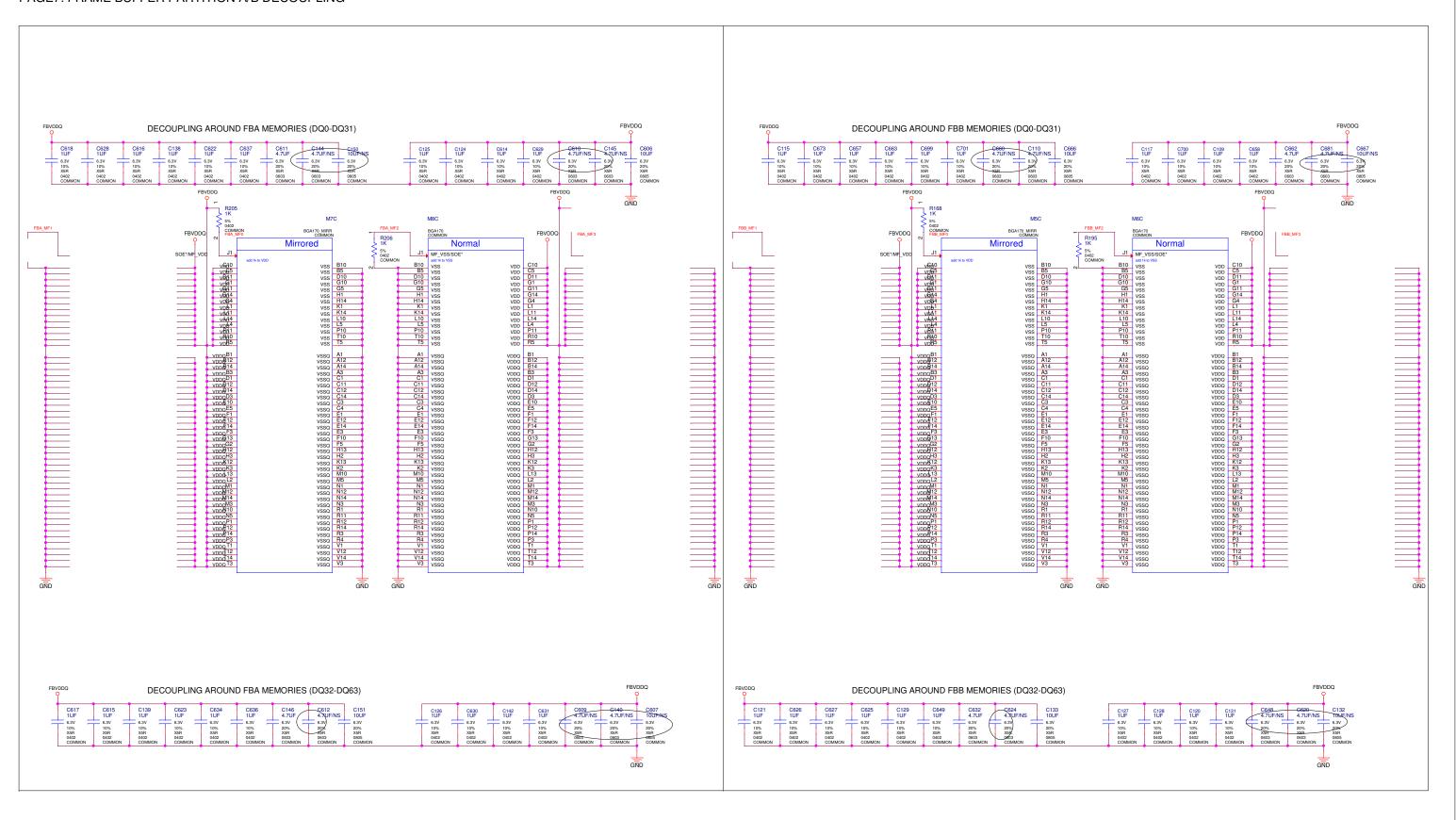


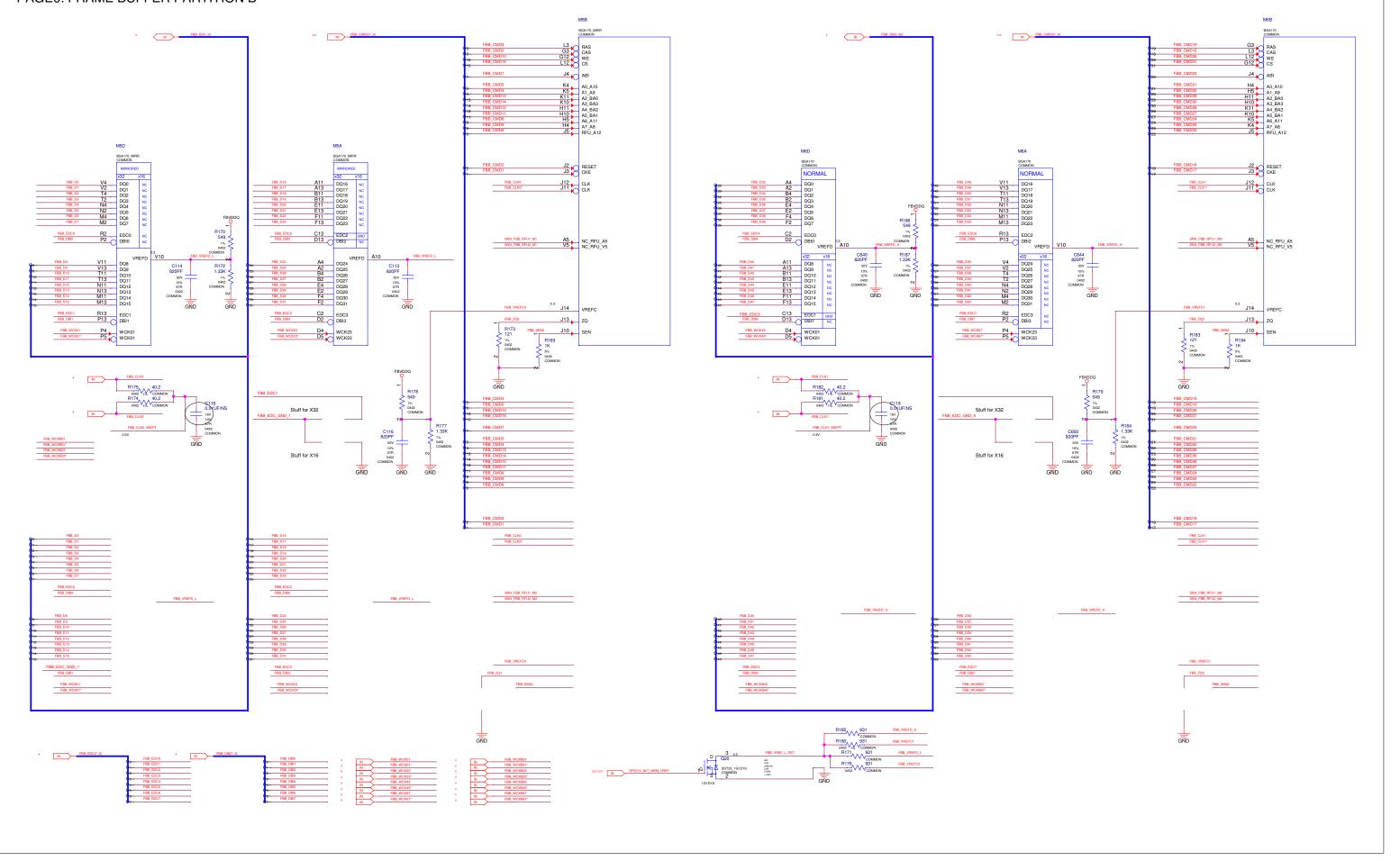


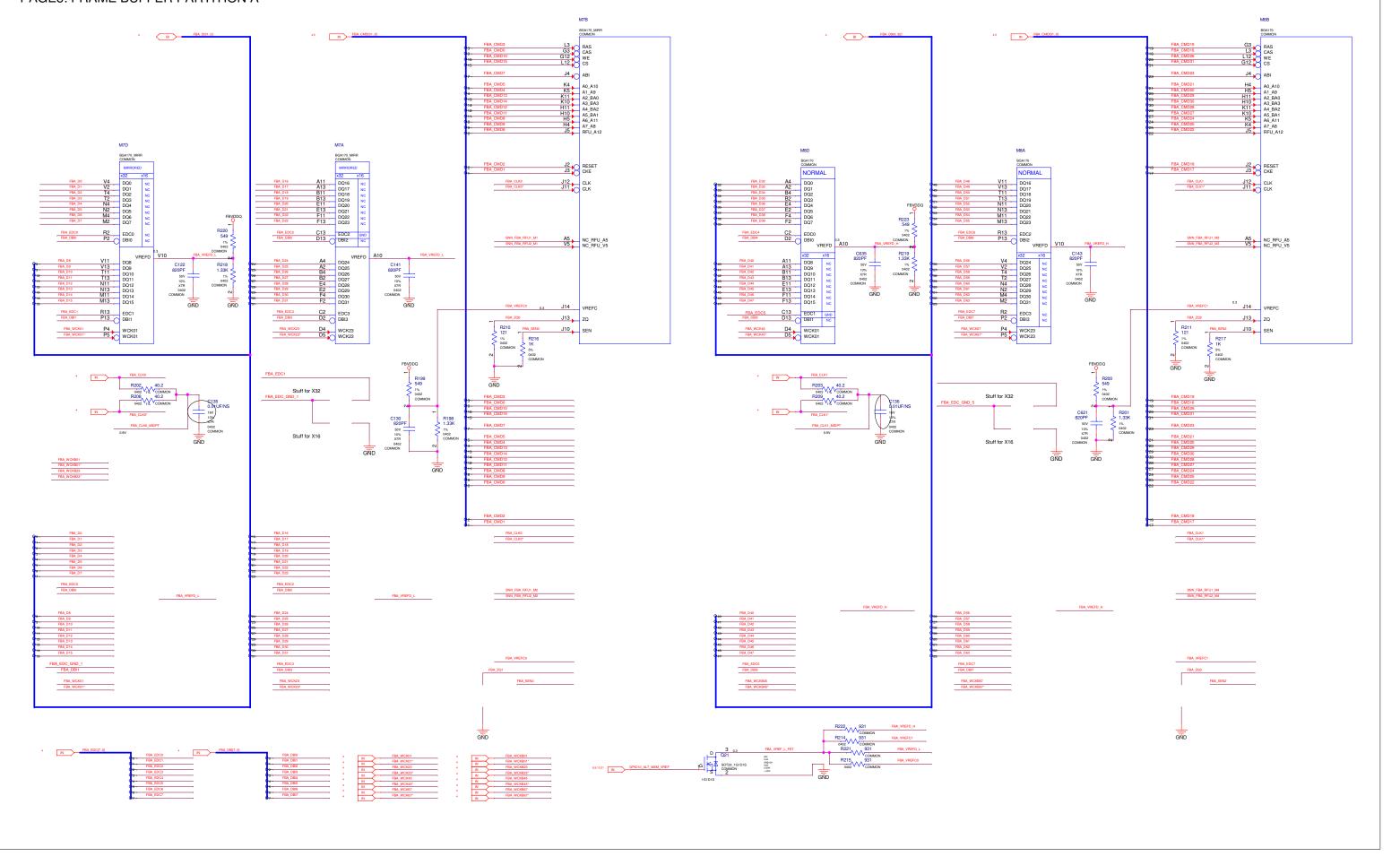


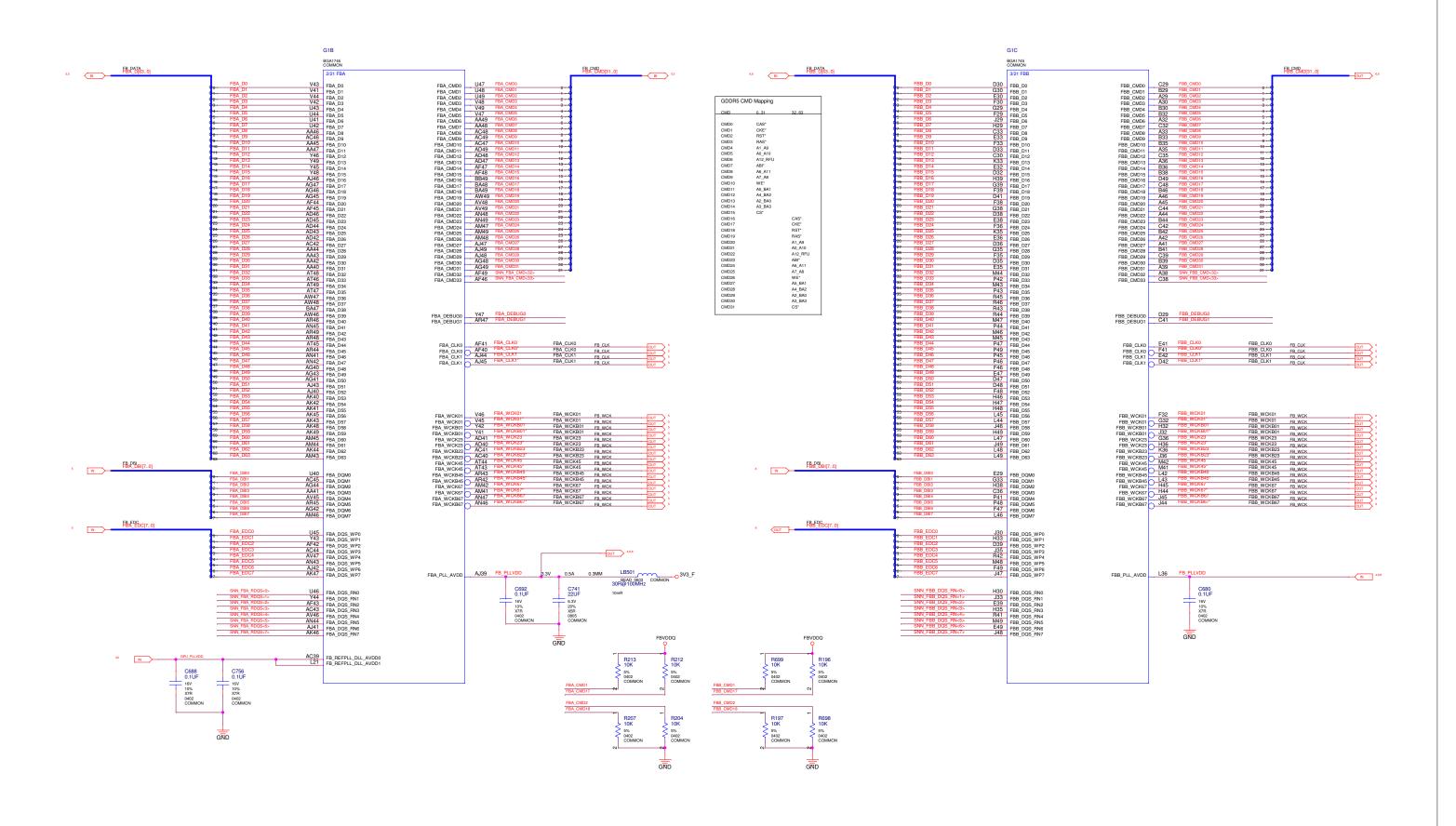






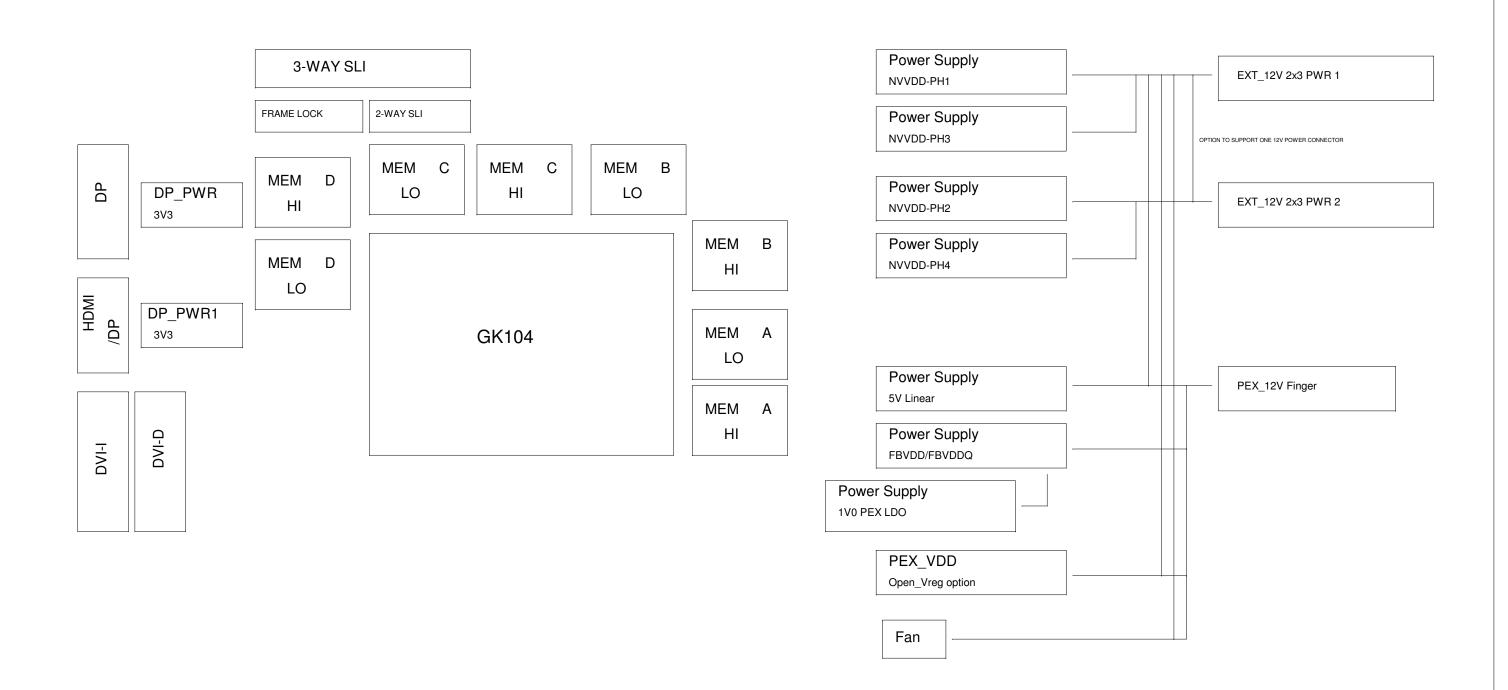






Page3: PCI Express





# 4/2GB GDDR5, 256/192b, 128Mx16/64Mx32 DVI-I+DVI-D (stacked) + HDMI/DP + DP

## SCH:A06

- 1: Change to INA3221
- 2:Change to 4021
- 3: Change FBVDDQ
- 4:8PIN ,6PIN colay
- 5: Reserver NVVDD PAD under GPU
- 6: add backplate hole
- 7:OVP Leval adjustable

SCH:A08

- 1:OV by jumpter
- 2:Dual BIOS