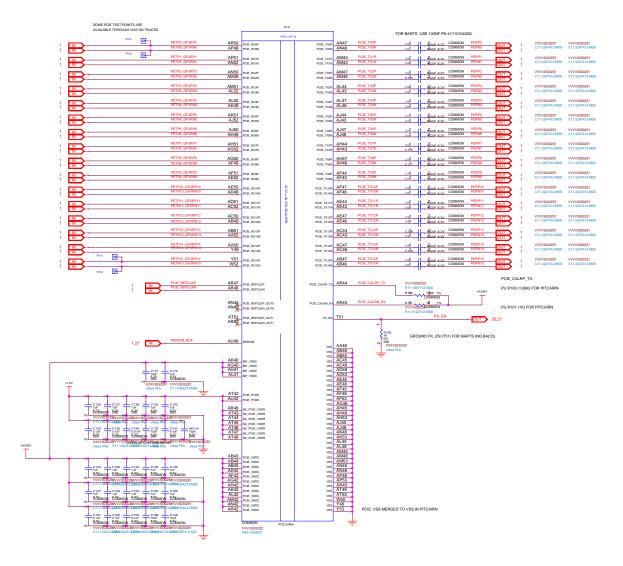
(2) CURACAO PCIE INTERFACE



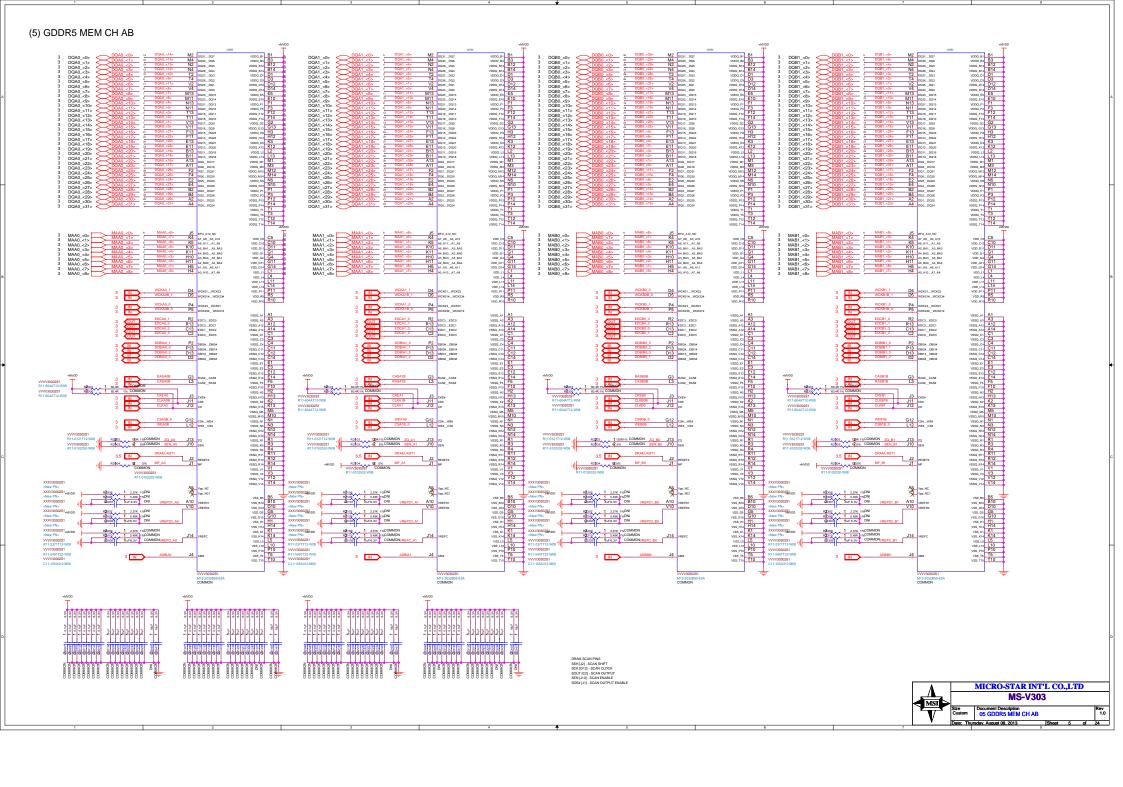


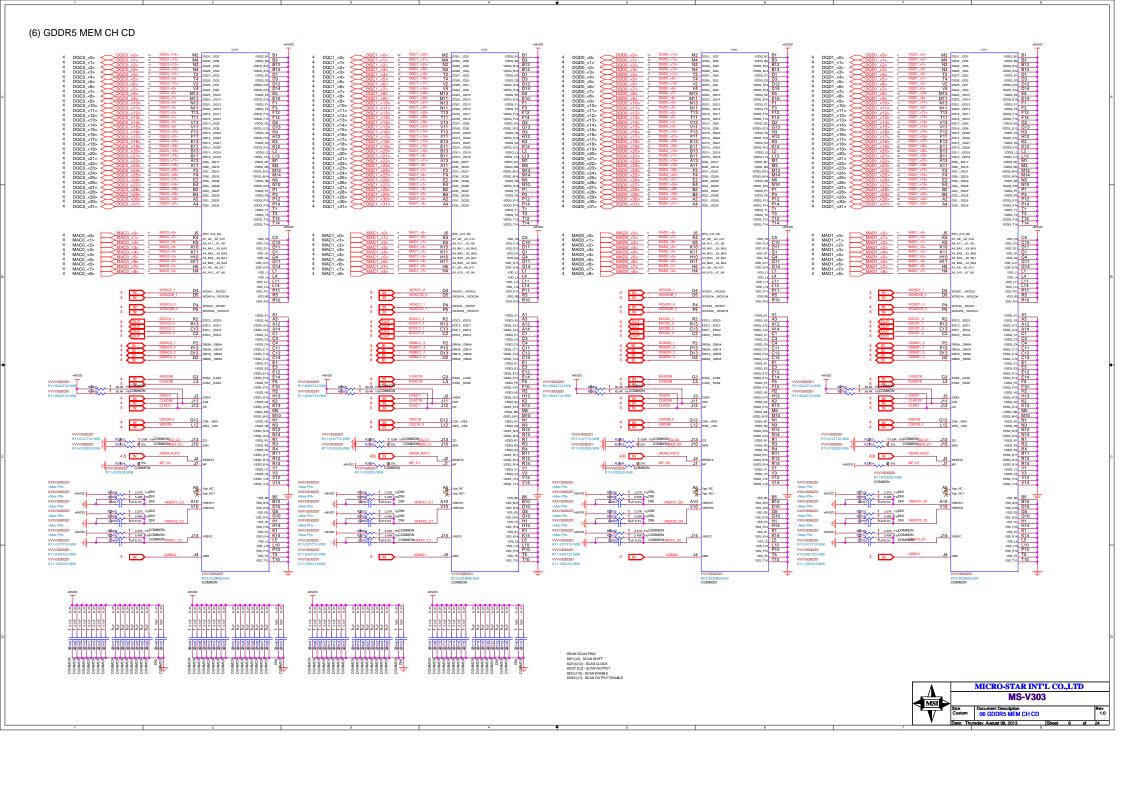
(3) CURACAO MEM INTERFACE CH AB | OSB | Ch | E34 | OSB | PART 4 OF 18 DOAQ - CBDOAQ - CB-0081.db 0081.db 0081.dc DOB9, 40DOB9, 45DOB9, 41DOB9, MAA1_<0> MAA1_<1> MAA1_<2> MAA1_<2> MAA1_<2> MAA1_<3> MAA1_<3 MAA1_<3> MAA1_<3 MAA1_ 5 MABO_<0> 5 MABO_<1> 5 MABO_<2> 5 MABO_<3> 5 MABO_<3> 5 MABO_<5> 6 MABO_<5> 5 MABO_<5> 5 MABO_<5> 6 MABO_<7> 5 MABO_<8> MABO_<8> MAAO_<0> MAAO_<1> MAAO_<2> MAAO_<3> MAAO_<4> MAAO_<5> MAAO_<6> MAAO_<6> MAAO_<7> MAAO_<8> MAB1_<0> MAB1_<1> MAB1_<2> MAB1_<2> MAB1_<3> MAB1_<4> MAB1_<5> MAB1_<5 M49 DOBAG_0 R46 DOBAG_1 H49 DOBAG_2 F46 DOBAG_3 ADBIA1 K39 G46 ADMAN _{АОВІВ1} Н18 CKEAI G42 CKEA CKEB1 L19 CKEB1 OUT R3602 40.3R 1% DNI XXXV30: «New PNs U42 NC_MEM_CALRPO USE INTERNAL VREF USE INTERNAL VREF U1Z NC_MEM_CALRN C3602 R3606 10F 100R 6.3V 1% DNI DNI XXXX3330239XXXV Cassos full BNY R3607 100R DNI | DRAM_RST1 | 12 | 1 | 42.98 | 1% | DRAM_RST1_RR R3612 5.1K 1% COMMON VVVV30302S1 R11-0512T12-W

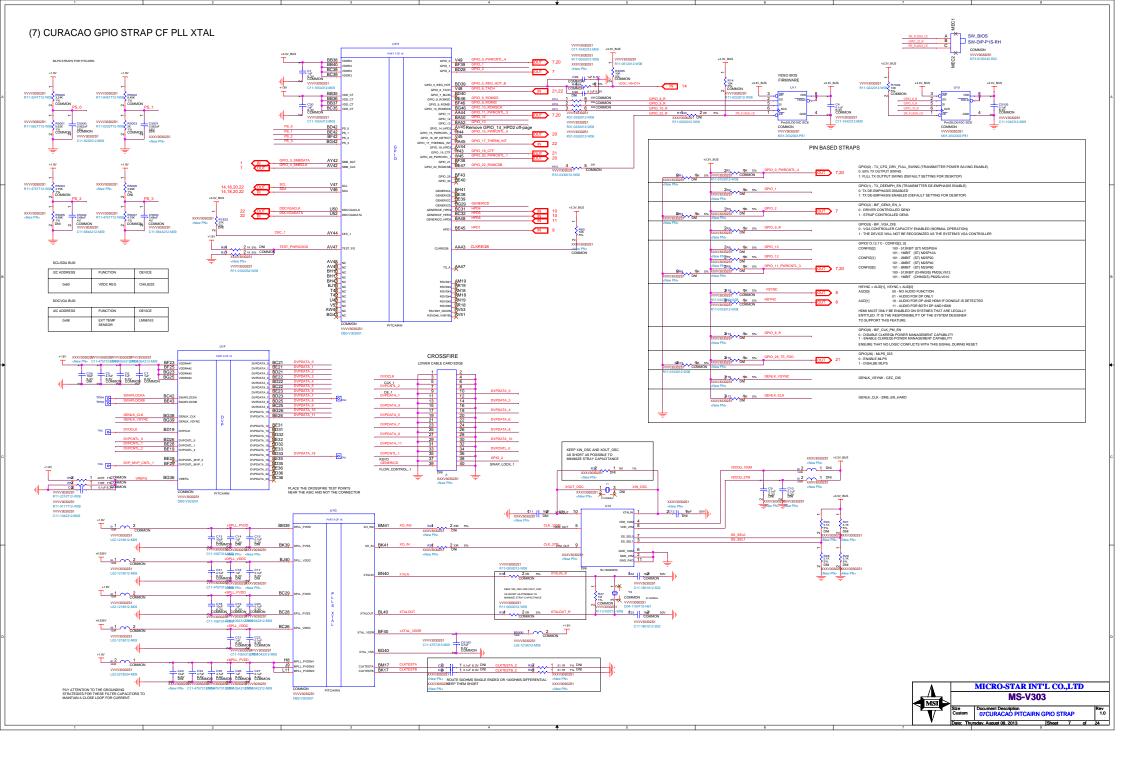


(4) CURACAO MEM INTERFACE CH CD 000 (b) AFP 10003 000 (c) AFP DOCU_45DOC 0001.4b 0001.4b 0001.4c 0001.4 DODD - clbDODD -00011.do 0001.do MACI,0 AC12 MAC MACI,1 AC11 MAC MACI,2 AF11 MAC MACI,3 AF12 MAC MACI,4 AH11 MAC MACI,6 AA12 MAC MACI,7 AA11 MAC MACI,8 AB10 MAC MACI,9 AM10 MAD1_<0> MAD1_<1> MAD1_<1> MAD1_<2> MAD1_<3> MAD1_<4> MAD1_<5> MAD1_<6> MAD1_<6> MAD1_<6> MAD1_<6> MAD1_<6> MAD1_<6> MACO_<0> MACO_<1> MACO_<2> MACO_<3> MACO_<4> MACO_<5> MACO_<6> MACO_<7> MACO_<7> MACO_<8> MAC1_<0> MAC1_<1> MAC1_<2> MAC1_<2> MAC1_<3> MAC1_<3> MAC1_<4> MAC1_<5> MAC1_<6> MAC1_<7> MAC1_<7> MAC1_<7> MADO_<0> MADO_<1> MADO_<2> MADO_<3> MADO_<4> MADO_<5> MADO_<6> MADO_<7> MADO_<7> MADO_<8> AJ8 DDBIDG_0 AU12 DDBIDG_1 AT3 DDBIDG_2 BA4 DDBIDG_3 ADBIC1 AB11 ADBIC1 AW10 ADBID1 BF7 CKEC1 AA9 CKEC1 OUT R3616 40.28 1% DNI XOXV30302S1 <New PNS 1 120R 1% MEM_CALRP VVVV30302S1 COMMON R11-0121T12-W08 BB17 MEM_CALRP2 BA16 R3621 100R 100R C3611 R3520 100R 1% DNI MVREFSD BA13 6 OUT DRAM_RST2 N2822 1 42.9R 1% DRAM_RST2_RR VVVV3030251 DOMMON BC18 R3627 5.1K 1% COMMON VVVV30302S1 R11-0512T12-W MICRO-STAR INT'L CO.,LTD Λ MS-V303 MSI Document Description 04 CURACAO PITCAIRN MEM CH CD

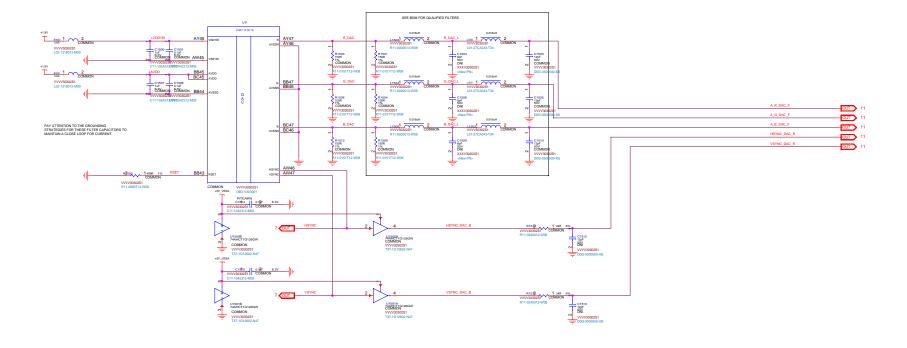
Rev 1.0







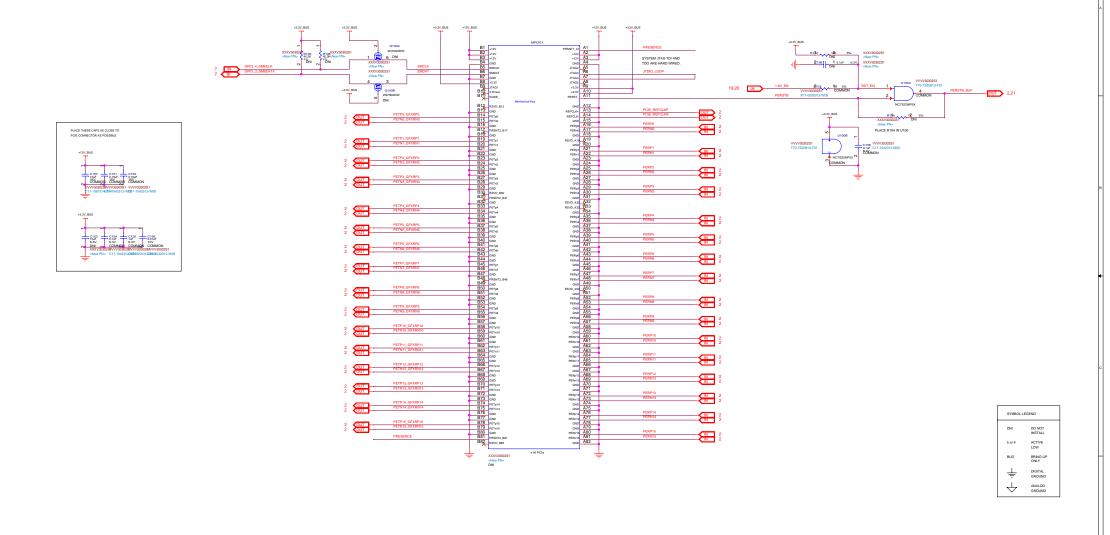
(8) CURACAO DAC



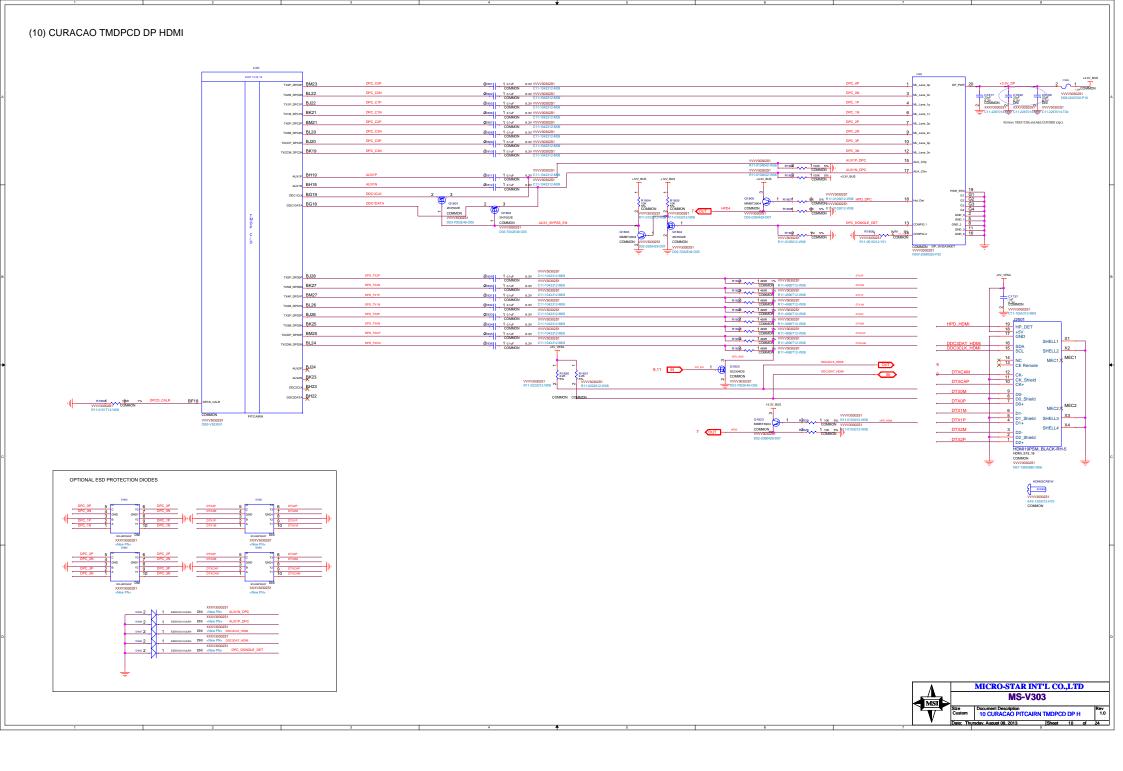
	_					A_R_DAC_F	
	D1500 2	ĸ	1	ESDEVSURU-KSLRH	DNI	A, R, DAC, F	
	D1501 2	К	1	ESSEVSURU-ISLEM	DNI	A_G_DAC_F	
I	DISSE 2	И	1	ESDEVSURU-IZIJAH	DNI	A_B_DAC_F	
I	01505 2	И	1	ESDEVOR:B-02LRH	DNI	HSYNC_DAC_R	
	D1506 2	И	1	ESCHYDR1B-02LRH	DNI	VSYNC_DAC_R	

	A		MICRO-STAR INT'L CO.,LTD				
	MSI	MS-V303					
		Size Custom	08 CURAÇÃO PITCAIRN DAC	Rev 1.0			
		Date: Thu	rsday, August 08, 2013 Sheet 8 of	24			
7			8				

(9) CURACAO TMDPAB sDVI BF33 DP_VDDR BG33 DP_VDDR BH32 DP_VDDR BH33 DP_VDDR BH34 DP_VDDR BH35 DP_VDDR B2 TMDS_Data2+ R1720 499R 1% COMMON R1721 499R 1% COMMON DPA_TX2N B1 TMDS_Data2 C1740 tuf COMMON VVVV30302S1 = C11-1052F00 + C1700 + C1701 + C1702 + C1738 88Kmpg BRY BRY BRY TX2M_DPAGN BL32 C1721 0.1ulf 6.3V COMMON TX1P_DPA1P BJ32 B10 TMDS_Data1+ C1722 0.1uF 6.3V COMMON R1722 499R 1% COMMON TXIM_DPAIN BK31 B9 TMDS_Date1 R1723 499R 1% COMMON тхор_прадр ВМЗ1 C1724 0.1uF 6.3V COMMON B18 TMDS_Date0+ R1724 499R 1% COMMON TXIM_DPA2N BL30 DPA_TXIN C1725 0.1uF 6.3V COMMON B17 TMDS_Date0-R1725 499R 1% COMMON DPA_TXCP TXCAP_DPA3P BJ30 C1726 0.1df 6.3V COMMON R1725 429R 1% COMMON B23 TMDS_Clock+ TXCAM_DPAIN BK29 B24 TMDS_Clock C1727 0.1uf 6.3V COMMON R1727 429R 1% COMMON B21 TMDS_DateS R1725 429R 1% COMMON B20 TMDS_Dated R1729 499R 1% COMMON R1730 499R 1% COMMON B4 TMDS_Date4 R1731 499R 1% COMMON DDCCLK_AUXBR BM29 BF28 DP_VDDC BF31 DP_VDDC BG31 DP_VDDC BH26 DP_VDDC BH27 DP_VDDC BH28 DP_VDDC BH29 DP_VDDC B13 TMDS_Date3+ R1732 499R 1% COMMON DDCDATA AUXIN BL28 63Kmon 63Kmon 68V R1733 429R 1% COMMON BF47 NC_DP_VDDC BF46 NC_DP_VDDC BG40 NC_DP_VDDC BG46 NC_DP_VDDC B7 DDC_Data TXSM_DPBON BK37 DPB_TXSN M09 C1729 0.fulf 6.3V тхир_првтр ВМЗ7 M09 C1730 0.1uF 6.3V TX4M DPB1N BL36 B16 Hot Plug Densi C11-1042312-M09 C1732 0.1uF 6.3V VVVV30302S1 COMMON TX3M_DP82N BK35 M09 C1733 0.1uF 6.3V COMMON тхсви_пиваи ВМЗ5 TXCBM_DPB3N BL34 DDCCLK_AUXSP DDCDATA_AUXSN BK43 4 1 R1703 10K 5% R1704 10K 5% COMMON BF36 OPTIONAL ESD PROTECTION DIODES D1701 2 D1782 2 01794 2 1 ESDS/30110-3018H DNI one 2 DITHS 2 1 ESDEVISION-ISSEM DNI Drifts 2 1 ESDEVAUNU-ISLEN DNI MICRO-STAR INT'L CO.,LTD MS-V303 MSI Size Document Description Custom 09 CURACAO PITCAIRN TMDPAB dDVI

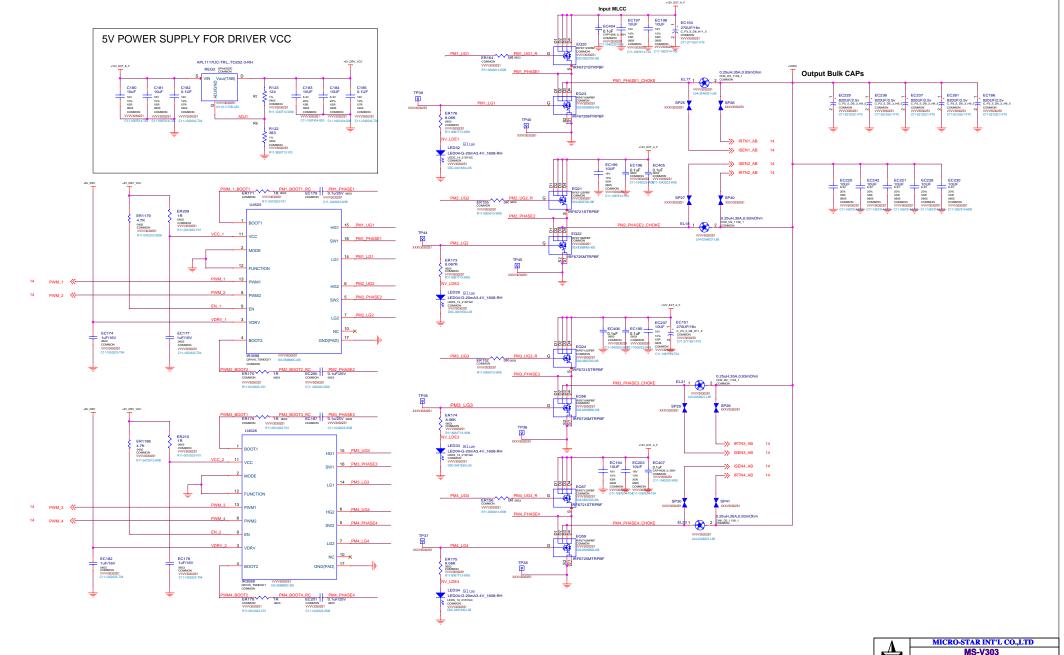




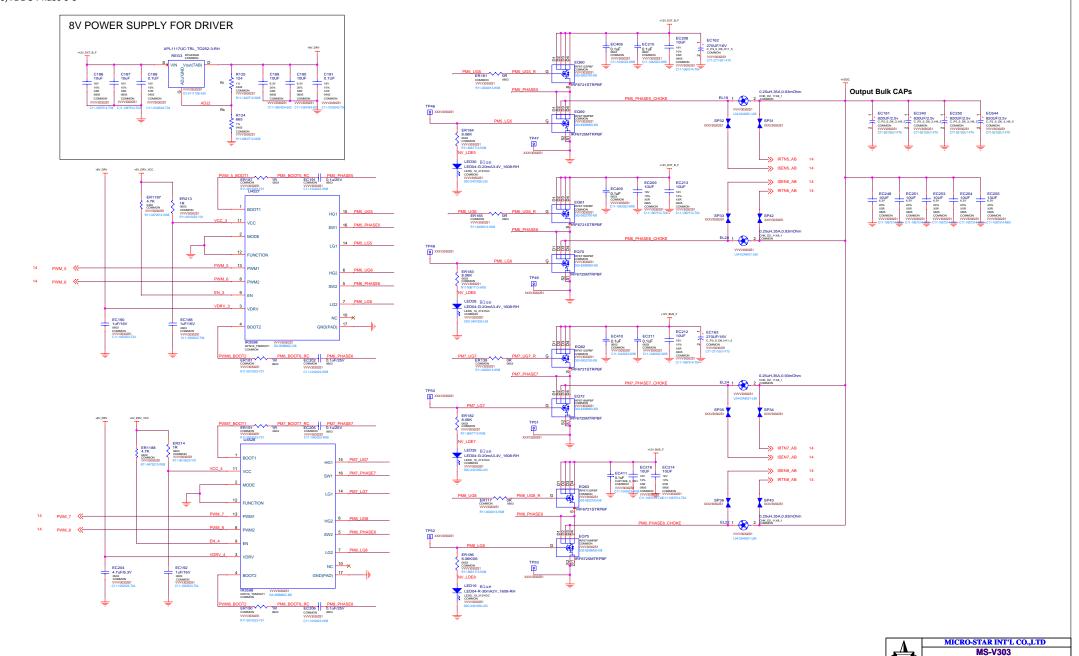


(12) CURACAO POWER AA13 VDDR1
AA6 VDDR1
AB12 VDDR1
AC13 VDDR1
AE6 VDDR1
AJ6 VDDR1
AL13 VDDR1 | Sec. | Artis | Artis | Artis | Sec. | Artis | CA108 fluF 6.3V 2005 ftuF 6.3V 2306 ftuF 6.3V Azsta 1 zar 4) Ozasa 1 zauf O218 1 ZuF 20219 1 20 COT Course for course Charle fur 63V Chara fur sav \$ CD 282 1 22 6 4V 2 2 2 4V S S Charlo flur 63N See hu say Page fur 631 Size fur east C285 870UF COMMON 9-20% 2-5V POSCAP 3-9A Benotm CAPT7343_1_90H C284 470UF COMMON +/-20% 2.5V POSCAP 3.9A 9mohm CAPT7343_1 Quan fur Add POSCAP in GPI I side 29-496 ftur 6.3V Pusy flue 63V 2033 fue 63V Bess for eav 2837 ftg 63V Bass for 63v 2001 1 20F 4V 2842 fuf 63N 2455 fur 63V G495 fur 63V 2457 fur 63V COMMON Mazas fur 63N S d278 fue 63V 2451 fur 63V 14g ftur 63V 2 S Q 4 so flur 63) OUT FB_VDDC AF41 FB_VSSC AG41 MICRO-STAR INT'L CO.,LTD Λ MS-V303 MSI Document Description
12 CURACAO PITCAIRN POWER Rev 1.0

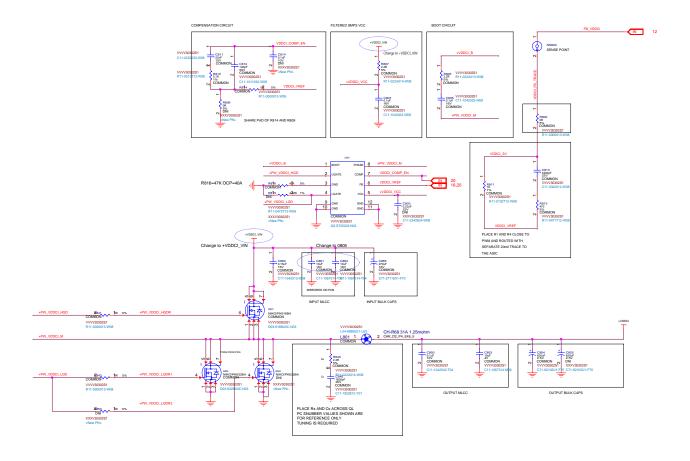




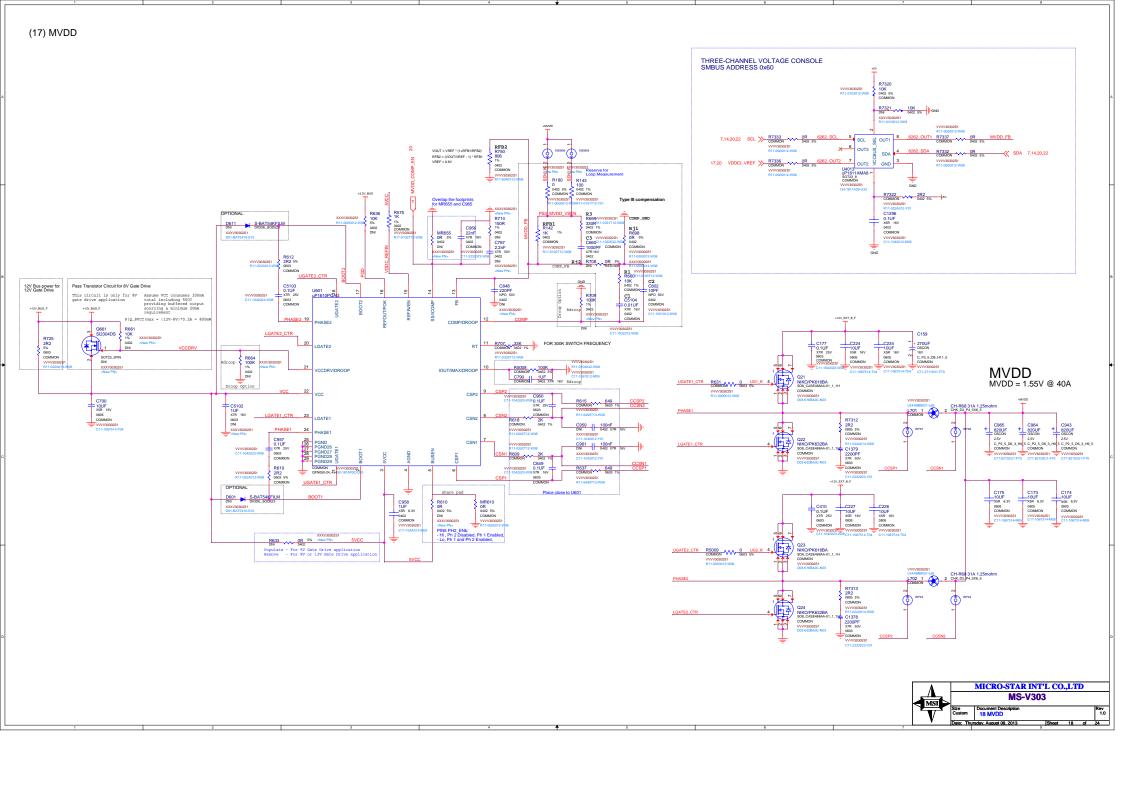
Document Description 15 VDDC Phase 1-4 day, August 08, 2013



Document Description 16 VDDC Phase 5-8 sday, August 08, 2013 (16) VDDCI



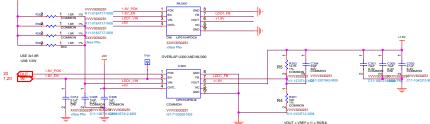




(18) SMALL RAIL REGULATOR

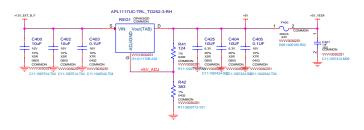


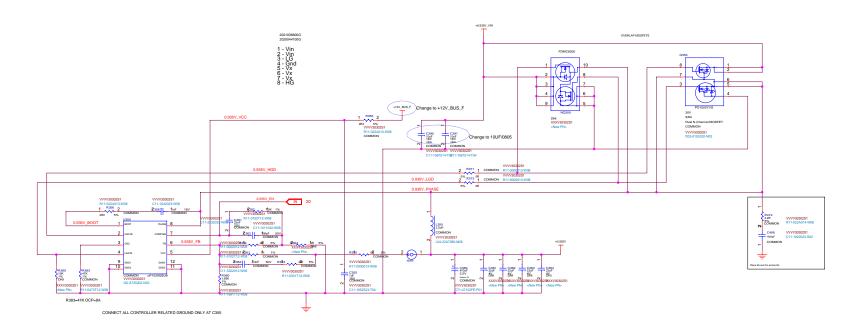




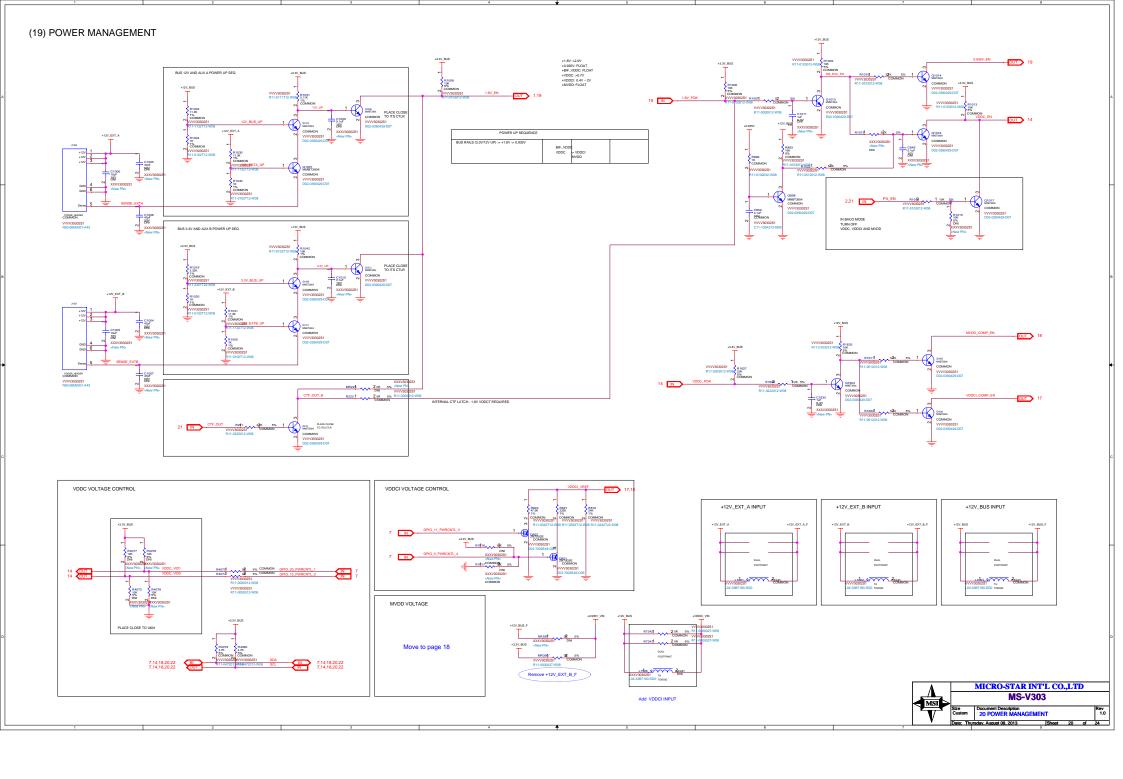
REGULATOR FOR +5V RAILS

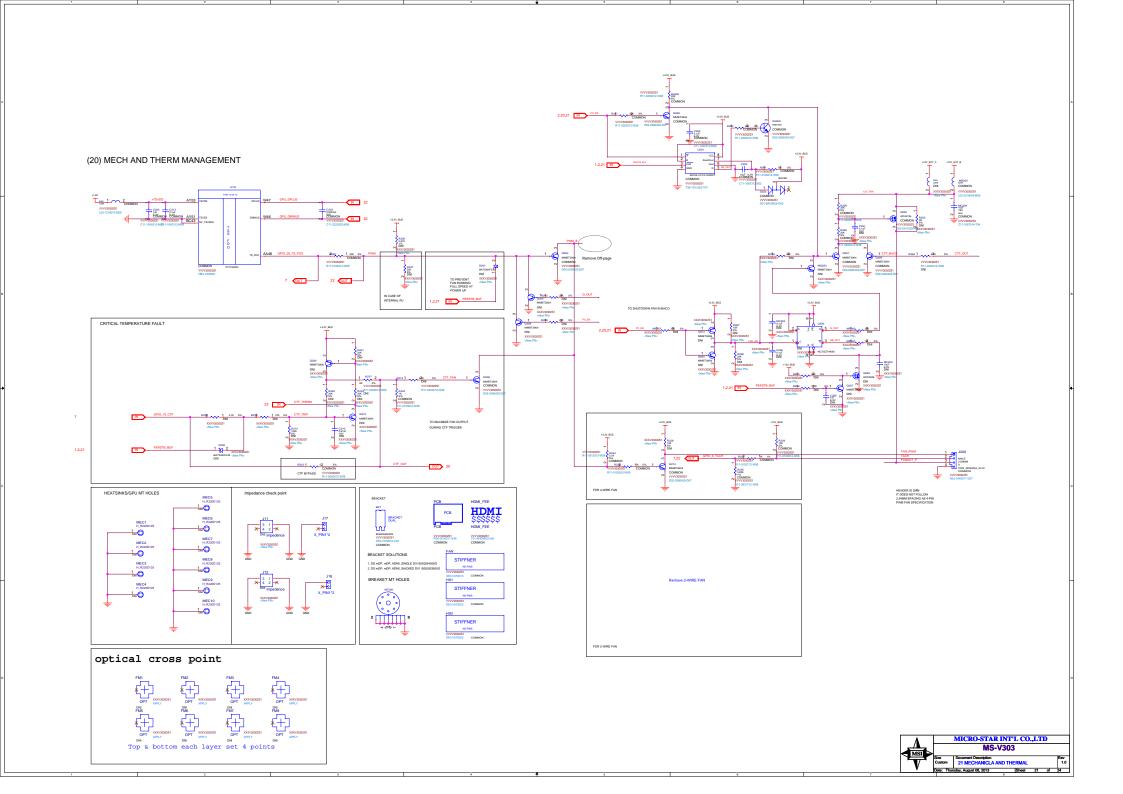
IOUT MAX = 150mA



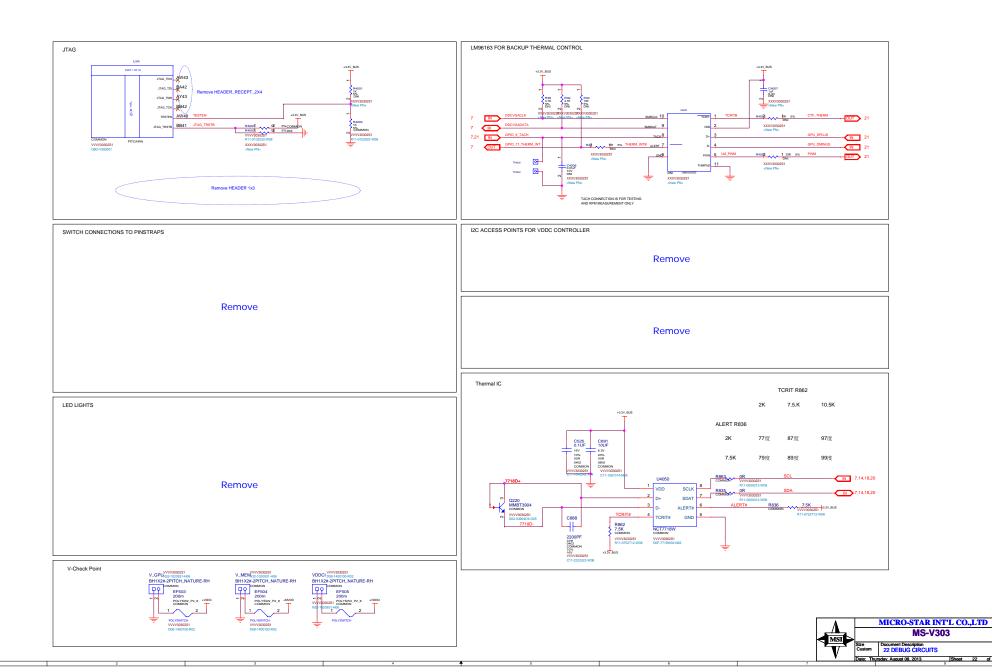


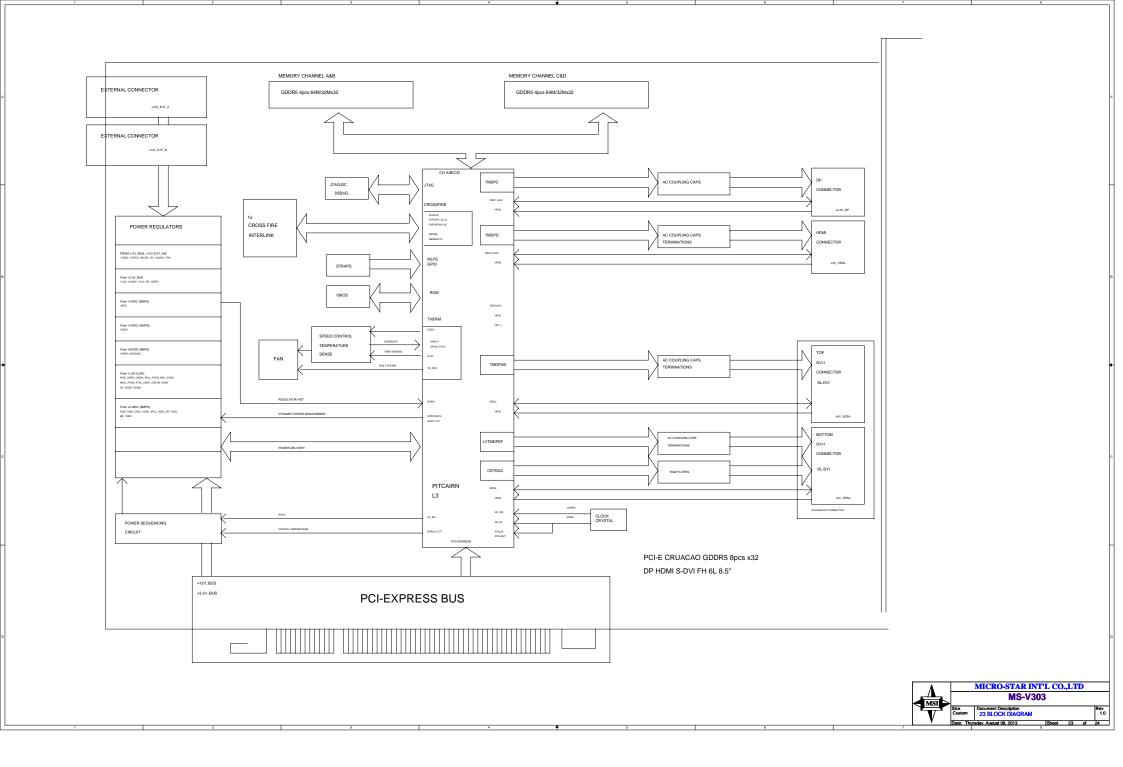






(21) DEBUG CIRCUIT





0 00A	12/07/2012	INITIAL SCHEMATIC BASED ON C400			
1 00B	30/10/2012	add R688,C98,C99			
1 00C	25/12/2012	remove L1720 ~ L1733			
		remove NC pin for DP_VDDR/DP_VDDC			
00D	19/06/2013	update power sequence to make 0.95V power up before vddc			
V303-1.0	01/08/2013	Page07 : Remove GPIO_14_HPD2 off-page			
		Add Dual-BIOS & Switch			
		Page10 : Change J2501 footprint and Library			
		Page11: Add optional ESD Protection DIODES			
		Page12 : Add C284/C285 (POSCAP)			
		Remove some 1UF/0402 of VDDC			
		Page14: Change VDDC controller to 8228G			
		Page15: Add Driver IR3598 circuit			
		Page16: Add Driver IR3598 circuit			
		Page17 : Change L801 footprint			
		Change VDDCI_VIN			
		Page18: Page14: Change MVDD controller to uP1610PQAG			
		Page19 : Change +5V circuit			
		Change 0.935V_VCC to +12V_BUS_F			
		Page20 : Add R7341/R7342/L1506			
		Del R1090~R1095			
		Page21: Remove 2-wire FAN circuit			
		Page22: Remove JTAG pin header			
		Remove SWITCH CONNECTIONS TO PINSTRAPS			
		Remove LED LIGHTS			
		Remove I2C ACCESS POINTS FOR VDDC CONTROLLER			
		Add V-Check Point and Thermal IC			
V303-1.1	06/08/2013	Page14: Controller change to IR3563B			