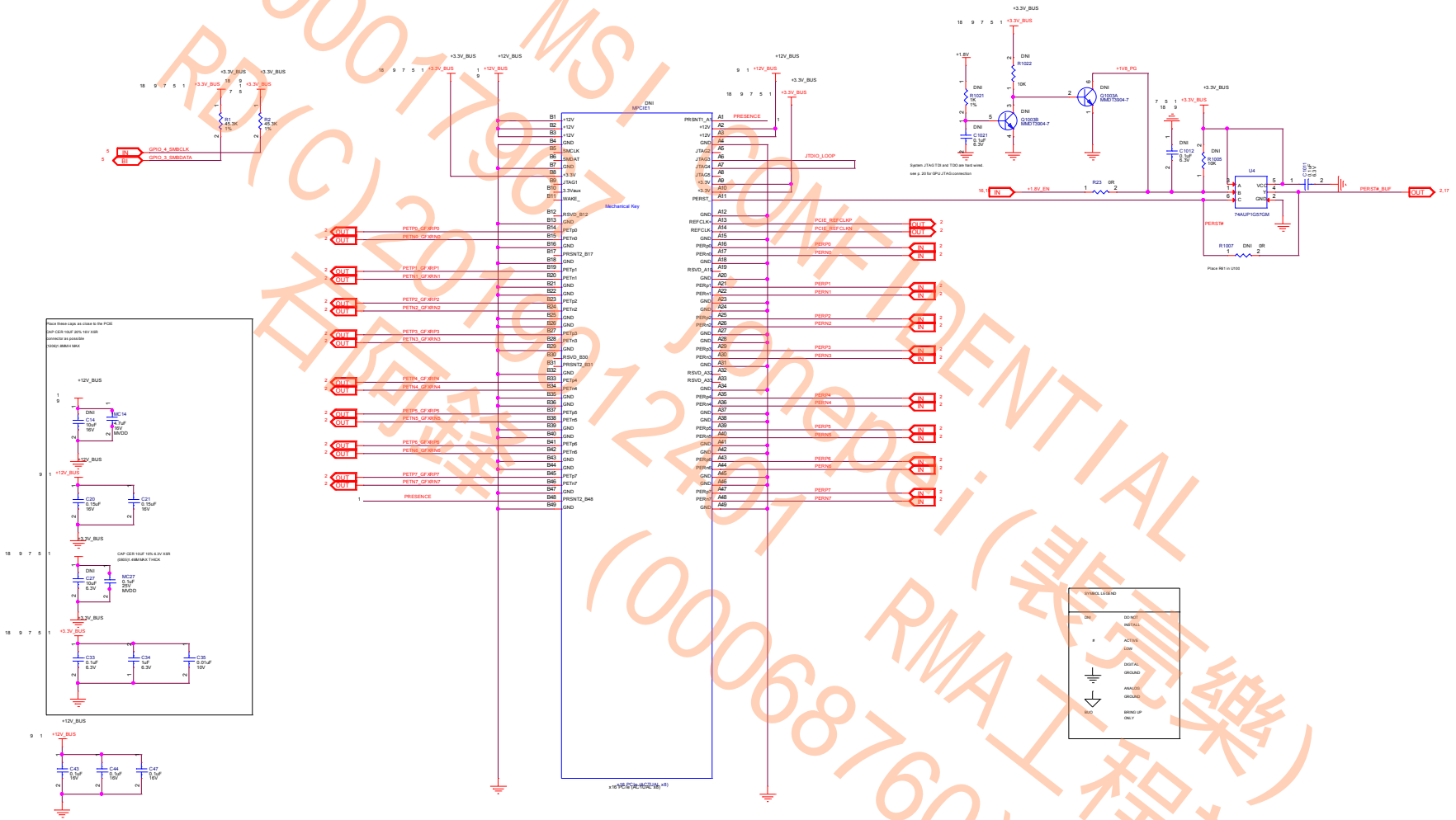

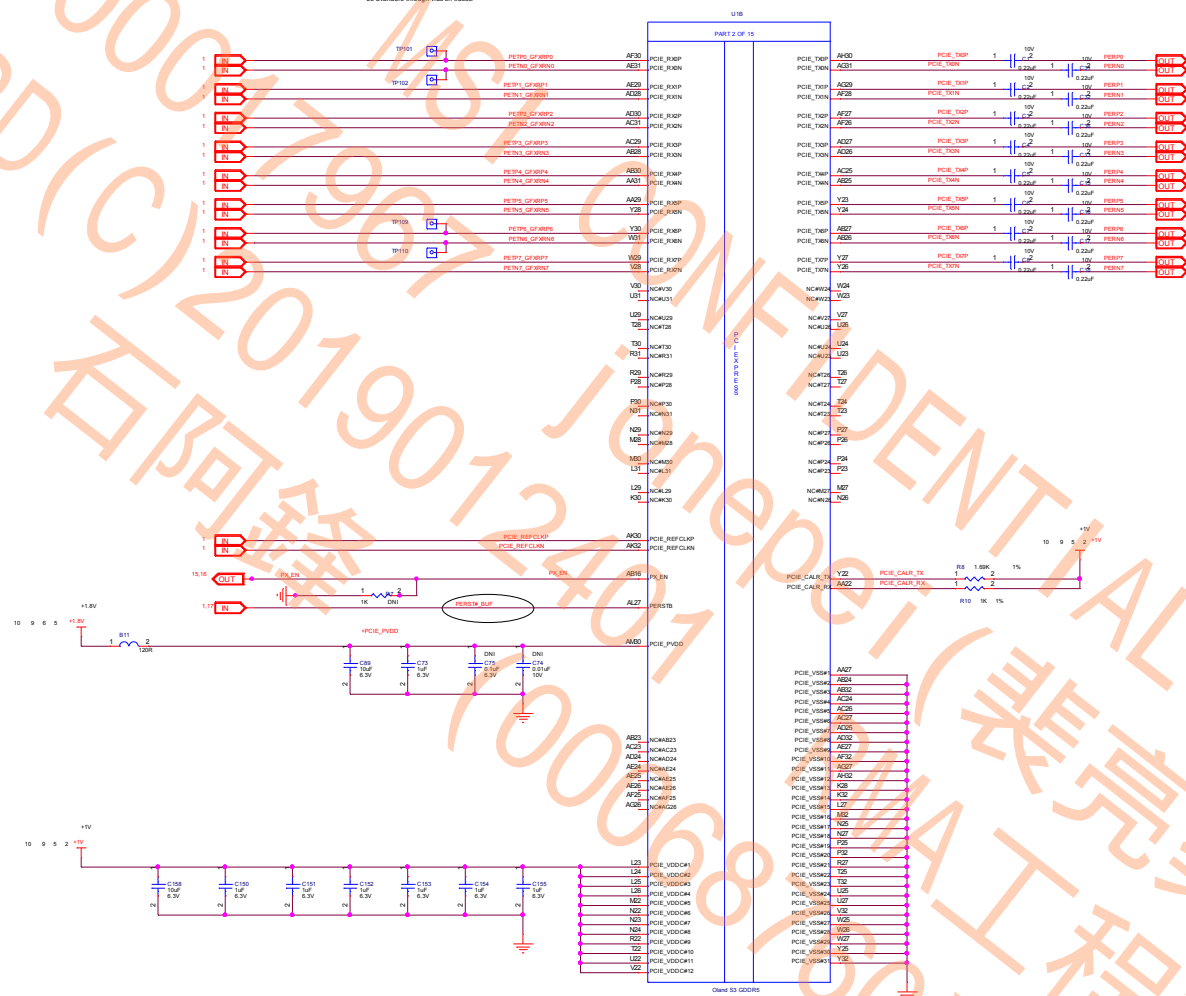


PCI-EXPRESS EDGE CONNECTOR



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SHEET: PCE Edge Connector			REV: P00		
DATE: Wed Aug 29 03:27:33 2018			TITLE:		
SHEET NUMBER: 1 OF 20			XXX		
DOCUMENT NUMBER: 105_d12900_00A					

NOTE: Some of the PCIE testpoints will be available through vias on traces.



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SHEET: 04and PCIex8 Interface

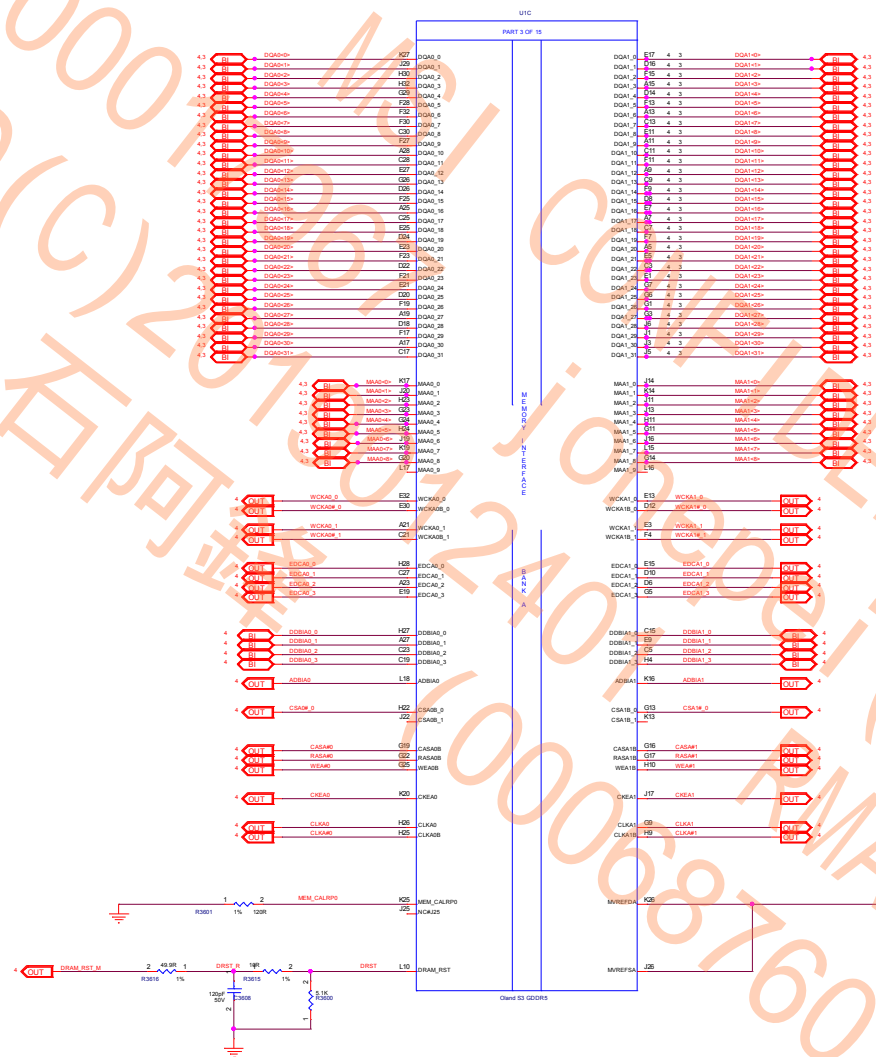
DATE: Thu Jan 05 05:41:57 2017


REV:	P00
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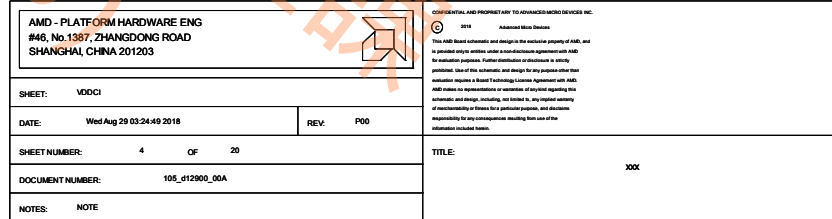
SHEET NUMBER: 2 OF 20

TITLE:

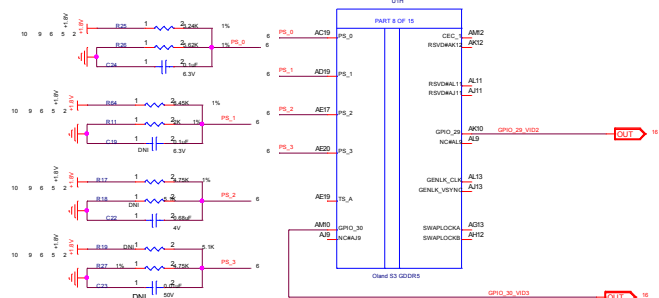
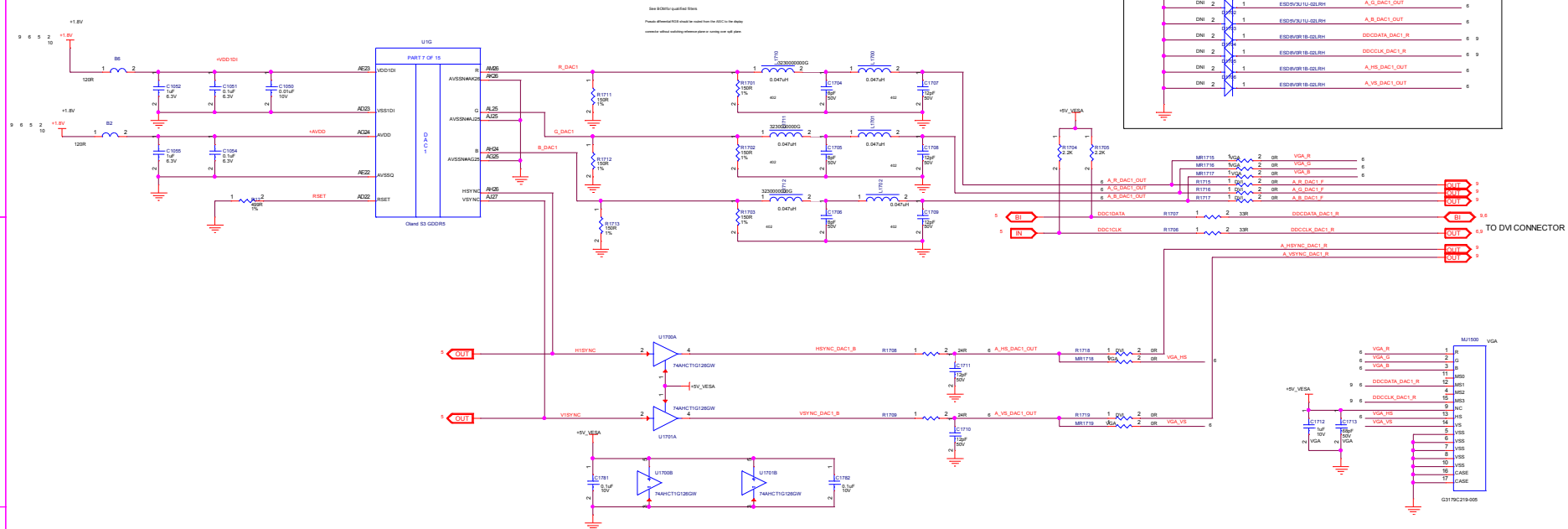
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DATE:	Wed Aug 29 03:24:49 2018	REV:	P00		
SHEET NUMBER:	3	OF	20	TITLE:	
DOCUMENT NUMBER:	105_012900_00A			XXX	
NOTES:	NOTE				



Oland DAC1



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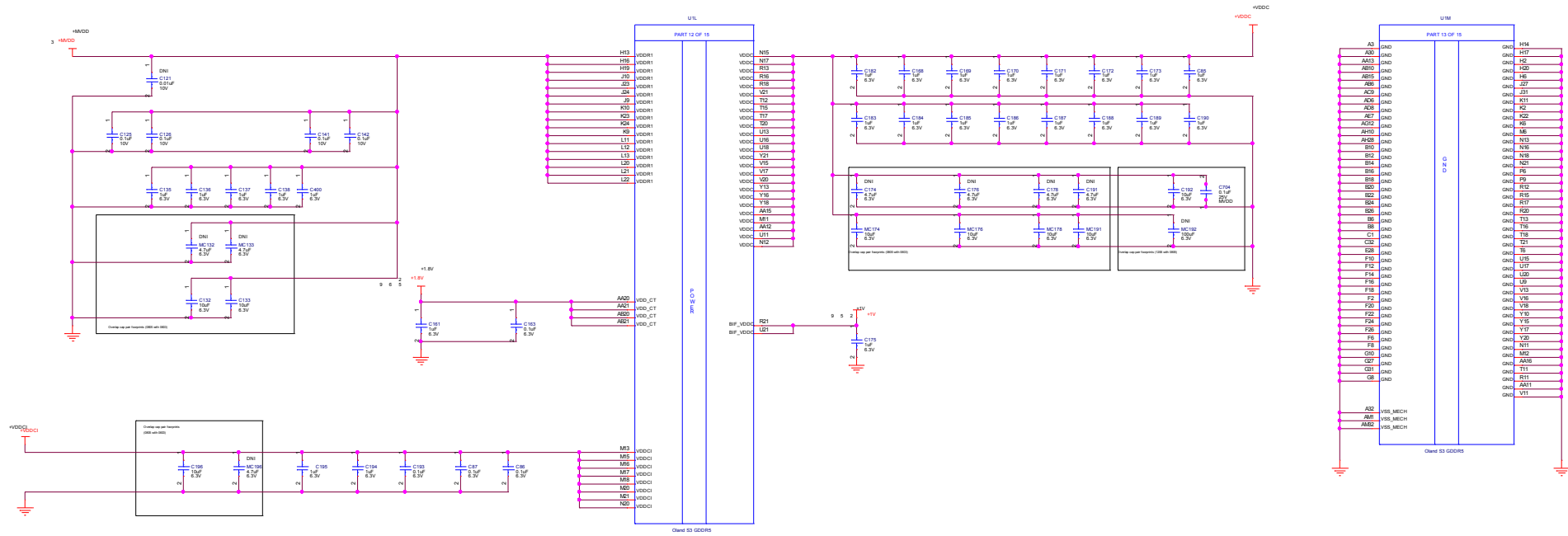
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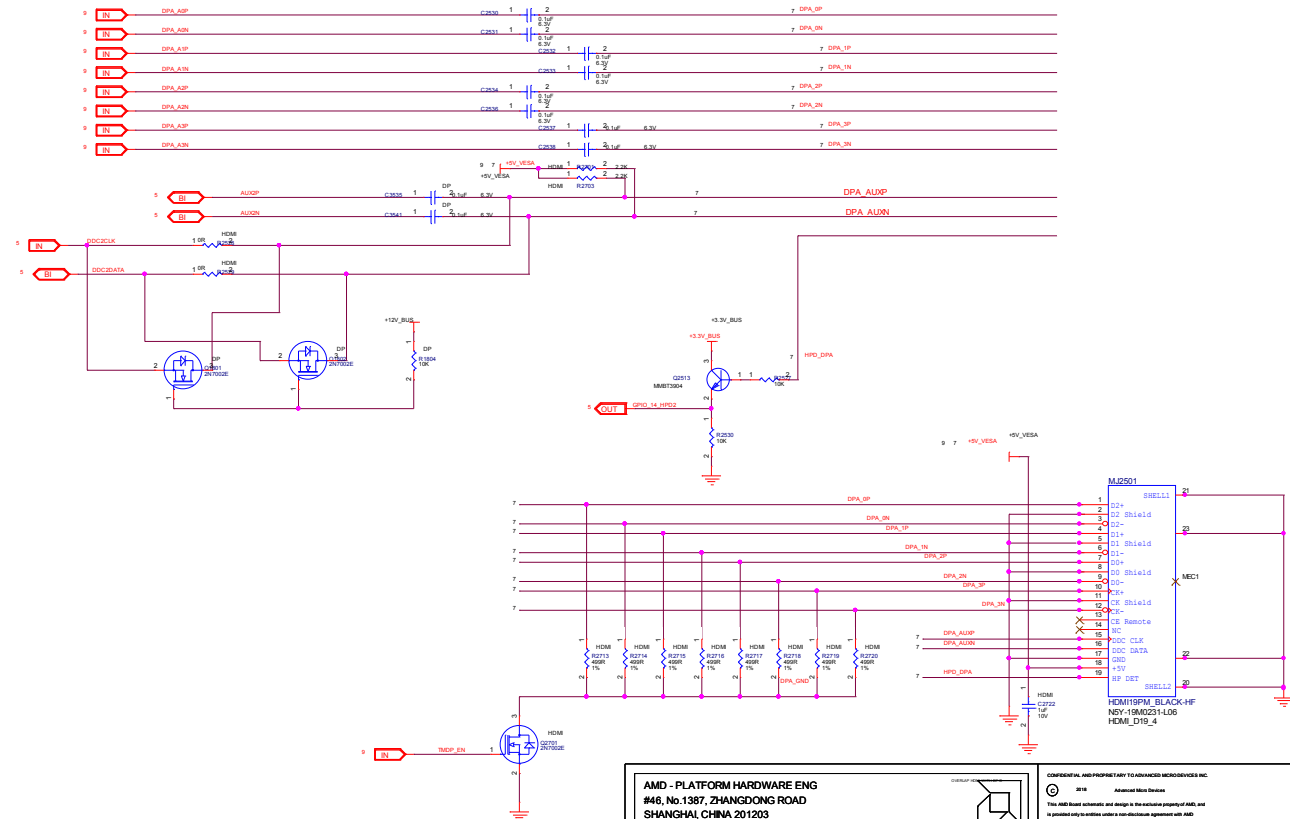
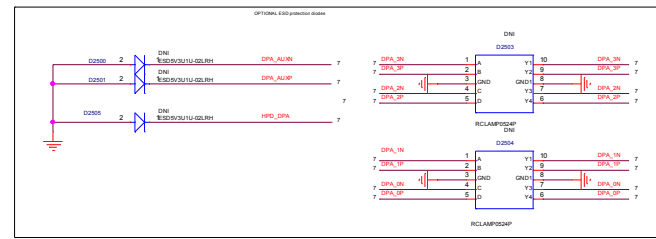
SHEET:	Oland DAC
DATE:	Thu Jan 1
SHEET NUMBER:	
DOCUMENT NUMBER:	


TITLE: _____

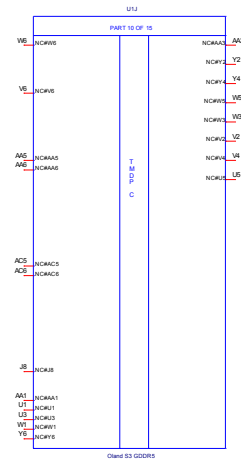
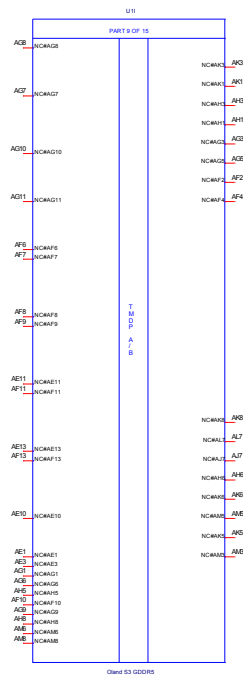
OLAND S3 Power & GND




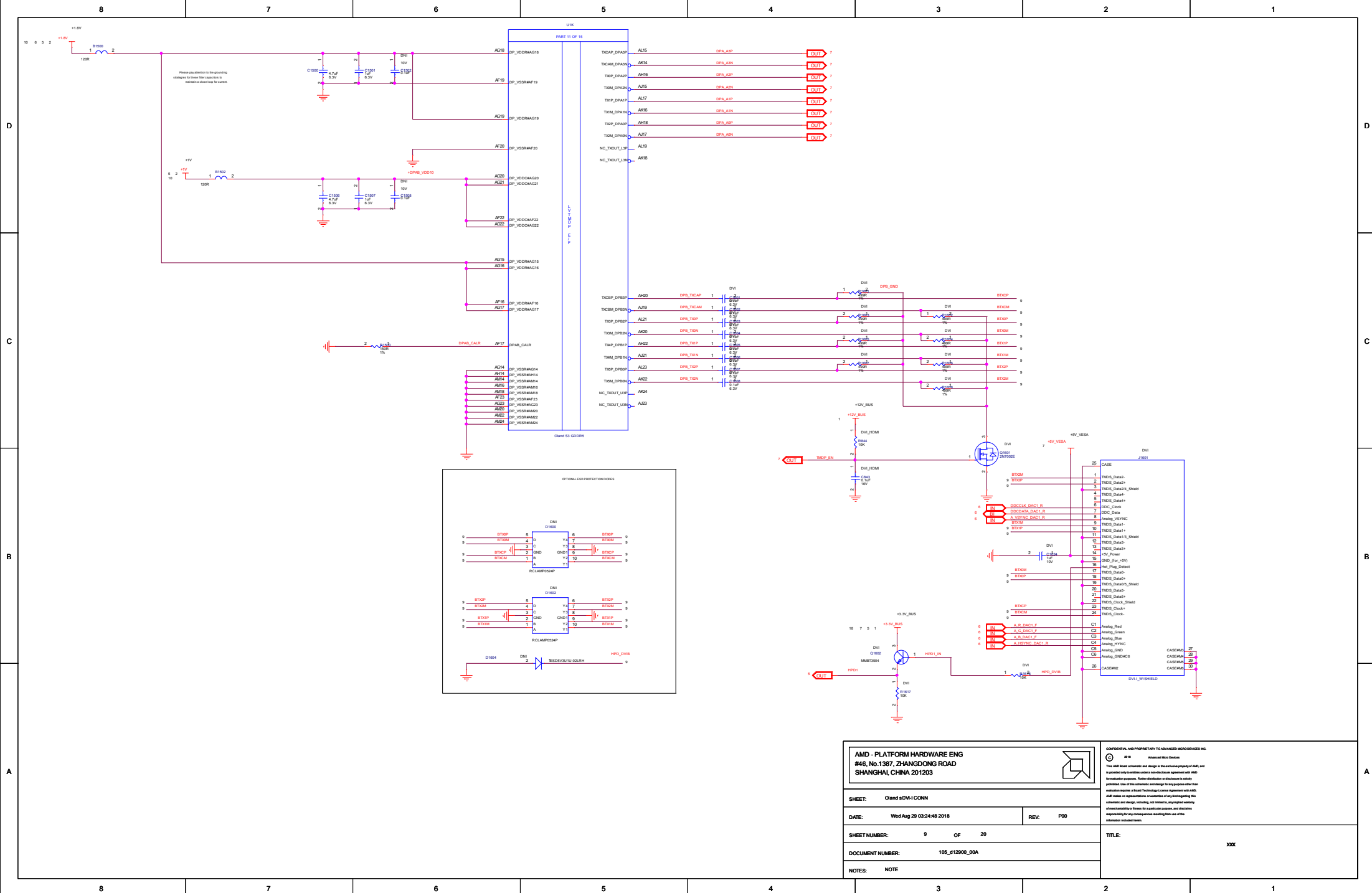
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SHEET: VDDCI		REV: P00		TITLE: XXX	
DATE: Wed Aug 29 03:24:49 2018					
SHEET NUMBER: 10 OF 20					
DOCUMENT NUMBER: 105_d12900_00A					
NOTES: NOTE					



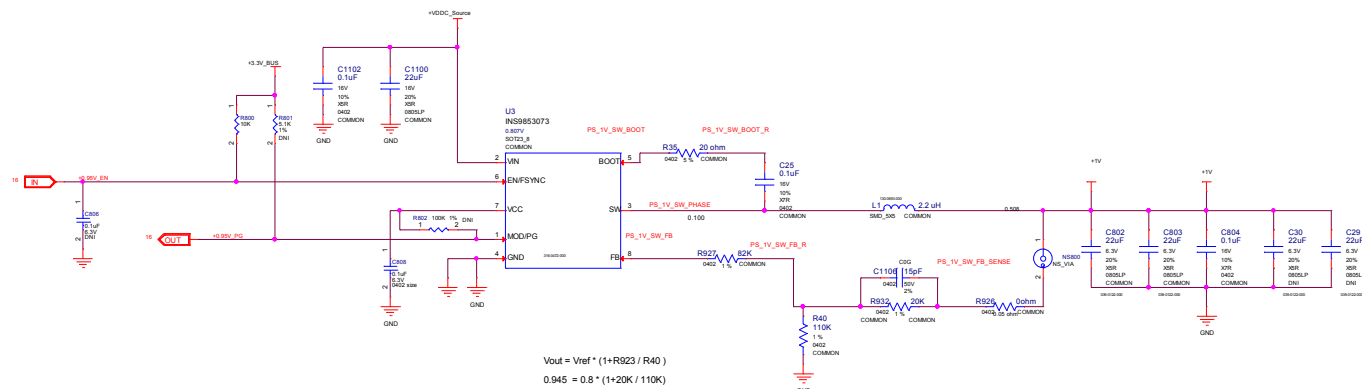
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<p>SHEET: Grand HDMI DP</p>		<p>REV: P00</p>	
<p>DATE: Thu Jan 05 05:41:56 2017</p>		<p>TITLE:</p>	
<p>SHEET NUMBER: 7 OF 20</p>		<p>XXX</p>	
<p>DOCUMENT NUMBER: 105_d12900_00A</p>			



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SHEET:	Grand Unused Modules				
DATE:	Wed Aug 29 03:24:48 2018			REV:	P00
SHEET NUMBER:	8 OF 20			TITLE:	
DOCUMENT NUMBER:	105_612900_00A			XXX	
NOTES:	NOTE				



LD0 #1:	$V_{in} = +1.9V \pm 2\%$	$V_{out} = +0.99V \pm 2\%$	$I_{out} = 1.44A (TV) RMS MAX$
PCB: 90 to 70mm sq. copper area for cooling			


$$0.945 = 0.8 * (1 + 20K / 110K)$$

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SHEET: VDDCI

DATE: Wed Aug 29 03:24:49 2018

REV: P00

SHEET NUMBER: 11 OF 20

DOCUMENT NUMBER: 105_d12900_00A

NOTES:	NOTE
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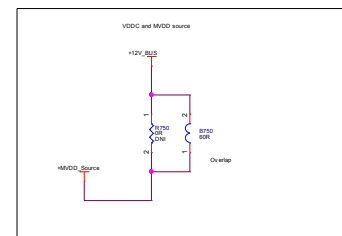
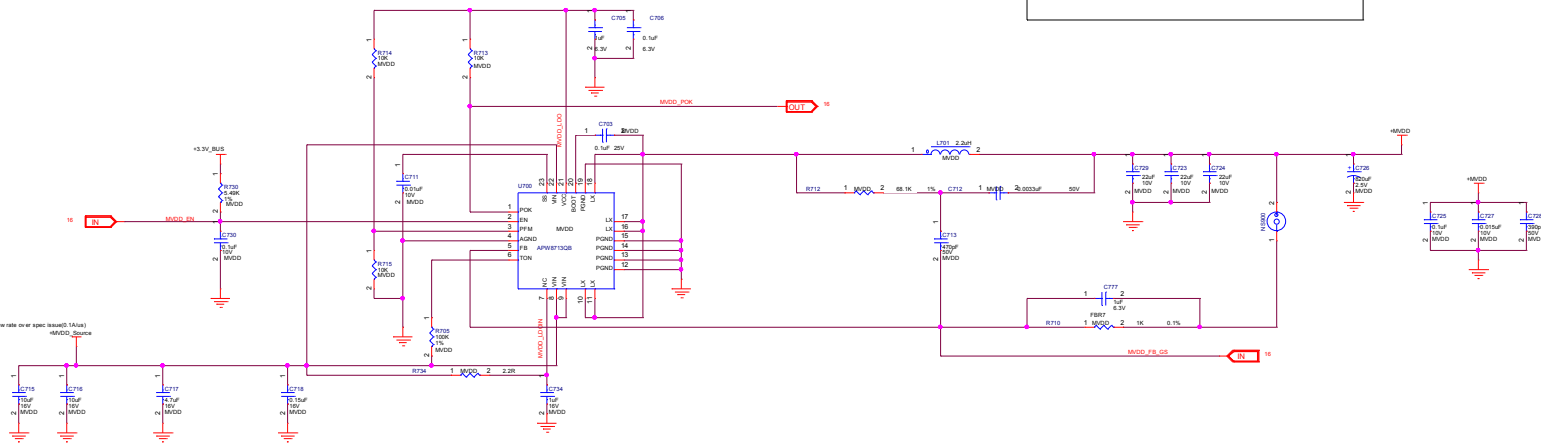
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TITLE

XXX



REV: P00

REV: P00

TITLE:

xxx

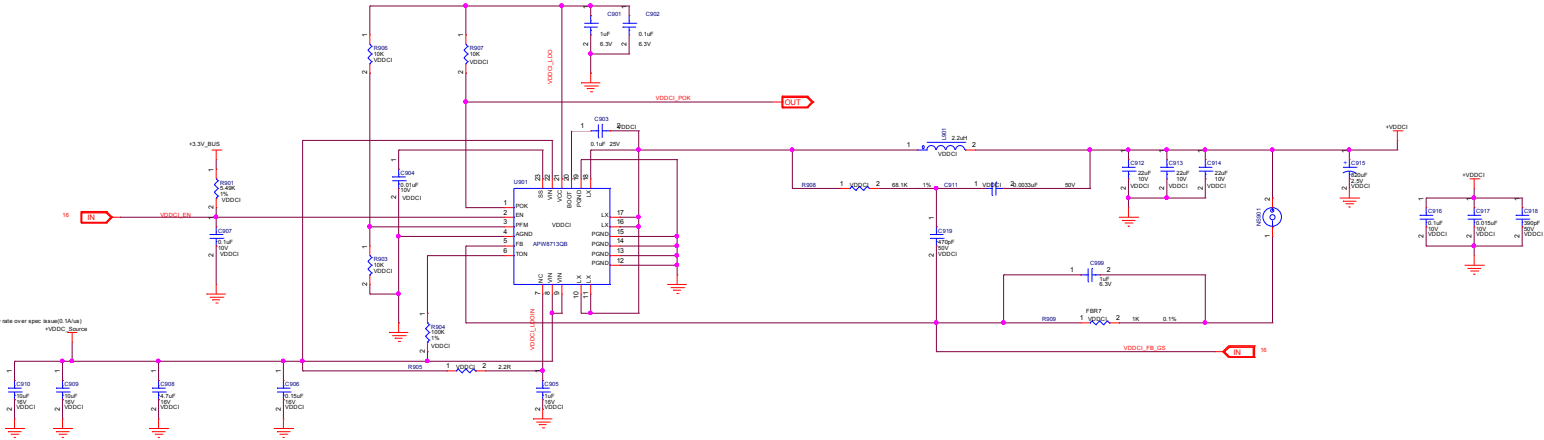
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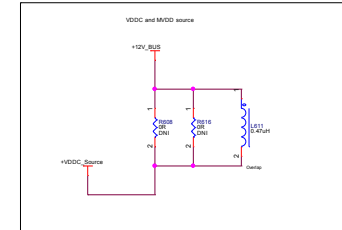
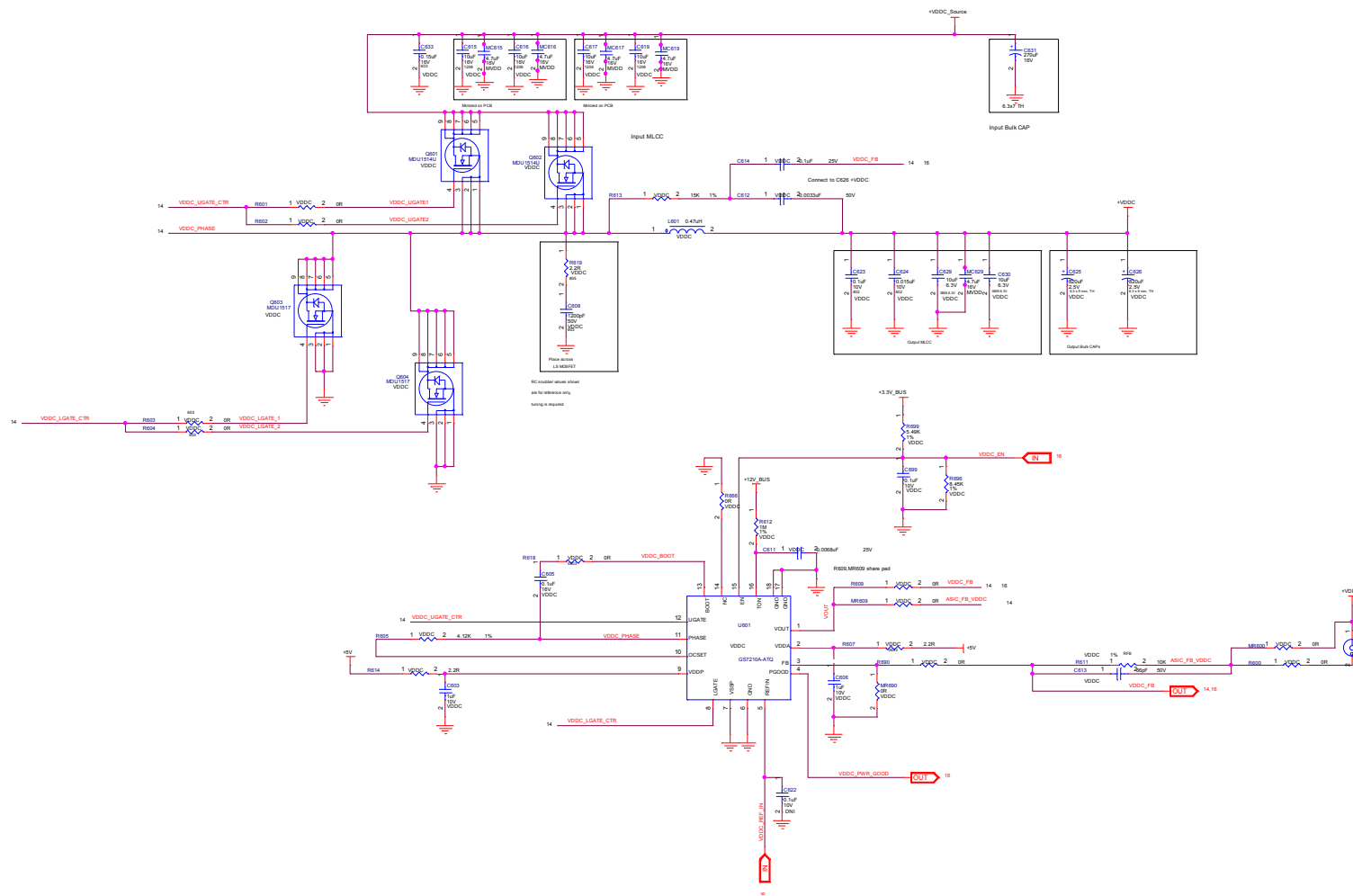
Register for MVD

Use rVDDC_Source to share L801 to eliminate PCIe 12V current slew rate over spec issue(0.1A/us)
rVDDC_Source



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SHEET: VDDC1	
DATE: Wed Aug 29 03:24:49 2018	REV: P00
SHEET NUMBER: 13 OF 20	
DOCUMENT NUMBER: 105_012900_00A	
NOTES: NOTE	

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SHEET: VDDC

DATE: Thu Jan 05 05:41:56 2017

REV: P00

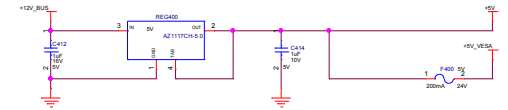
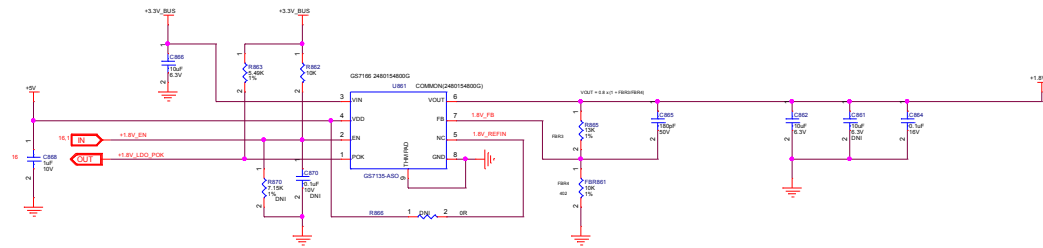
SHEET NUMBER: 14 OF 20

TITLE:

xxx

DOCUMENT NUMBER: 105_d12900_00A

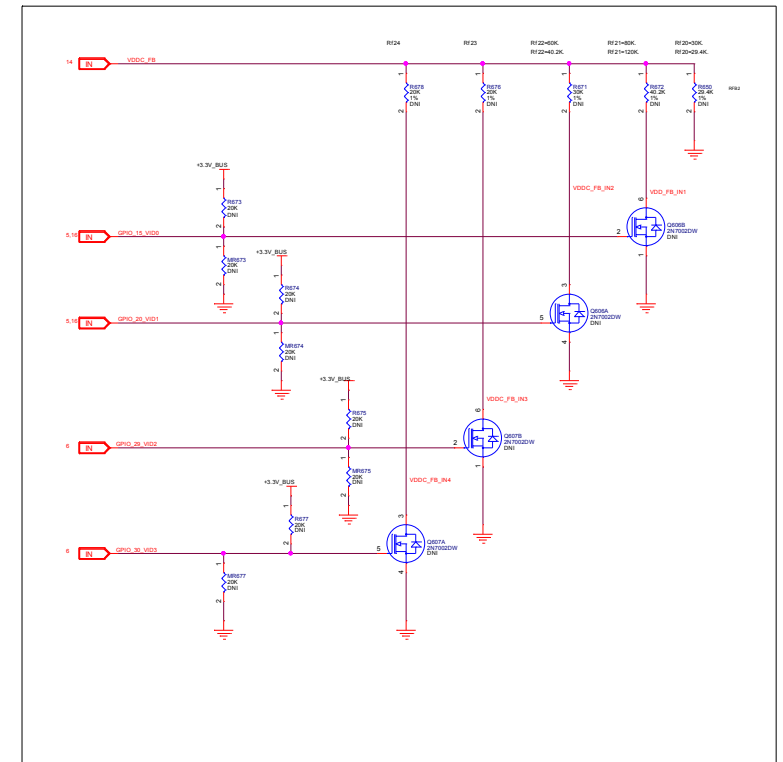
LDO R2:	Vin = 3.00V to 3.60V (3.3V +/- 5%)	Vout = +1.8V +/- 2%;	Iout = 1.6A (TbV) RMS MAX
PCB: 50 to 70mm sq. copper area for cooling			



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TITLE: XXX

POWER SEQUENCE
+1.8V-2.8ms->AVDD-3.4ms->0.95V-4.3ms->VDDC
0.95V must ramp up before VDDC

[illegible][illegible]

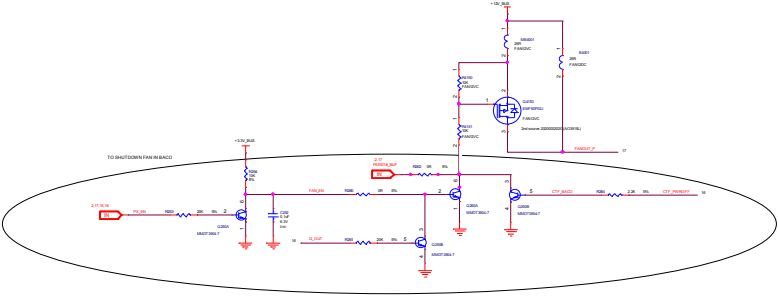
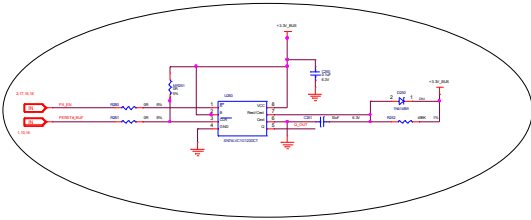
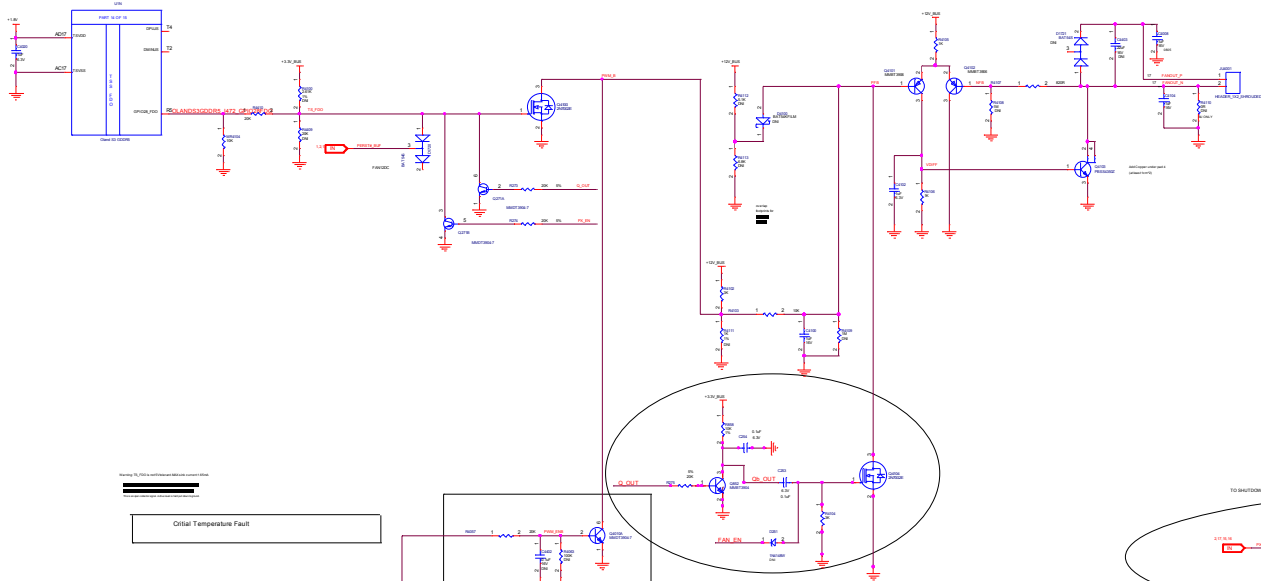
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TITLE:	XXX
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Initial PBD	OPN0000 Rev0000 OPN0000 Rev0000 For testing
Initial PBD (Initial PBD)	OPN0000 Rev0000 OPN0000 Rev0000 For testing

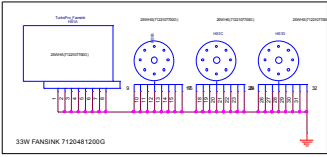
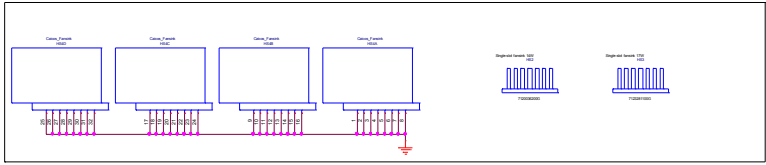
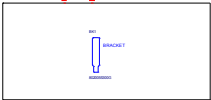
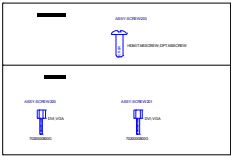


Mechanical and Thermal Management



Critical Temperature Fault

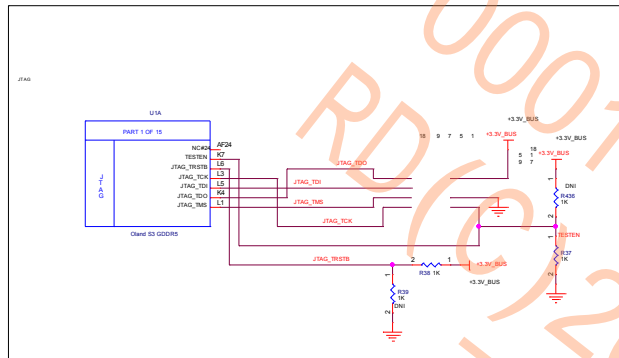
Critical Temperature Fault



Shanghai VCA Model D14	8000000000
Shanghai VCA D14M1 D14	8000000000
Shanghai VCA D14M1 D14	8000000000
Shanghai VCA D14M1 D14	8000000000

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DATE: 2018 Aug 28 10:34:00		REV: P00	
SHEET NUMBER: 17 OF 20		DOCUMENT NUMBER: V00000000	
NOTES: NOTE		TITLE: XXX XXX	

FM1 SW_FB
FM2 SW_FB
FM3 SW_FB
FM4 SW_FB
FM5 SW_FB
FM6 SW_FB

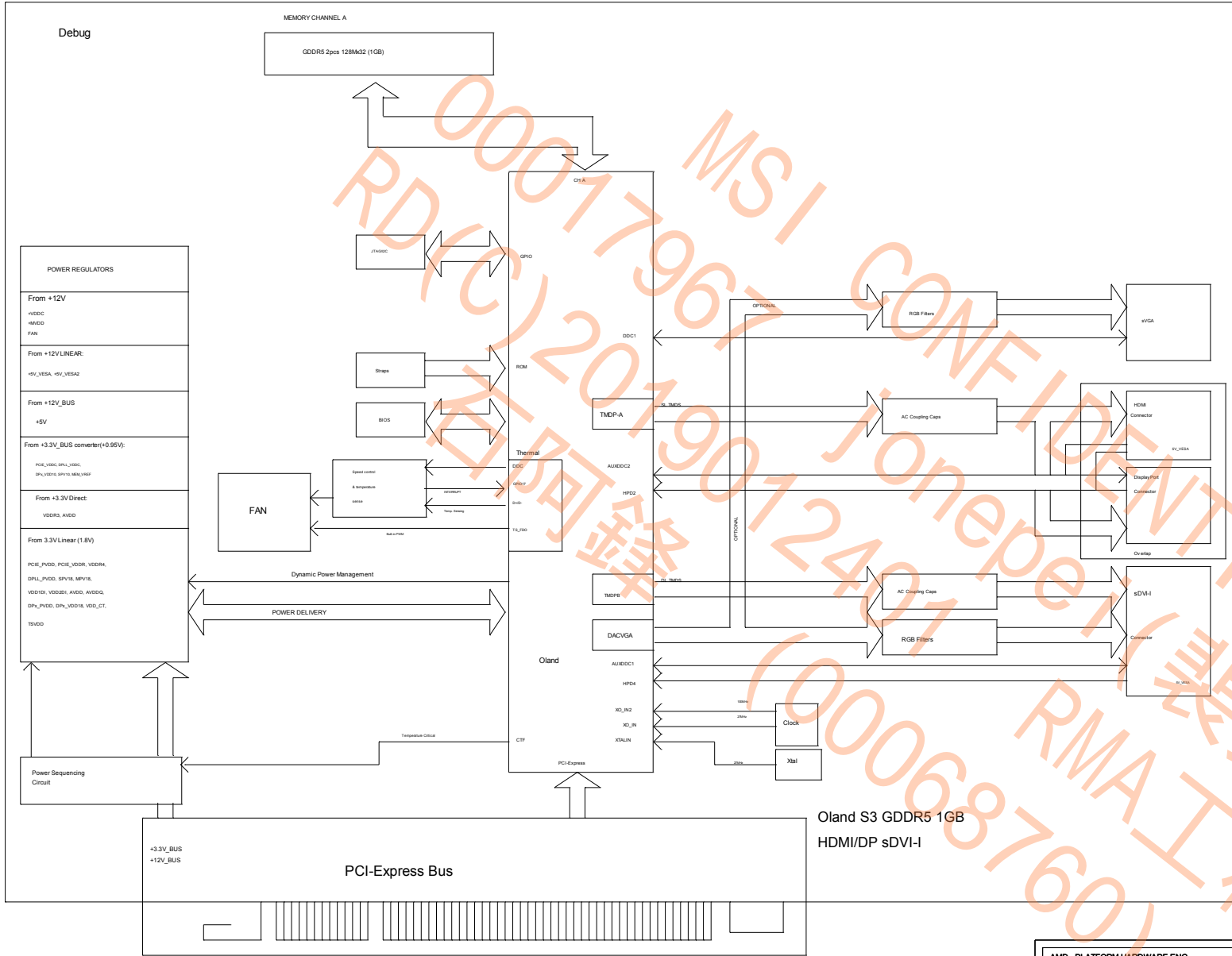


TOP
Single end
Memory data
45 ohm +/- 5 ohm
5.12 mils

Bottom
Single end
Address trunk
45 ohm +/- 5 ohm
5.12 mils

TOP
Different
TMDS
85 ohm +/- 10 %
4.33 mils / 5.51 mils

Bottom
Different
PEX_PCIE
85 ohm +/- 10 %
4.33 mils / 5.51 mils




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SHEET: Diagram		
DATE: Thu Jan 05 05:41:15 2017	REV: P00	
SHEET NUMBER: 19 OF 20		
DOCUMENT NUMBER: 105_d12900_00A		

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TITLE: XXX

	8	7	6	5	4	3	2	1	
D			TITLE		SCHEMATIC NO.		DATE		
			Obase D3 GD0RS 1GB HEMDP4DVI-IVGA		105-012800-00A				
	REVISION HISTORY		NOTE: THIS SCHEMATIC REPRESENTS THE PCB. IT DOES NOT REPRESENT ANY SPECIFIC BOARD. FOR CORRELATED APPLICATIONS/PRODUCTS, PLEASE ALWAYS CONTACT THE PRODUCT SPECIFICATION. PLEASE CONTACT OUR REPRESENTATIVE TO OBTAIN LATEST INFORMATION TO THE APPLICATION BOARD.					REV	
	SCH REV	PCB REV	DATE	REVISION DESCRIPTION					P00
	01	001	2017-01-05	Initial Obase D300 schematic based on Obase D300-00A					
C									
B									
A									

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SHEET: History

DATE: Thu Jan 05 05:41:18 2017

SHEET NUMBER: 20 OF 20

DOCUMENT NUMBER: 105_012800_00A

REV: P00

TITLE: XXX

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