

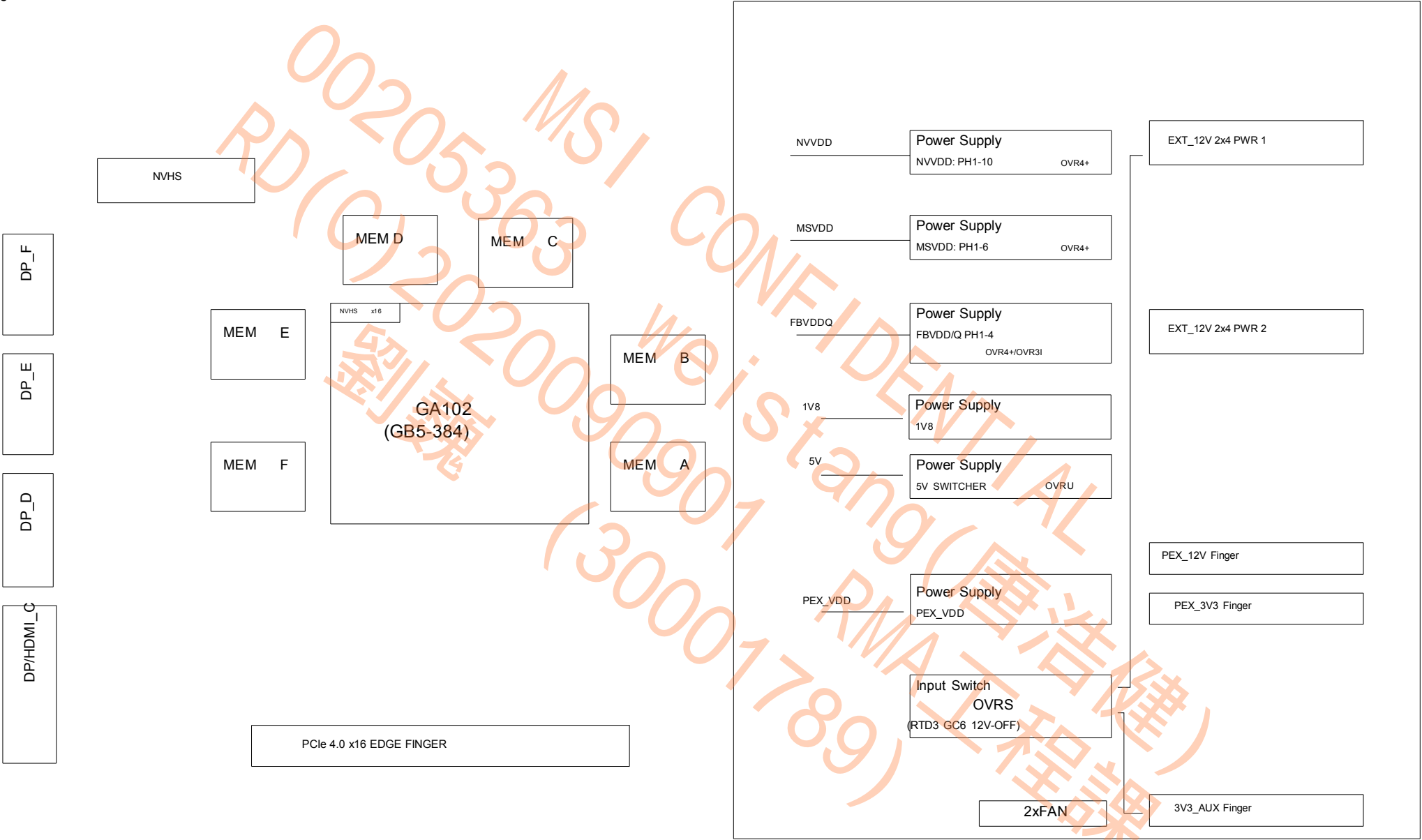
# PG132-A02

370W, FH Std PCB, 384b, GDDR6x 2CH X16  
DP + DP + DP + HDMI/DP

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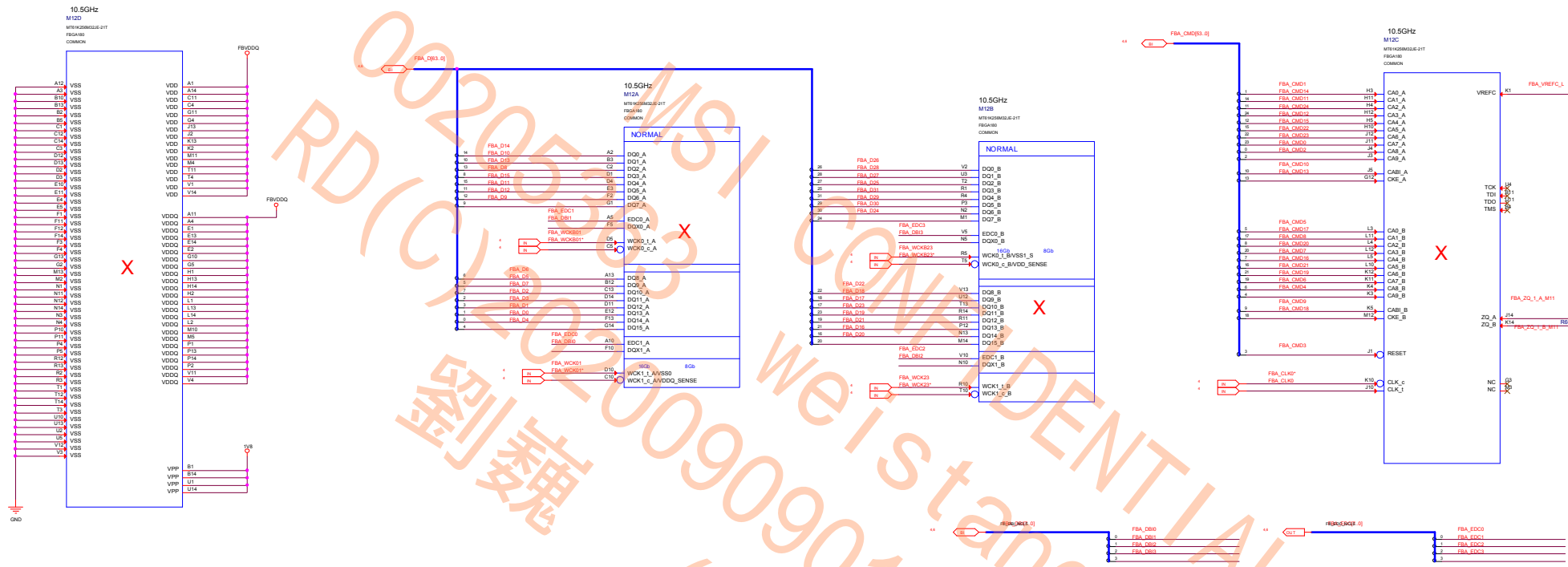
Block Diagram



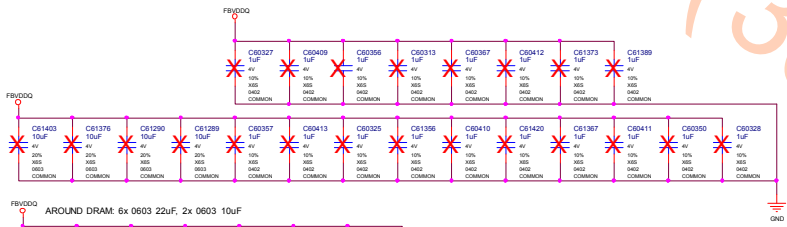




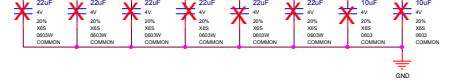
MEMORY: FBA Partition 31..0



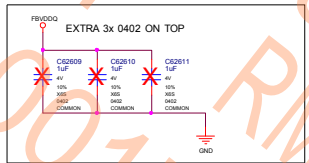
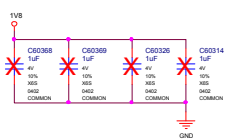
CLOSE TO DRAM: 4x 0603 10uF, 18x 0402 1uF



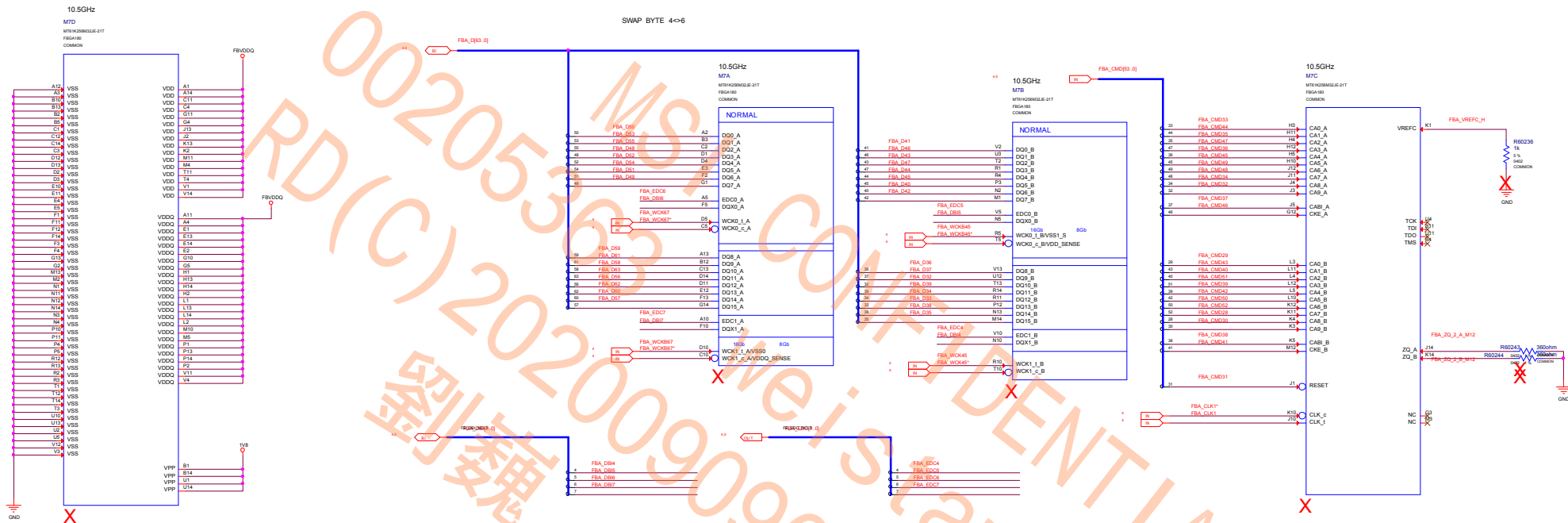
AROUND DRAM: 6x 0603 22uF, 2x 0603 10uF



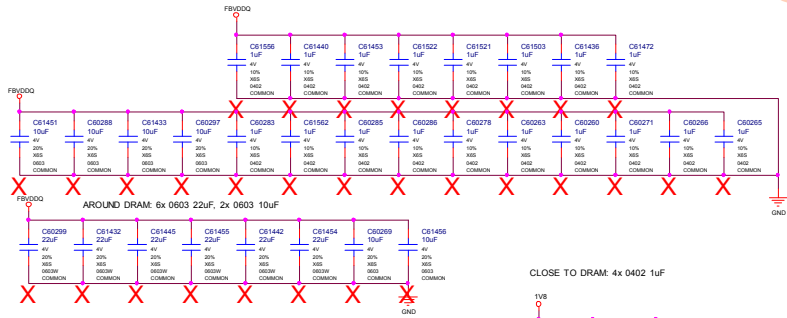
CLOSE TO DRAM: 4x 0402 1uF



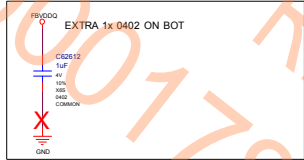
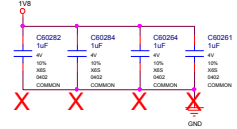
MEMORY: FBA Partition 63..32



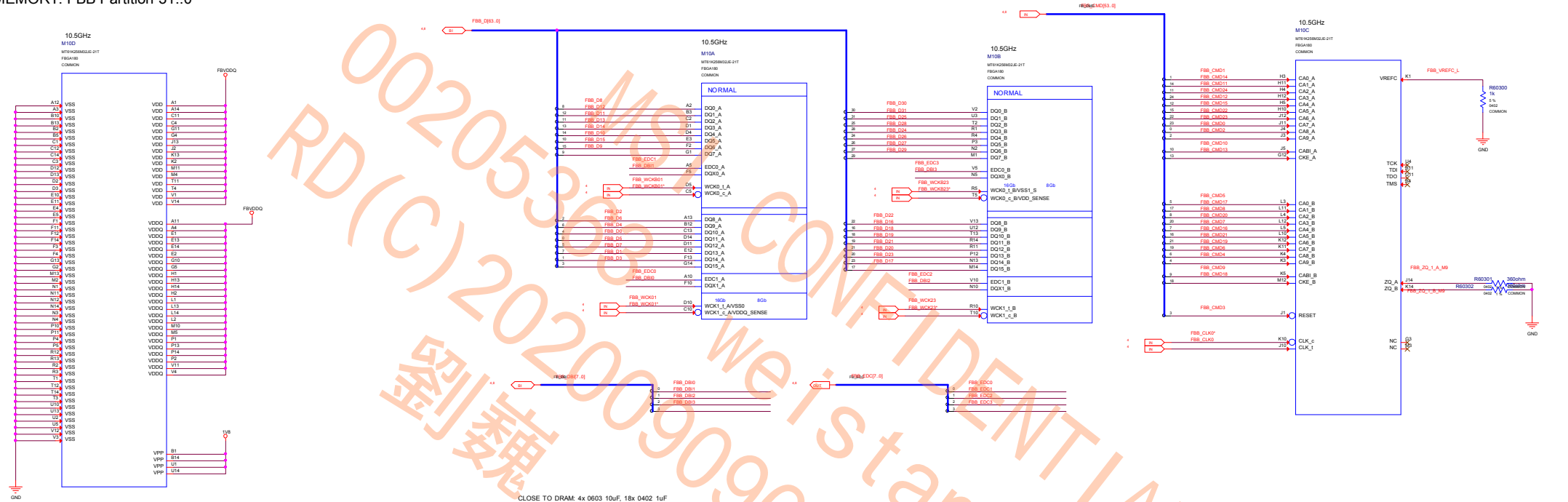
CLOSE TO DRAM: 4x 0603 10uF, 18x 0402 1uF



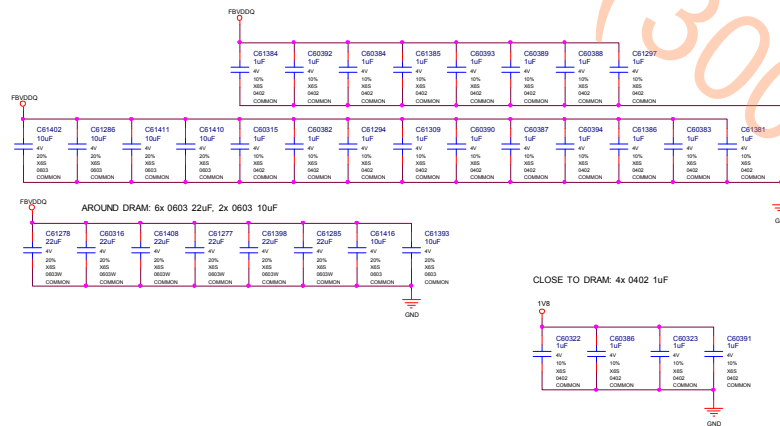
CLOSE TO DRAM: 4x 0402 1uF



MEMORY: FBB Partition 31..0



CLOSE TO DRAM: 4x 0603 10uF, 18x 0402 1uF



CLOSE TO DRAM: 4x 0402 1uF

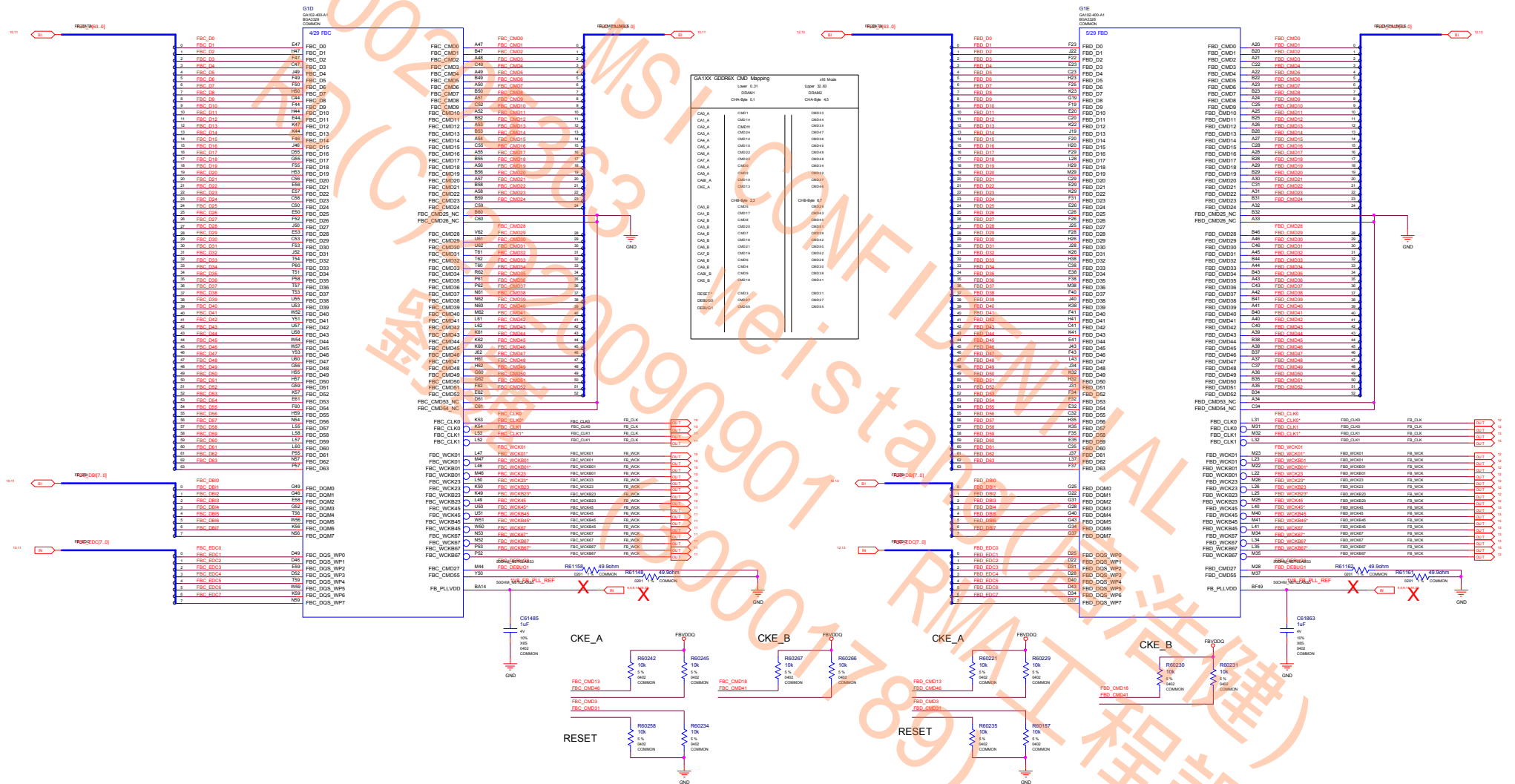


10.5GHz  
M11D





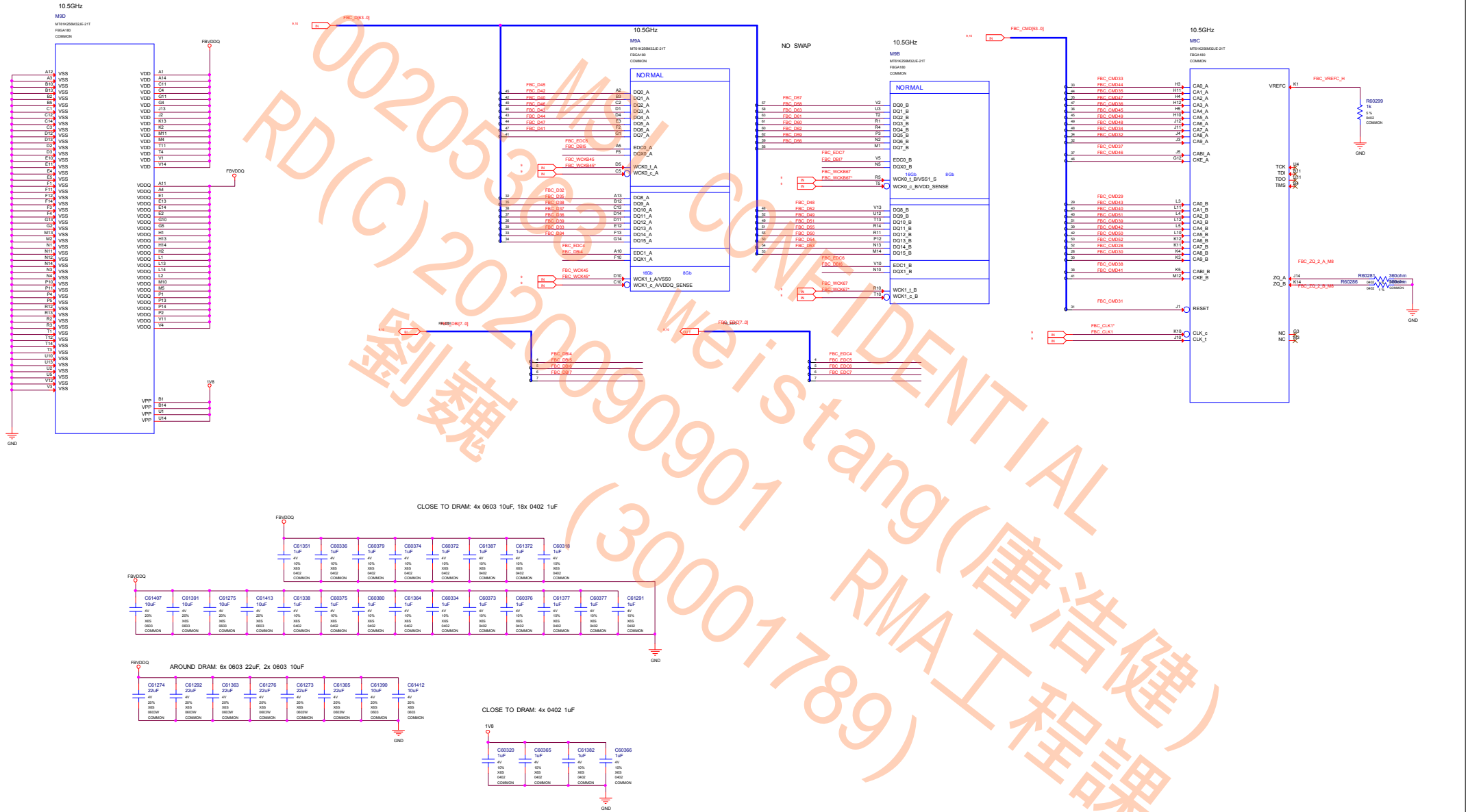
MEMORY: GPU Partition C/D



MEMORY: FBC Partition 31..0



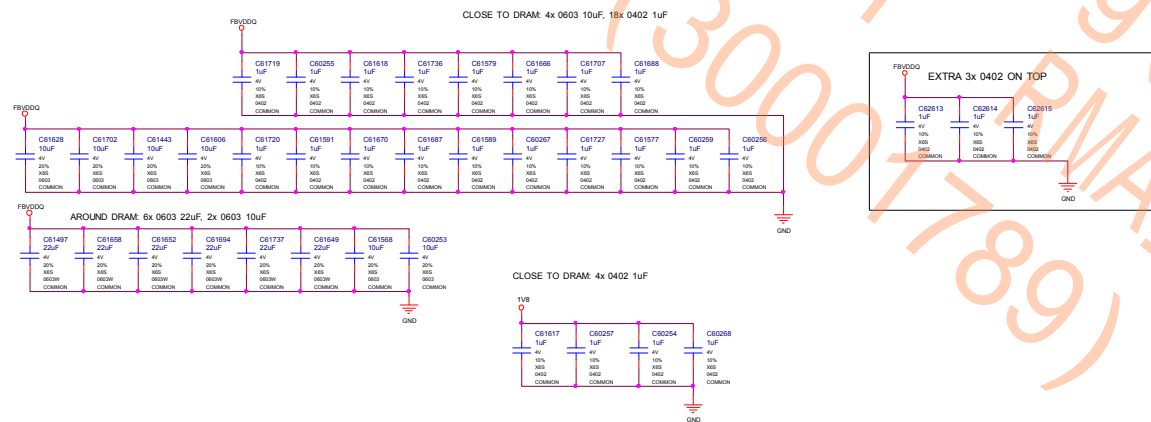
MEMORY: FBC Partition 63..32



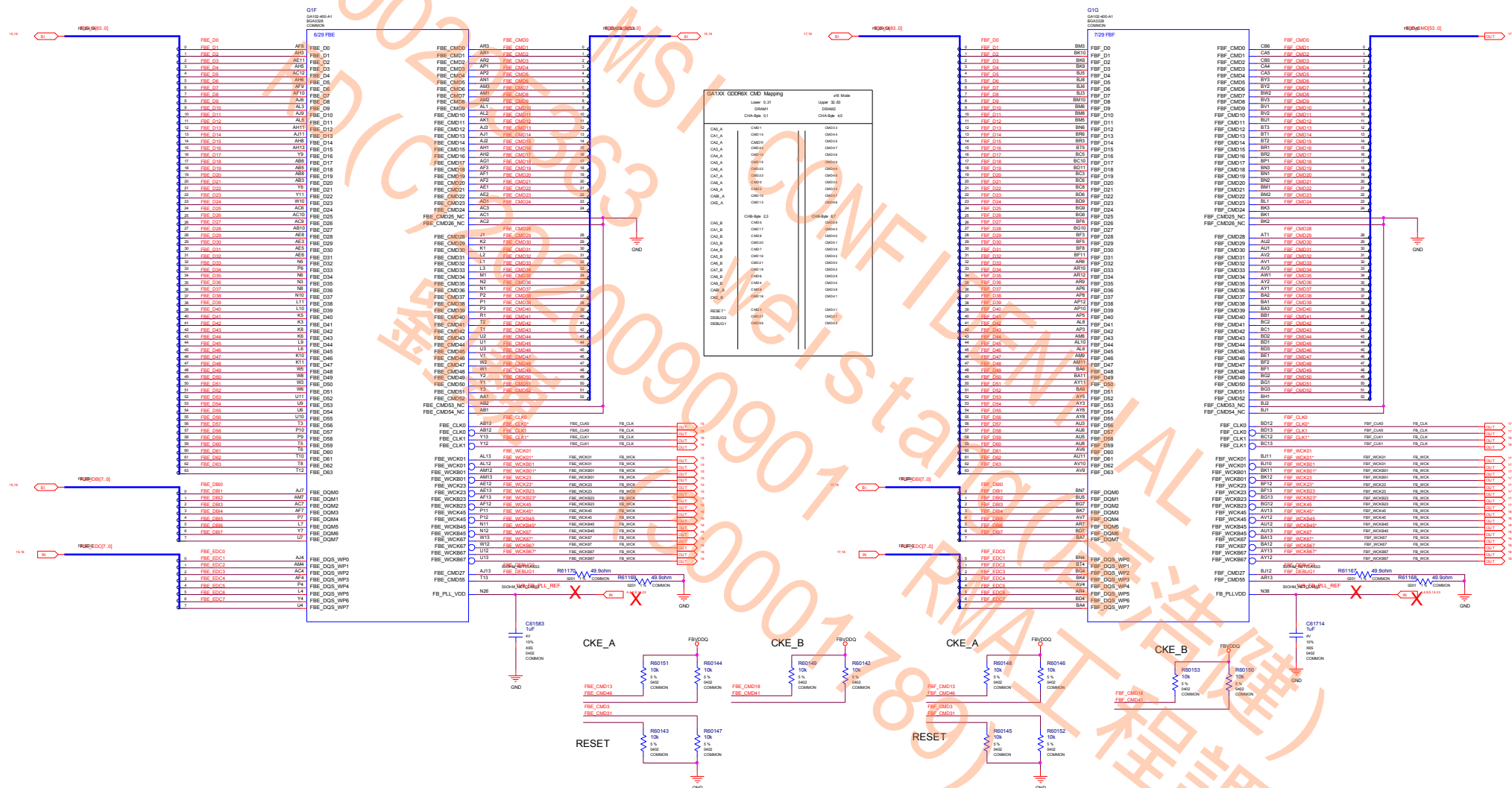
MEMORY: FBD Partition 31..0



MEMORY: FBD Partition 63..32



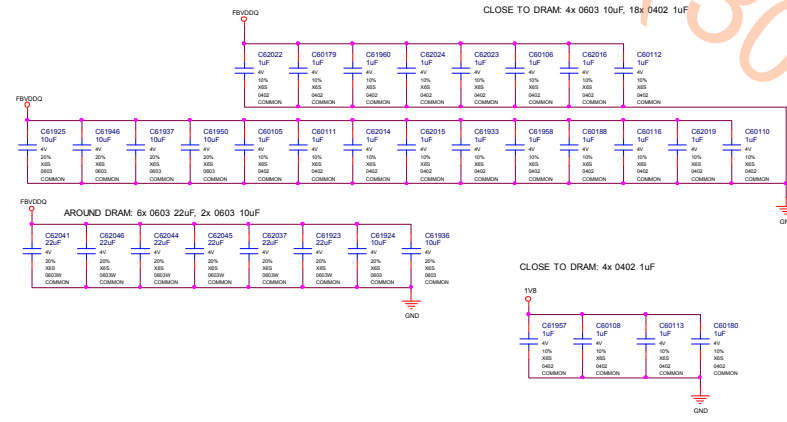
MEMORY: GPU Partition E/F



The image shows a PCB layout for the M2B module, specifically the top and bottom views. The top view includes the M2B module, M2B100, and various components like capacitors and resistors. The bottom view shows the reverse side of the module with similar components. The layout is labeled with various component values and part numbers.

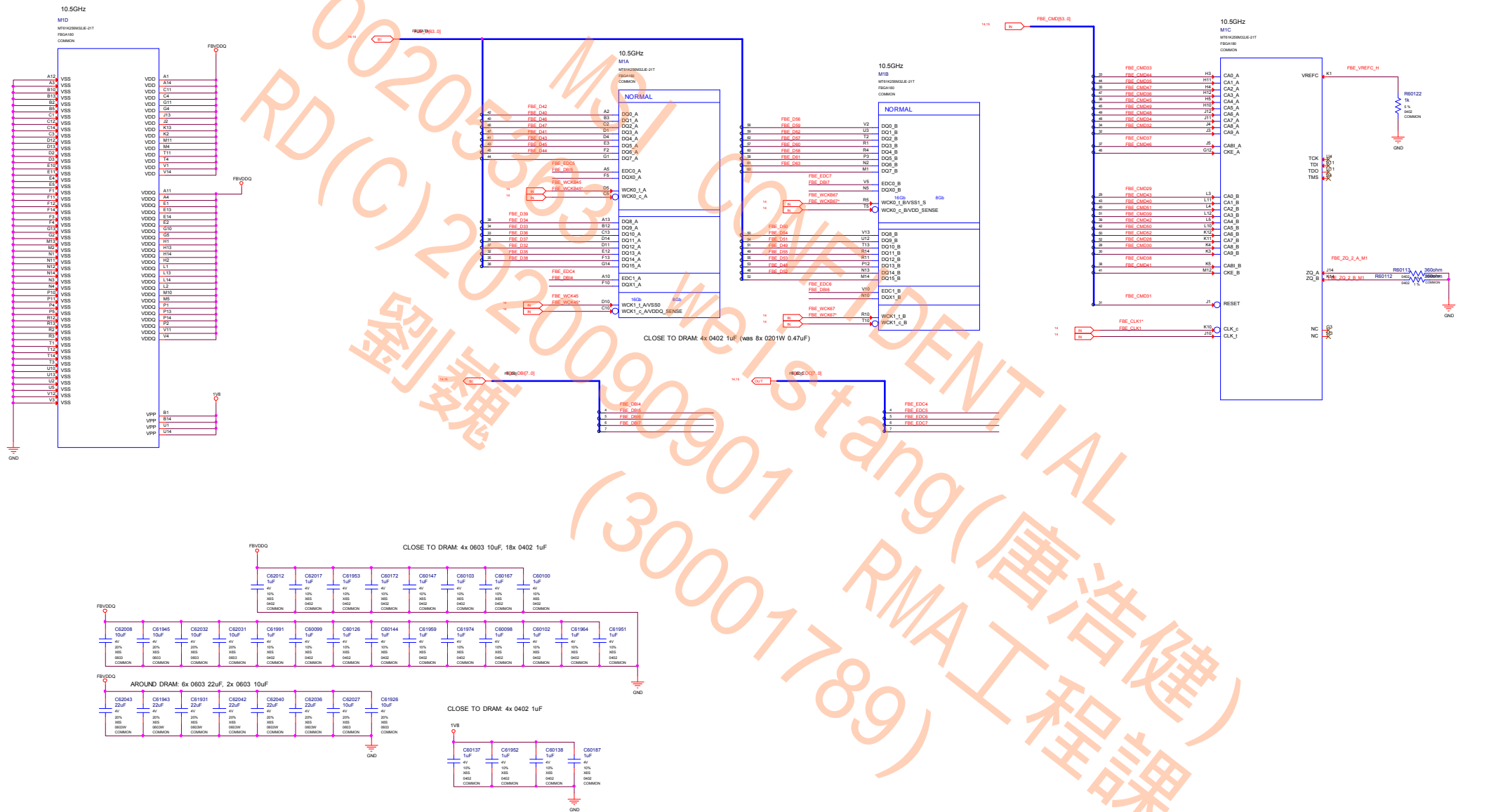
**Top View Components:**

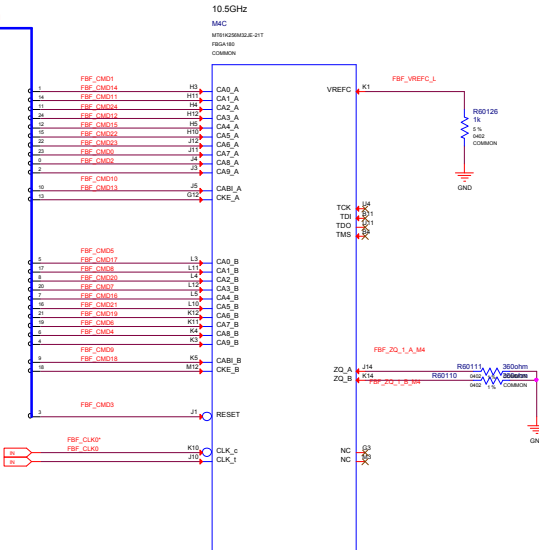
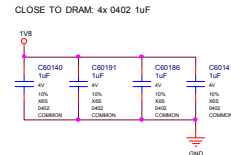
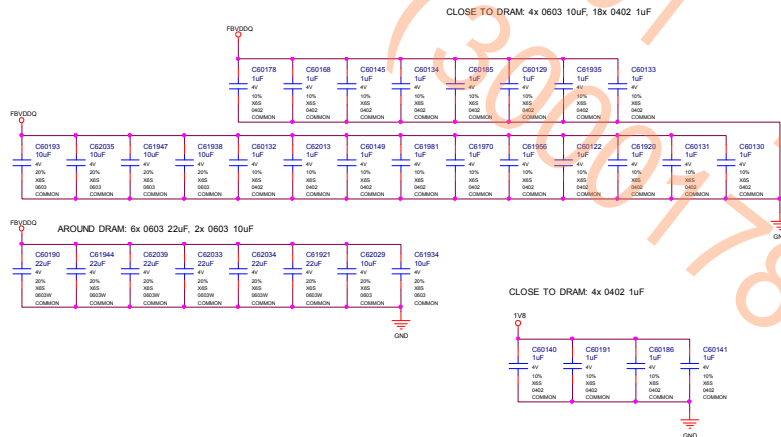
- M2B:** 10.5GHz, M2B100, COMMON
- M2B100:** 10.5GHz, M2B100, COMMON
- Capacitors:** C6102, C6103, C6104, C6105, C6106, C6107, C6108, C6109, C6110, C6111, C6112, C6113, C6114, C6115, C6116, C6117, C6118, C6119, C6120, C6121, C6122, C6123, C6124, C6125, C6126, C6127, C6128, C6129, C6130, C6131, C6132, C6133, C6134, C6135, C6136, C6137, C6138, C6139, C6140, C6141, C6142, C6143, C6144, C6145, C6146, C6147, C6148, C6149, C6150, C6151, C6152, C6153, C6154, C6155, C6156, C6157, C6158, C6159, C6160, C6161, C6162, C6163, C6164, C6165, C6166, C6167, C6168, C6169, C6170, C6171, C6172, C6173, C6174, C6175, C6176, C6177, C6178, C6179, C6180, C6181, C6182, C6183, C6184, C6185, C6186, C6187, C6188, C6189, C6190, C6191, C6192, C6193, C6194, C6195, C6196, C6197, C6198, C6199, C6200, C6201, C6202, C6203, C6204, C6205, C6206, C6207, C6208, C6209, C6210, C6211, C6212, C6213, C6214, C6215, C6216, C6217, C6218, C6219, C6220, C6221, C6222, C6223, C6224, C6225, C6226, C6227, C6228, C6229, C6230, C6231, C6232, C6233, C6234, C6235, C6236, C6237, C6238, C6239, C6240, C6241, C6242, C6243, C6244, C6245, C6246, C6247, C6248, C6249, C6250, C6251, C6252, C6253, C6254, C6255, C6256, C6257, C6258, C6259, C6260, C6261, C6262, C6263, C6264, C6265, C6266, C6267, C6268, C6269, C6270, C6271, C6272, C6273, C6274, C6275, C6276, C6277, C6278, C6279, C6280, C6281, C6282, C6283, C6284, C6285, C6286, C6287, C6288, C6289, C6290, C6291, C6292, C6293, C6294, C6295, C6296, C6297, C6298, C6299, C6300, C6301, C6302, C6303, C6304, C6305, C6306, C6307, C6308, C6309, C6310, C6311, C6312, C6313, C6314, C6315, C6316, C6317, C6318, C6319, C6320, C6321, C6322, C6323, C6324, C6325, C6326, C6327, C6328, C6329, C6330, C6331, C6332, C6333, C6334, C6335, C6336, C6337, C6338, C6339, C6340, C6341, C6342, C6343, C6344, C6345, C6346, C6347, C6348, C6349, C6350, C6351, C6352, C6353, C6354, C6355, C6356, C6357, C6358, C6359, C6360, C6361, C6362, C6363, C6364, C6365, C6366, C6367, C6368, C6369, C6370, C6371, C6372, C6373, C6374, C6375, C6376, C6377, C6378, C6379, C6380, C6381, C6382, C6383, C6384, C6385, C6386, C6387, C6388, C6389, C6390, C6391, C6392, C6393, C6394, C6395, C6396, C6397, C6398, C6399, C6400, C6401, C6402, C6403, C6404, C6405, C6406, C6407, C6408, C6409, C6410, C6411, C6412, C6413, C6414, C6415, C6416, C6417, C6418, C6419, C6420, C6421, C6422, C6423, C6424, C6425, C6426, C6427, C6428, C6429, C6430, C6431, C6432, C6433, C6434, C6435, C6436, C6437, C6438, C6439, C6440, C6441, C6442, C6443, C6444, C6445, C6446, C6447, C6448, C6449, C6450, C6451, C6452, C6453, C6454, C6455, C6456, C6457, C6458, C6459, C6460, C6461, C6462, C6463, C6464, C6465, C6466, C6467, C6468, C6469, C6470, C6471, C6472, C6473, C6474, C6475, C6476, C6477, C6478, C6479, C6480, C6481, C6482, C6483, C6484, C6485, C6486, C6487, C6488, C6489, C6490, C6491, C6492, C6493, C6494, C6495, C6496, C6497, C6498, C6499, C6500, C6501, C6502, C6503, C6504, C6505, C6506, C6507, C6508, C6509, C6510, C6511, C6512, C6513, C6514, C6515, C6516, C6517, C6518, C6519, C6520, C6521, C6522, C6523, C6524, C6525, C6526, C6527, C6528, C6529, C6530, C6531, C6532, C6533, C6534, C6535, C6536, C6537, C6538, C6539, C6540, C6541, C6542, C6543, C6544, C6545, C6546, C6547, C6548, C6549, C6550, C6551, C6552, C6553, C6554, C6555, C6556, C6557, C6558, C6559, C6560, C6561, C6562, C6563, C6564, C6565, C6566, C6567, C6568, C6569, C6570, C6571, C6572, C6573, C6574, C6575, C6576, C6577, C6578, C6579, C6580, C6581, C6582, C6583, C6584, C6585, C6586, C6587, C6588, C6589, C6590, C6591, C6592, C6593, C6594, C6595, C6596, C6597, C6598, C6599, C6600, C6601, C6602, C6603, C6604, C6605, C6606, C6607, C6608, C6609, C6610, C6611, C6612, C6613, C6614, C6615, C6616, C6617, C6618, C6619, C6620, C6621, C6622, C6623, C6624, C6625, C6626, C6627, C6628, C6629, C6630, C6631, C6632, C6633, C6634, C6635, C6636, C6637, C6638, C6639, C6640, C6641, C6642, C6643, C6644, C6645, C6646, C6647, C6648, C6649, C6650, C6651, C6652, C6653, C6654, C6655, C6656, C6657, C6658, C6659, C6660, C6661, C6662, C6663, C6664, C6665, C6666, C6667, C6668, C6669, C6670, C6671, C6672, C6673, C6674, C6675, C6676, C6677, C6678, C6679, C6680, C6681, C6682, C6683, C6684, C6685, C6686, C6687, C6688, C6689, C6690, C6691, C6692, C6693, C6694, C6695, C6696, C6697, C6698, C6699, C6700, C6701, C6702, C6703, C6704, C6705, C6706, C6707, C6708, C6709, C6710, C6711, C6712, C6713, C6714, C6715, C6716, C6717, C6718, C6719, C6720, C6721, C6722, C6723, C6724, C6725, C6726, C6727, C6728, C6729, C6730, C6731, C6732, C6733, C6734, C6735, C6736, C6737, C6738, C6739, C6740, C6741, C6742, C6743, C6744, C



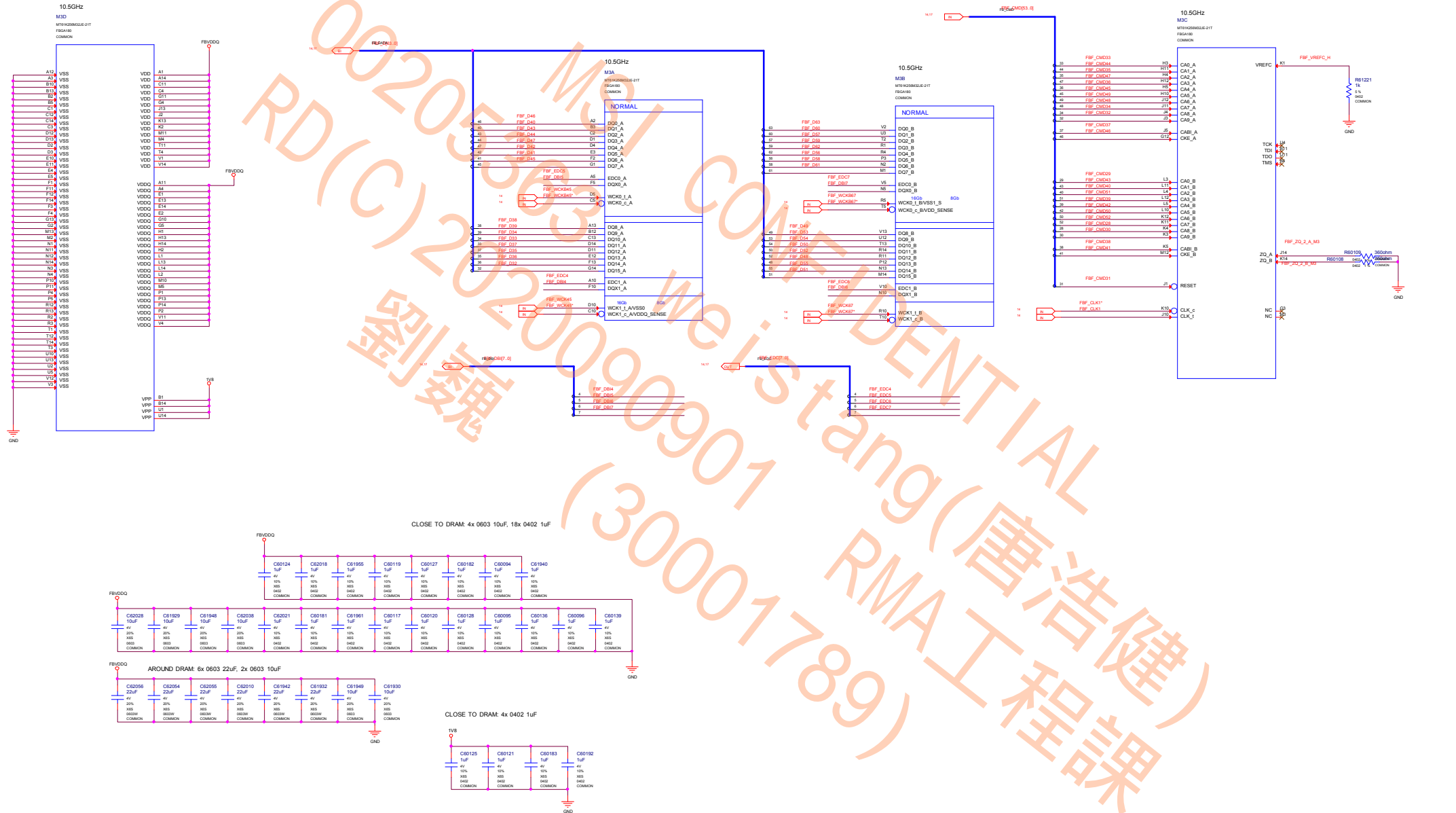


MEMORY: FBE Partition 63..32

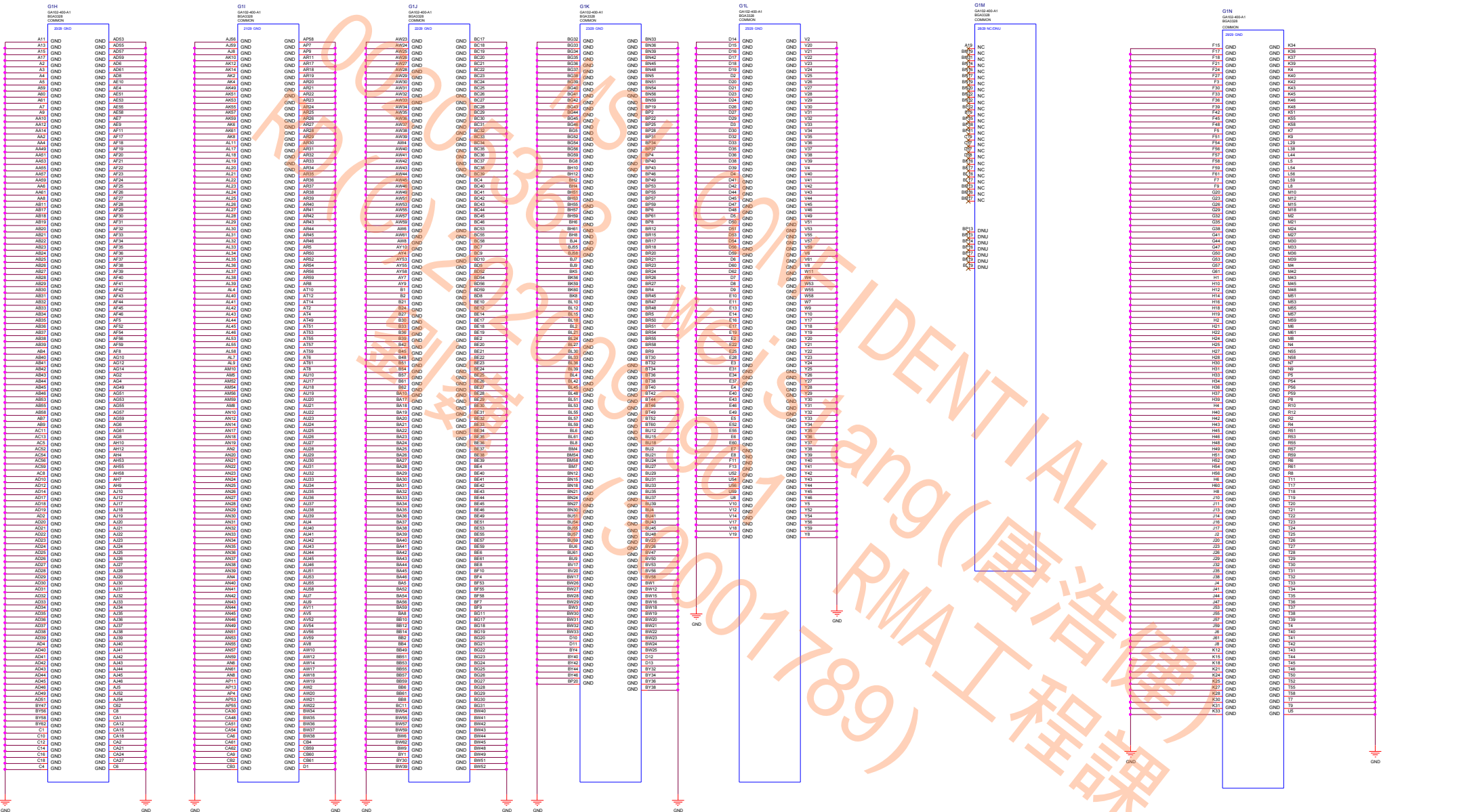




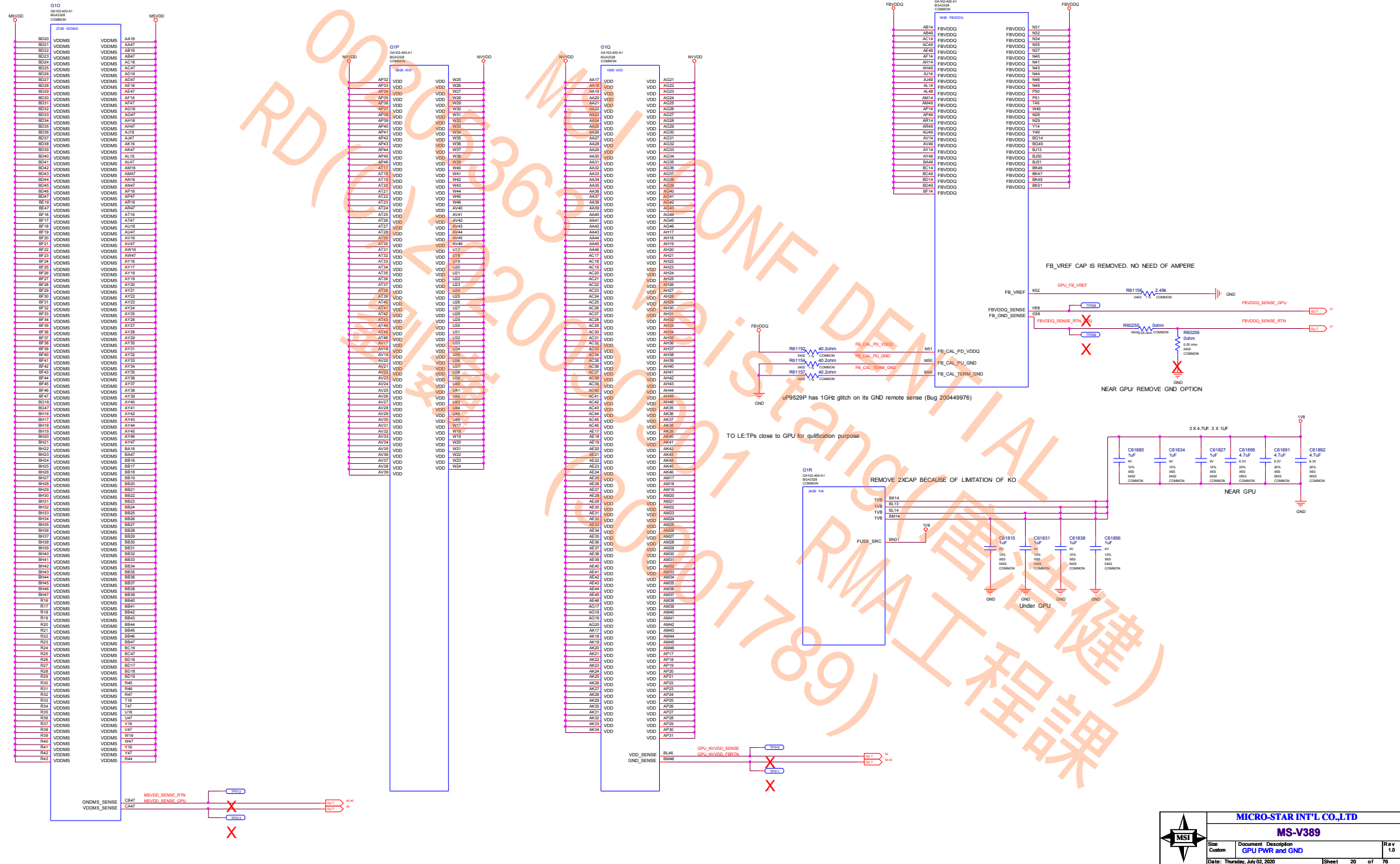
MEMORY: FBF Partition 63..32



GPU GND, RFUs & RSVD

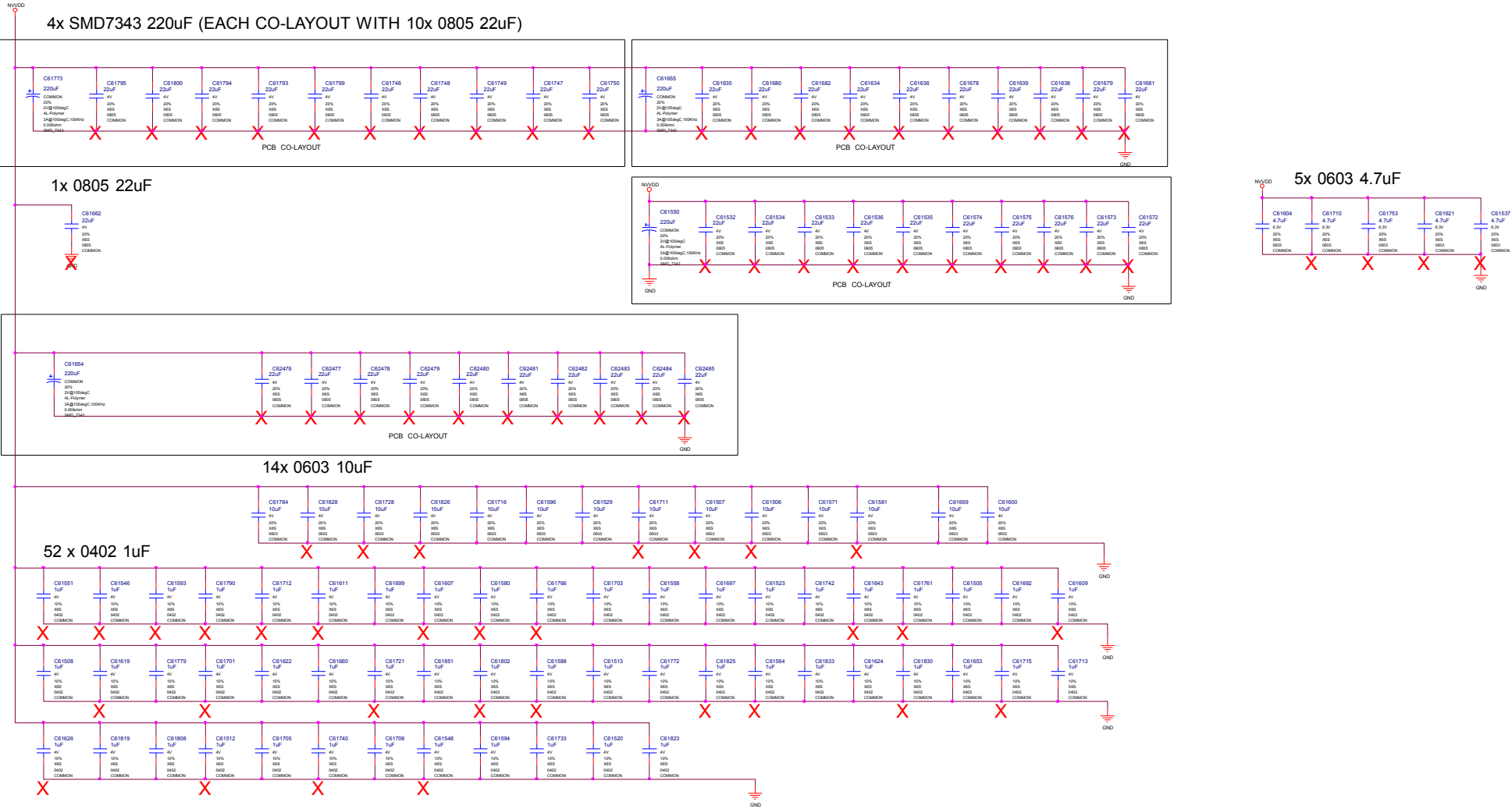


## GPU PWR and GND

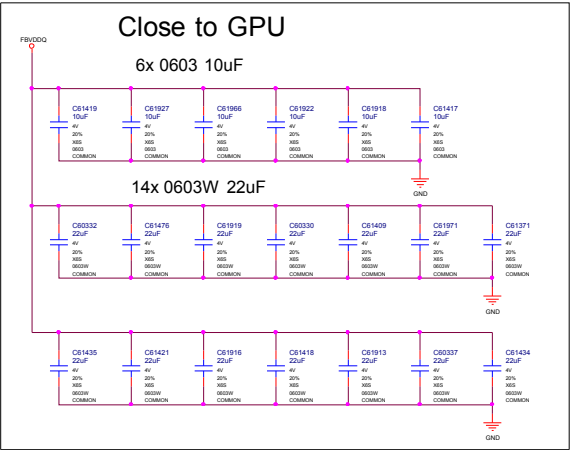
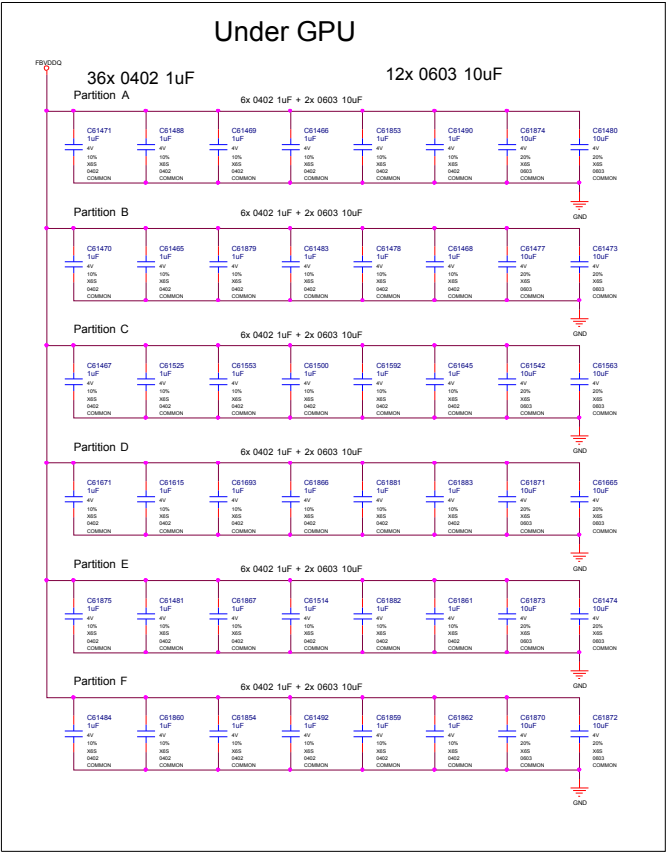


NVVDD

UNDER GPU



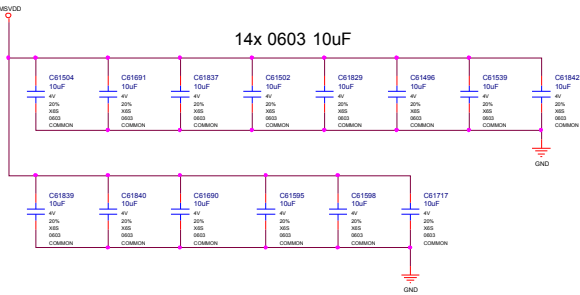
FBVDDQ



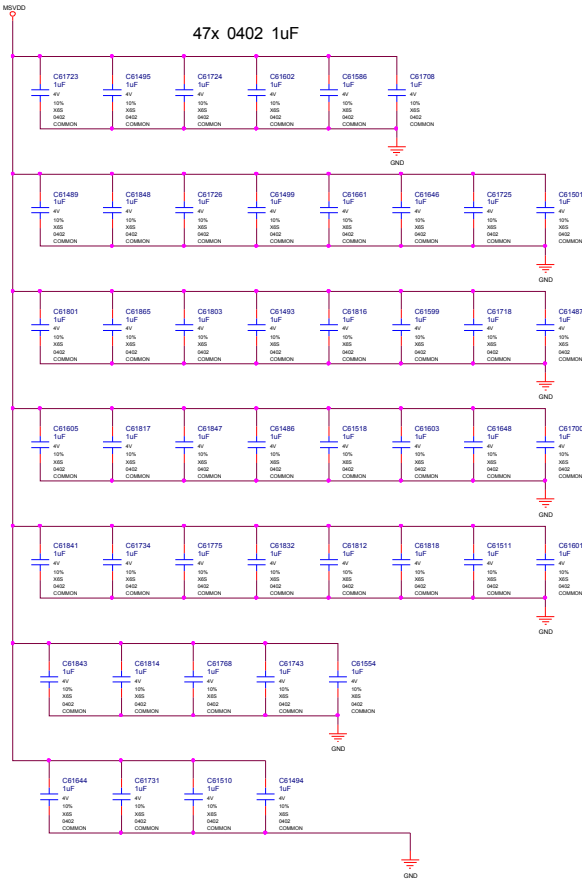
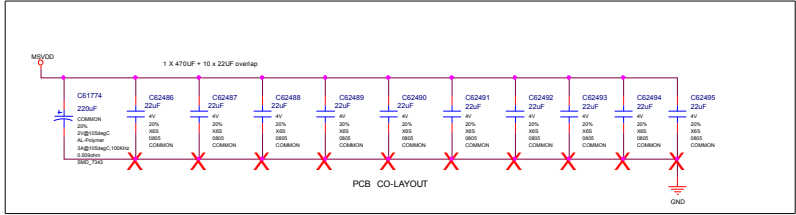
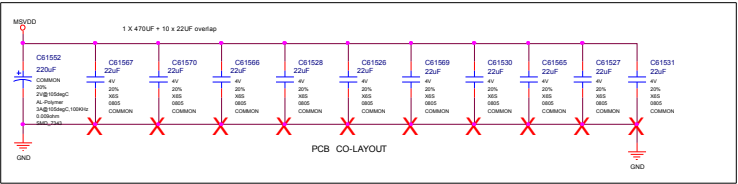


# UNDER GPU

MSVDD



2x SMD7343 220uF (EACH CO-LAYOUT WITH 10x 22uF 0805)



REMOVED 3X 1uF 0402 DUE TO SPACE RESTRICTION

# BLANK

Figure 10 is a schematic diagram of a 12V power distribution system. The system is divided into two main sections: "NEAR GPU" and "UNDER GPU".

**NEAR GPU Section:**

- C00169:** 3.2V fuse, connected to 12V and ground.
- C00092:** 3.2V fuse, connected to 12V and ground.
- C00160:** 6.3V fuse, connected to 12V and ground.
- C00173:** 3.2V fuse, connected to 12V and ground.
- C00175:** 6.3V fuse, connected to 12V and ground.

**UNDER GPU Section:**

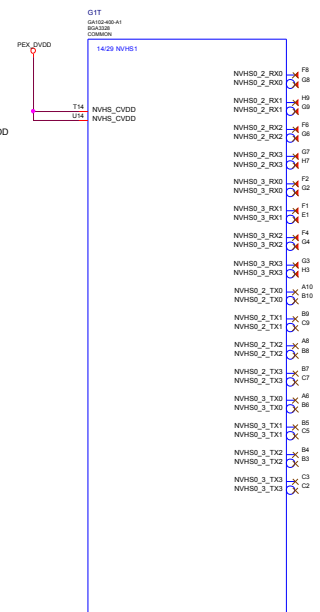
- C01797:** 4.7V fuse, connected to 12V and ground.
- C01788:** 3.2V fuse, connected to 12V and ground.
- C01763:** 6.3V fuse, connected to 12V and ground.
- C01876:** 4V fuse, connected to 12V and ground.
- C01760:** 4V fuse, connected to 12V and ground.
- C01878:** 4V fuse, connected to 12V and ground.
- C01732:** 4V fuse, connected to 12V and ground.
- C01776:** 4V fuse, connected to 12V and ground.
- C01741:** 4V fuse, connected to 12V and ground.
- C01777:** 4V fuse, connected to 12V and ground.

A note at the bottom right states: "REMOVE ONE BECAUSE OF KO LIMITATION".

LED -> NO

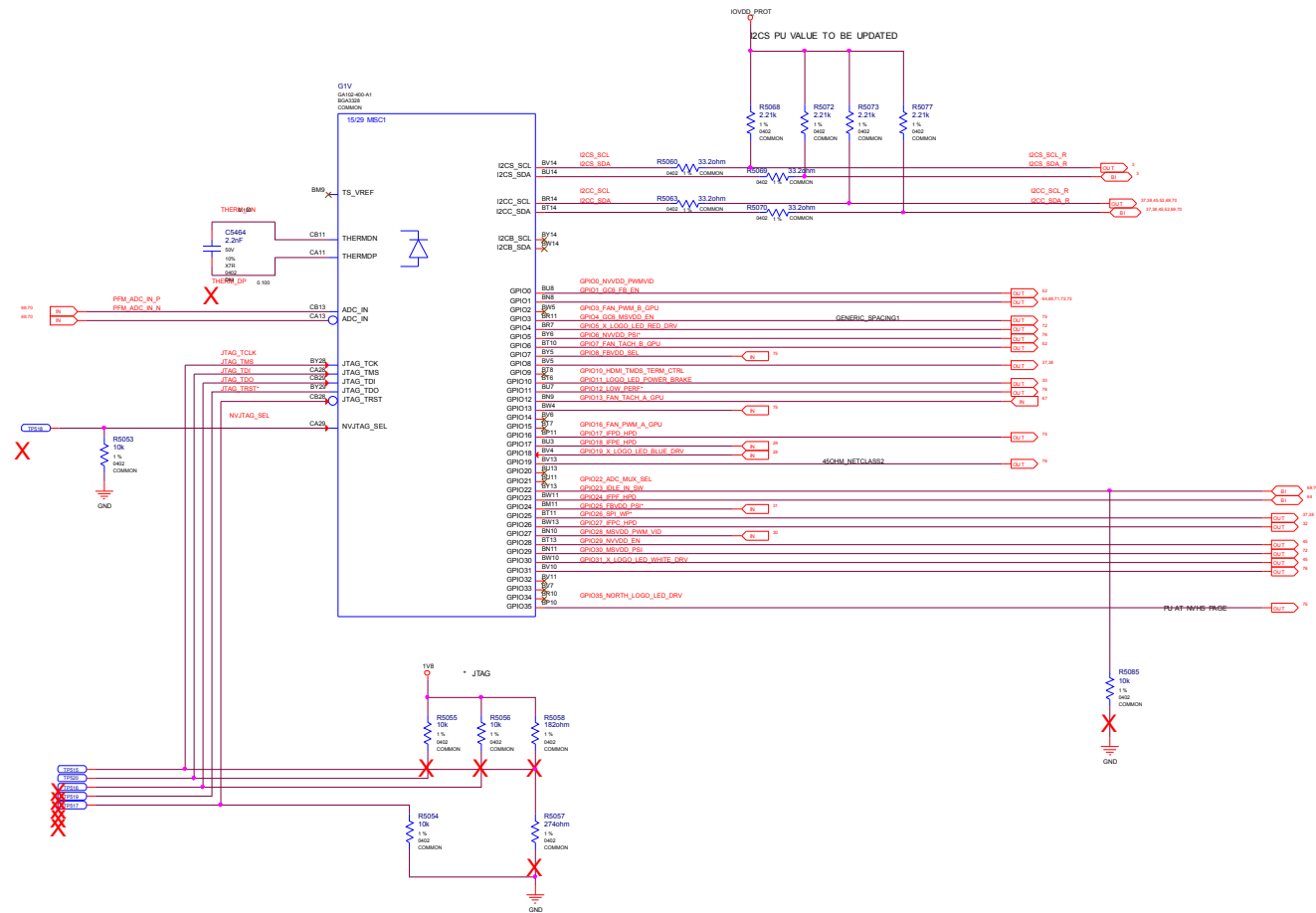
Diagram illustrating the FBVDDQ plane layout and capacitor removal locations:

- NEAR GPU** (Left side)
- UNDER GPU** (Right side)
- Capacitors and their values:**
  - C91905: 10uF
  - C91877: 4.7uF
  - C91822: 4.7uF
  - C91820: 4.7uF
  - C91807: 10uF
  - C91887: 1uF
  - C91808: 10uF
  - C91845: 1uF
  - C91846: 1uF
  - C91844: 1uF
- Removal locations:** Indicated by red 'X' marks on the bottom rail.
- Text annotations:**
  - REMOVED 4x 0805 TO OPTIMIZE FBVDDQ PLANE
  - REMOVE ONE BECAUSE OF KO LIMITATION

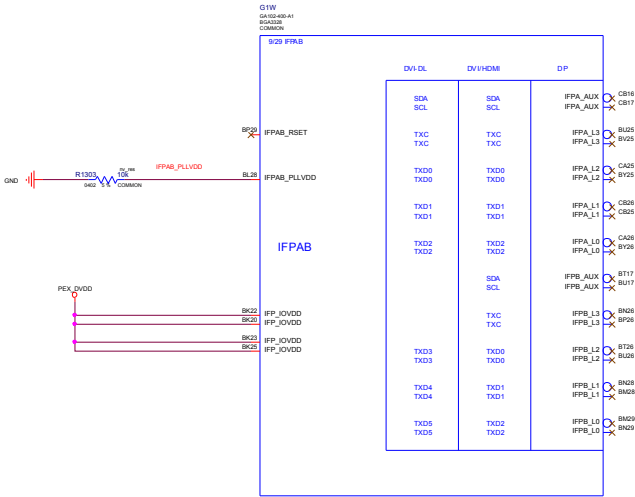


NVHS\_REFCLK  
NVHS\_REFCLK  
EXT\_REFCLK\_S

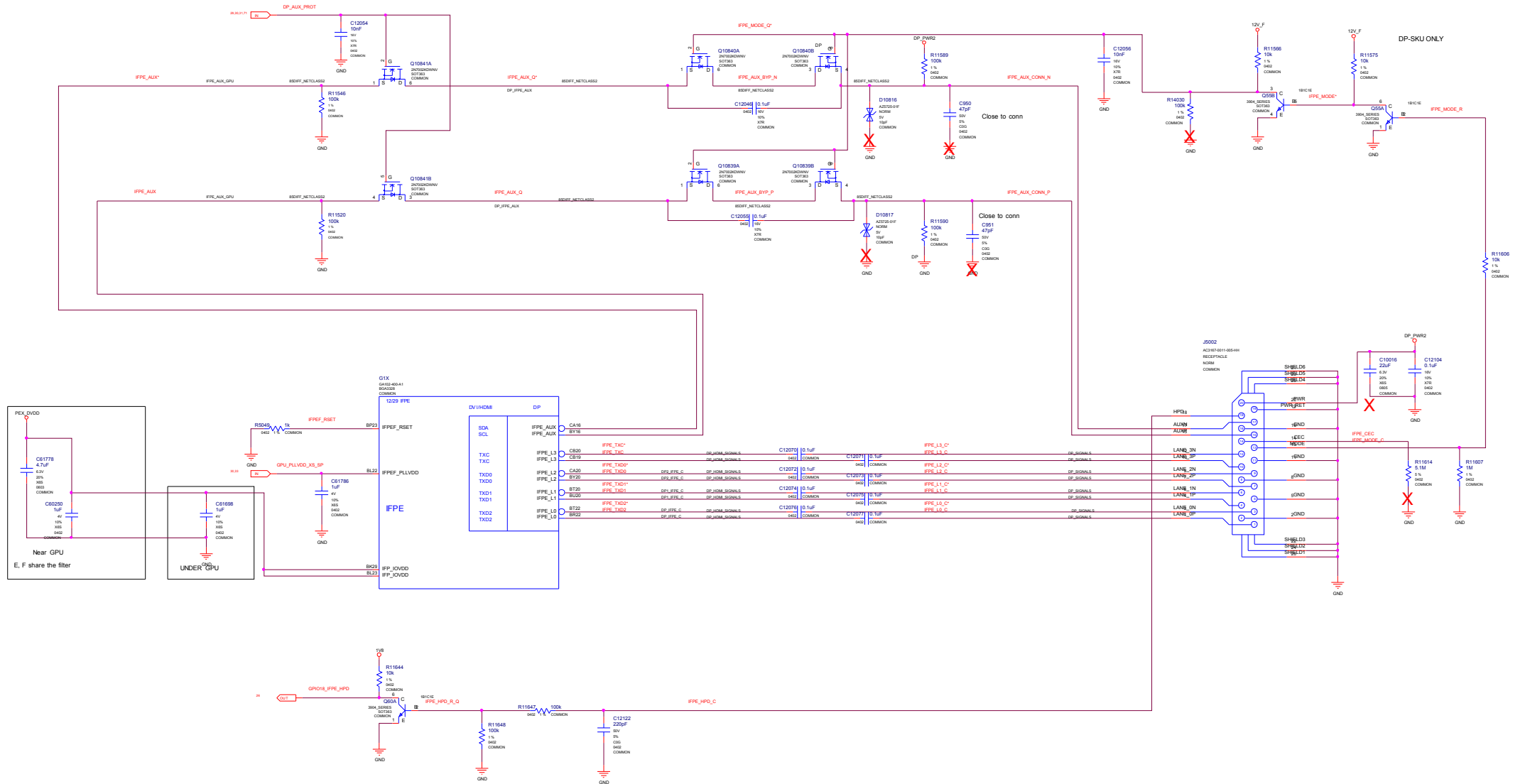
MISC1: Fan, Thermal, JTAG, GPIO, STEREO



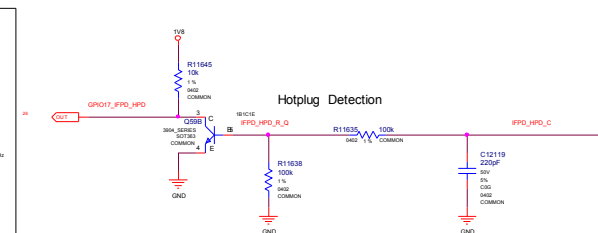
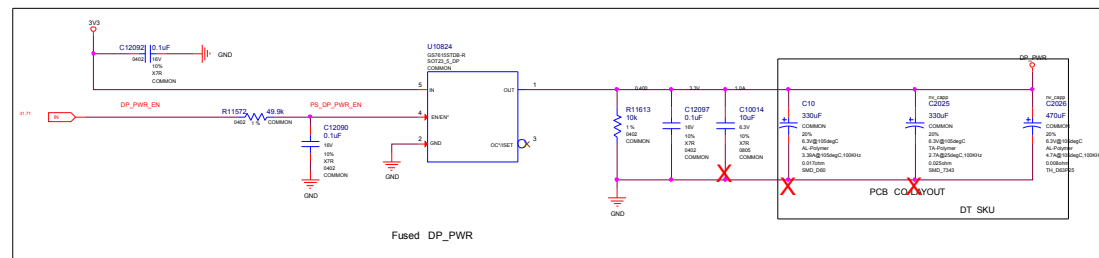
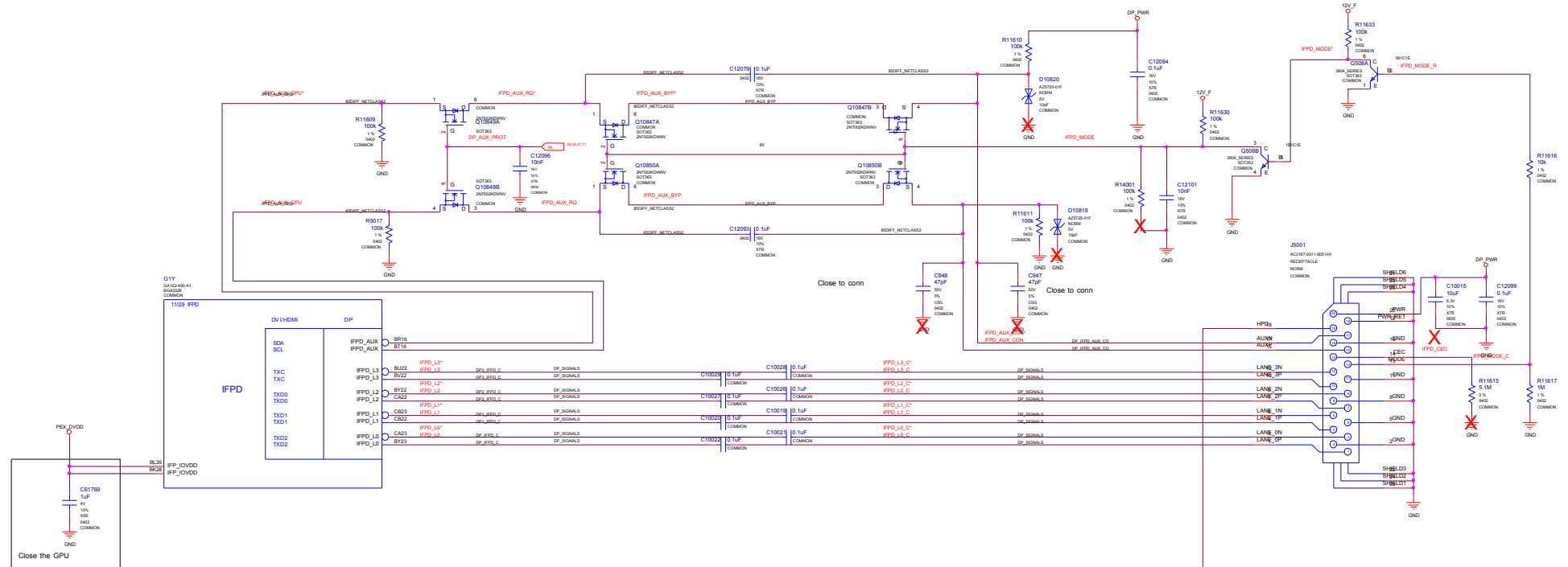
IFPA UNUSED, IFPB UNUSED



IFPE DP

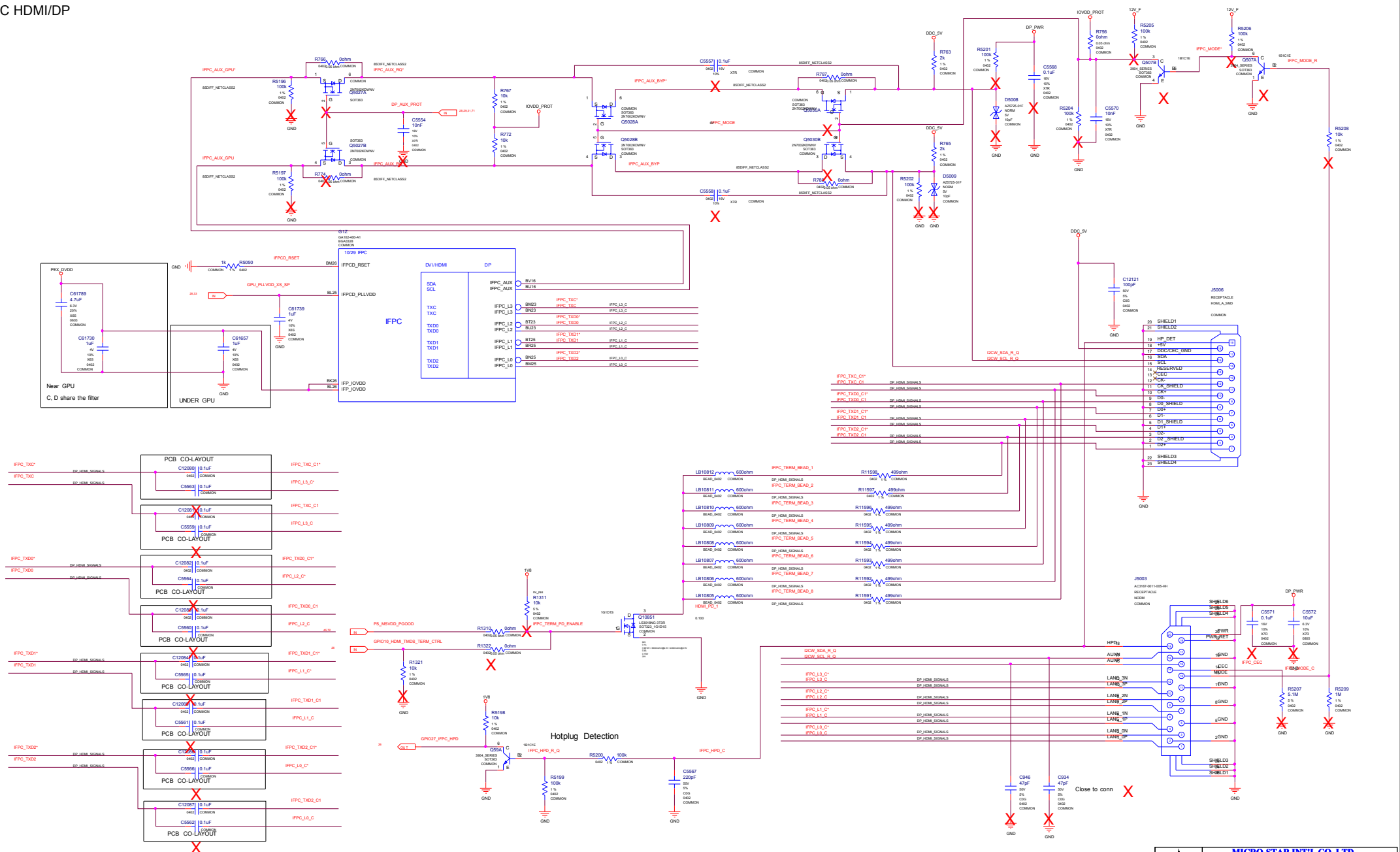


## IFPD DP





## IFPC HDMI/DP





## MISC: ROM, Straps

GROUP0		GROUP1		GROUP2		GROUP3	
STRAP2	STRAP1	STRAP0	RAMCFG[4:0]	STRAP2	STRAP1	STRAP0	RAMCFG[4:0]
L	L	L	00000	L	L	L	RAMCFG MICRON 8Gb G6X 19Gbps x16 (161-0430-900)
H	L	L	00100	L	L	L	RAMCFG MICRON 8Gb G6X 19Gbps x8 (161-0430-900)
H	L	H	00101	L	L	H	RAMCFG MICRON 8Gb G6X 21Gbps x8 (161-0431-900)
L	L	M	01000	L	L	M	RAMCFG MICRON 8Gb G6X 20Gbps x16 (161-0424-900)
L	L	H	00001	L	L	H	RAMCFG MICRON 8Gb G6X 21Gbps x16 (161-0431-900)
L	M	H	01010	L	M	H	RAMCFG MICRON 8Gb G6X 19Gbps x16 (161-0430-900)

ROM_SO	ROM_SI	ROM_SCLK	SMARTFAN[2:0]_FS_OVERT	1:ENABLE 0:DISABLE	DEFAULT
H	H	H	0111	FS_OVERT ENABLE	
L	L	L	0000	FS_OVERT DISABLE	

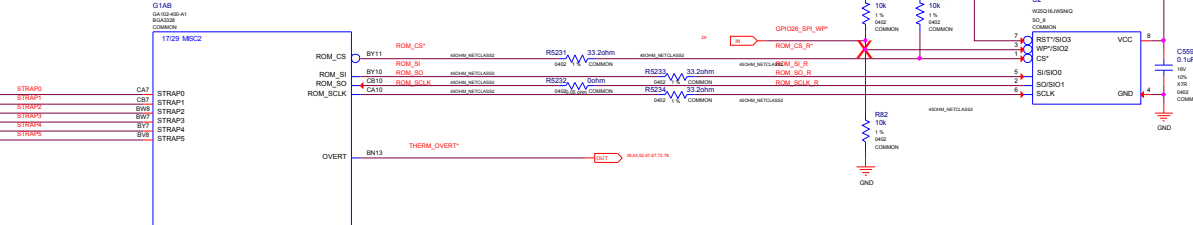
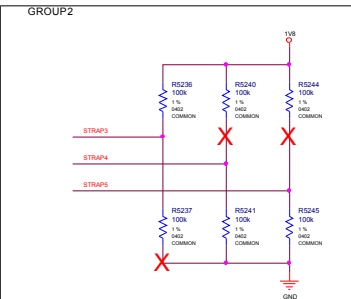
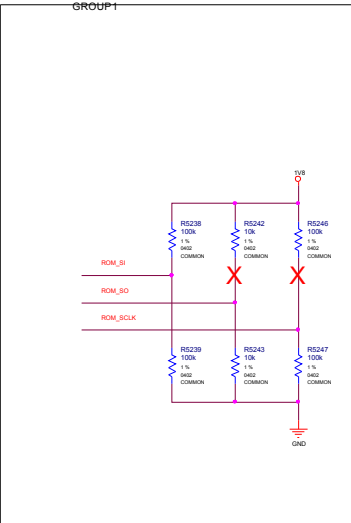
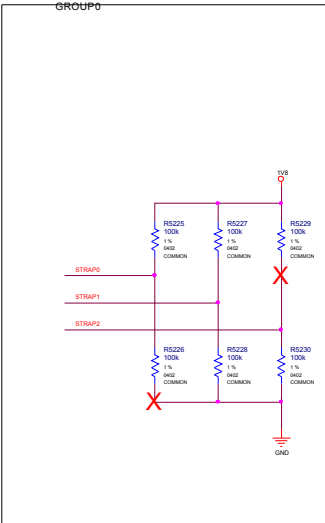
STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1
L	L	L	0	0	0	0

H=High :Tied to 1.8V  
M=Middle:Tied to 0.9V  
L=Low :Tied to 0V

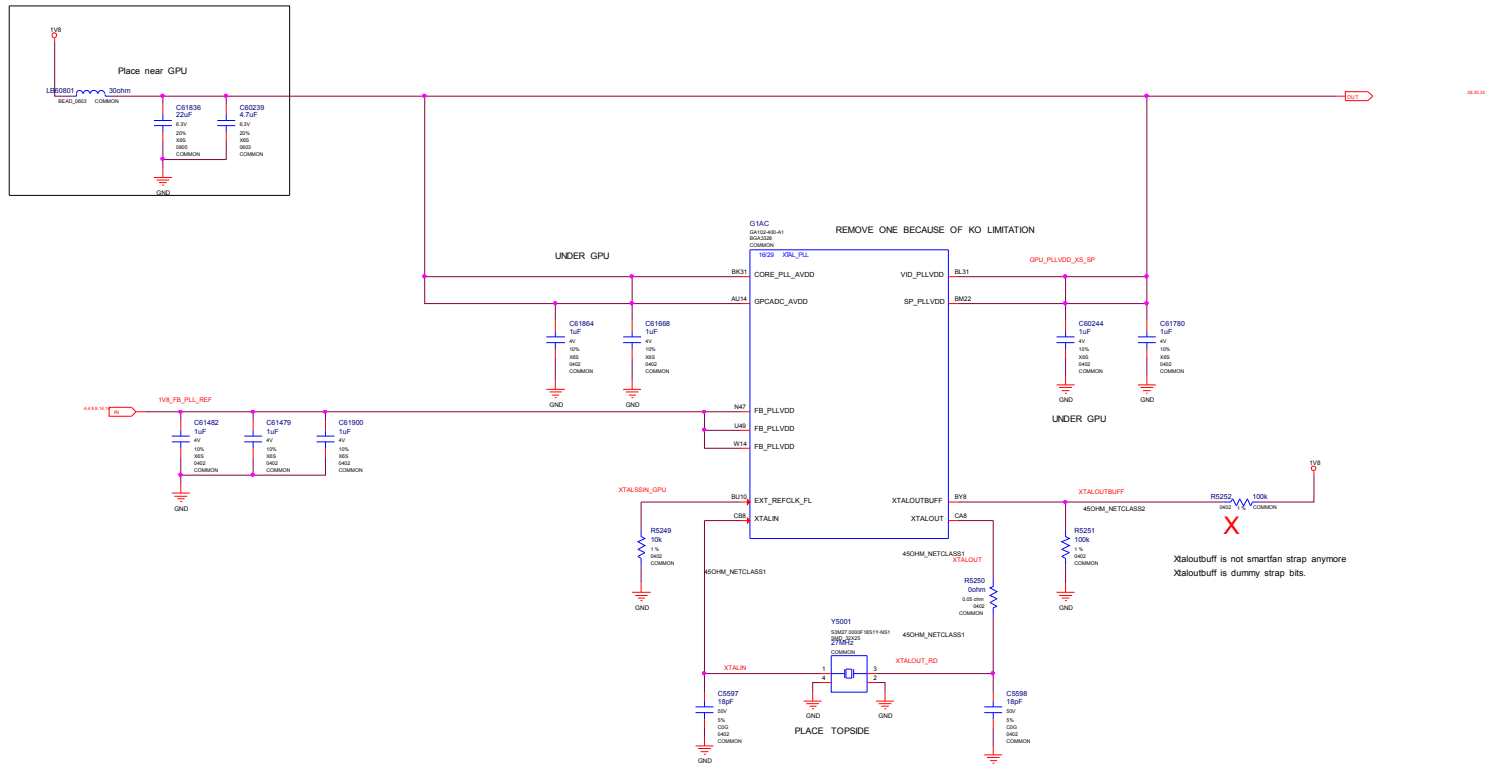
<-A01 PCB

<-OLD

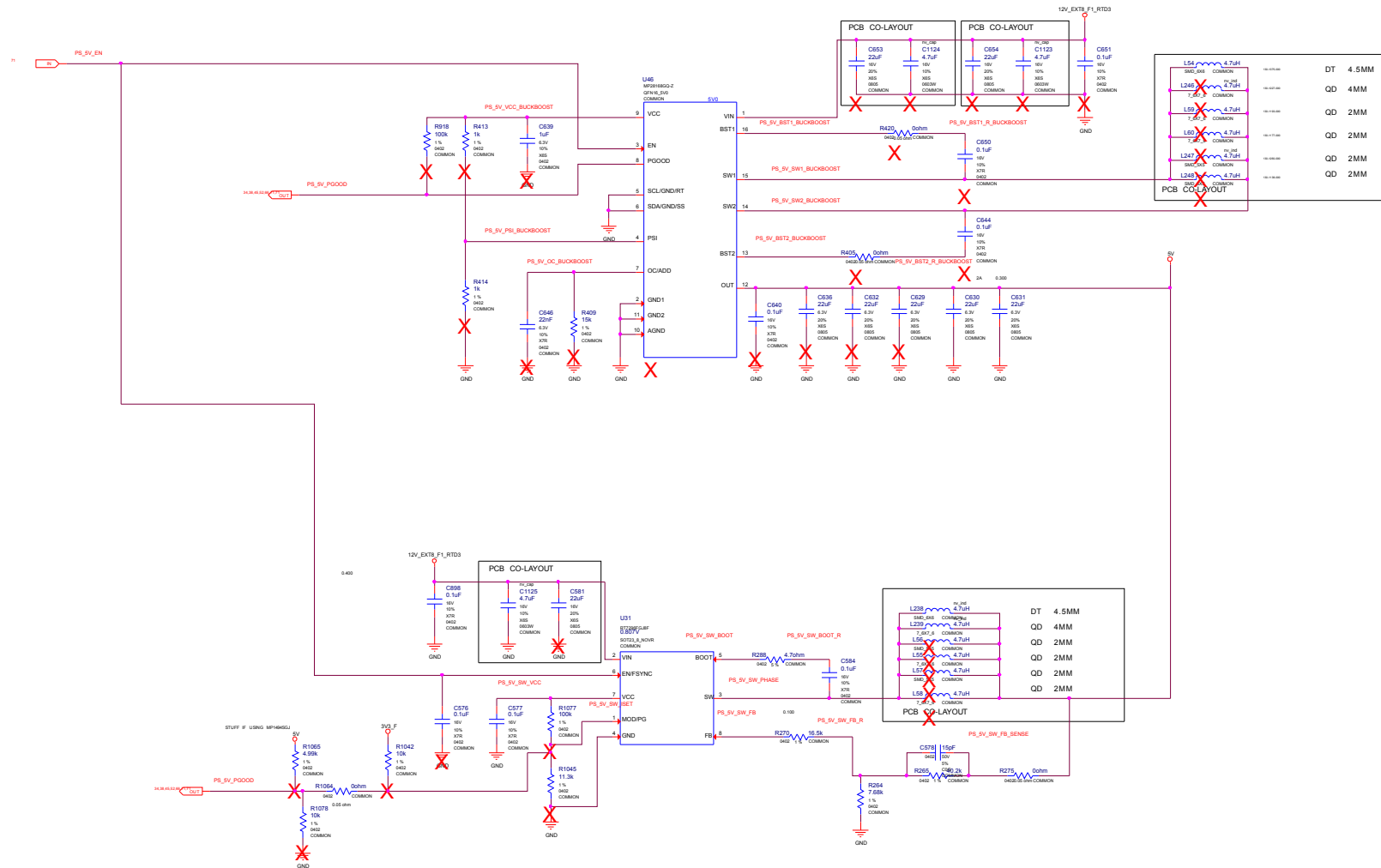
<-LATEST A02 PCB



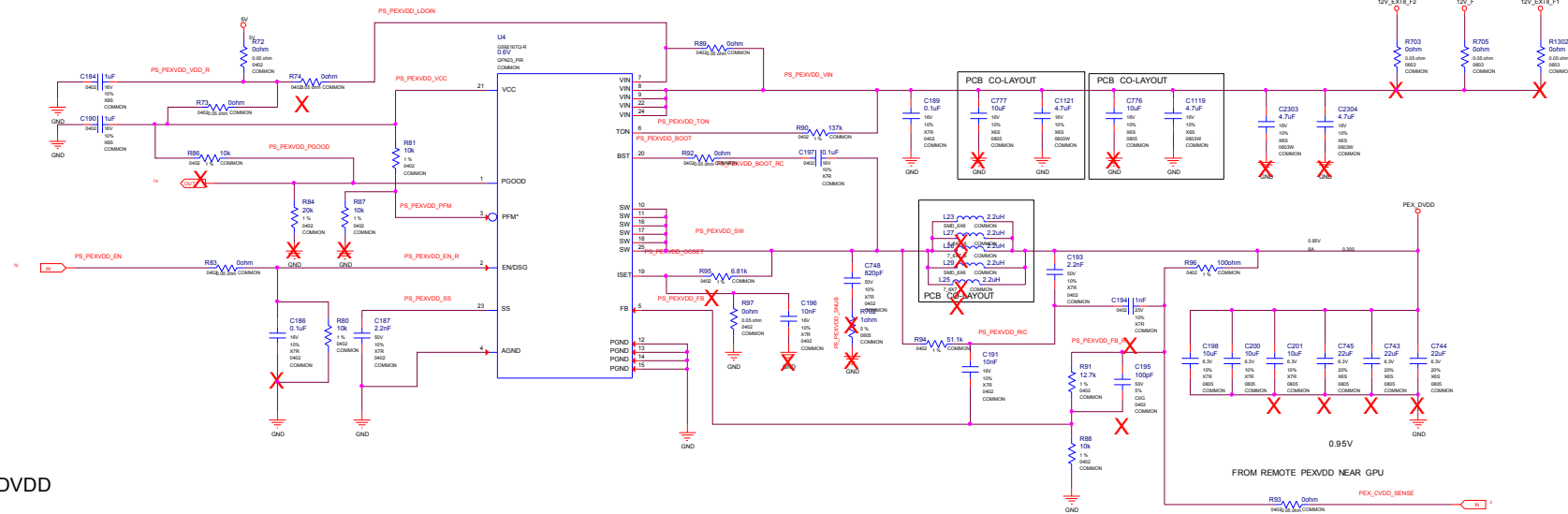
## MISC: XTAL. PLL



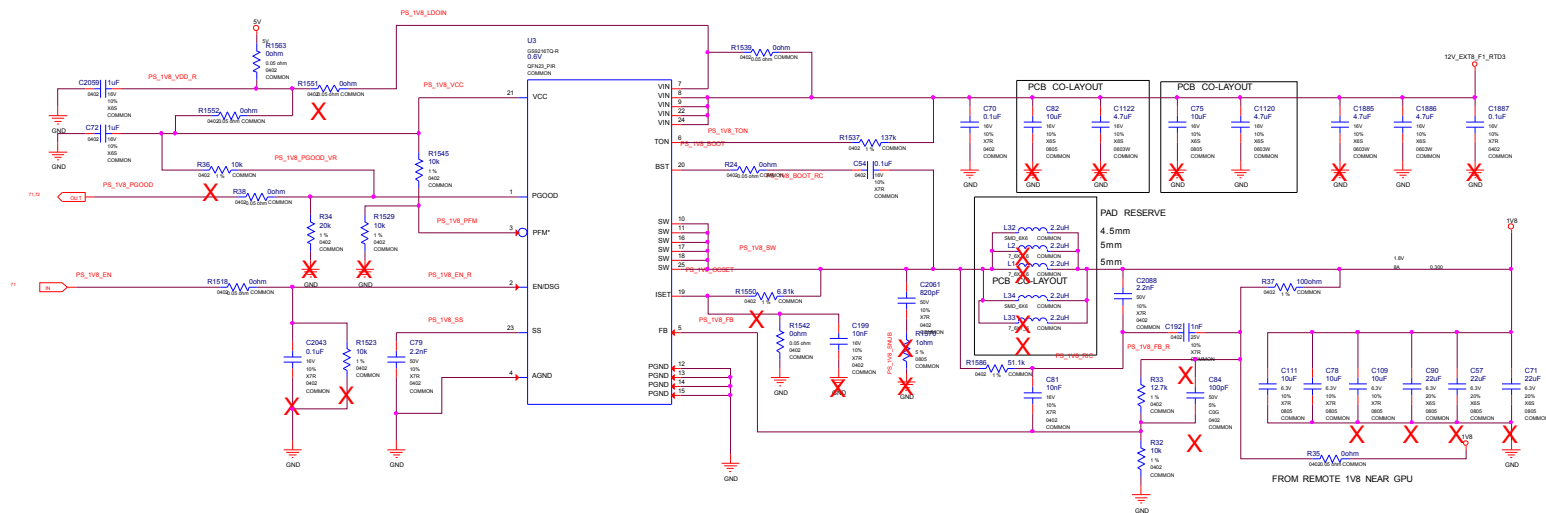
PS: 5V, 5V\_BACKUP



## PS: PEX\_DVDD 1V8 Rail



PEX\_DVDD



1V8

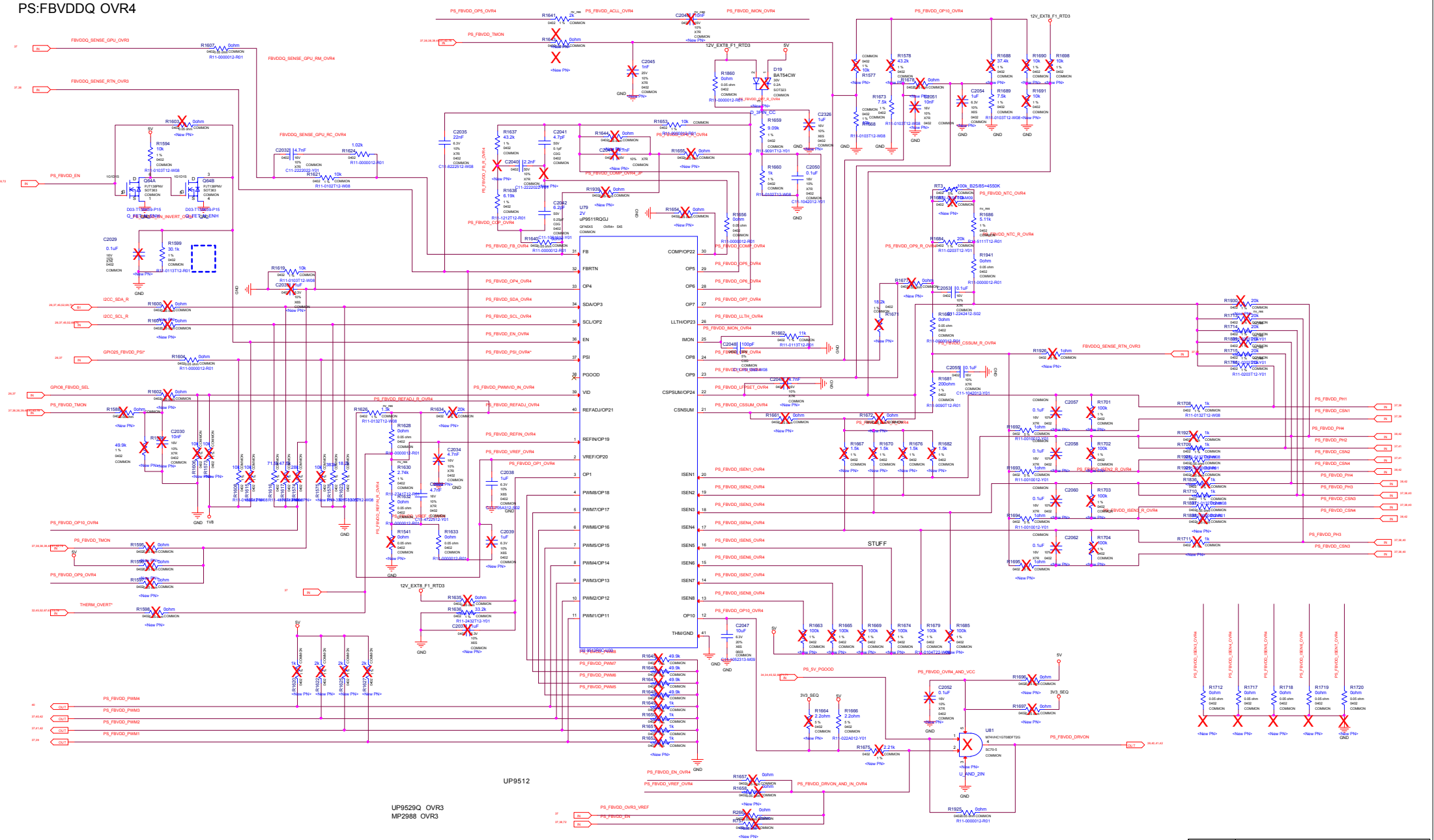
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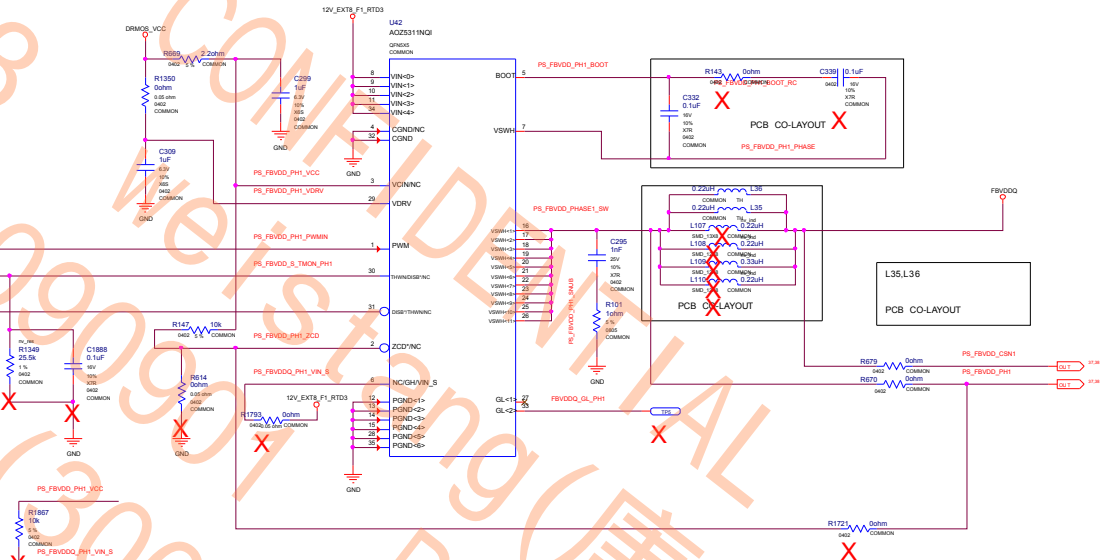
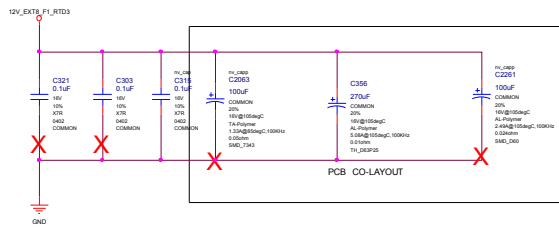
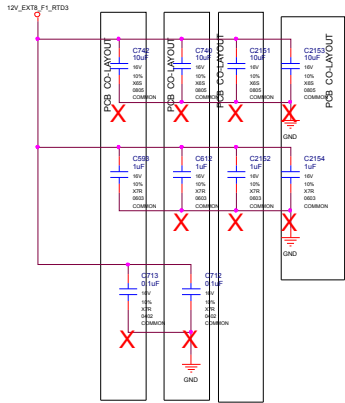
PIN12 MP2988 VIN SENSE 1/16 DIVIDER  
PIN12 UP9529 REFOUT



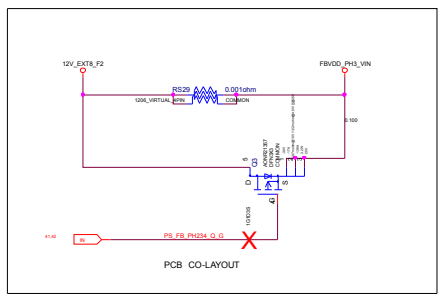
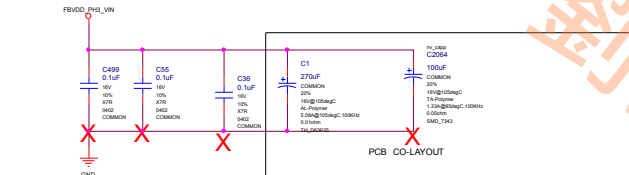
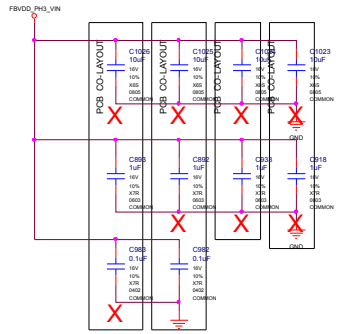
PS:FBVDDQ OVR4



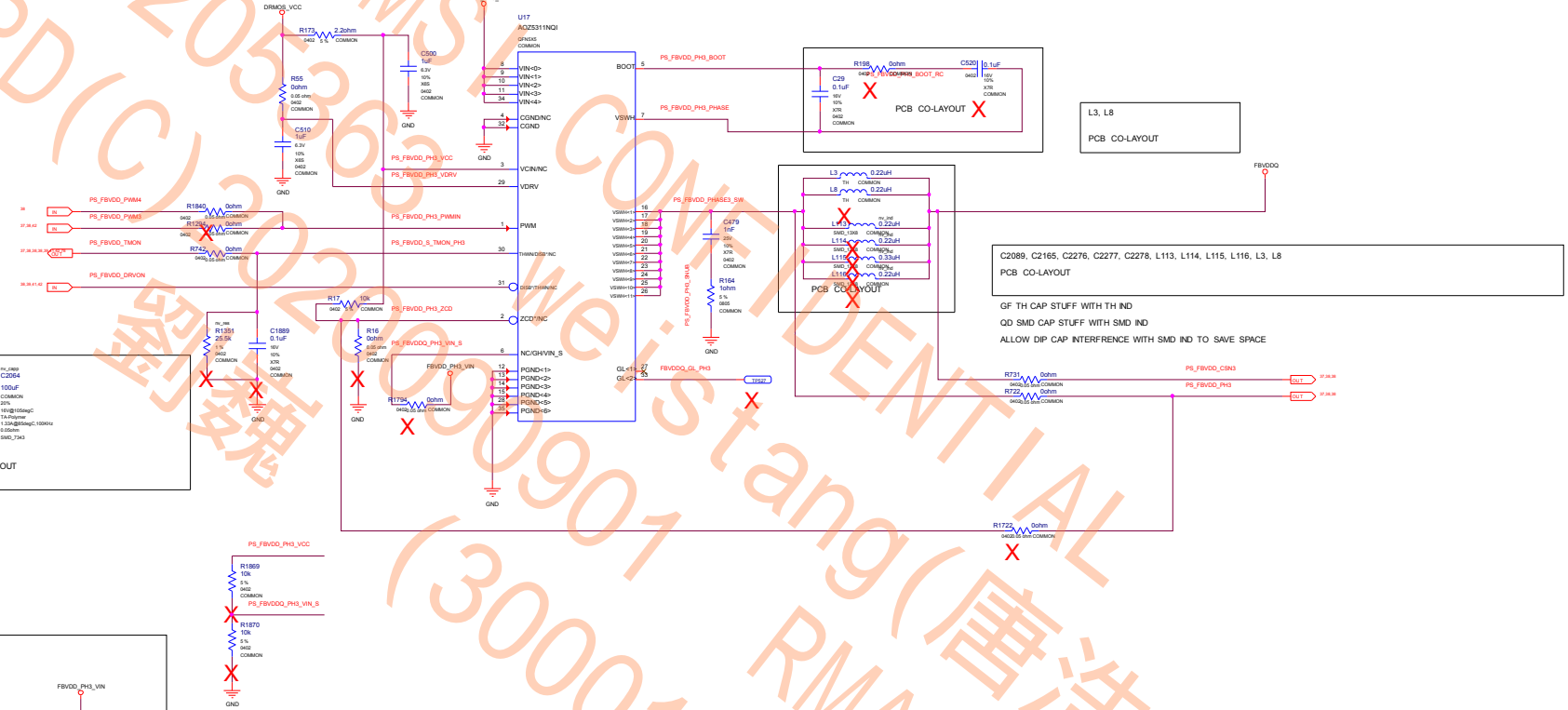
## PS: FBVDD PH1



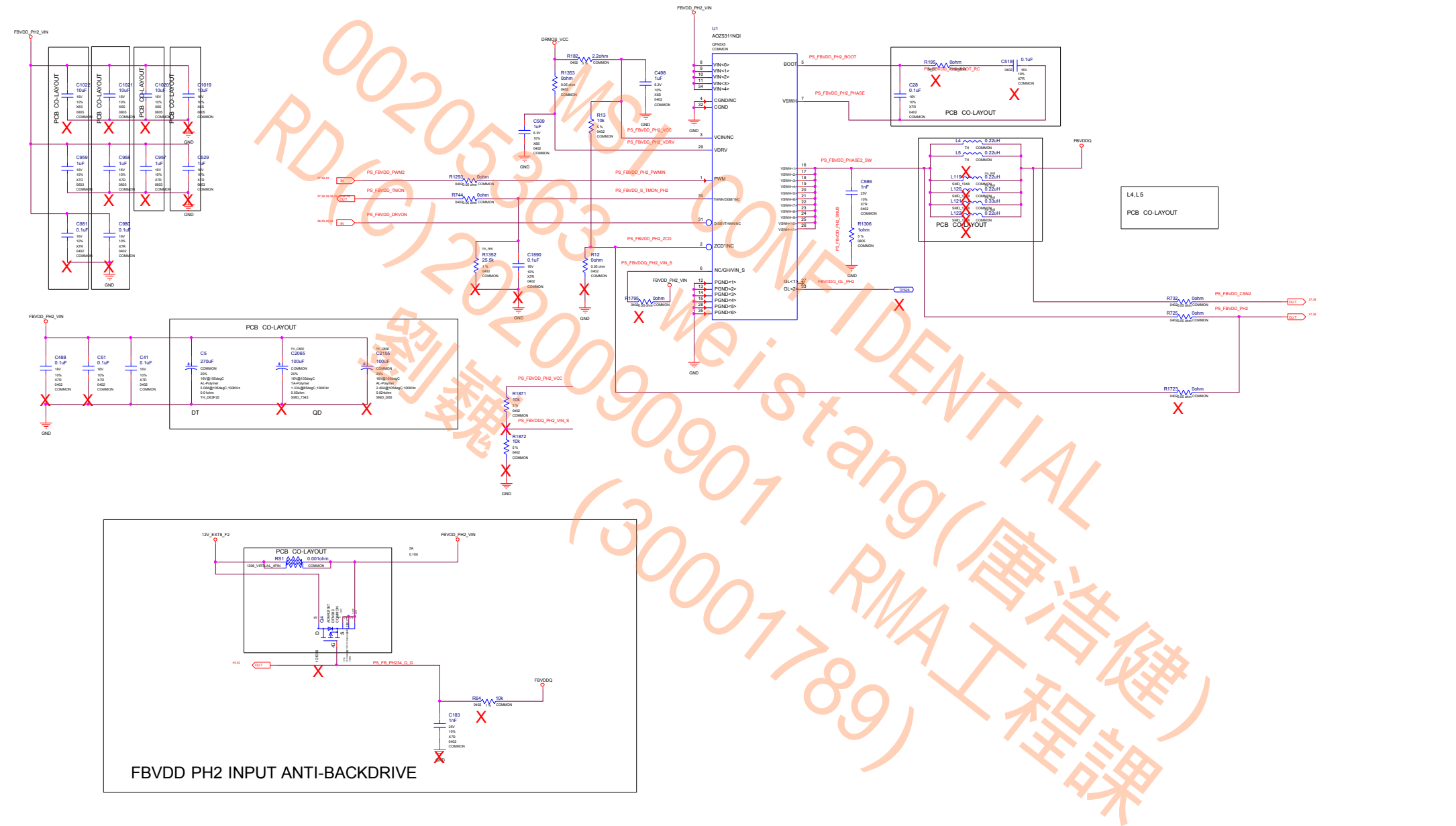
## PS: FBVDD PH3



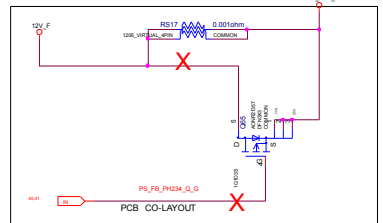
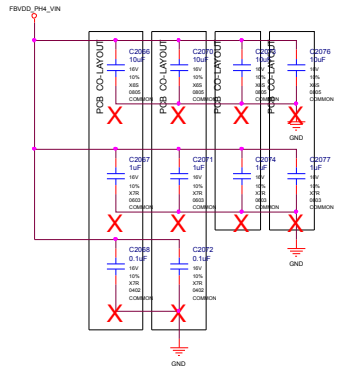
### FBVDD PH3 INPUT ANTI-BACKDRIVE



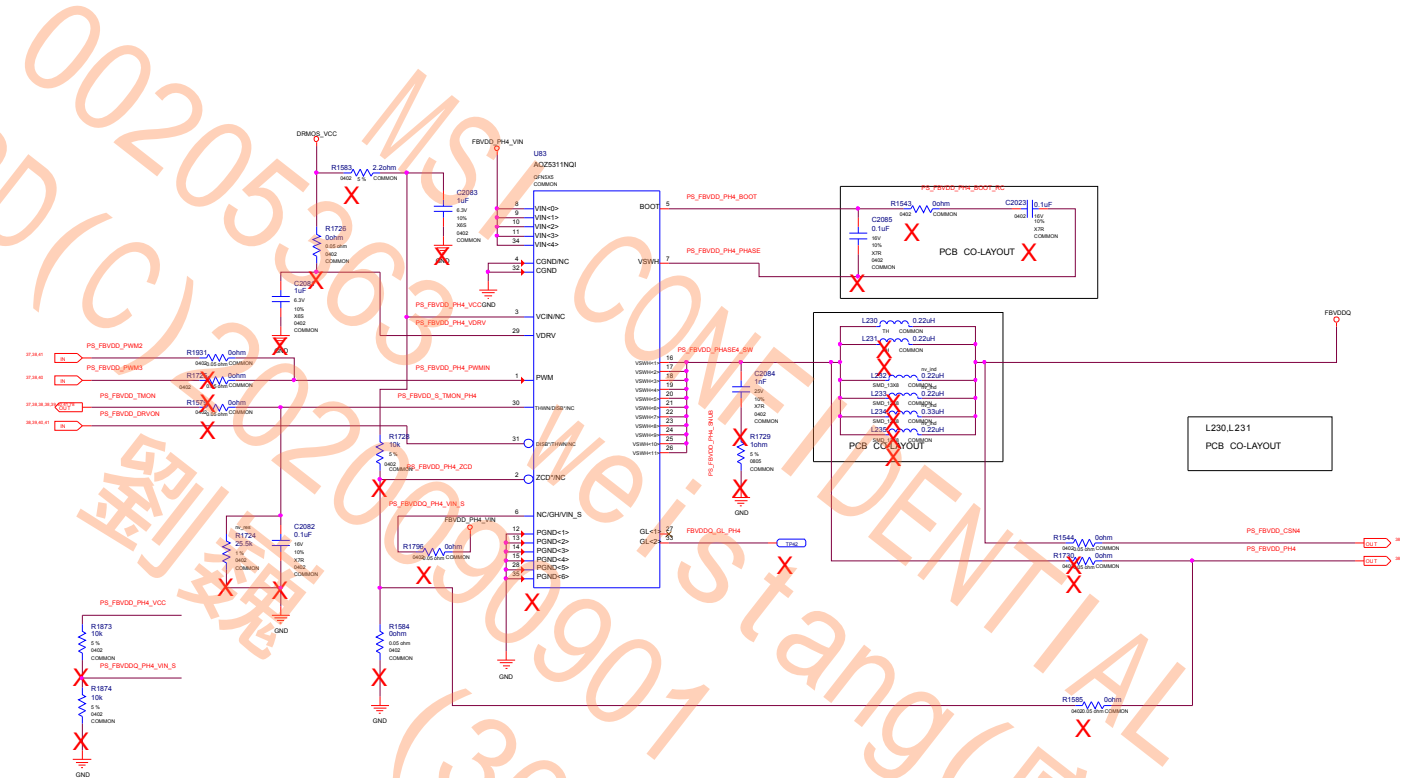
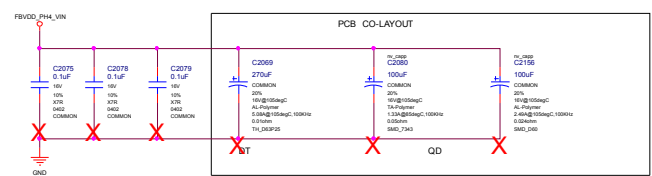
PS: FBVDD PH2



# PS: FBVDD PH4

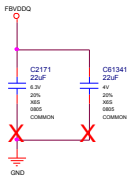
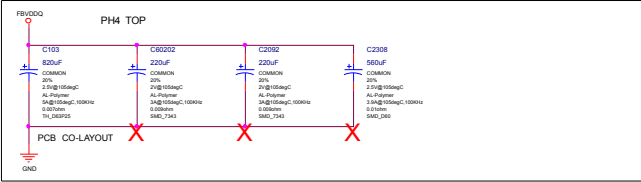
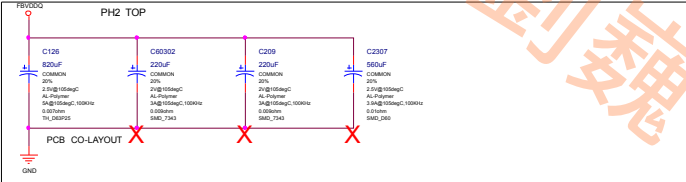
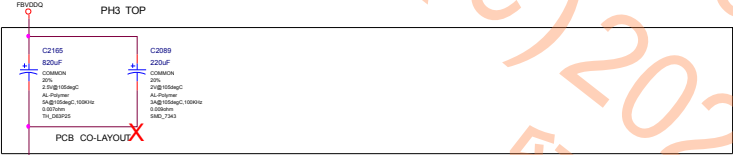


## FBVDD PH3 INPUT ANTI-BACKDRIVE

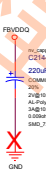
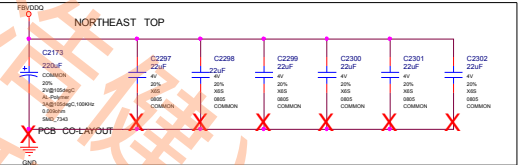
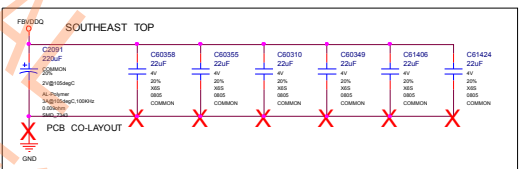
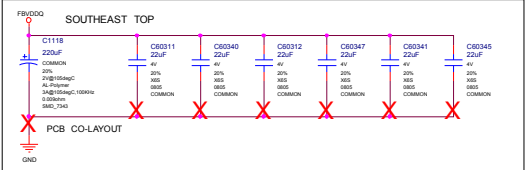
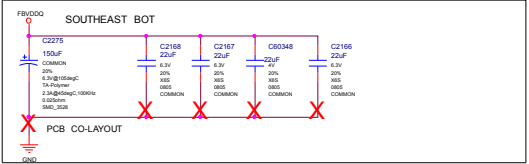
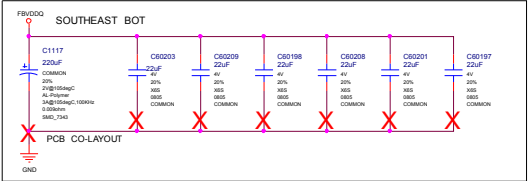
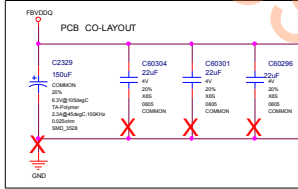
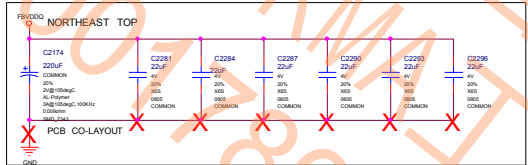
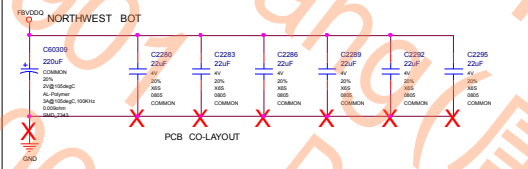
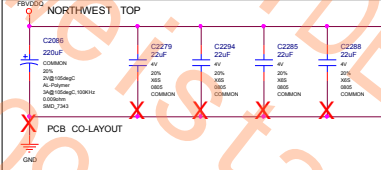
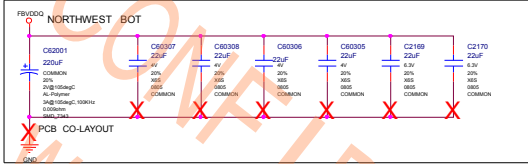
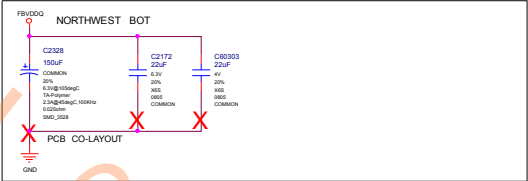


PS: FBVDD OUTPUT CAP

3x 820uF TH FBVDD OUTPUT BULK CAPS

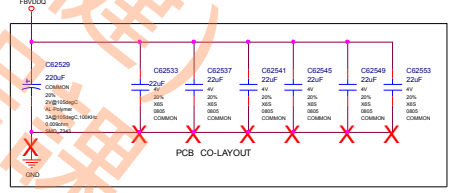
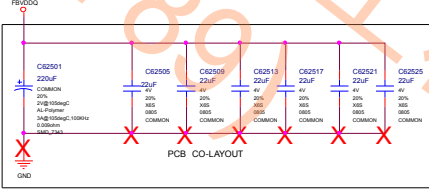
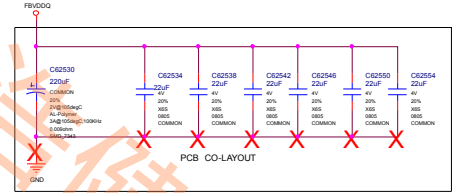
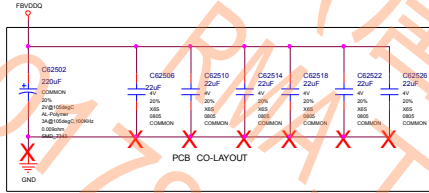
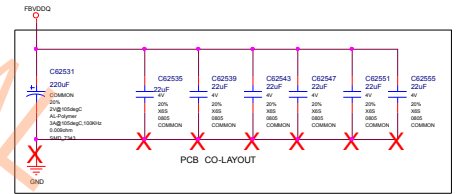
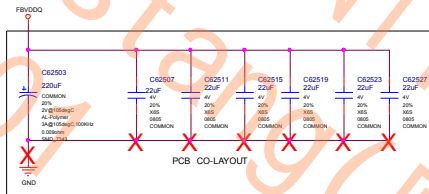
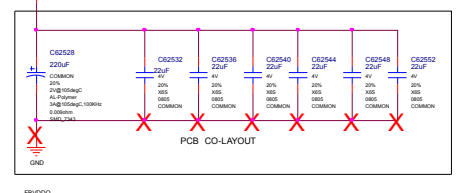
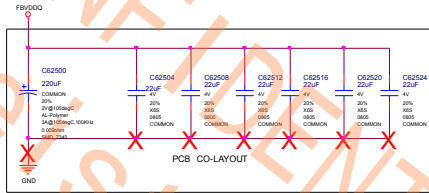
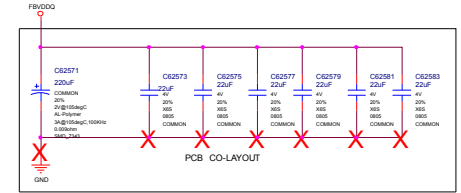
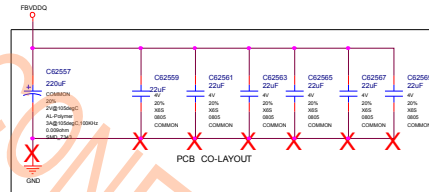
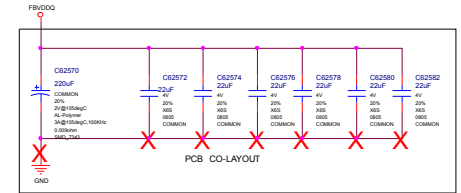
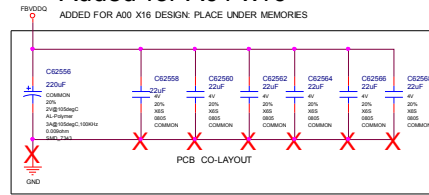


CLOSE TO FB PH3 OUTPUT STUFF

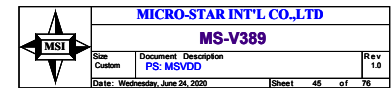


# Added for A01 x16

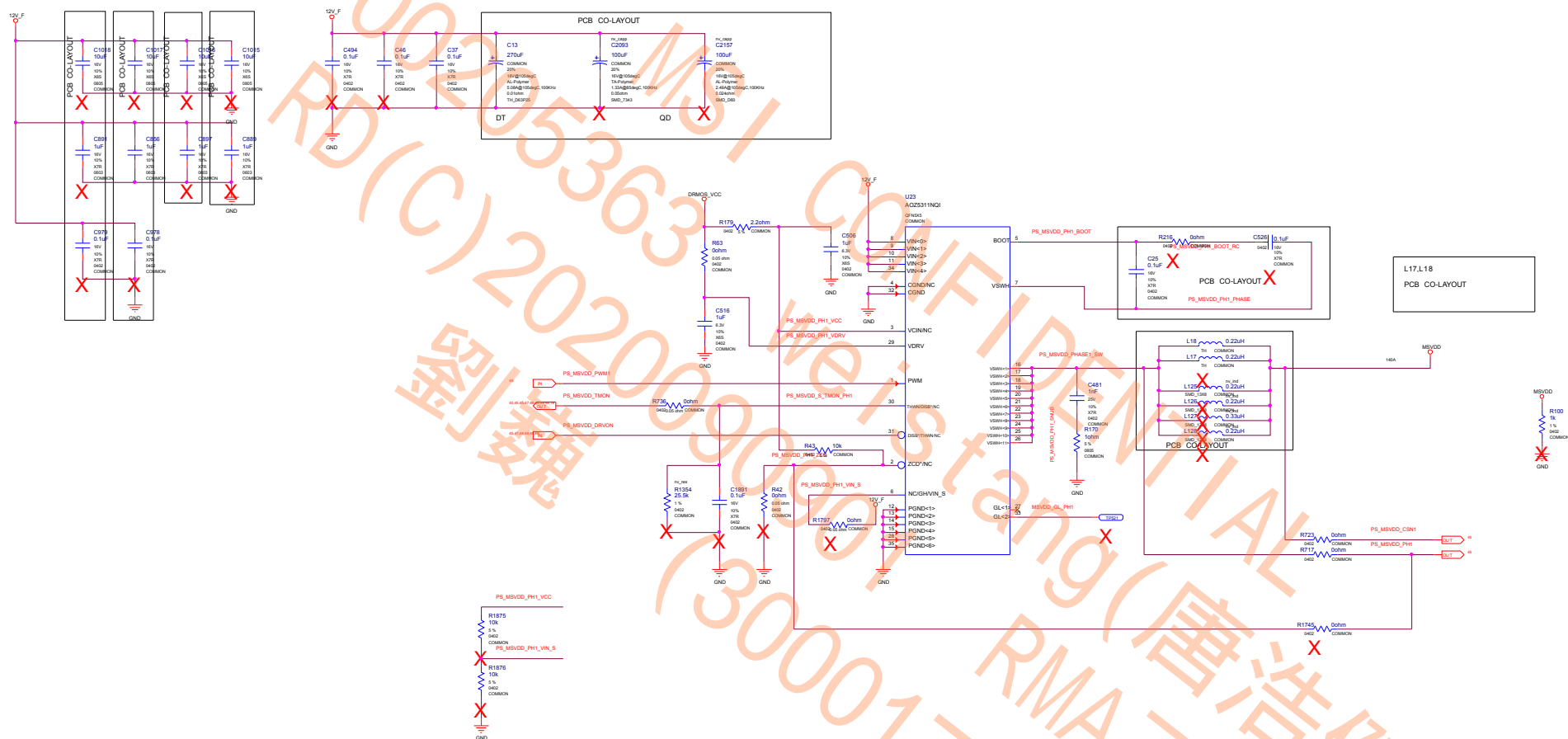
ADDED FOR A00 X16 DESIGN: PLACE UNDER MEMORIES

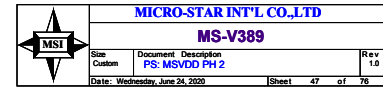




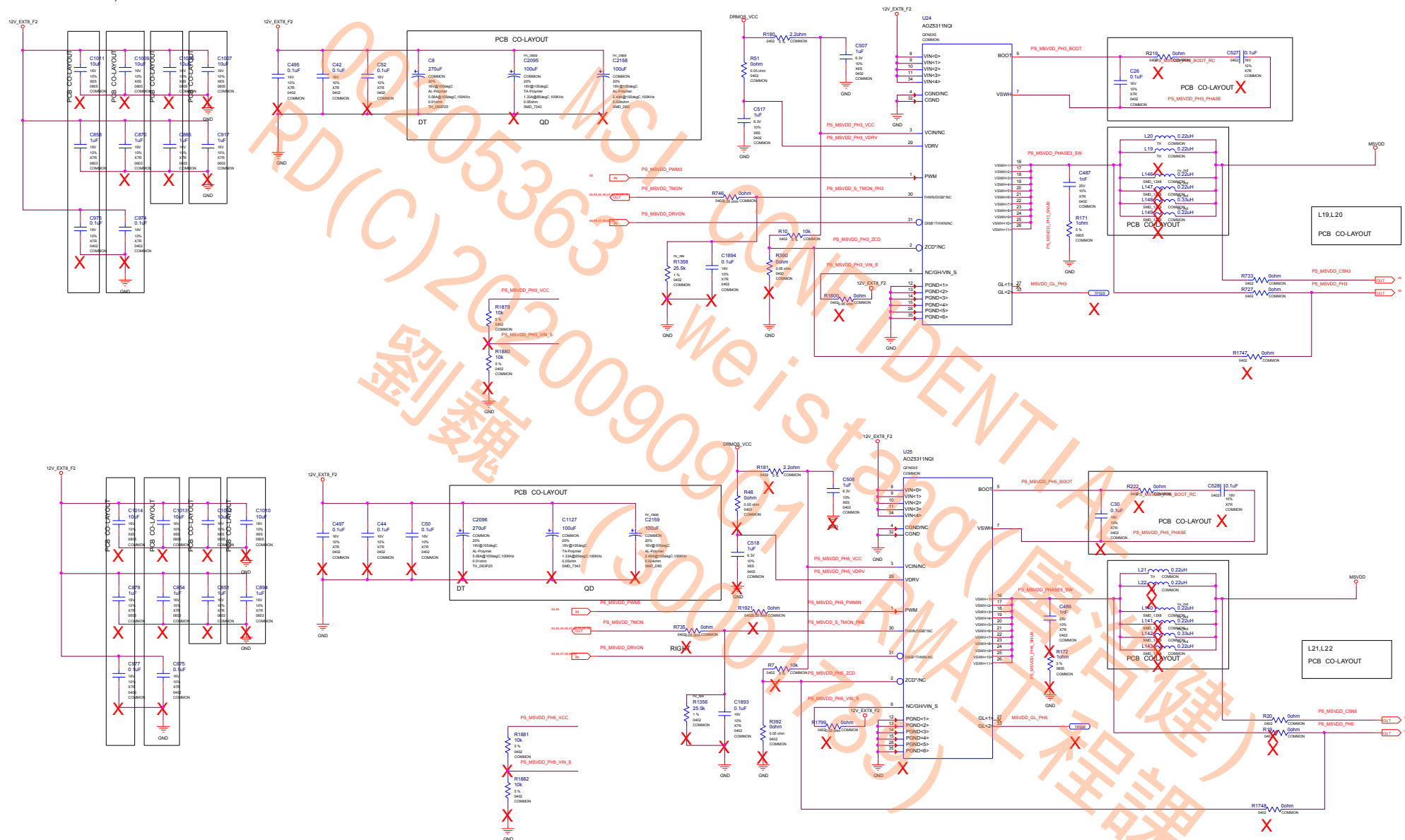
[illegible]

## PS: MSVDD Phase 1

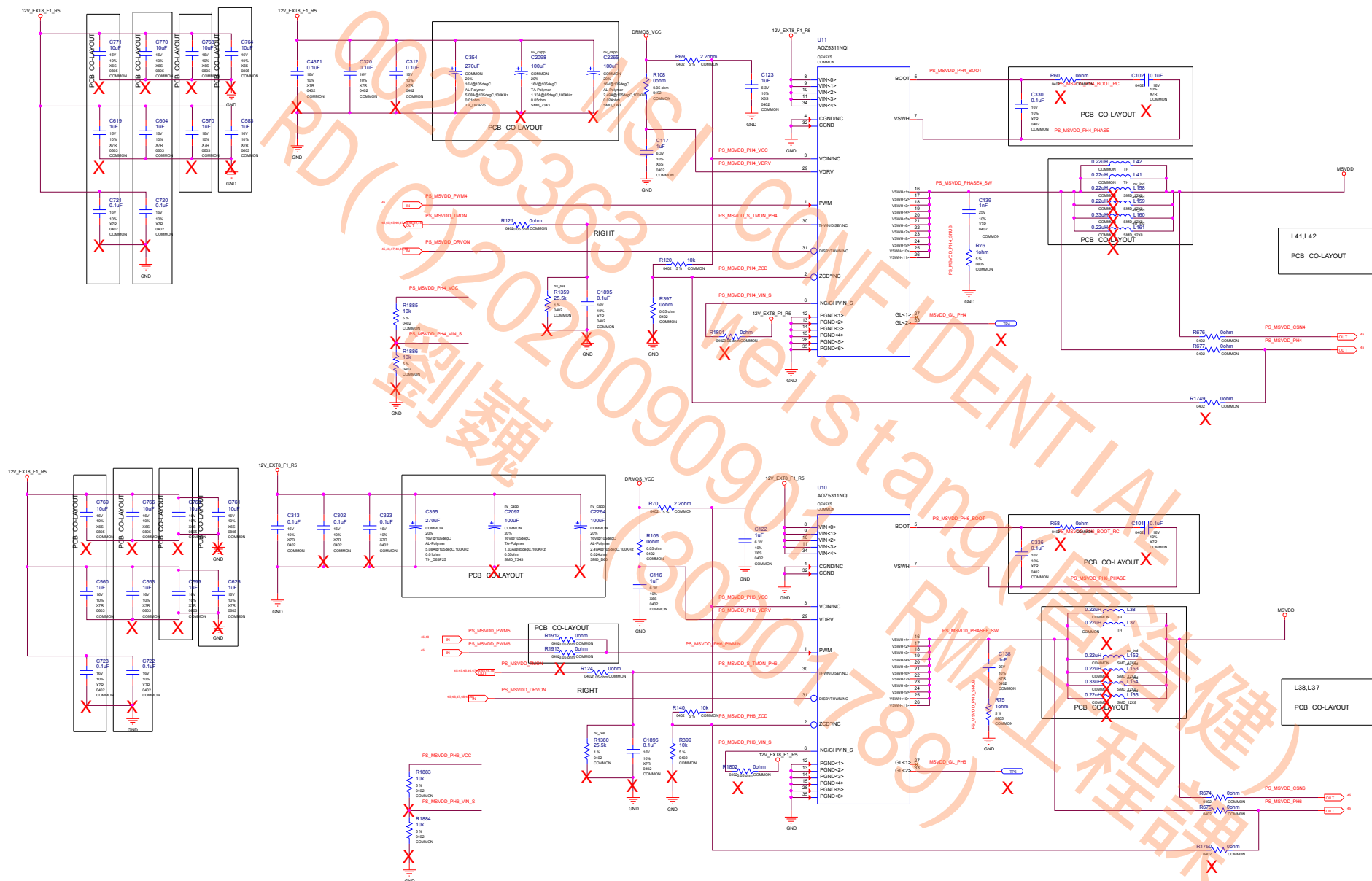




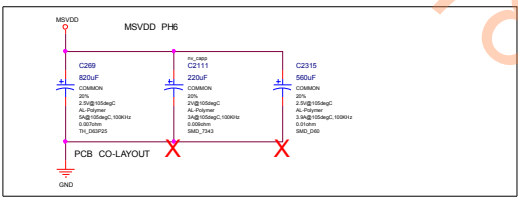
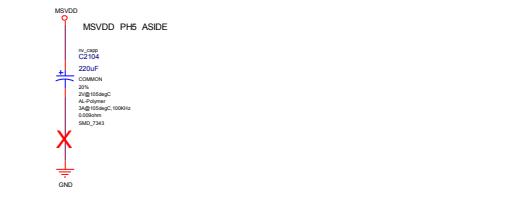
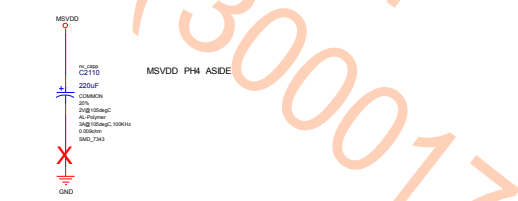
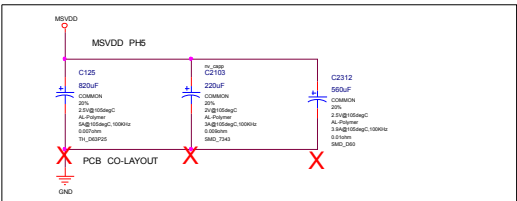
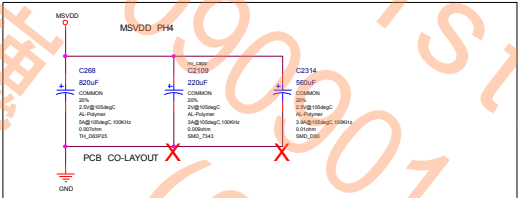
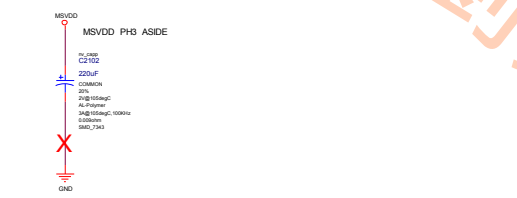
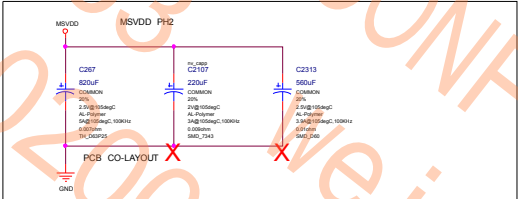
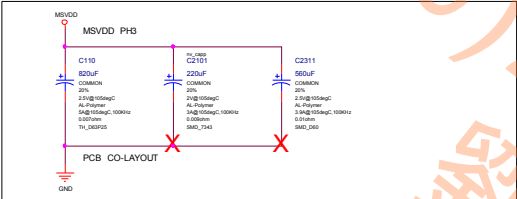
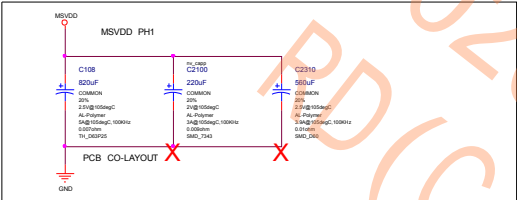
## PS:MSVDD Phase 3, 5



## PS: MSVDD PH 4,6



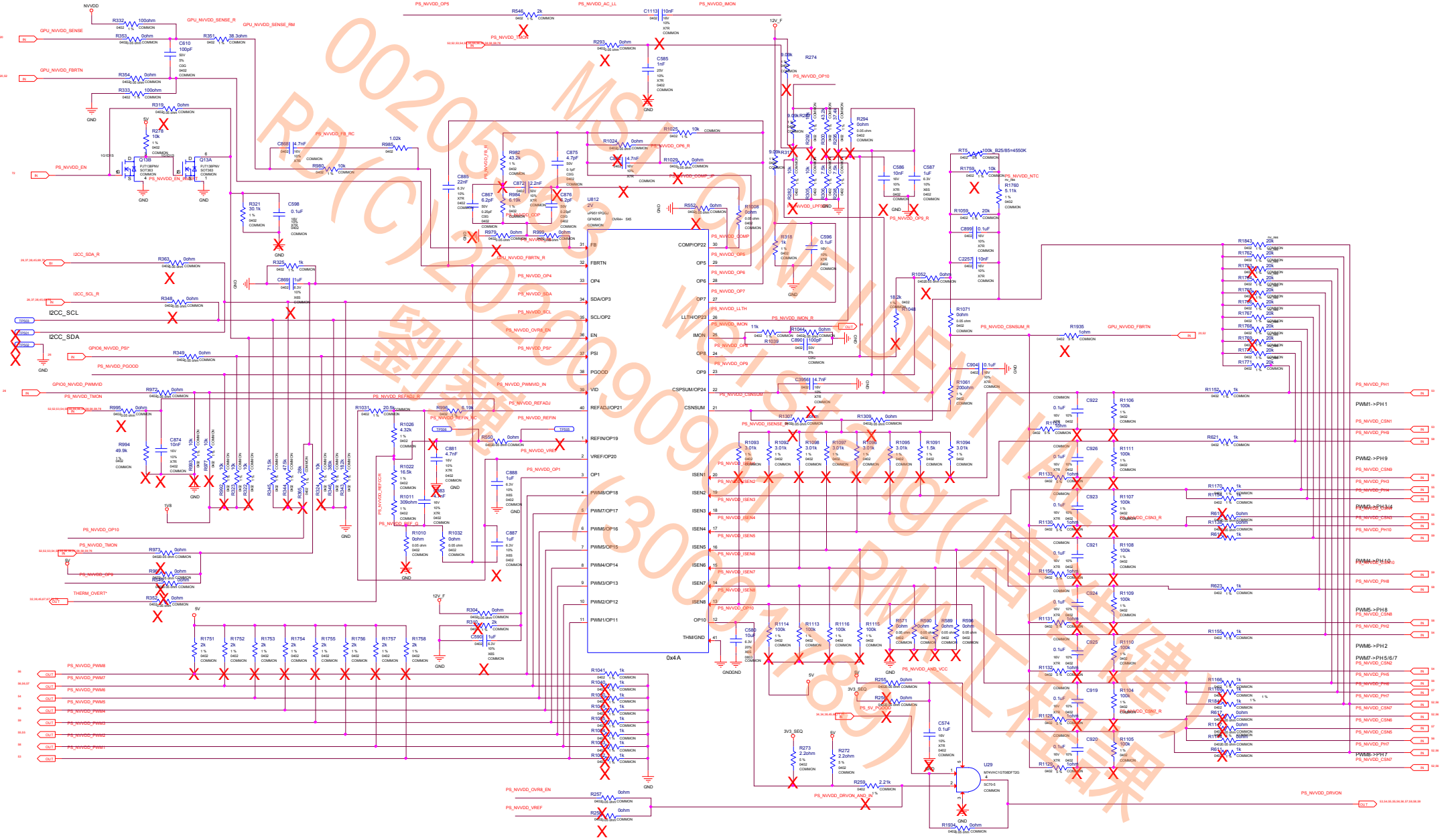
PS:MSVDD OUTPUT CAP (TOP)



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RD(C)2020090901  
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weistang (唐浩健)  
RMA工程課  
(30001789)

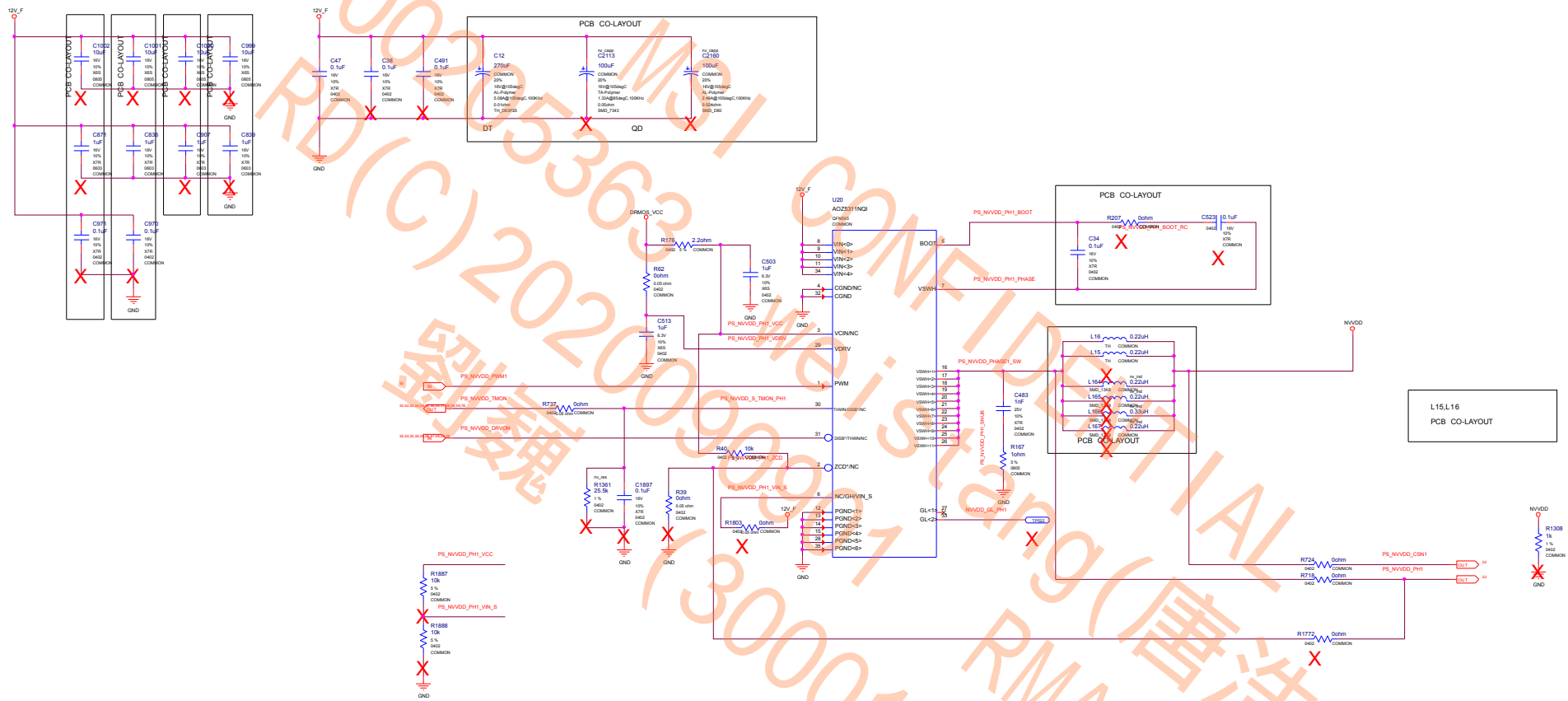
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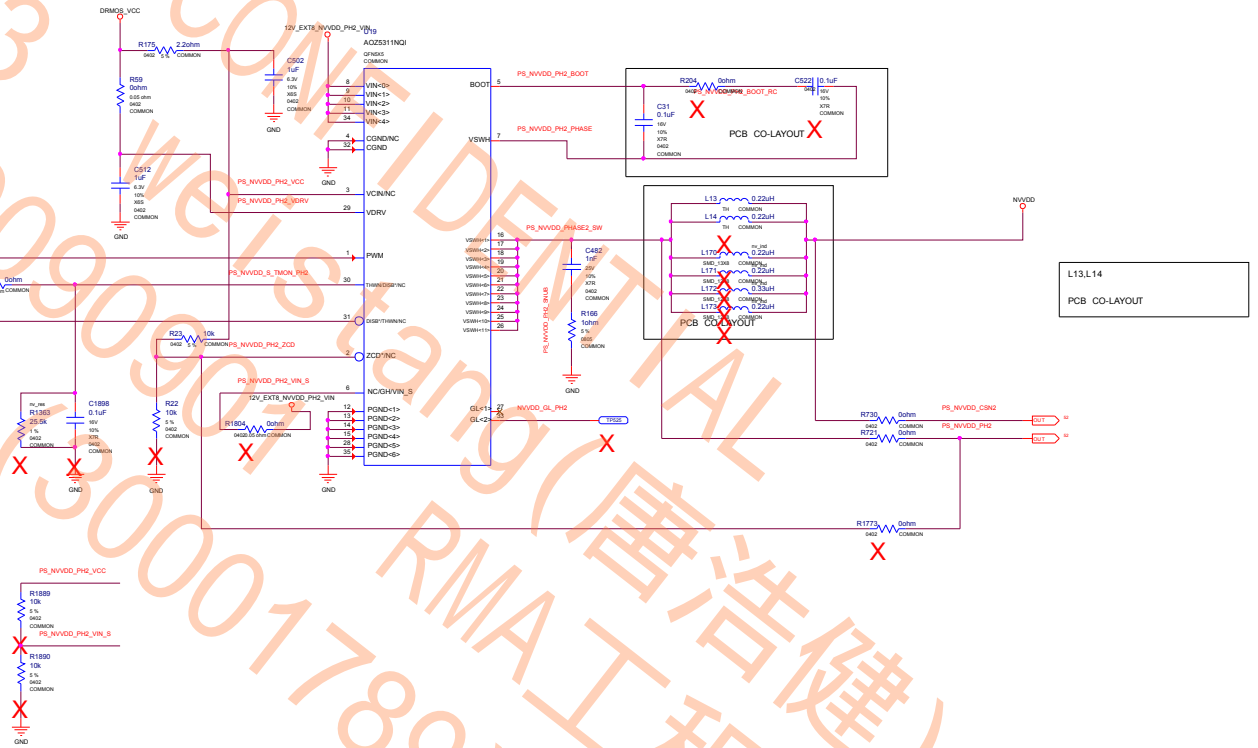
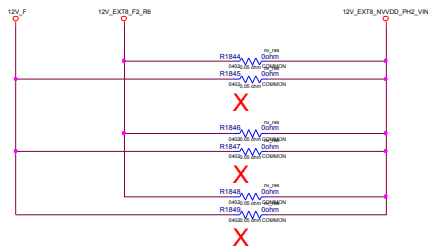
## PS: NVVDD Controller\_OVR8



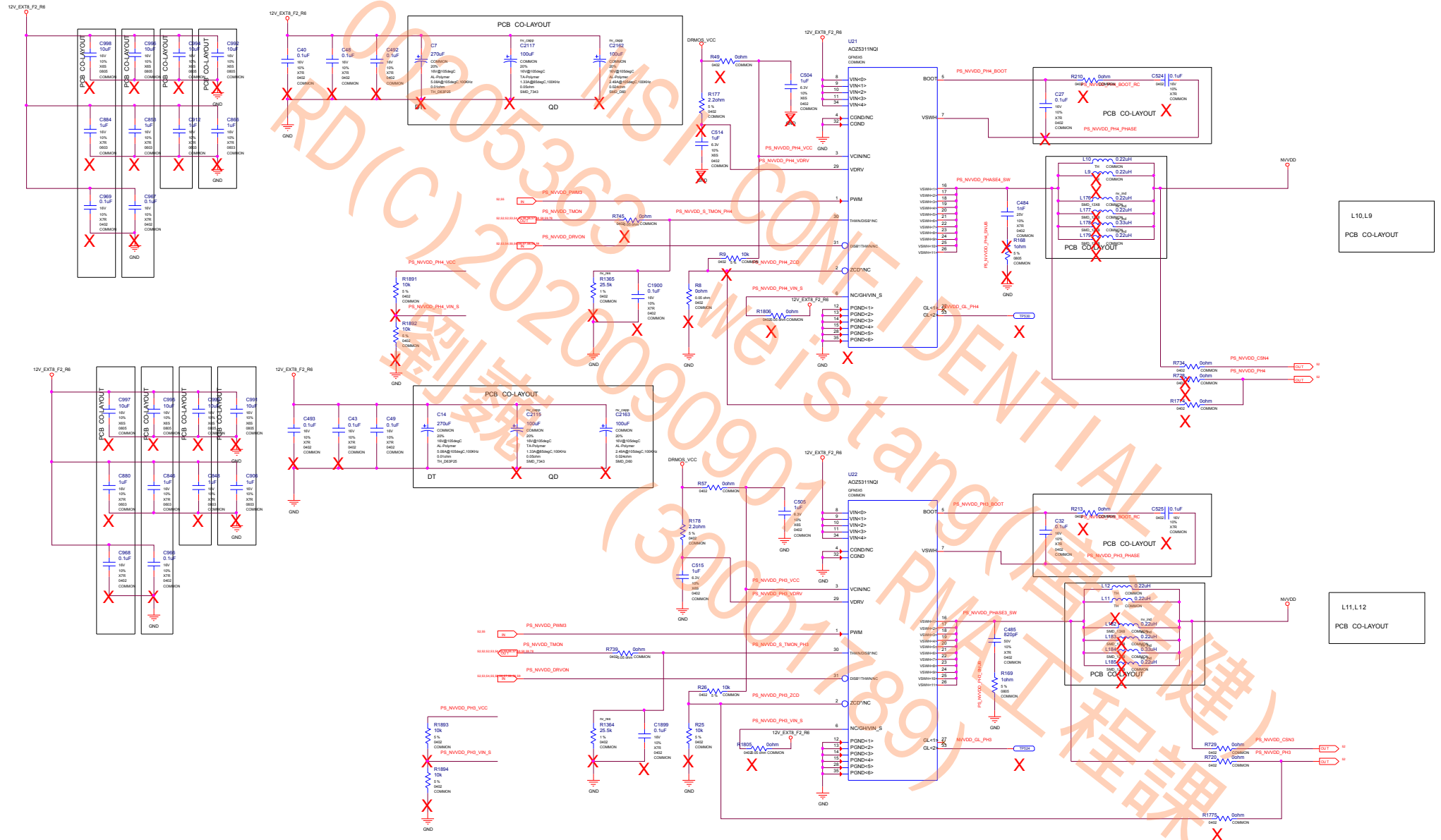


PS: NVVDD Phase 1

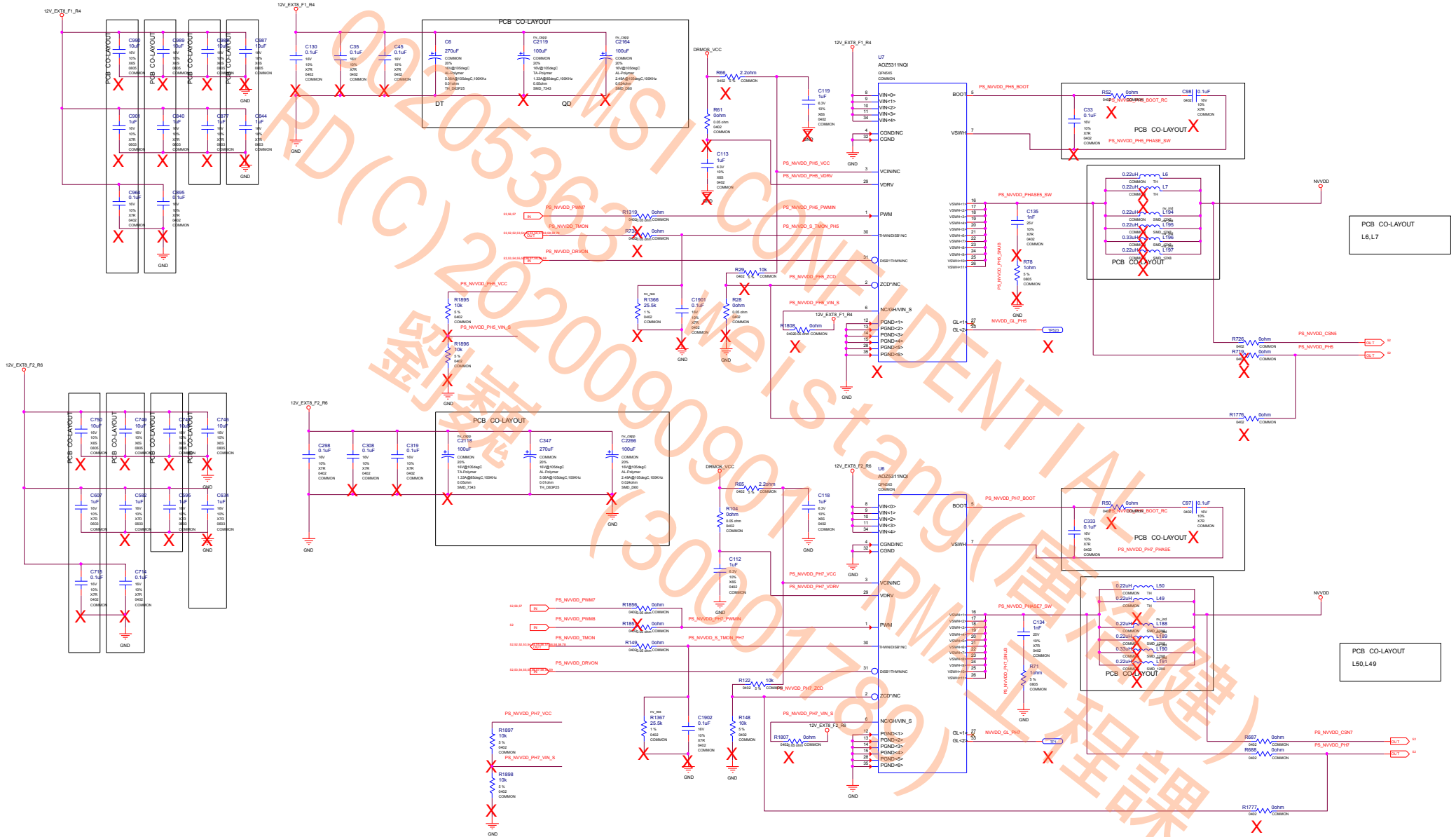


[illegible]

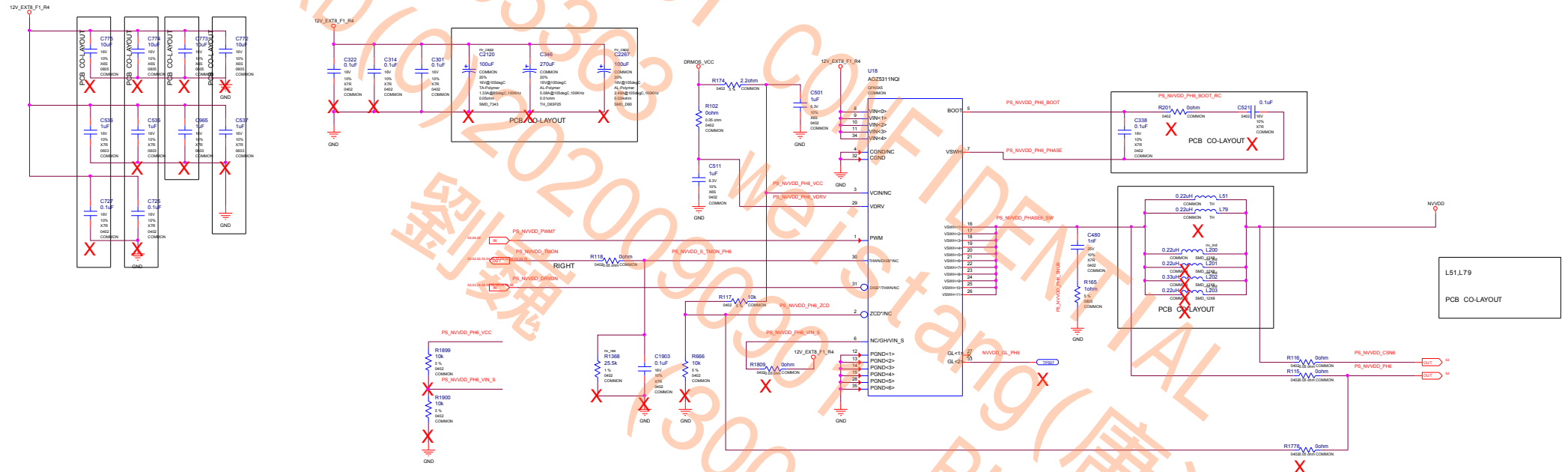
PS: NVVDD Phase 3 (PWM6), PHASE 4(PWM2)



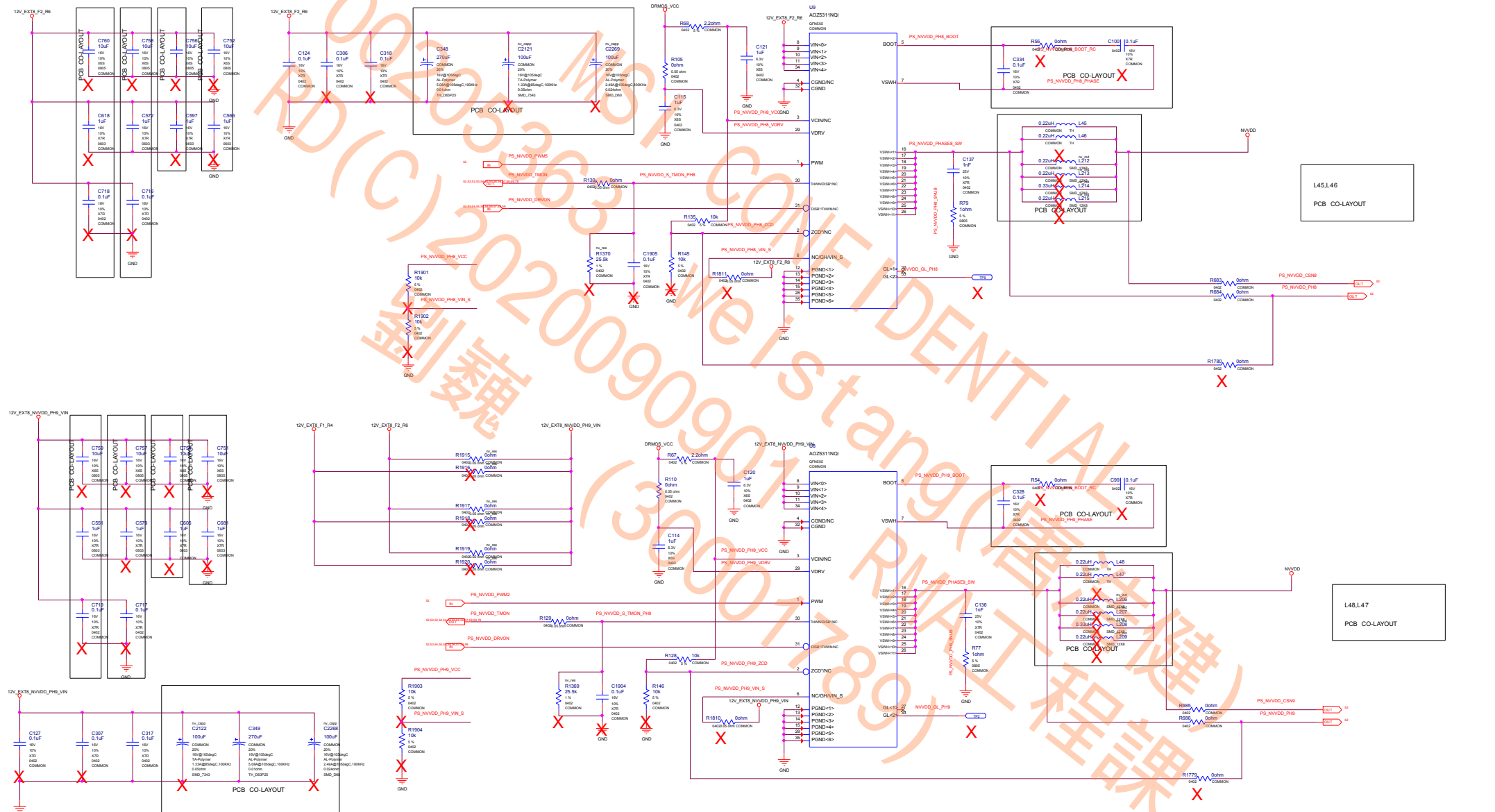
## PS: NVVDD PH 5(PWM2), PH7(PWM3)



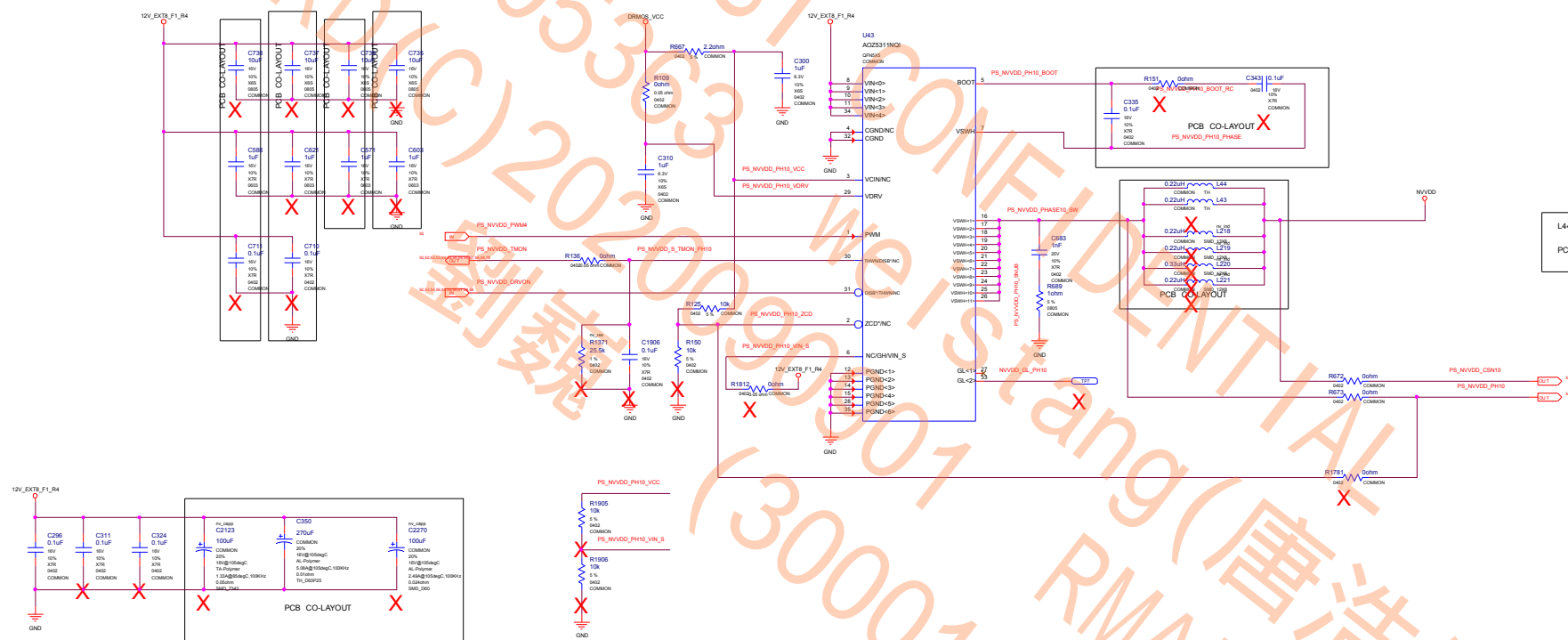
## PS: NVVDD Phase 6(PWM7)



## PS: NVVDD PH 8 (PWM5), PH 9(PWM8)



## PS: NVVDD Phase 10 (PWM8)



PCB CO-LAYOUT

C131, C132, C133, C140, C141, C142, C2106, C2107, C2133, C2134, C2135, C2137, C264, C265, C266, C267, C61, C64, C65, C66, C67, C68, C69, C77, C80, C83, C88

PCB CO-LAYOUT

C103, C105, C106, C107, C108, C110, C1208, C125, C1254, C1256, C1267, C128, C144, C145, C146, C151, C152, C153, C154, C159, C160, C161, C162, C170, C173, C2092, C2100, C2101, C2102, C2103, C2104, C2138, C2139, C2140, C2141, C2142, C2143, C2144, C2272, C230, C231, C232, C233, C243, C245, C246, C248, C253, C254, C255, C256, C270, C274, C275, C276, C283, C289, C290, C291, C60202, C62004, C62005

C163, C164, C166, C167, C2087, C2105, C2109, C2110, C2111, C224, C225, C261, C268, C269, C60354, C61428, C62, C63, C89, C91, C96

PCB CO-LAYOUT

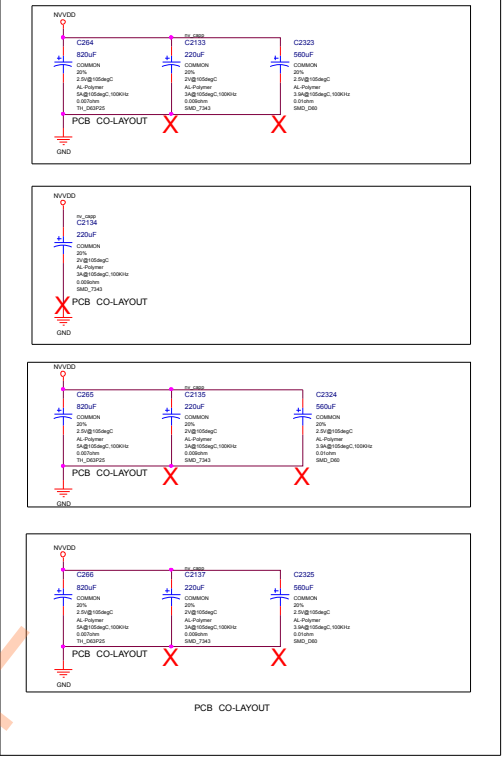
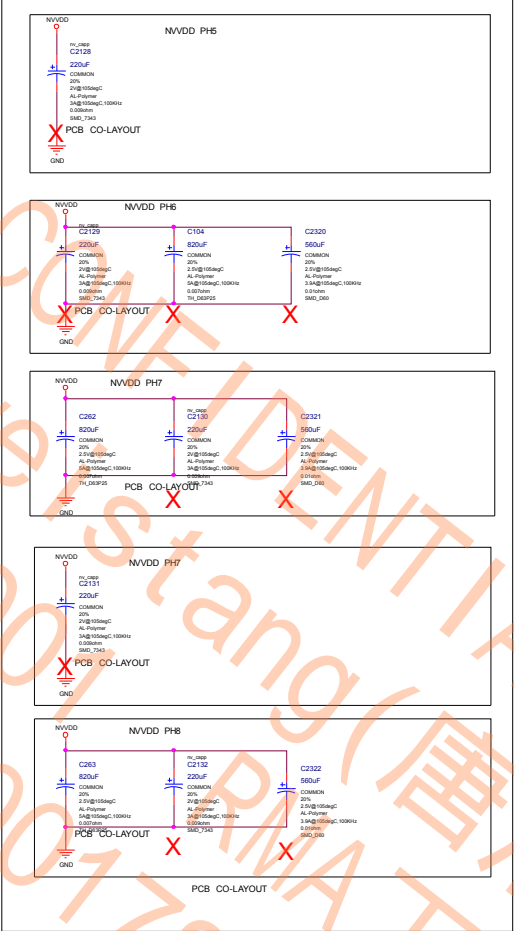
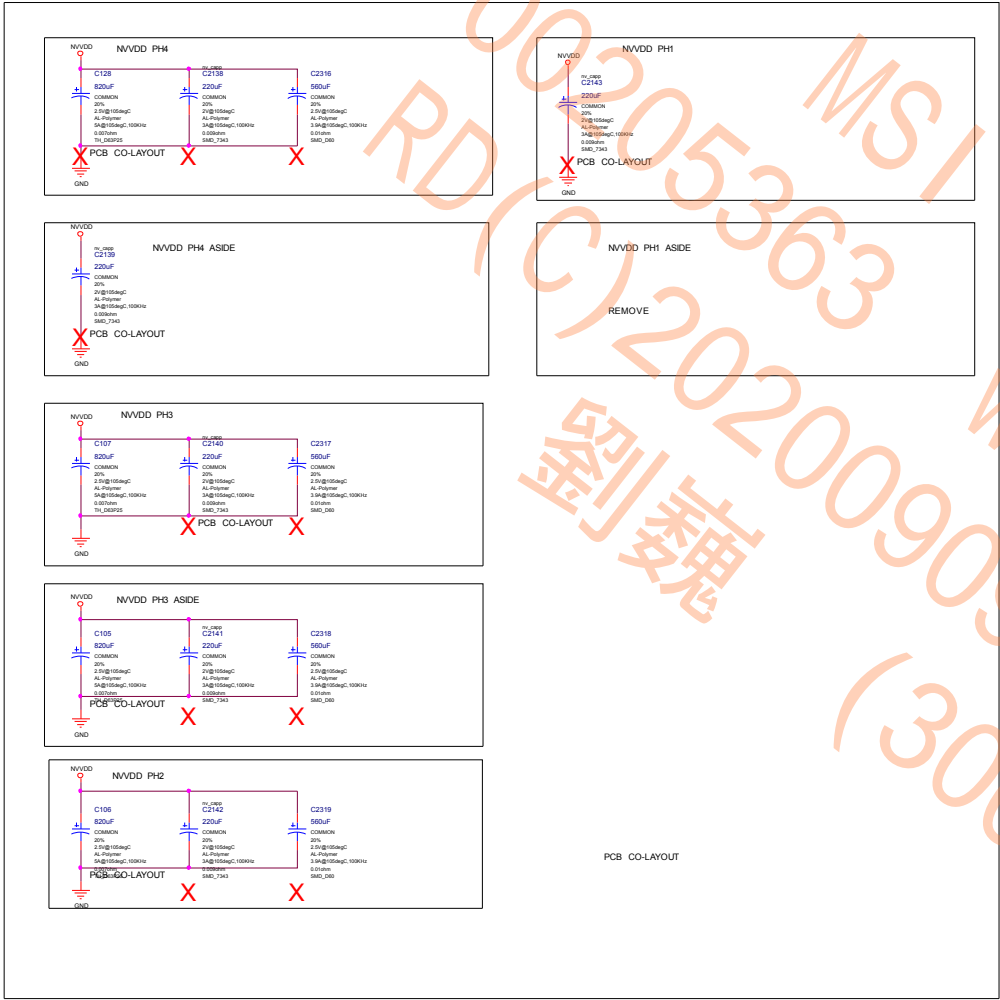


2020\_06\_17

- 1.Fix J1,J2 to reverse 8 pins
- 2.Change L238,L52,L53 footprint

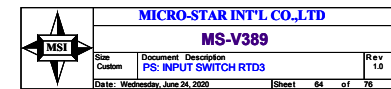
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RD(C)2020090901  
weistang(唐浩健)  
劉魏  
(30001789)  
RMA工程課

PS: NVDD OUTPUT CAP(TOP)



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RD(C)20200901  
Weistang (唐浩健)  
RMA工程課  
(30001789)  
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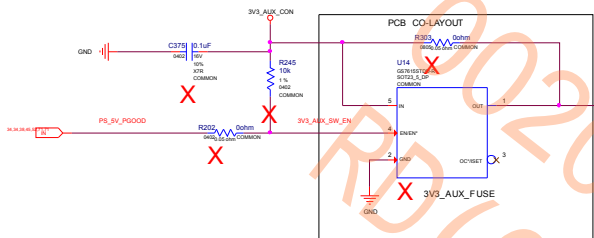
GPIO1	GPIO23	SWITCH	VOUT
0	0	0	12V_F
0	1	0	12V_F
1	0	0	12V_F
1	1	1	3V3A



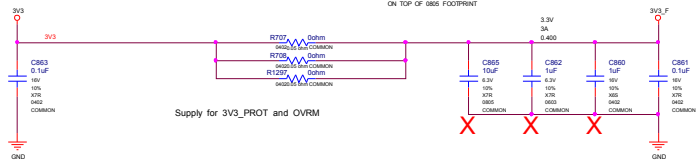
MSI CONFIDENTIAL  
00205363  
RD(C)2020090901  
唐浩健 (30001789)  
weistang (唐浩健)  
RMA工程課

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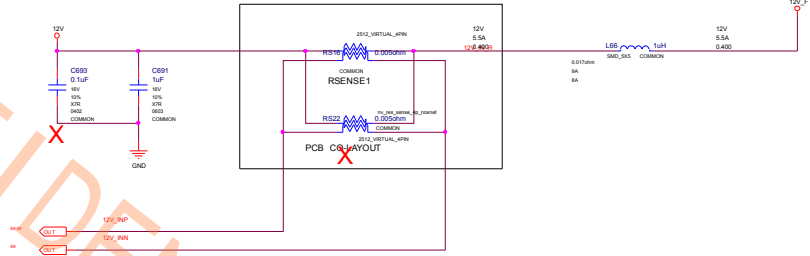
PS: Inputs, Filtering, and Monitoring



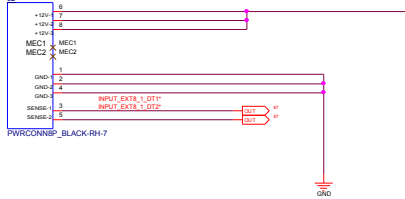
PEX 3V3 INPUT - 10W



PEX\_12V INPUT - 66W

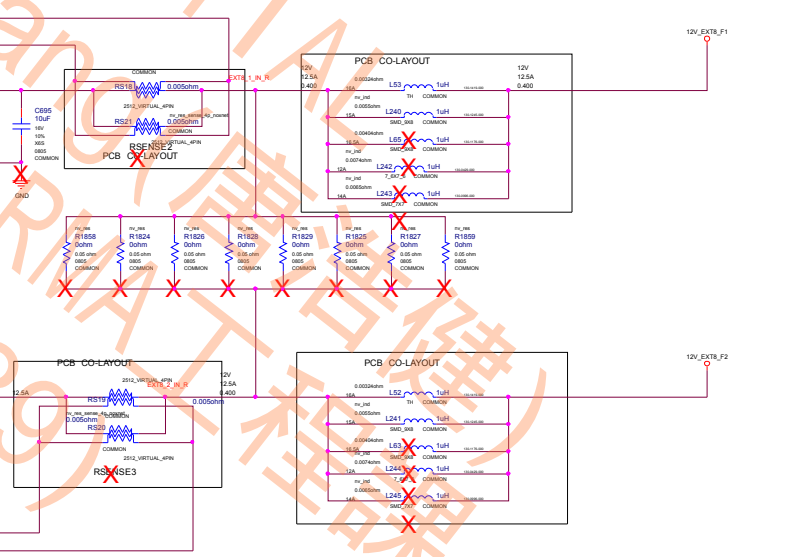
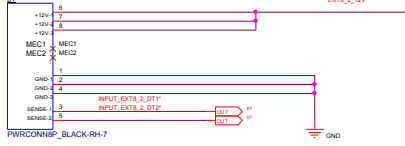


PEX8 INPUT 1 - 2x4 PCIe CON 150W

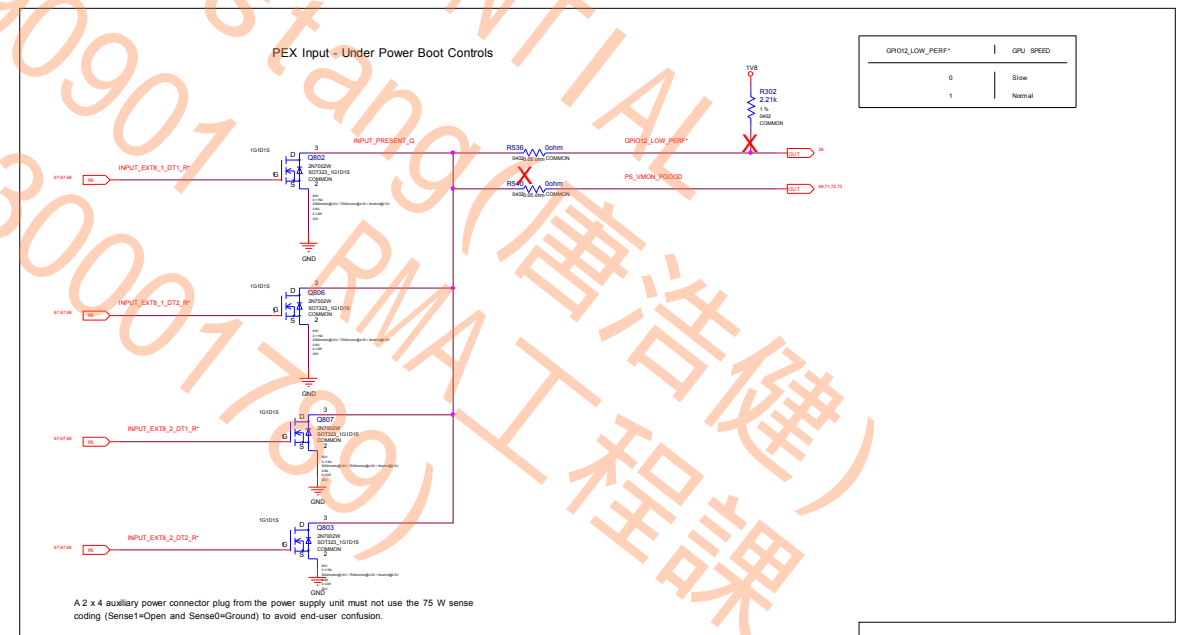
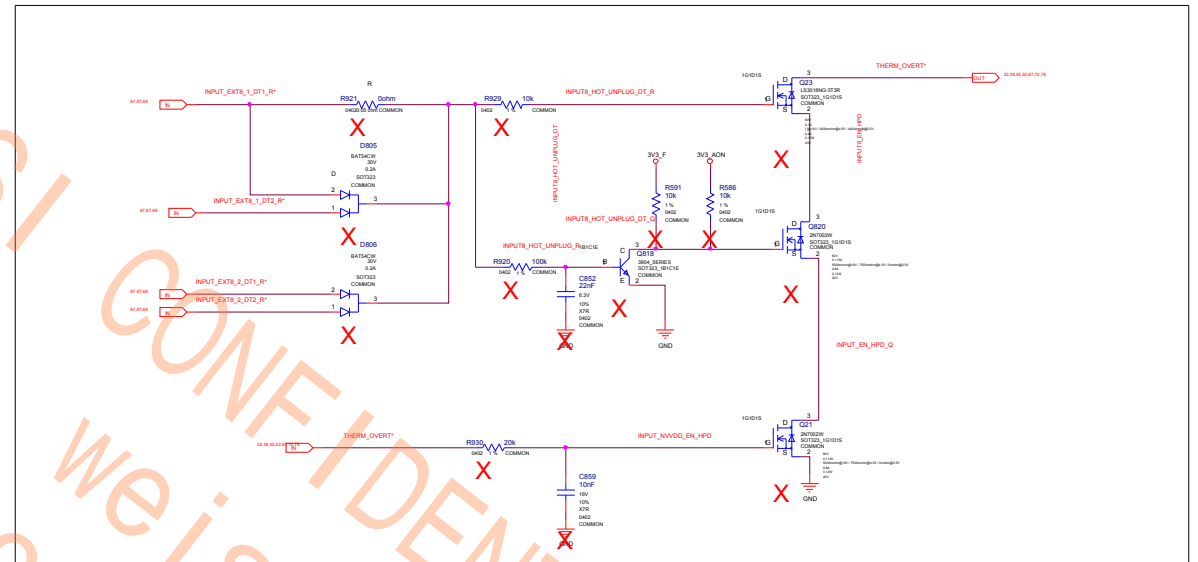
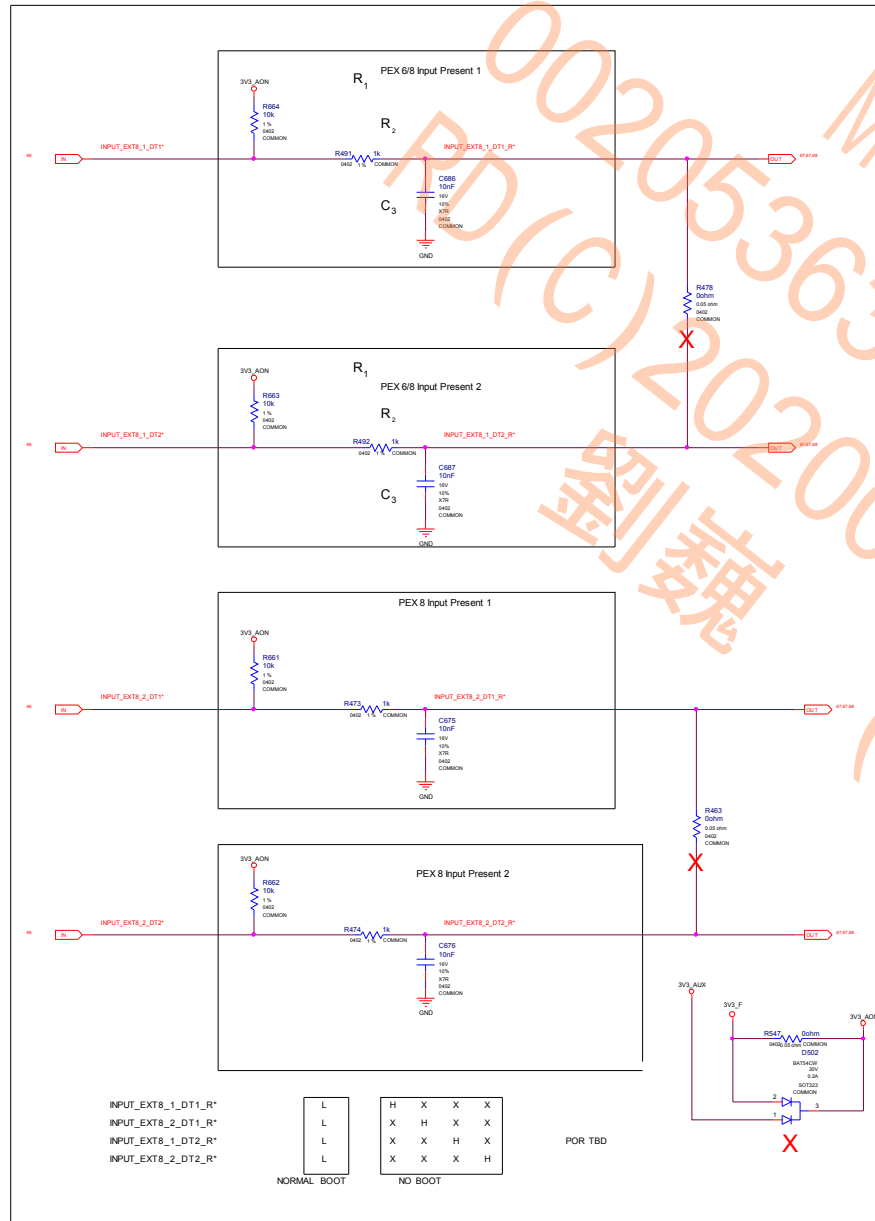


QD SKU CABLE

PEX8 INPUT 2 - 2x4 PCIe CON 150W

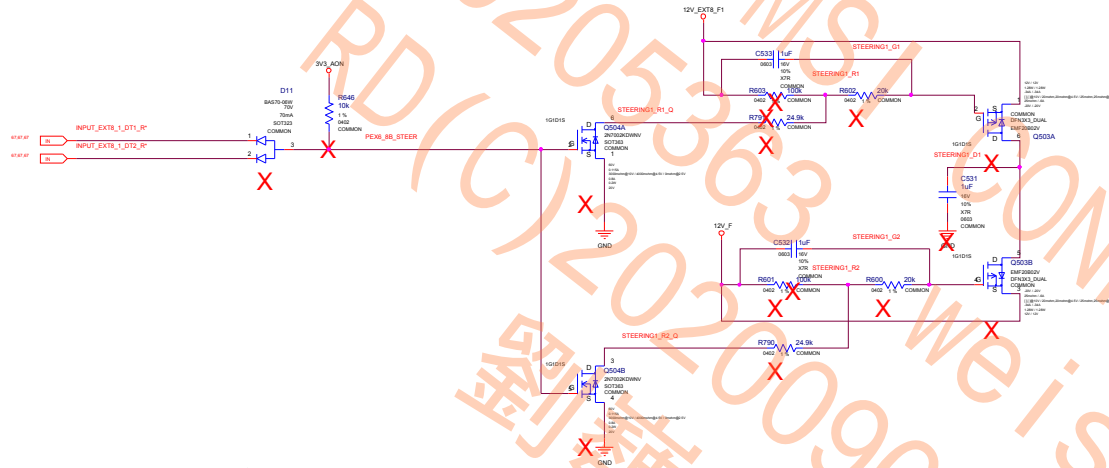


PS: 12V Current Steering & Hot Unplug Detect



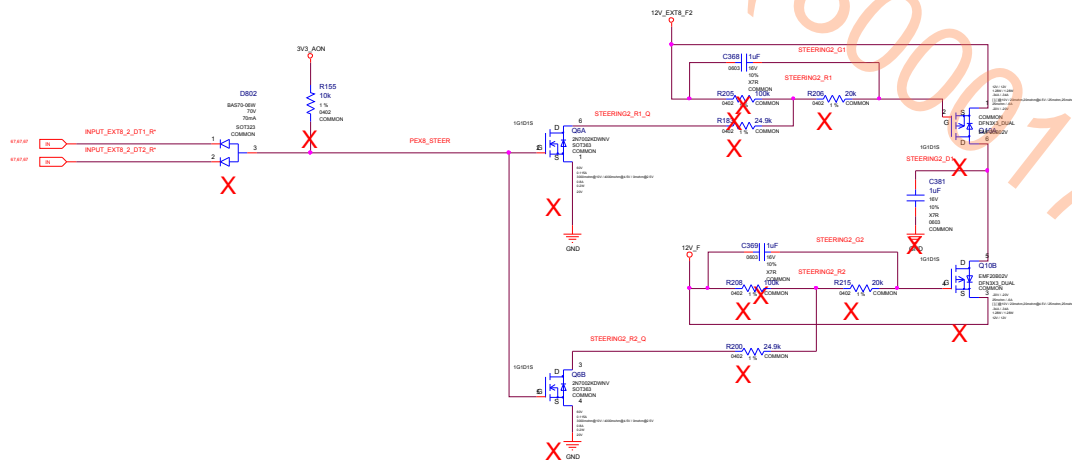
12V CURRENT STEERING (UNDER POWER BOOT):

PEX12V AND 12V\_EXT8\_F1



NO STUFF BY DEFAULT REF ONLY

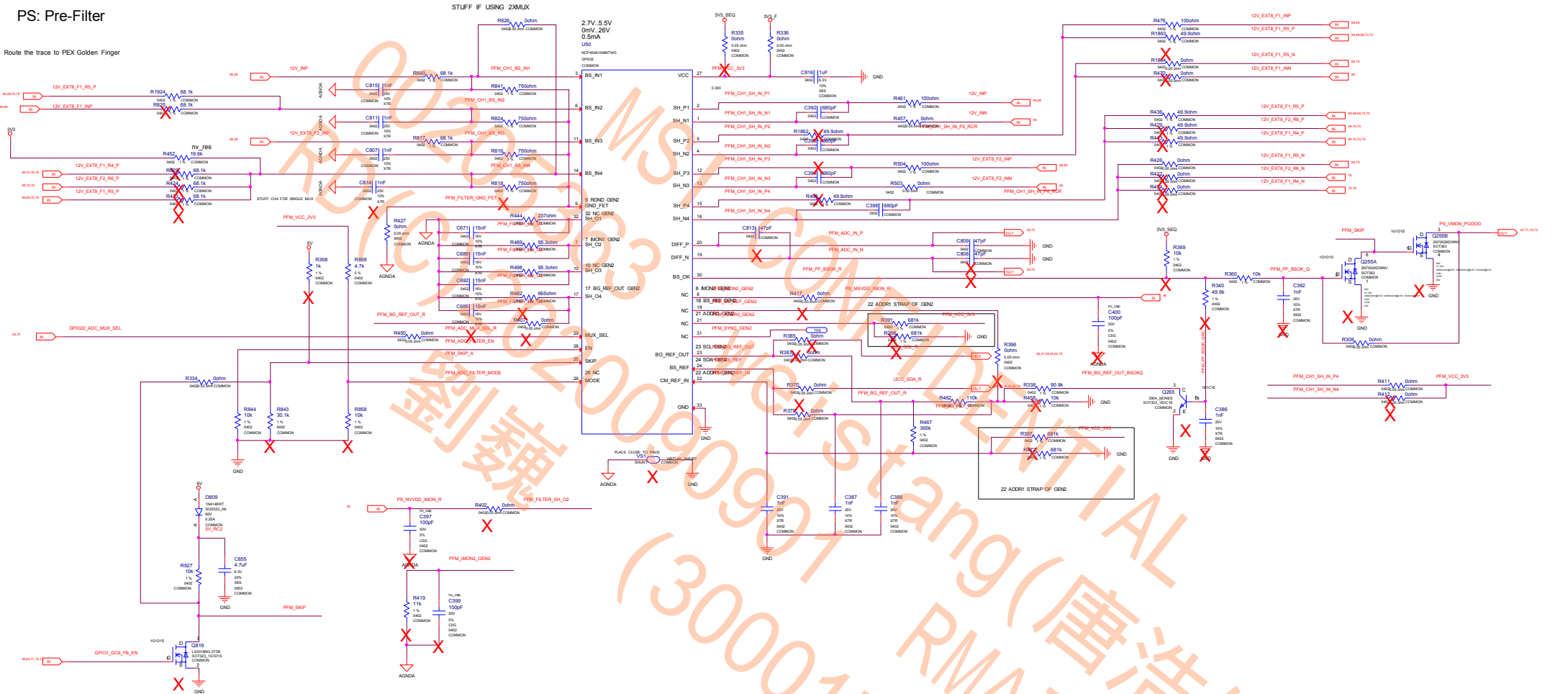
PEX12V AND 12V\_EXT8\_F2



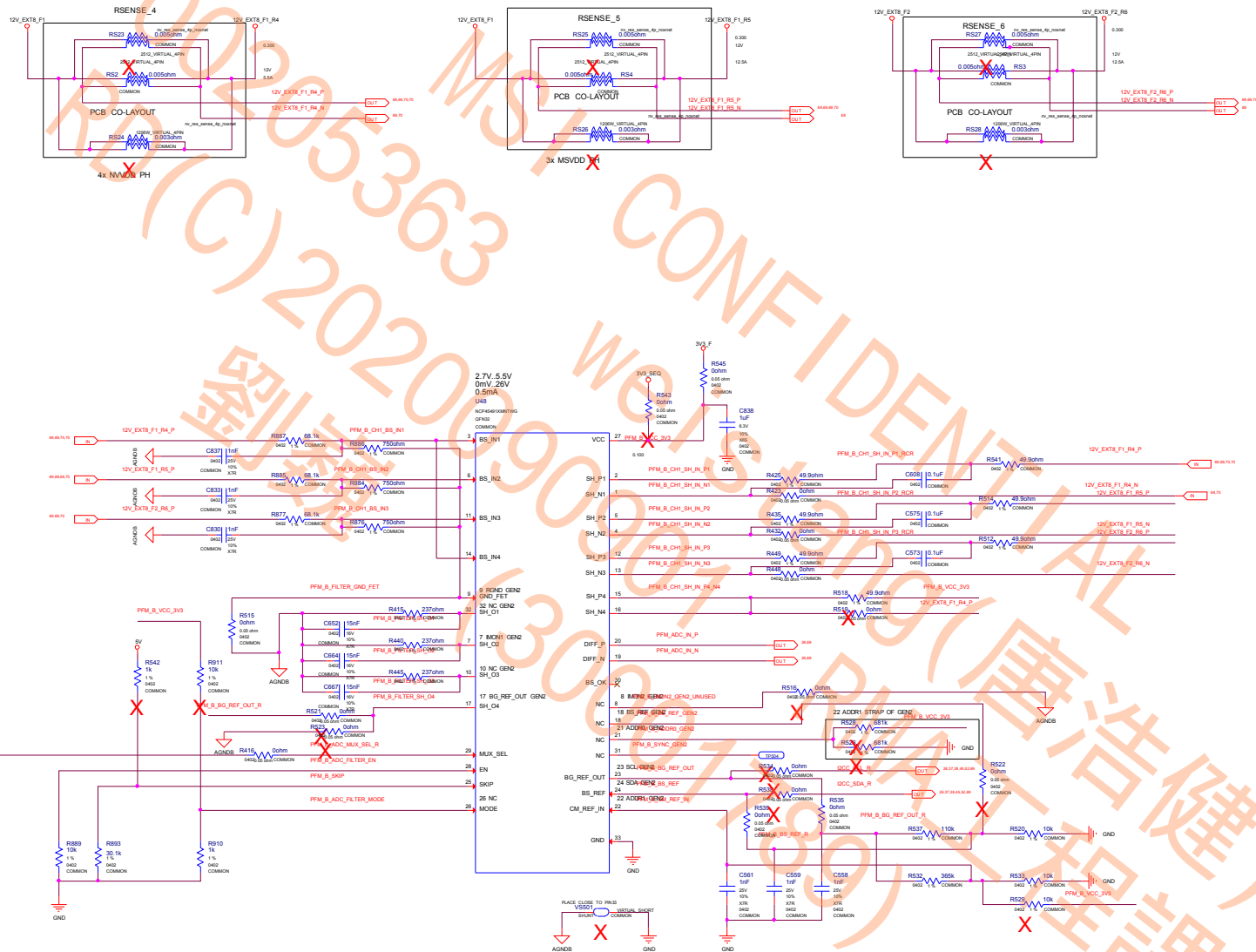


PS: Pre-Filter

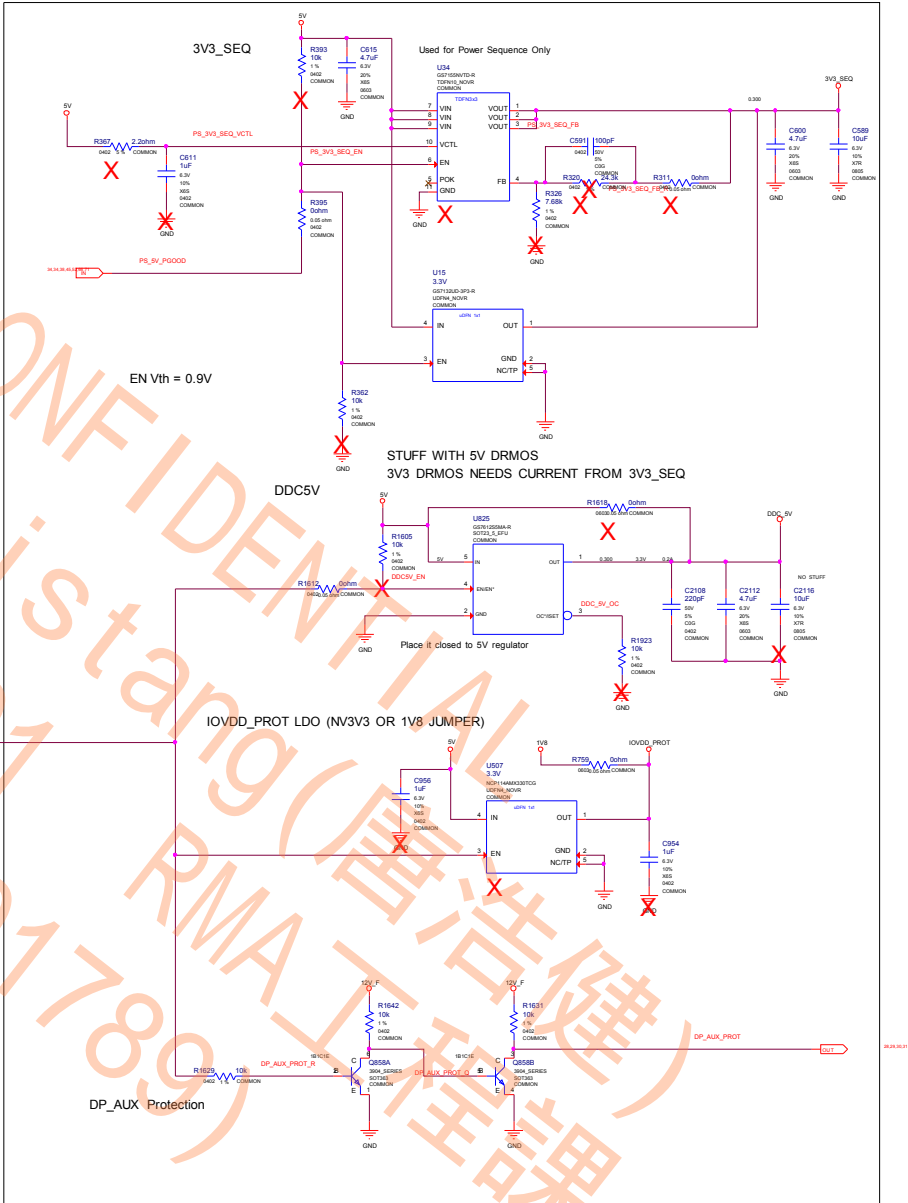
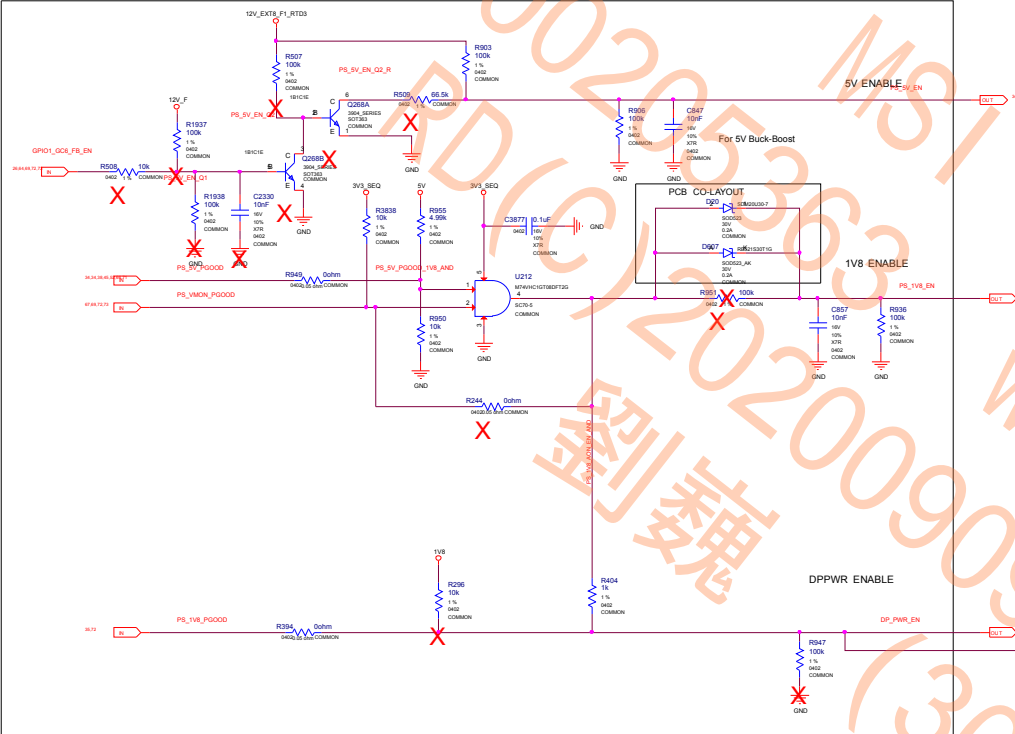
Route the trace to PEX Golden Finger



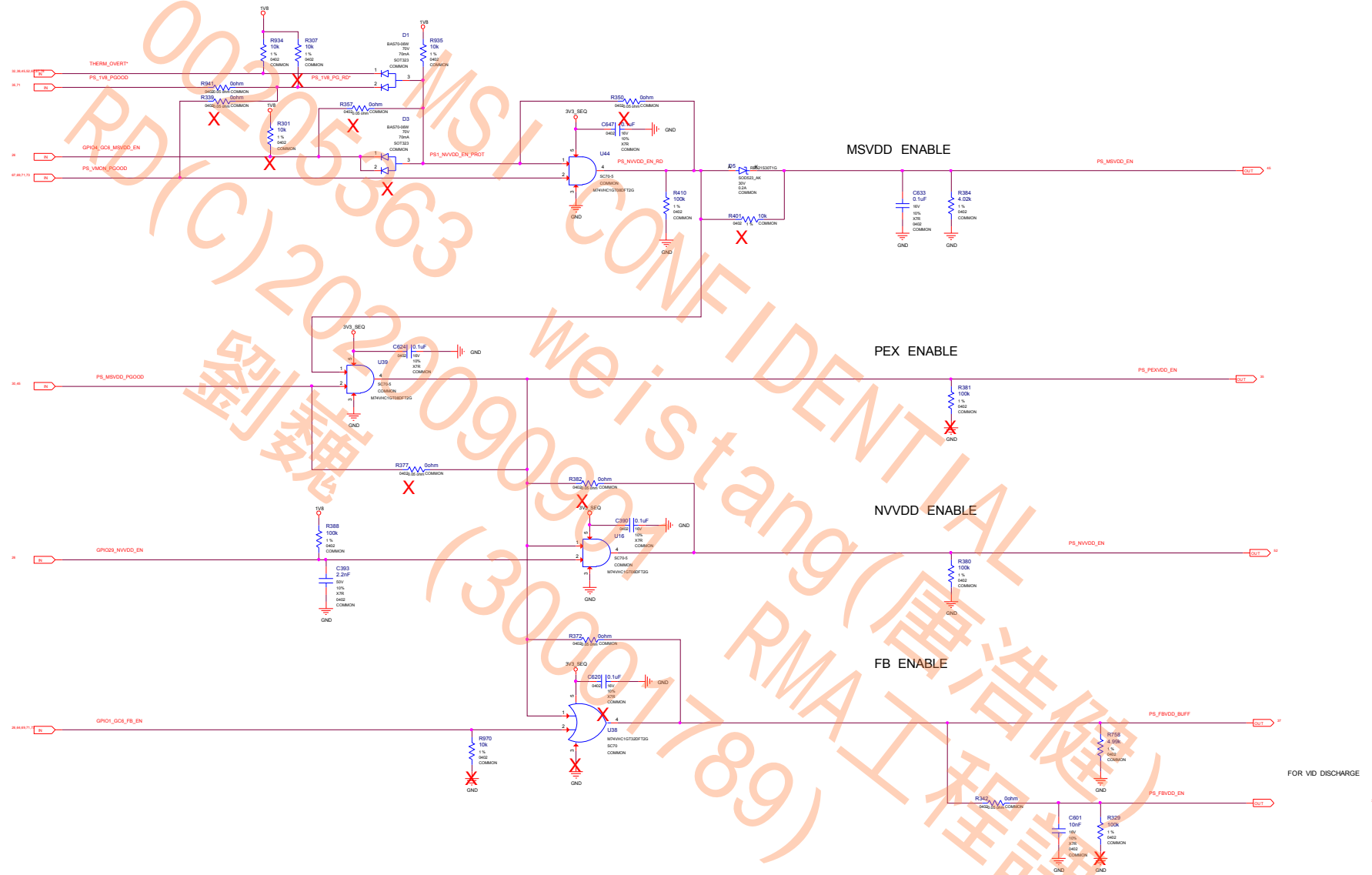
PS: Pre-Filter B



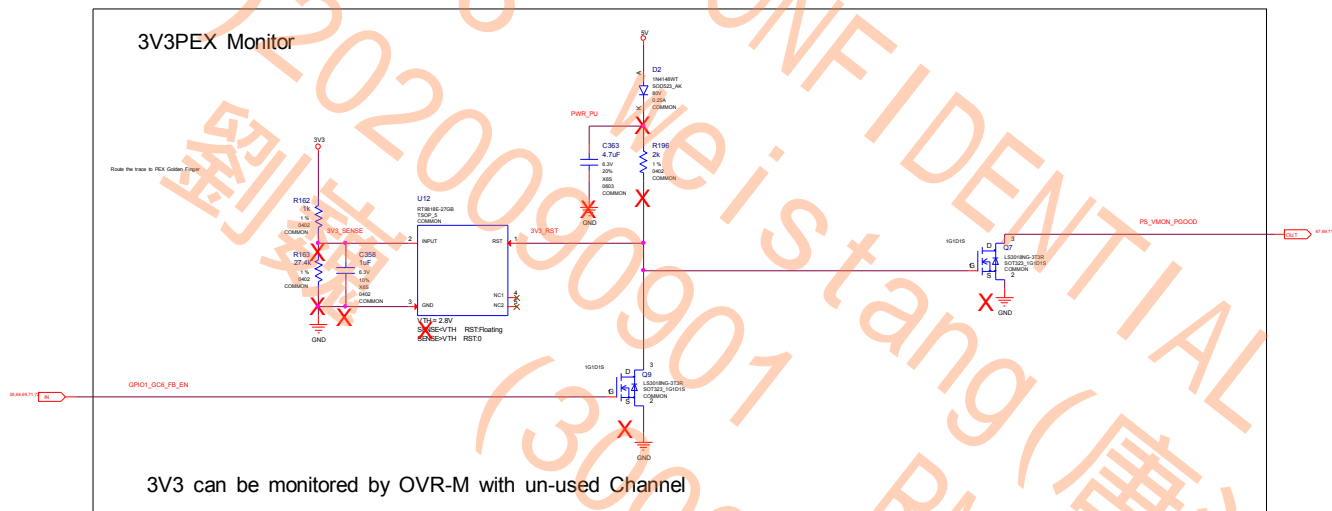
SEQUENCE:5V,1V8,3V3\_SEQ ENABLE



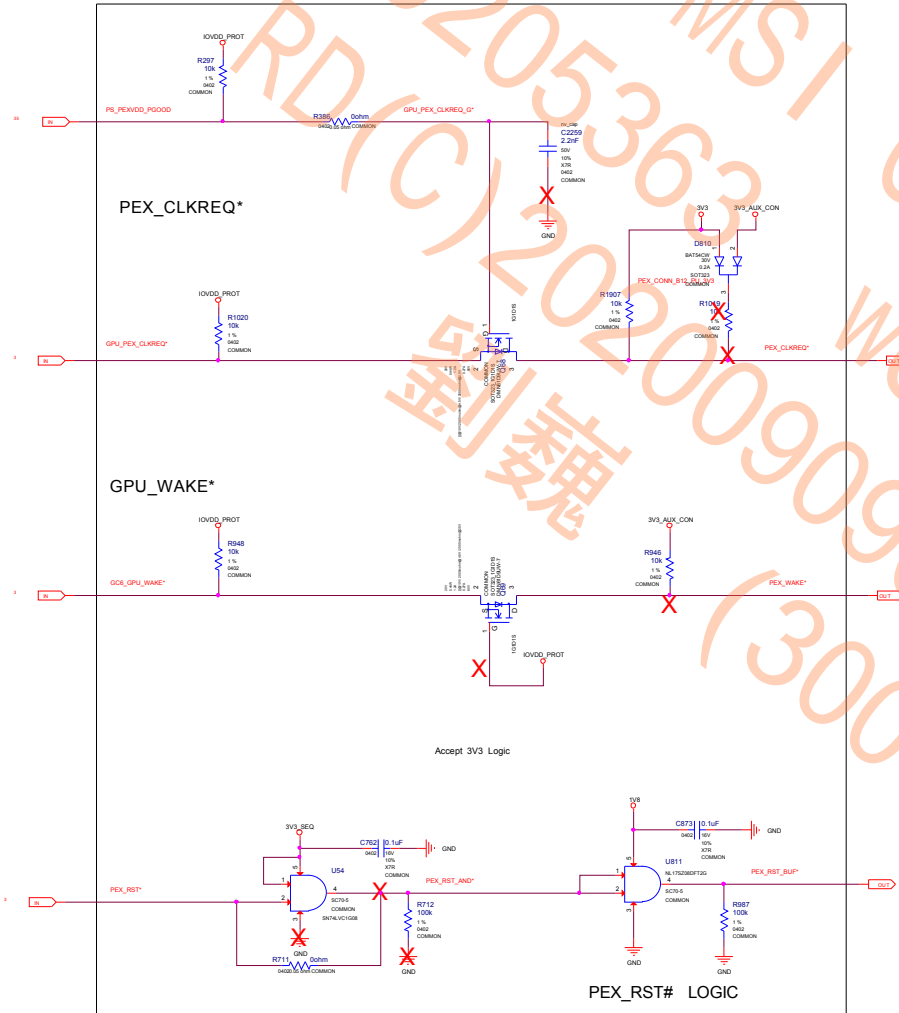
SEQUENCE:NV,PEX,FB ENABLE



Sequence: 3V3 MONITOR



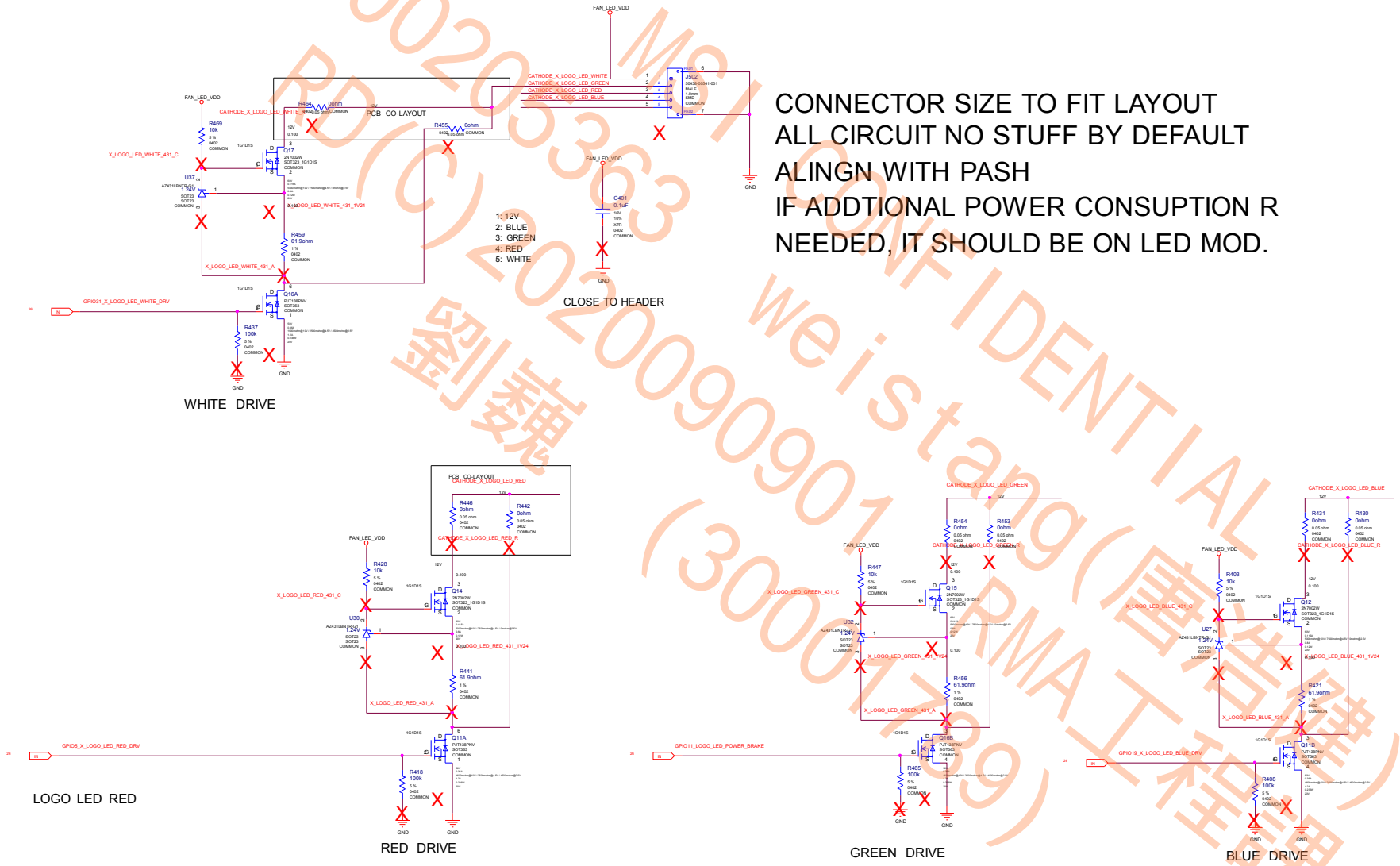
SEQUENCE:MISC



[illegible]

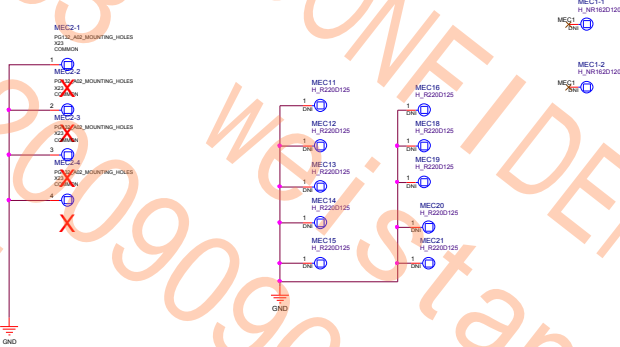
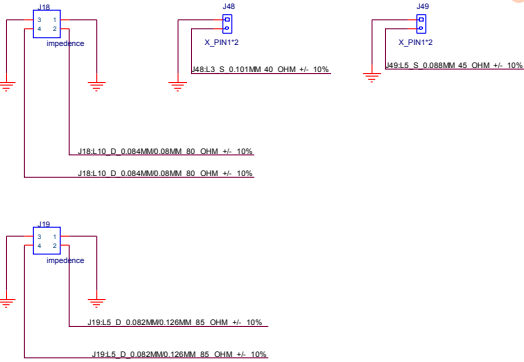
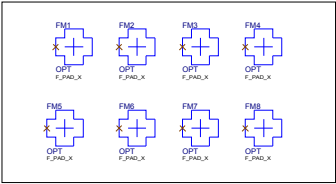
MISC: RGBW LED REF

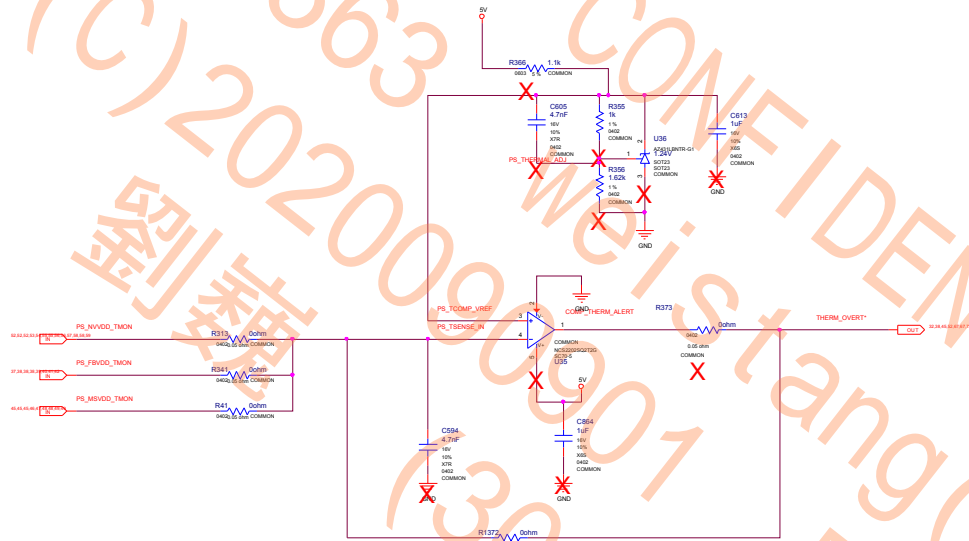
CONNECTOR SIZE TO FIT LAYOUT  
ALL CIRCUIT NO STUFF BY DEFAULT  
ALIGN WITH PASH  
IF ADDITIONAL POWER CONSUMPTION R  
NEEDED, IT SHOULD BE ON LED MOD.





Mechanical: Mounting holes



[illegible]

PCI TERM

