

A	B	C	D	E	F	G	H
---	---	---	---	---	---	---	---

1

1

23

4

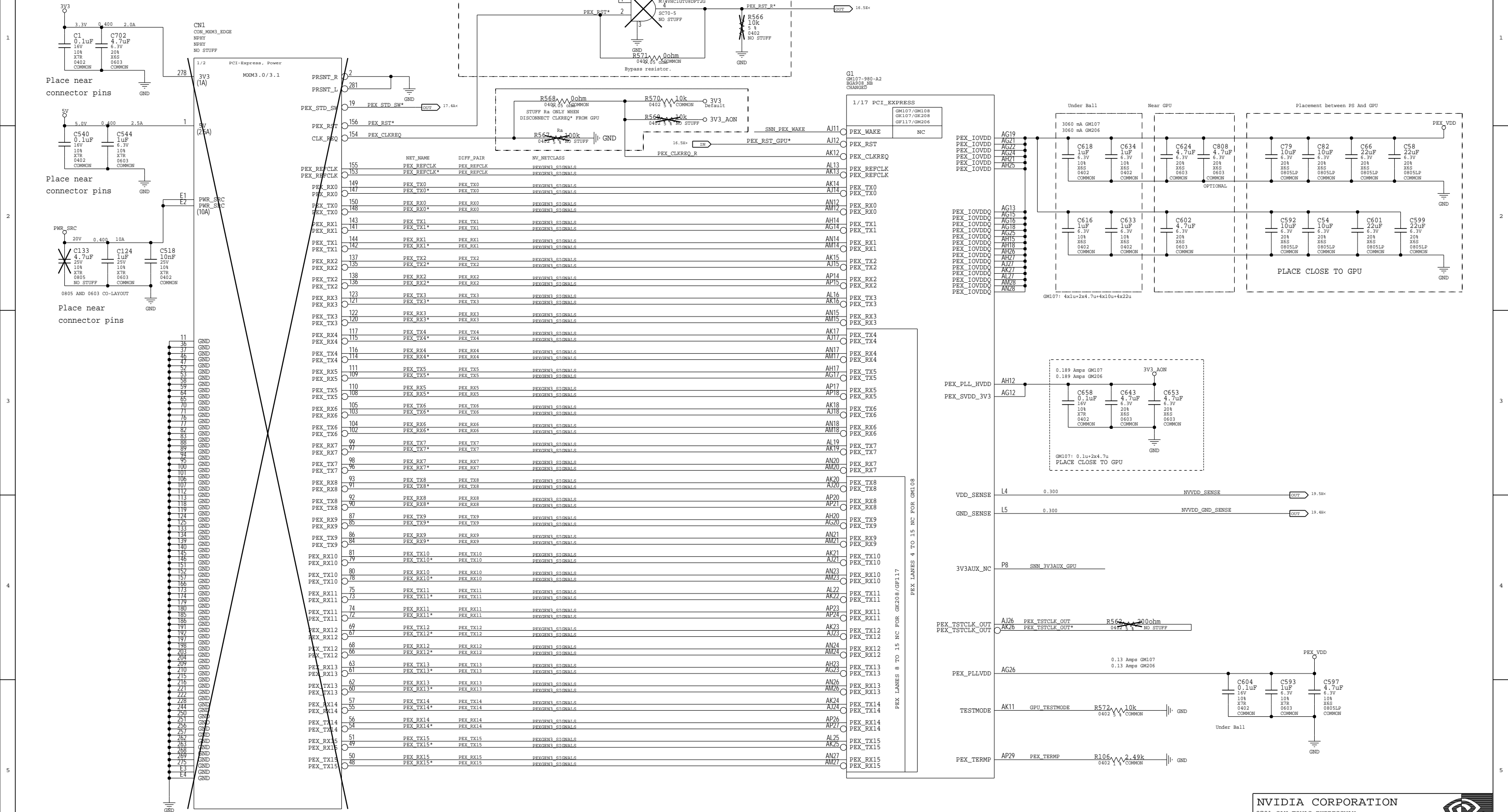


5

5

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PCI EXPRESS INTERFACE



NVIDIA CORPORATION

2701 SAN TOMAS EXPRESSWAY

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NV_PN	600-52757-0512-400
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PCB REV	P2757-B02
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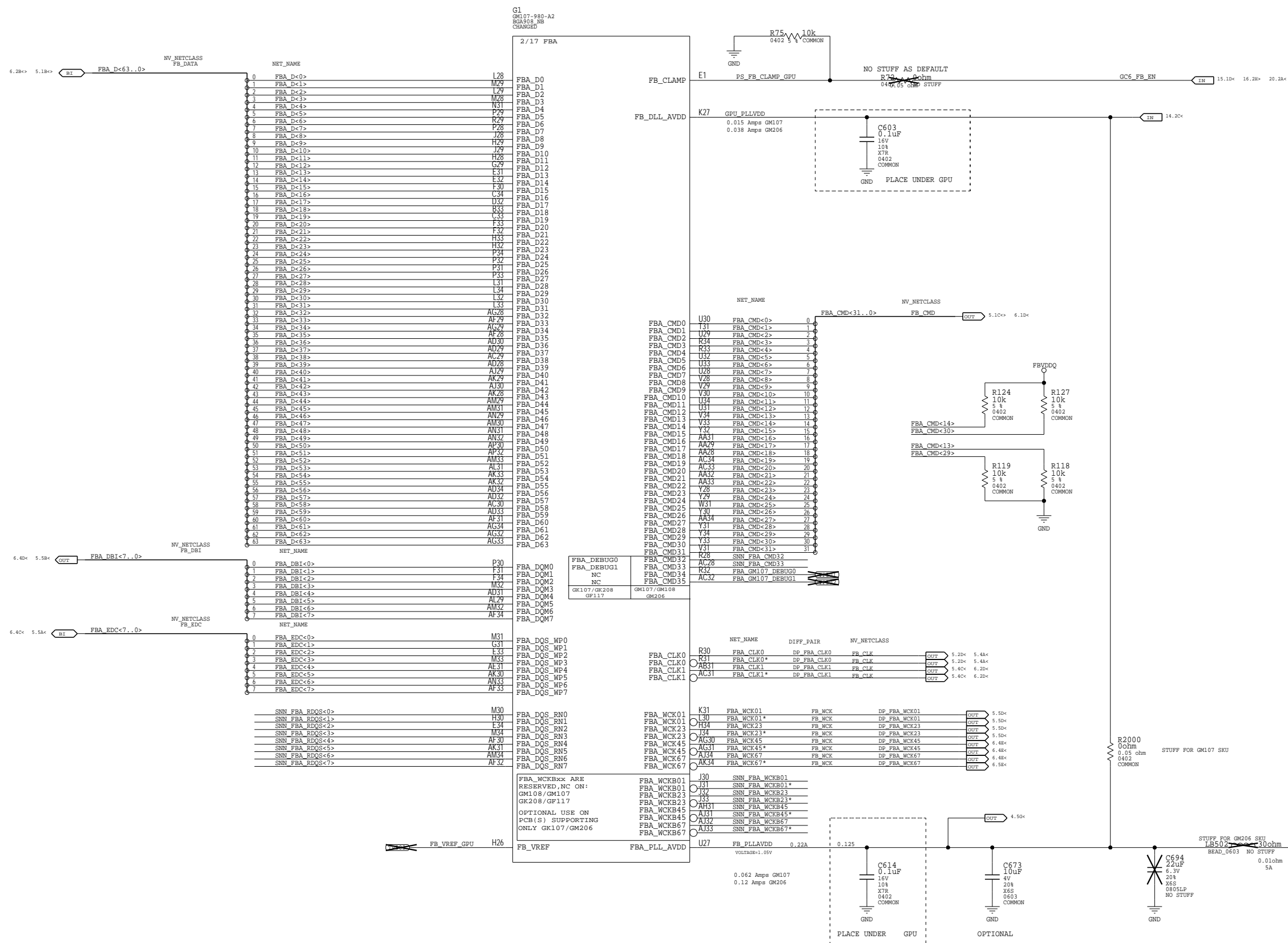
BOM REV	B
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PAGE	2 OF 20
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DATE	02-MAR-2016
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FRAME BUFFER A GPU INTERFACE




```

GDDR5_BGA170_MIRR COMMAND MAP
-----0x3132_61-----
CMD0 CS*
CMD1 A3_BA3
CMD2 A2_BA0
CMD3 A4_BA2
CMD4 A5_BA1
CMD5 WE*
CMD6 A7_A8
CMD7 A6_A11
CMD8 ABI*
CMD9 A12_RFU
CMD10 A0_A10
CMD11 A1_A9
CMD12 RAS*
CMD13 RST*
CMD14 CEK*
CMD15 CAS*
CMD16 CS*
CMD17 A3_BA3
CMD18 A2_BA0
CMD19 A4_BA2
CMD20 A5_BA1
CMD21 WE*
CMD22 A7_A8
CMD23 A6_A11
CMD24 ABI*
CMD25 A12_RFU
CMD26 A0_A10
CMD27 A1_A9
CMD28 RAS*
CMD29 RST*
CMD30 CEK*
CMD31 CAS*

```

ASSEMBLY	SKU512 GM107 x32 4GB GDDR5 MEMORY
PAGE DETAIL	FRAME BUFFER A GPU INTERFACE

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2701 SAN TOMAS EXPRESSWAY				
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NV_PN		600-52757-0512-400		
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BOM REV	B	DATE	02-MAR-2016	

FRAME BUFFER B GPU INTERFACE

G1
GM107-980-A2
BGA908_NB
CHANGED

GDDR5_BGA170_MIRR COMMAND MAP

0...31	32...63
CMD0	CS*
CMD1	A3_BA3
CMD2	A2_BA0
CMD3	A4_BA2
CMD4	A5_BA1
CMD5	WE*
CMD6	A7_A8
CMD7	A6_A11
CMD8	AB1*
CMD9	A12_RFU
CMD10	A0_A10
CMD11	A1_A9
CMD12	RAS*
CMD13	RST*
CMD14	CKE*
CMD15	CAS*
CMD16	
CMD17	CS*
CMD18	A1_BA3
CMD19	A2_BA0
CMD20	A4_BA2
CMD21	A5_BA1
CMD22	WE*
CMD23	A7_A8
CMD24	A6_A11
CMD25	AB1*
CMD26	A12_RFU
CMD27	A0_A10
CMD28	A1_A9
CMD29	RAS*
CMD30	RST*
CMD31	CKE*
	CAS*

8.28< 7.18< BI FBB_D<63...0>

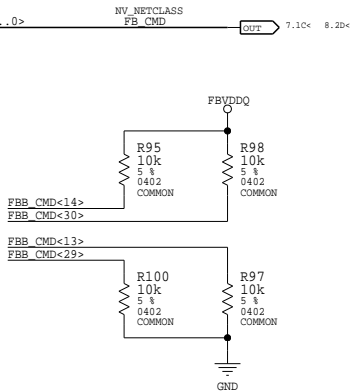
NV_NETCLASS
FB_DATA

NET_NAME

0	FBB_D<0>	G9
1	FBB_D<1>	F8
2	FBB_D<2>	C8
3	FBB_D<3>	F9
4	FBB_D<4>	F11
5	FBB_D<5>	G11
6	FBB_D<6>	F12
7	FBB_D<7>	G12
8	FBB_D<8>	G6
9	FBB_D<9>	F5
10	FBB_D<10>	E6
11	FBB_D<11>	F6
12	FBB_D<12>	F4
13	FBB_D<13>	G4
14	FBB_D<14>	E7
15	FBB_D<15>	C7
16	FBB_D<16>	C2
17	FBB_D<17>	D4
18	FBB_D<18>	D3
19	FBB_D<19>	C1
20	FBB_D<20>	B3
21	FBB_D<21>	C4
22	FBB_D<22>	B5
23	FBB_D<23>	C5
24	FBB_D<24>	A11
25	FBB_D<25>	C11
26	FBB_D<26>	D11
27	FBB_D<27>	B11
28	FBB_D<28>	D8
29	FBB_D<29>	A8
30	FBB_D<30>	C8
31	FBB_D<31>	B8
32	FBB_D<32>	F24
33	FBB_D<33>	G24
34	FBB_D<34>	E24
35	FBB_D<35>	G24
36	FBB_D<36>	D21
37	FBB_D<37>	E21
38	FBB_D<38>	G21
39	FBB_D<39>	F21
40	FBB_D<40>	G27
41	FBB_D<41>	D27
42	FBB_D<42>	G26
43	FBB_D<43>	E27
44	FBB_D<44>	E29
45	FBB_D<45>	F29
46	FBB_D<46>	E30
47	FBB_D<47>	D30
48	FBB_D<48>	A32
49	FBB_D<49>	C31
50	FBB_D<50>	C32
51	FBB_D<51>	B32
52	FBB_D<52>	D29
53	FBB_D<53>	A29
54	FBB_D<54>	C29
55	FBB_D<55>	B29
56	FBB_D<56>	B21
57	FBB_D<57>	C23
58	FBB_D<58>	A21
59	FBB_D<59>	C21
60	FBB_D<60>	B24
61	FBB_D<61>	C24
62	FBB_D<62>	B26
63	FBB_D<63>	C26

3/17 FBB
ALL PINS NC FOR GM108/
GK208/GP117

FBB_CMD0	D13	FBB_CMD<0>	0
FBB_CMD1	E14	FBB_CMD<1>	1
FBB_CMD2	F14	FBB_CMD<2>	2
FBB_CMD3	A12	FBB_CMD<3>	3
FBB_CMD4	B12	FBB_CMD<4>	4
FBB_CMD5	C14	FBB_CMD<5>	5
FBB_CMD6	B14	FBB_CMD<6>	6
FBB_CMD7	G15	FBB_CMD<7>	7
FBB_CMD8	F15	FBB_CMD<8>	8
FBB_CMD9	F15	FBB_CMD<9>	9
FBB_CMD10	D15	FBB_CMD<10>	10
FBB_CMD11	A14	FBB_CMD<11>	11
FBB_CMD12	D14	FBB_CMD<12>	12
FBB_CMD13	B15	FBB_CMD<13>	13
FBB_CMD14	C17	FBB_CMD<14>	14
FBB_CMD15	F18	FBB_CMD<15>	15
FBB_CMD16	D18	FBB_CMD<16>	16
FBB_CMD17	F18	FBB_CMD<17>	17
FBB_CMD18	A20	FBB_CMD<18>	18
FBB_CMD19	B20	FBB_CMD<19>	19
FBB_CMD20	C18	FBB_CMD<20>	20
FBB_CMD21	B18	FBB_CMD<21>	21
FBB_CMD22	G18	FBB_CMD<22>	22
FBB_CMD23	G17	FBB_CMD<23>	23
FBB_CMD24	F17	FBB_CMD<24>	24
FBB_CMD25	D16	FBB_CMD<25>	25
FBB_CMD26	A18	FBB_CMD<26>	26
FBB_CMD27	D17	FBB_CMD<27>	27
FBB_CMD28	A17	FBB_CMD<28>	28
FBB_CMD29	B17	FBB_CMD<29>	29
FBB_CMD30	E17	FBB_CMD<30>	30
FBB_CMD31	G14	FBB_CMD<31>	31
FBB_CMD32	G20	SNN FBB_CMD32	
FBB_CMD33	C12	SNN FBB_CMD33	
FBB_CMD34	C20	FBB_GM107_DEBUG0	
FBB_CMD35	C20	FBB_GM107_DEBUG1	



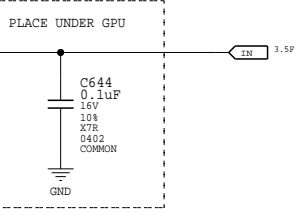
FBB_DEBUG0	GM107	GM107/GM206
FBB_DEBUG1	NC	
NC		
NC		
GM107		GM107/GM206

NET_NAME	DIFF_PAIR	NV_NETCLASS
D12	FBB_CLK0	DP_FBB_CLK0
E12	FBB_CLK0*	DP_FBB_CLK0
E20	FBB_CLK1	DP_FBB_CLK1
F20	FBB_CLK1*	DP_FBB_CLK1

F8	FBB_WCK01	FB_WCK	DP_FBB_WCK01	OUT	7.50<	7.44<
A5	FBB_WCK01*	FB_WCK	DP_FBB_WCK01	OUT	7.50<	7.44<
A5	FBB_WCK23	FB_WCK	DP_FBB_WCK23	OUT	7.50<	7.44<
A6	FBB_WCK23*	FB_WCK	DP_FBB_WCK23	OUT	7.50<	7.44<
D24	FBB_WCK45	FB_WCK	DP_FBB_WCK45	OUT	8.58<	8.58<
D25	FBB_WCK45*	FB_WCK	DP_FBB_WCK45	OUT	8.58<	8.58<
B27	FBB_WCK67	FB_WCK	DP_FBB_WCK67	OUT	8.58<	8.58<
C27	FBB_WCK67*	FB_WCK	DP_FBB_WCK67	OUT	8.58<	8.58<

FBB_WCKBxx ARE
RESERVED, NC ON:
GM108/GM107
OPTIONAL USE ON
PCB(S) SUPPORTING
ONLY GK107/GM206

FBB_PLL_AVDD
0.062 Amps GM107
0.12 Amps GM206



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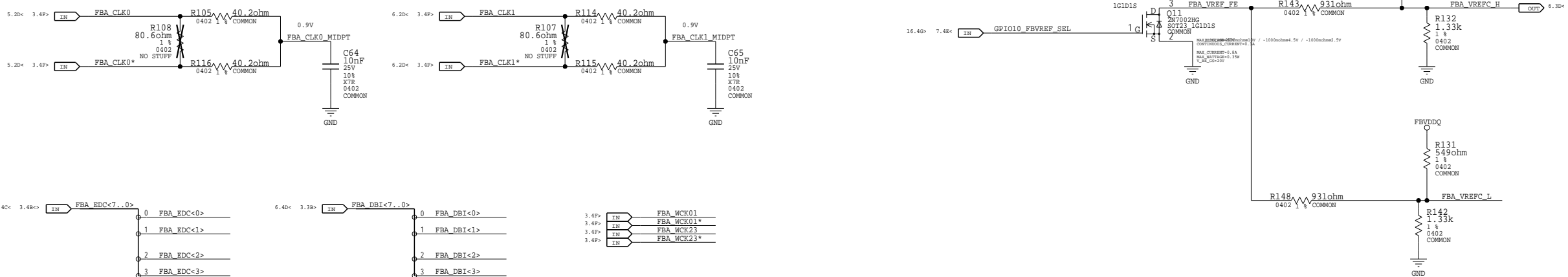
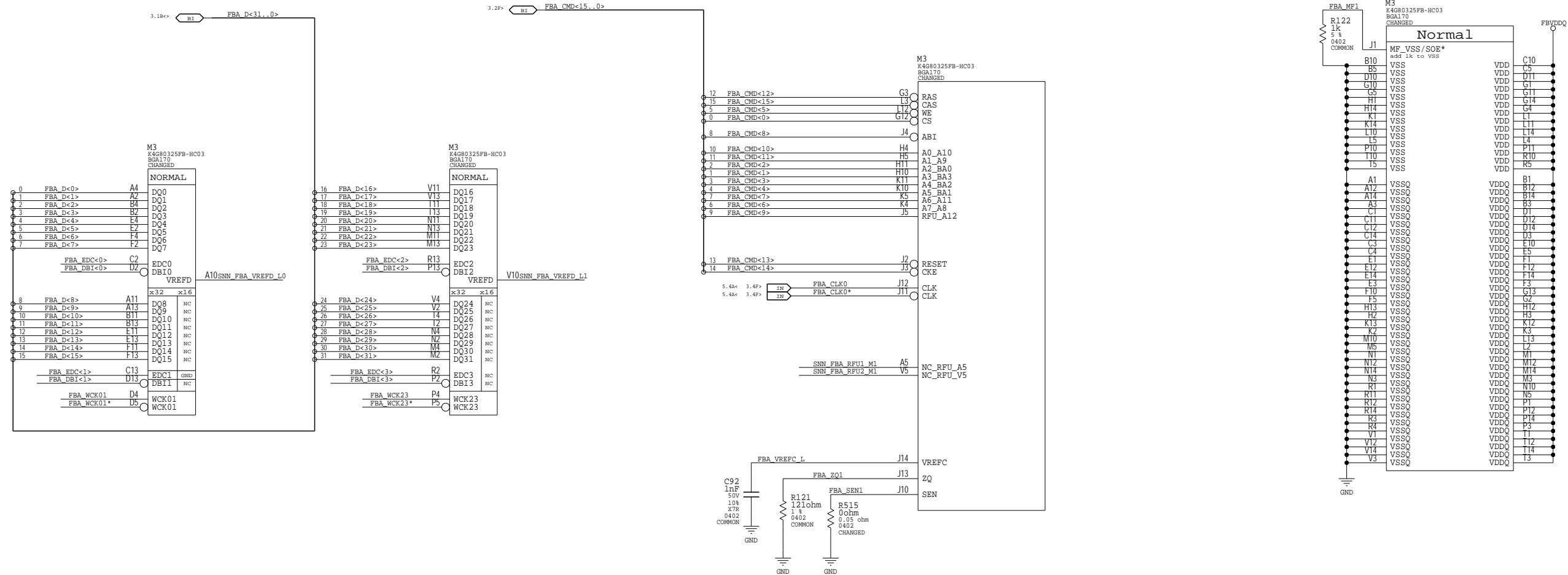
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FRAME BUFFER PARTITION A [31..0]



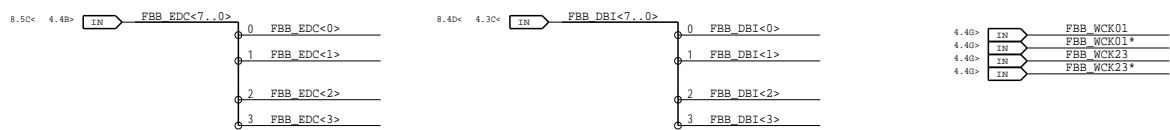
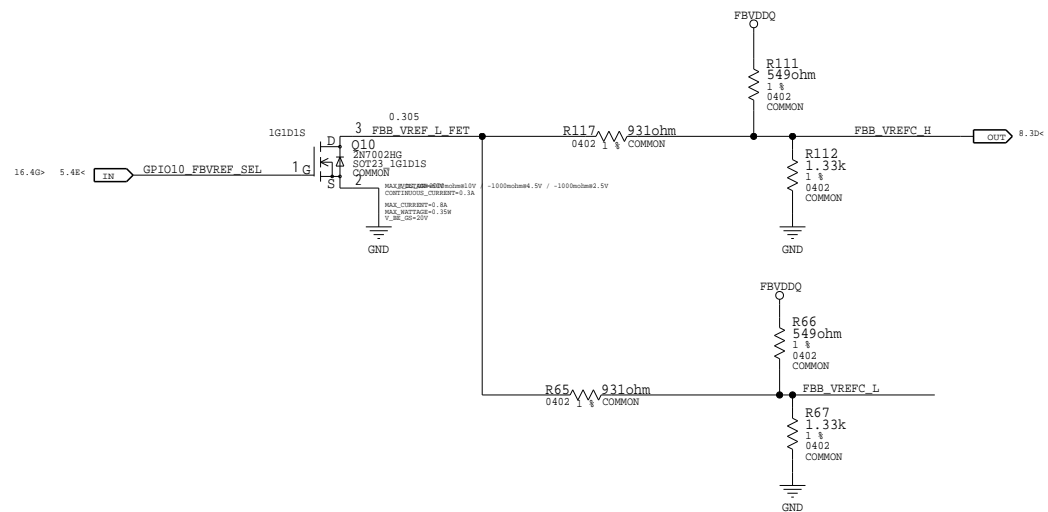
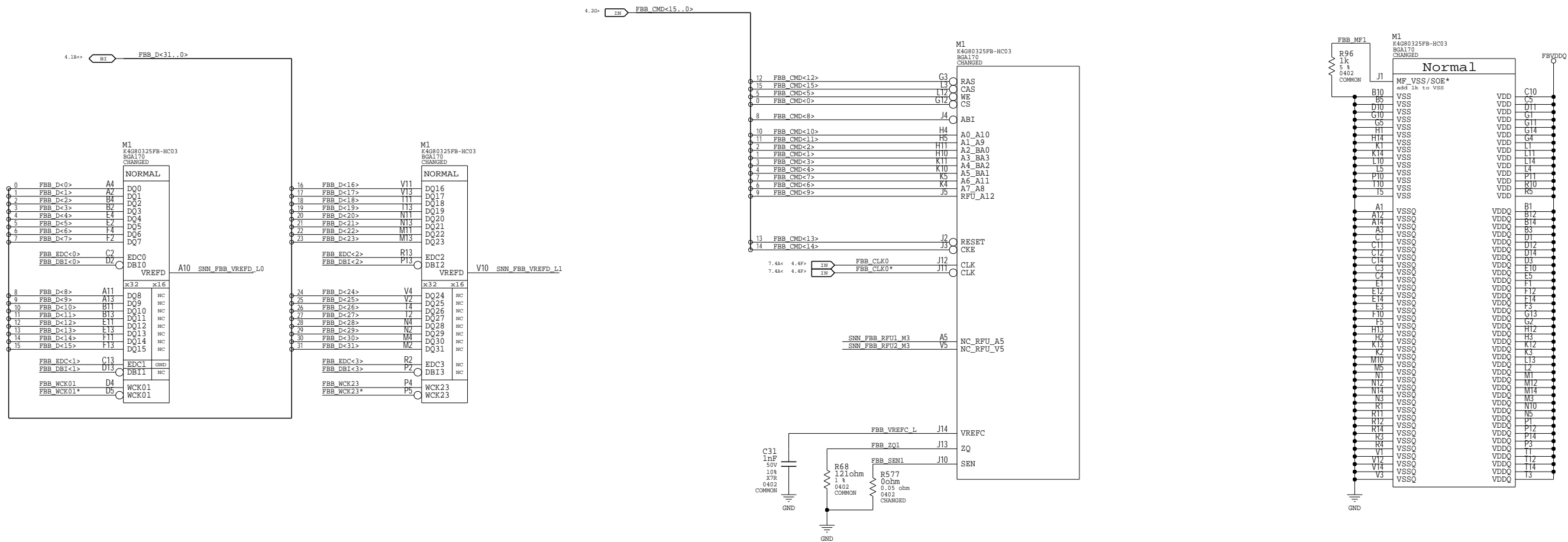
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FRAME BUFFER PARTITION B [31..0]
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ASSEMBLY	SKU512 GM107 x32 4GB GDDR5 MEMORY
PAGE DETAIL	FRAME BUFFER PARTITION B [31.0]

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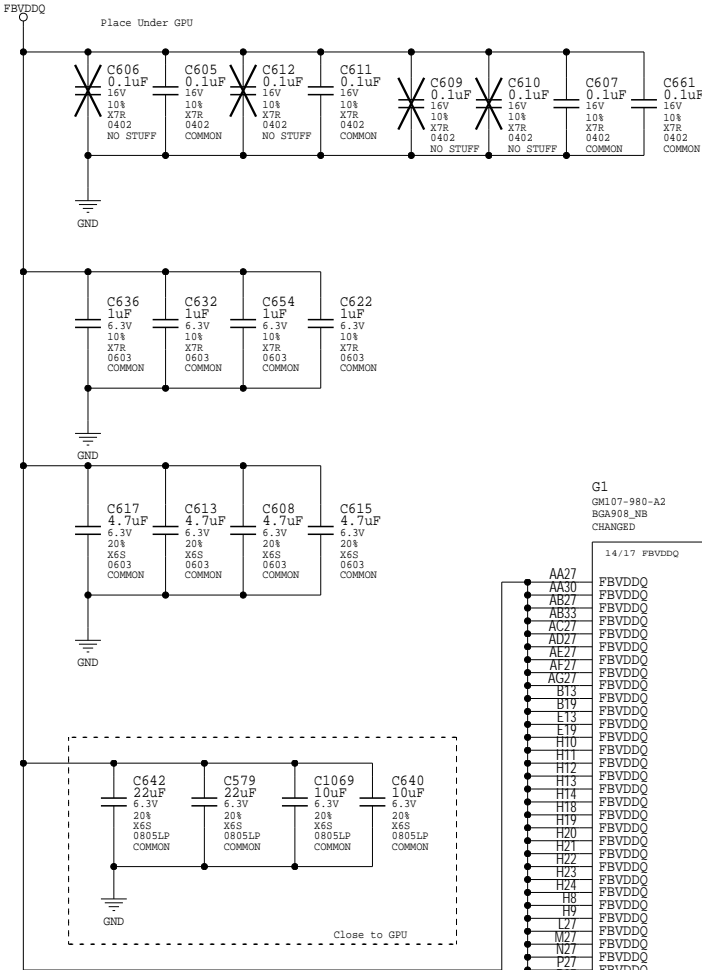
1

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MEMORY DECOUPLING CAPS

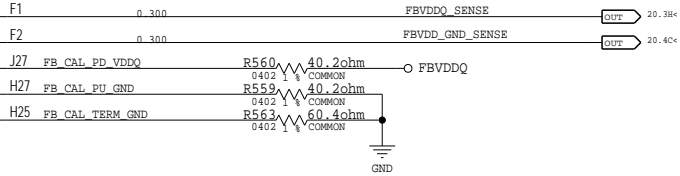
0402 x 0.1uF x4(4pcs Option)
0603 x 1uF x4
0603 x 4.7uF x4
0805 X 10uF x2
0805 X 22uF x2



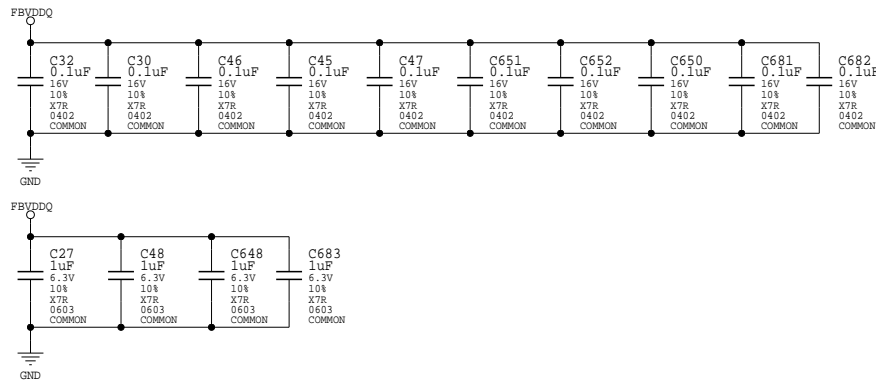
G1
GM107-980-A2
BGAP98_NB
CHANGED

- 14/17 FBVDDQ
- AA27 FBVDDQ
 - AA30 FBVDDQ
 - AB27 FBVDDQ
 - AB33 FBVDDQ
 - AC27 FBVDDQ
 - AD27 FBVDDQ
 - AE27 FBVDDQ
 - AF27 FBVDDQ
 - AG27 FBVDDQ
 - B13 FBVDDQ
 - B19 FBVDDQ
 - E13 FBVDDQ
 - E19 FBVDDQ
 - H10 FBVDDQ
 - H11 FBVDDQ
 - H12 FBVDDQ
 - H13 FBVDDQ
 - H14 FBVDDQ
 - H18 FBVDDQ
 - H19 FBVDDQ
 - H20 FBVDDQ
 - H21 FBVDDQ
 - H22 FBVDDQ
 - H23 FBVDDQ
 - H24 FBVDDQ
 - H8 FBVDDQ
 - L7 FBVDDQ
 - M27 FBVDDQ
 - N27 FBVDDQ
 - P27 FBVDDQ
 - K27 FBVDDQ
 - L27 FBVDDQ
 - L30 FBVDDQ
 - L33 FBVDDQ
 - Y27 FBVDDQ
 - W33 FBVDDQ

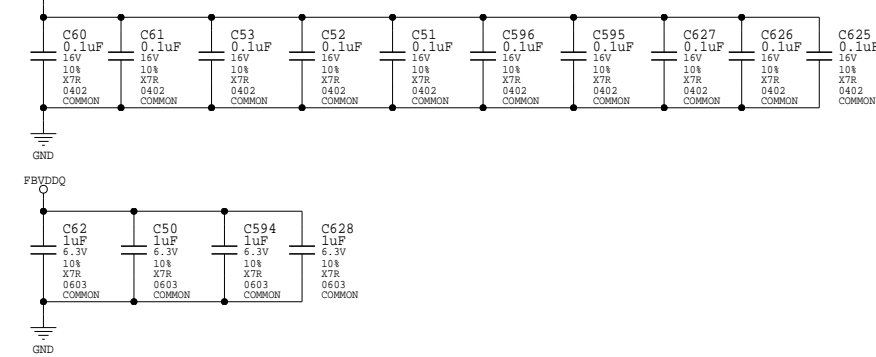
B16	FBVDDQ_AON	FBVDDQ
E16	FBVDDQ_AON	FBVDDQ
H15	FBVDDQ_AON	FBVDDQ
H16	FBVDDQ_AON	FBVDDQ
V27	FBVDDQ_AON	FBVDDQ
W27	FBVDDQ_AON	FBVDDQ
W30	FBVDDQ_AON	FBVDDQ
W33	FBVDDQ_AON	FBVDDQ
GM107/GM108		GM107/ GM206/ GM117/ GM206
FB_VDDQ_SENSE		FBVDDQ
FB_GND_SENSE		FBVDDQ
FB_CAL_PD_VDDQ		FBVDDQ
FB_CAL_PU_GND		FBVDDQ
FB_CALTERM_GND		FBVDDQ



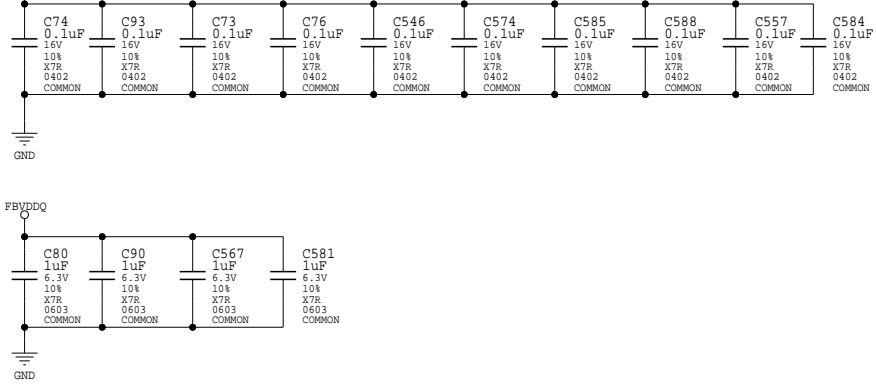
Place Near Partition B Memory



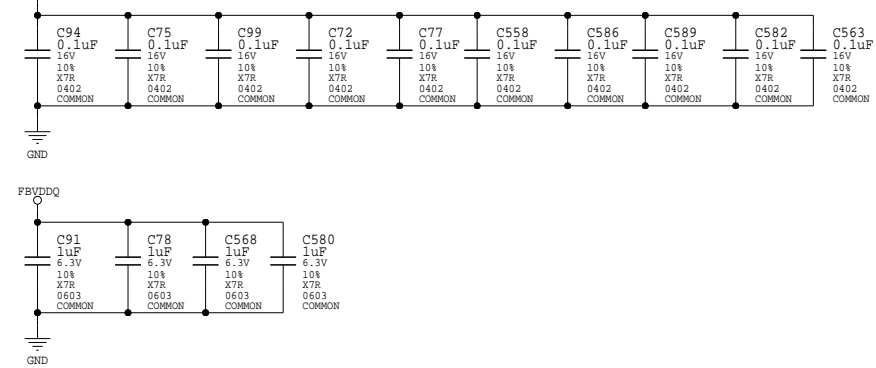
Place Near Partition B Memory



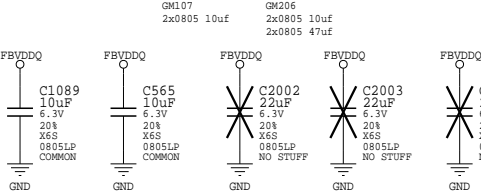
Place Near Partition A Memory



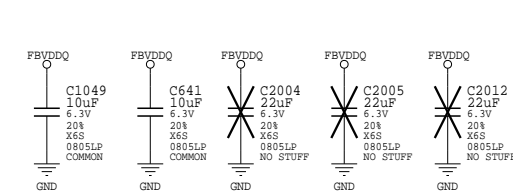
Place Near Partition A Memory



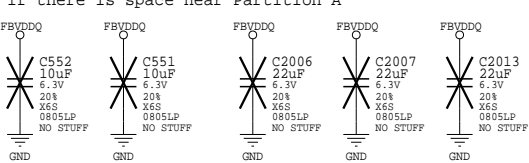
Place Near Partition A Memory



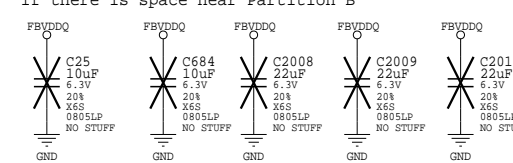
Place Near Partition B Memory



Place as option
If there is space near Partition A



Place as option
If there is space near Partition B



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BOM REV B

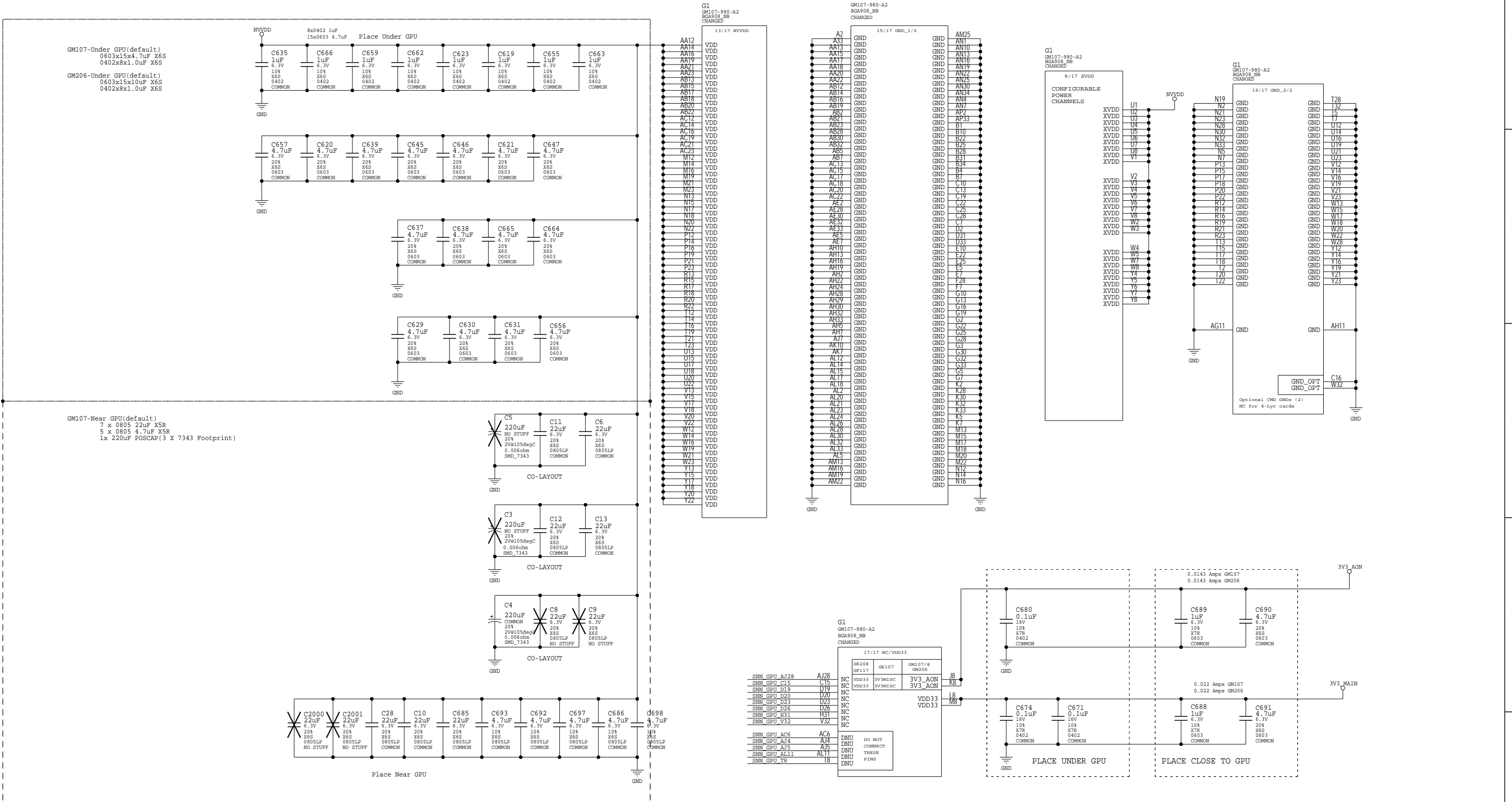
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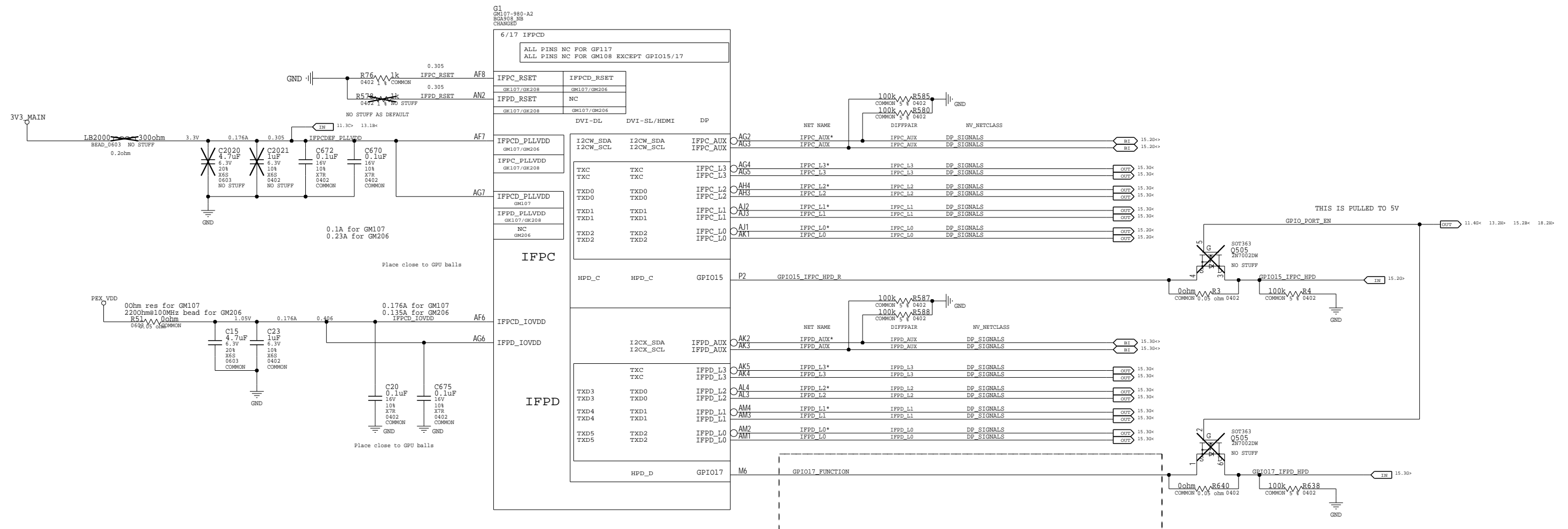
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ASSEMBLY	SKU512 GM107 x32 4GB GDDR5 MEMORY
PAGE DETAIL	MEMORY DECOUPLING CAPS

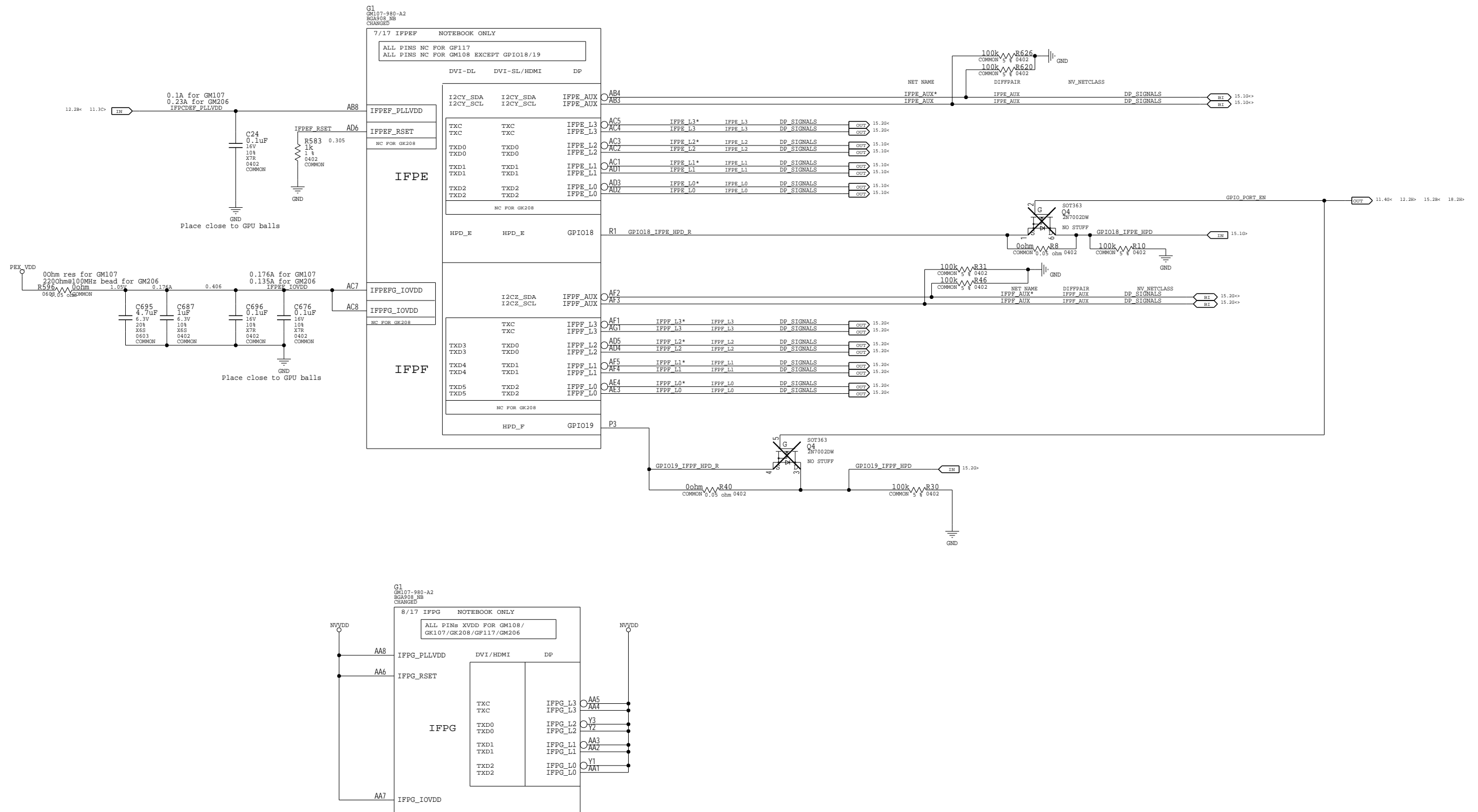
GPU VDD\DCPLNG\GND



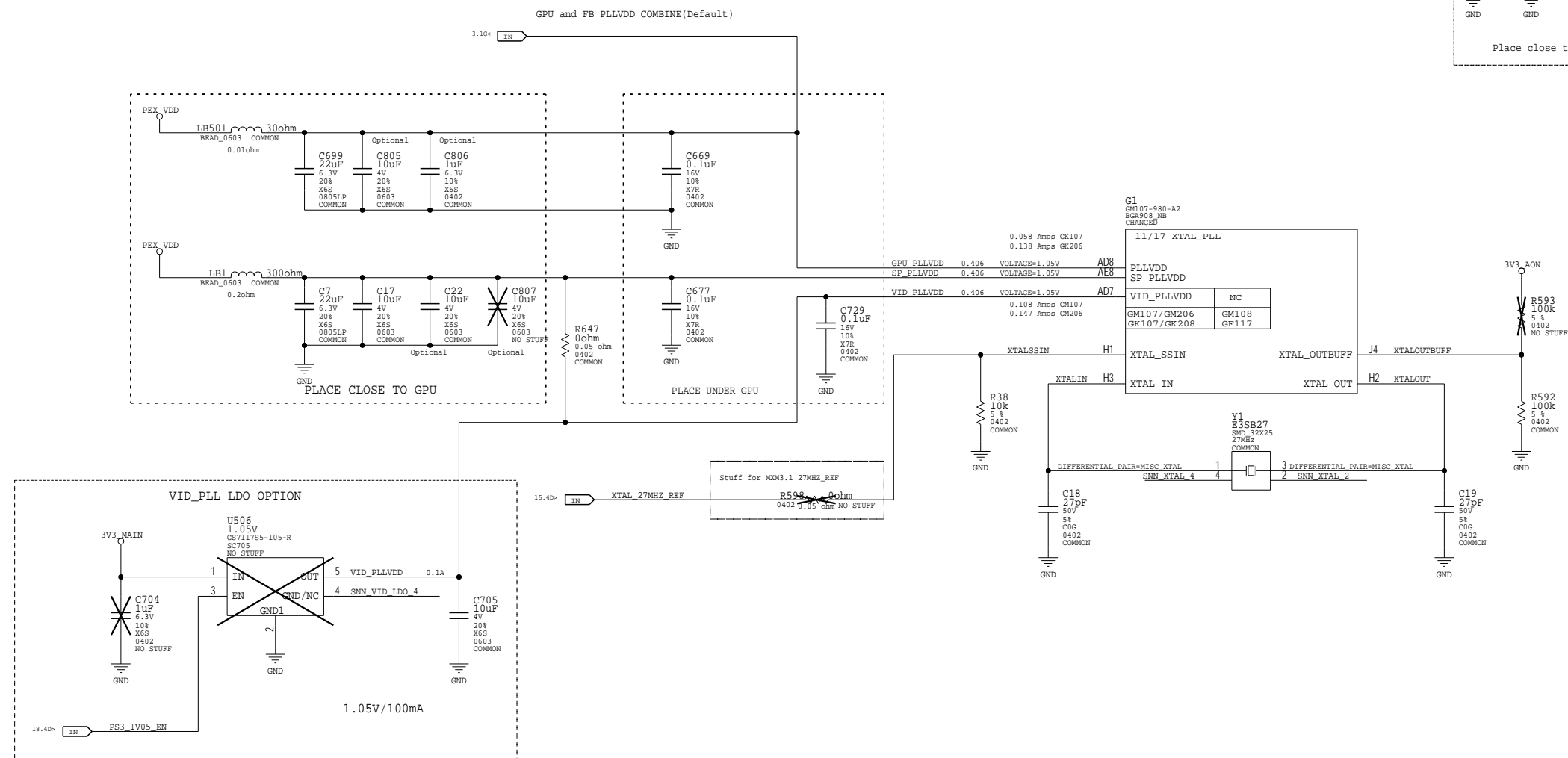
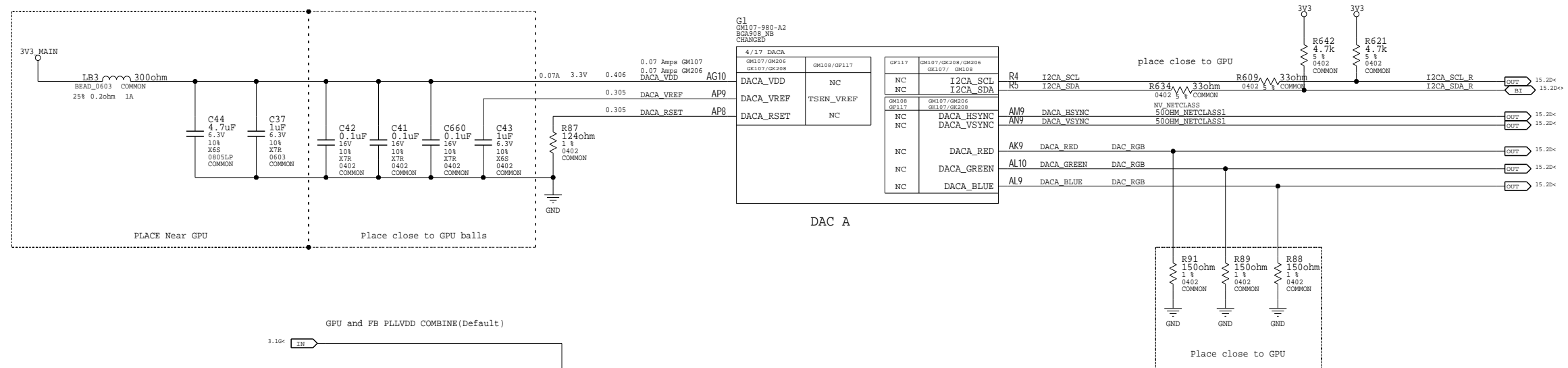
DP LINKS C,D



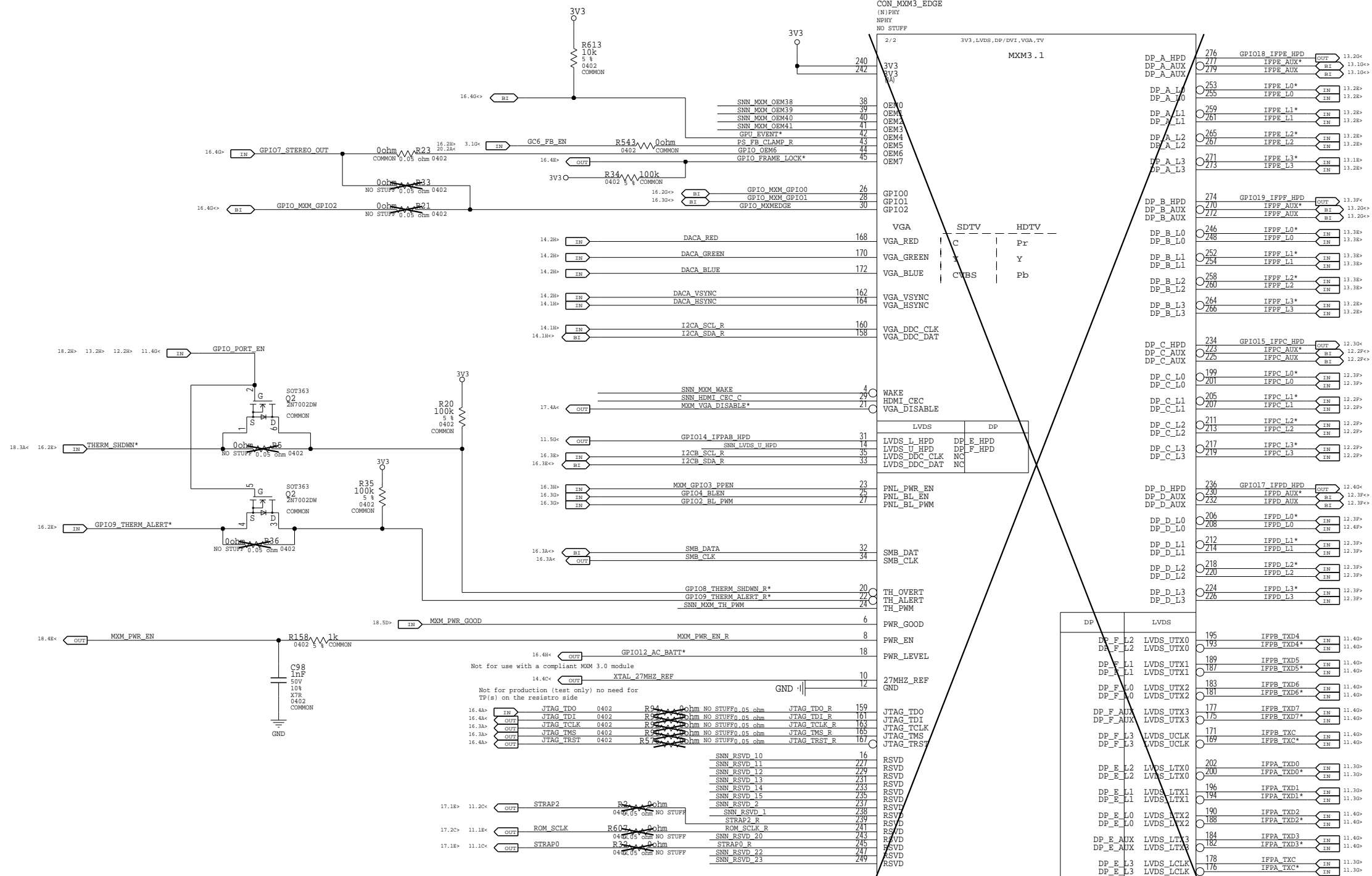
DP LINKS E, F, G



DACA PLL, CRYSTAL



MXM CONNECTOR, IO-SECTION



ASSEMBLY	SKU512 GM107 x32 4GB GDDR5 MEMORY
PAGE DETAIL	MXM CONNECTOR, IO-SECTION

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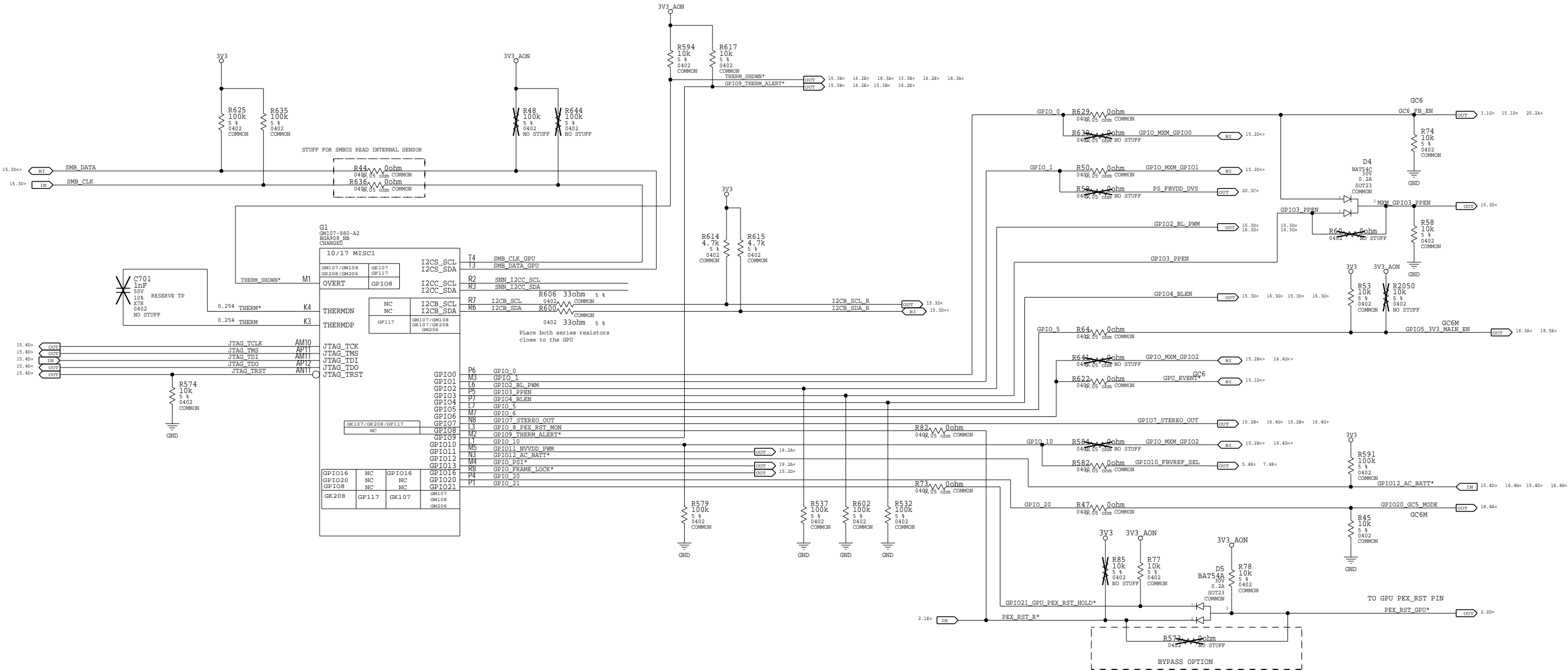
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GPIO, JTAG, TEMP SENSOR



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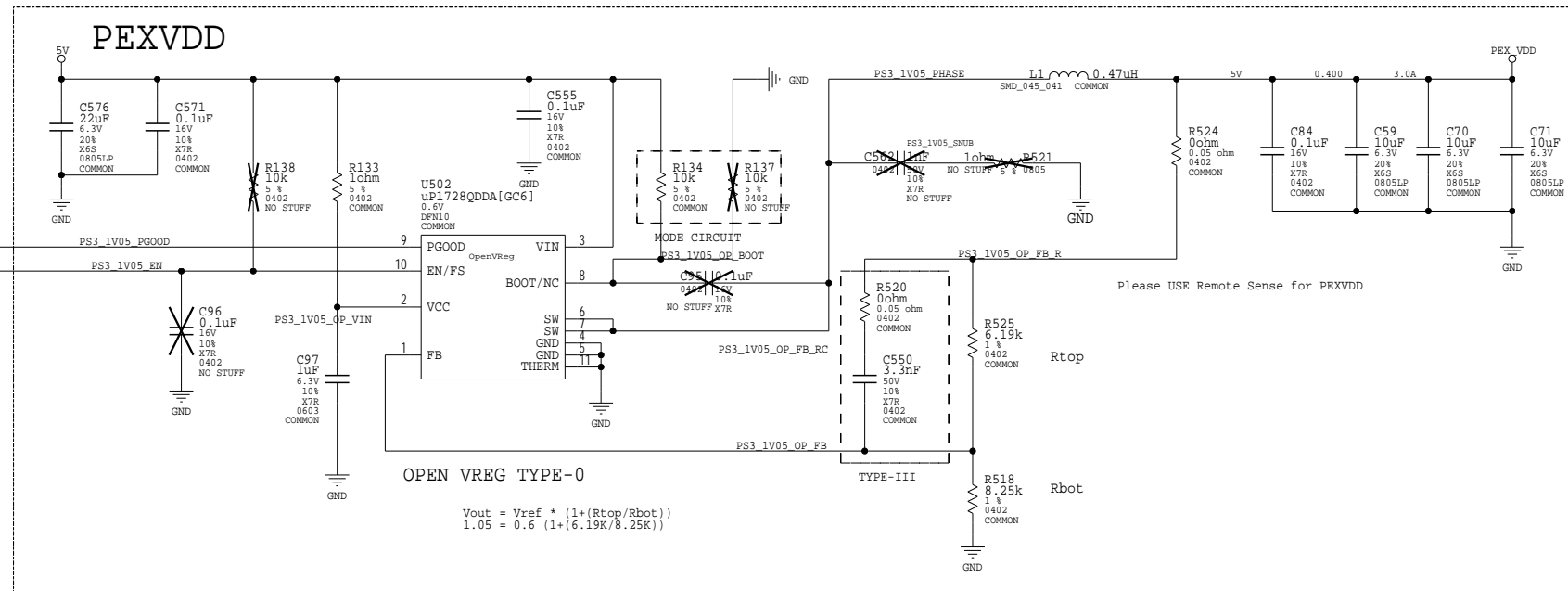
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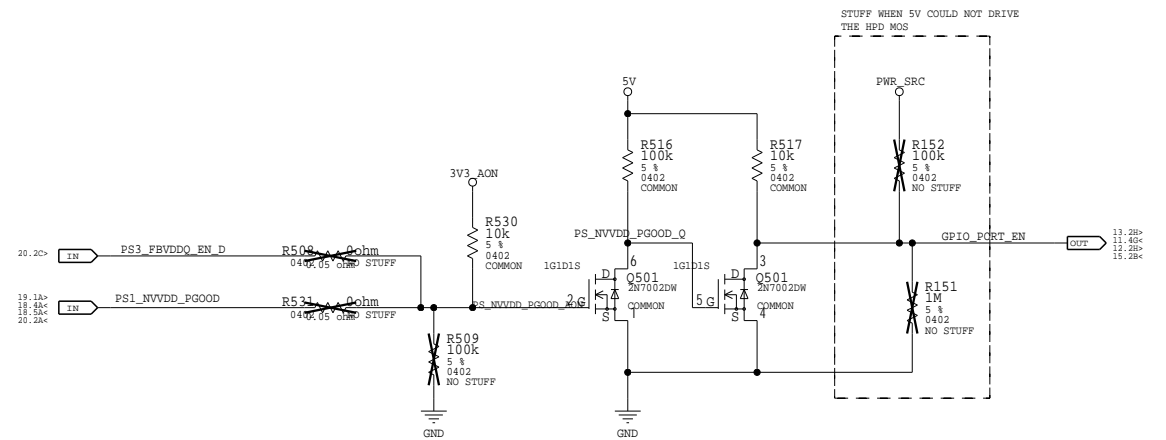
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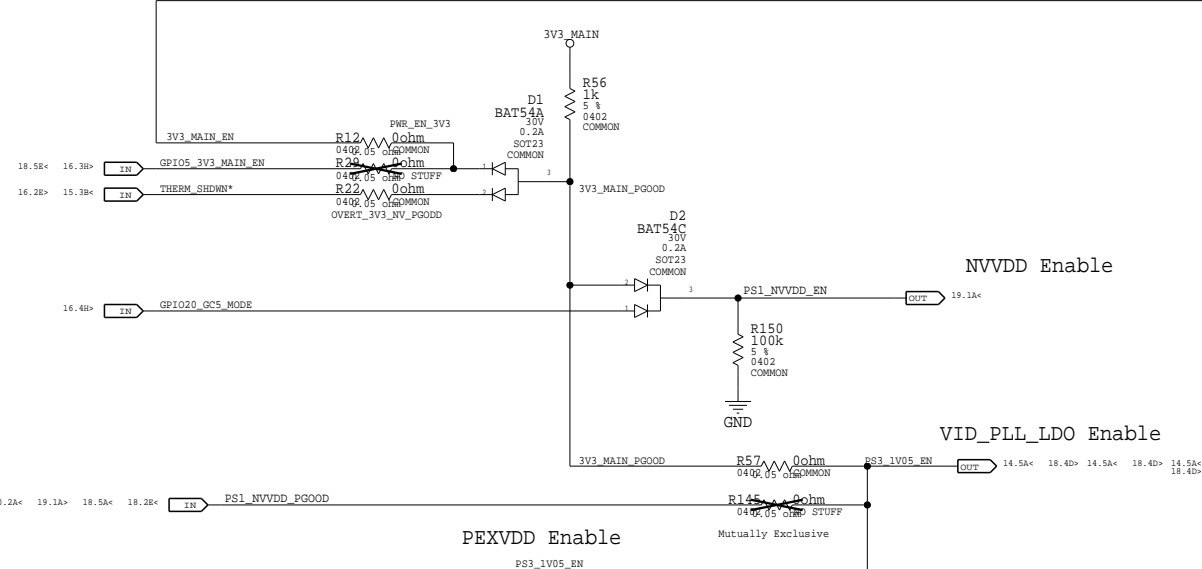
PEXVDD 3V3_AON 3V3_MAIN



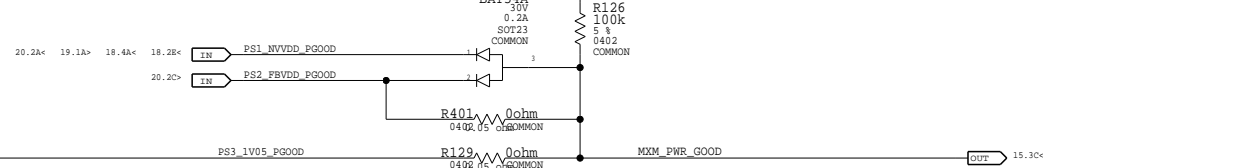
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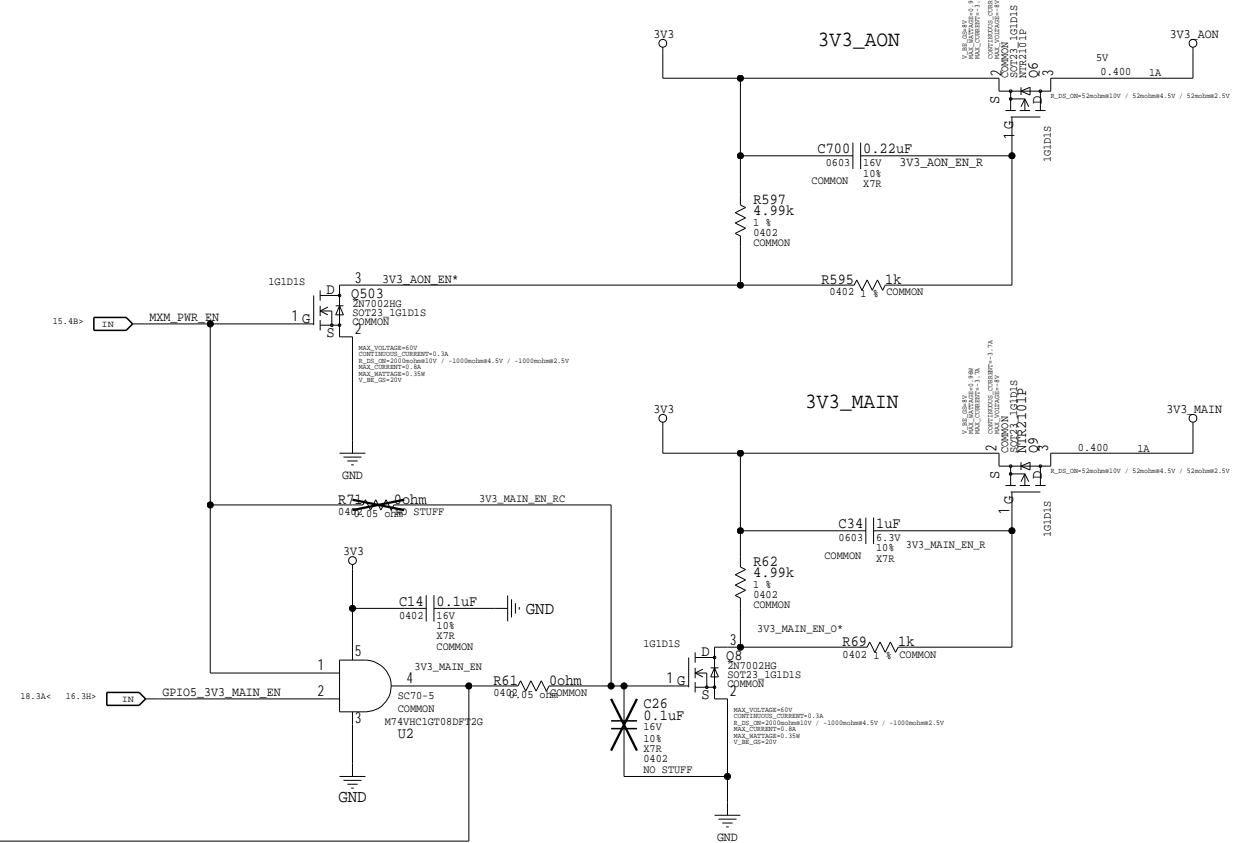
NVVDD&PEXVDD ENABLE



MXM POWER GOOD



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3V3_MAIN&3V3_AON  ENABLE
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ASSEMBLY	SKU512 GM107 x32 4GB GDDR5 MEMORY
PAGE DETAIL	PEXVDD, 3V3_AON, 3V3_MAIN

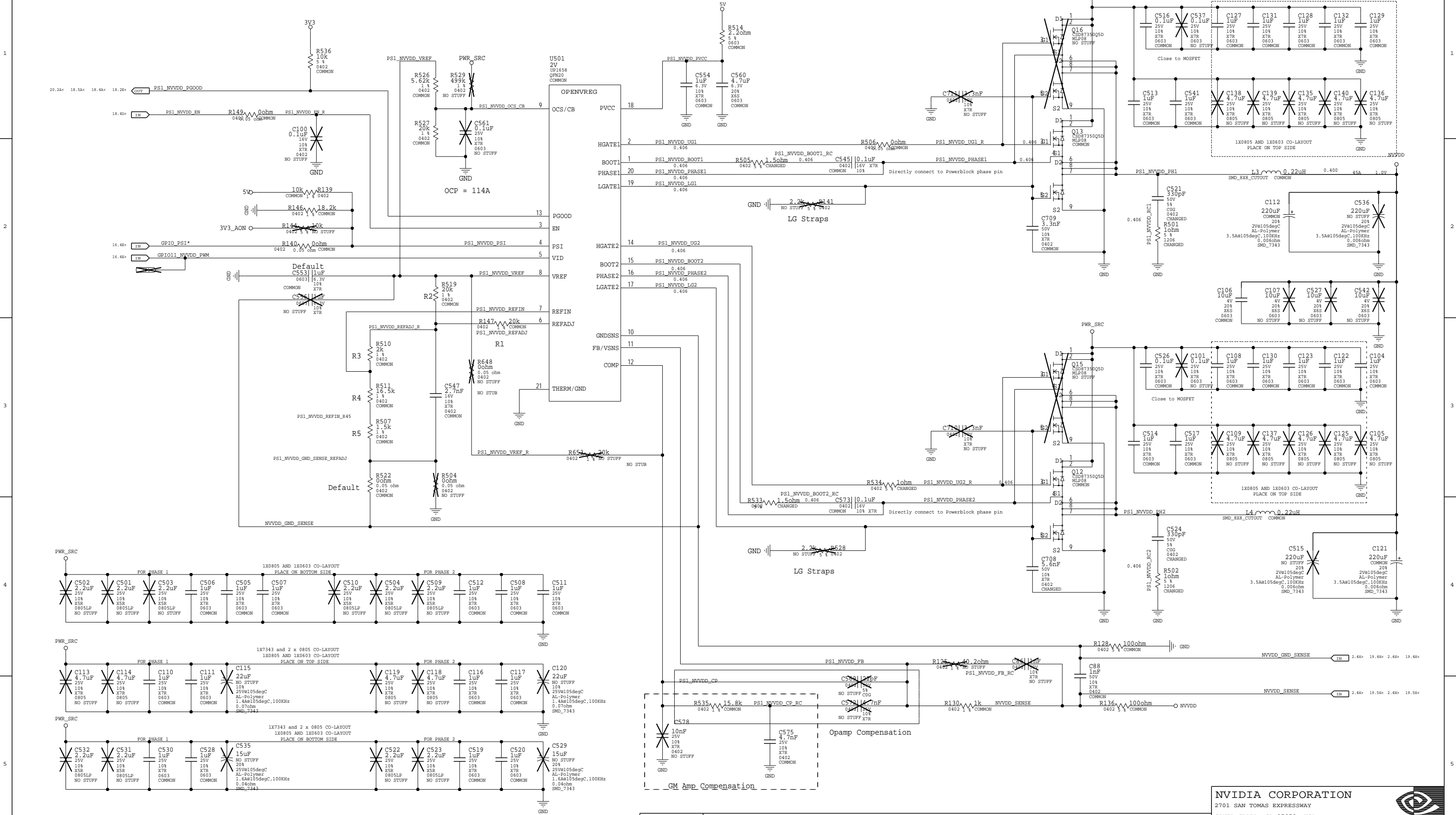
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2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA



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NVDD POWER SUPPLY



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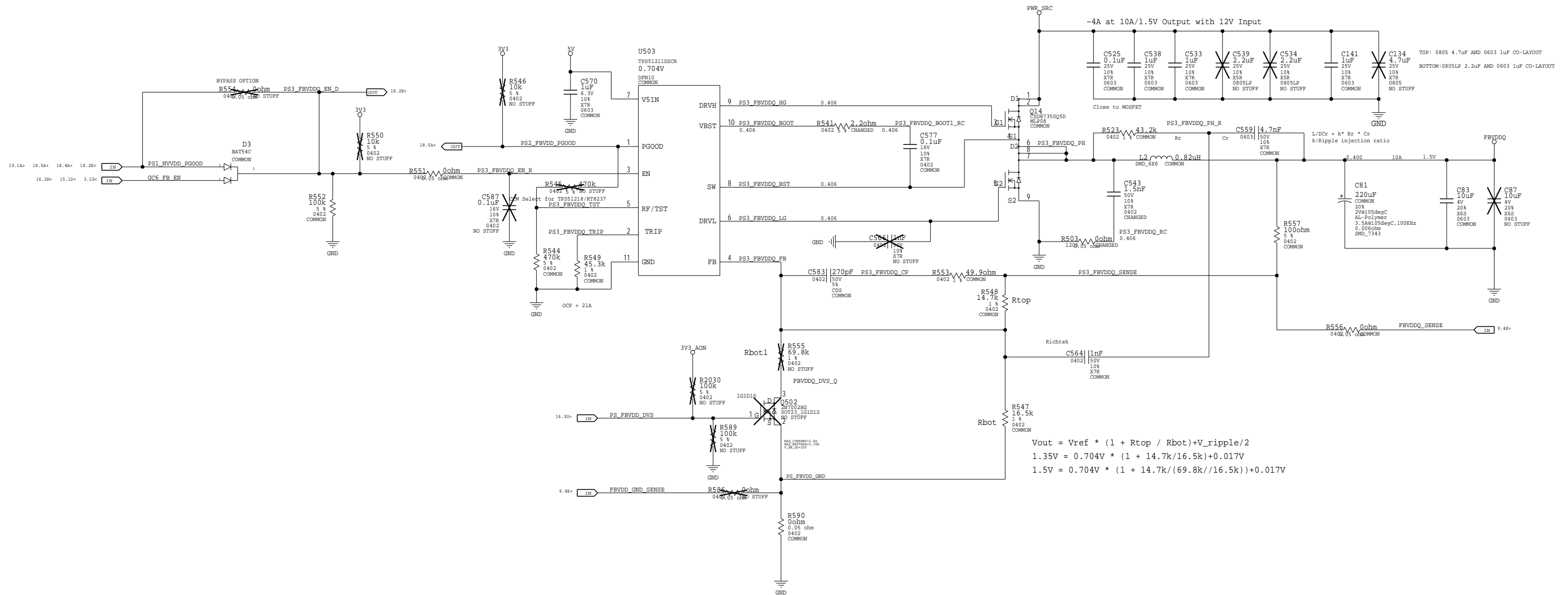
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FBVDDQ POWER SUPPLY


$$\begin{aligned} V_{out} &= V_{ref} * (1 + R_{top} / R_{bot}) + V_{ripple}/2 \\ 1.35V &= 0.704V * (1 + 14.7k/16.5k) + 0.017V \\ 1.5V &= 0.704V * (1 + 14.7k/(69.8k//16.5k)) + 0.017V \end{aligned}$$

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