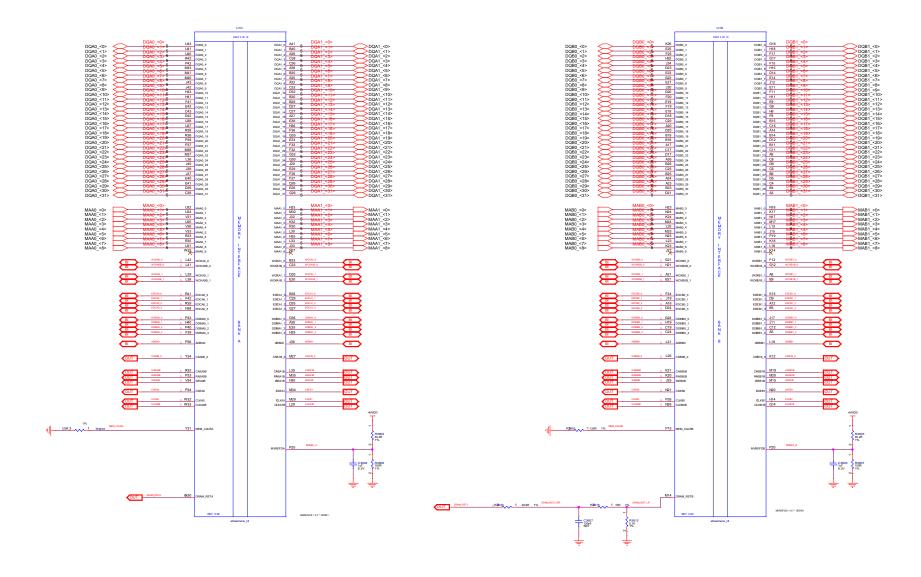


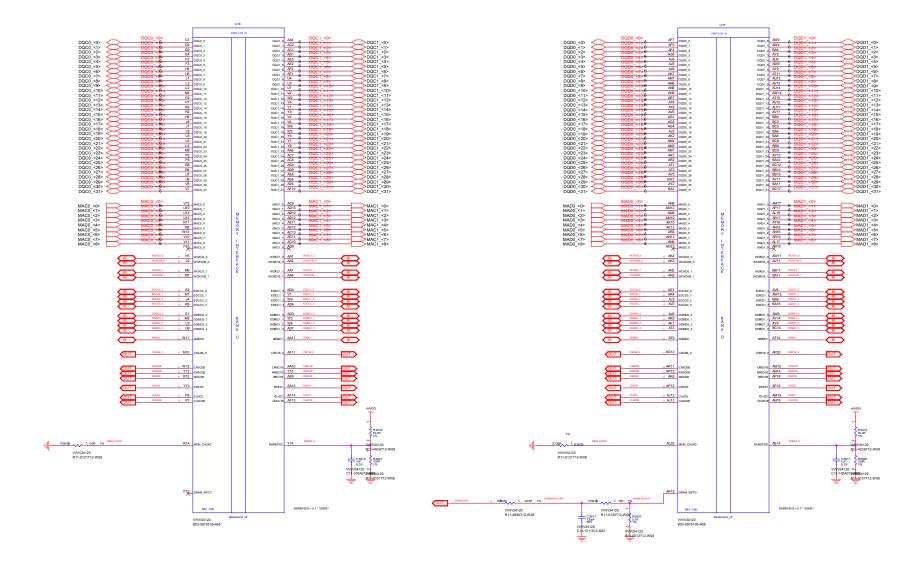


(3) ELLESMERE MEM INTERFACE CH A/B

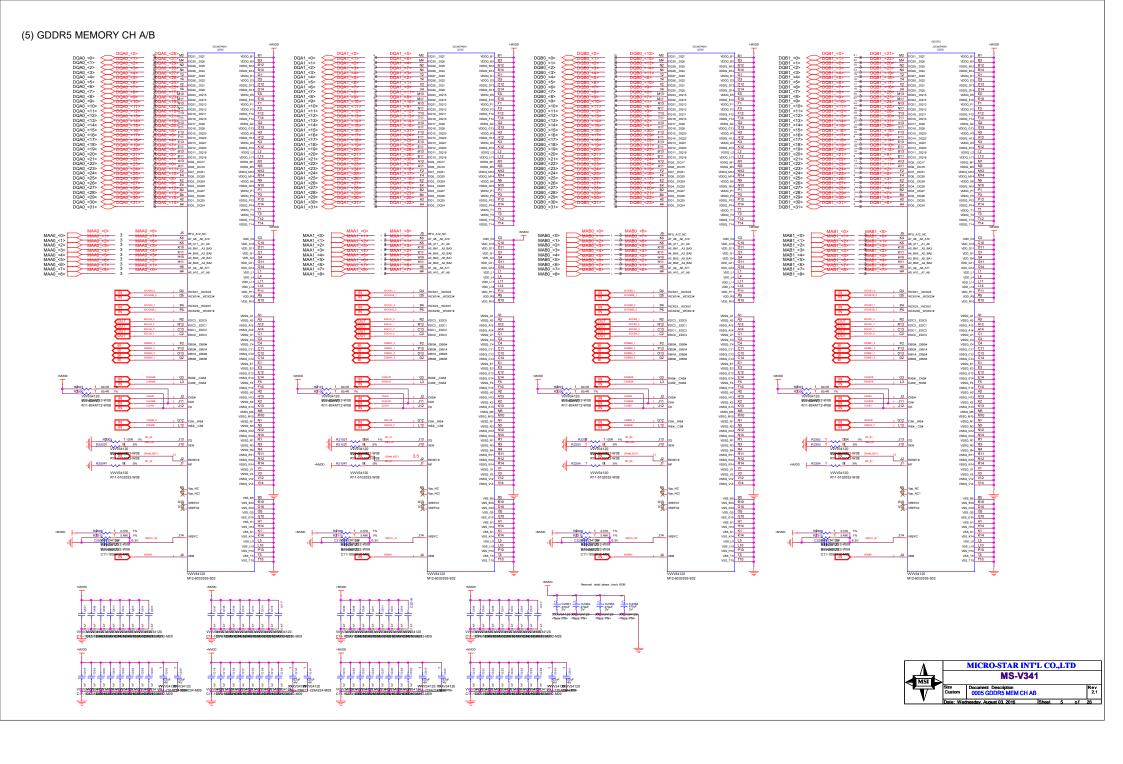


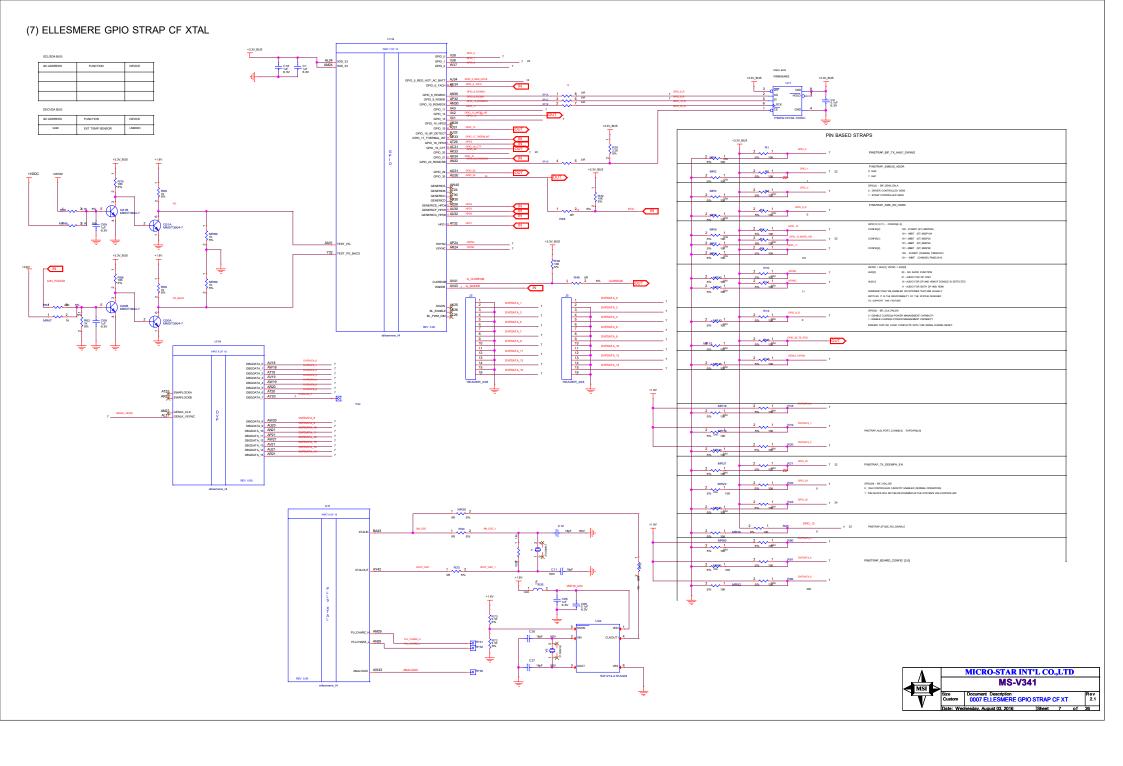


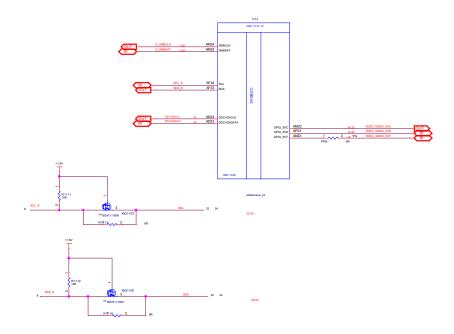
(4) ELLESMERE MEM INTERFACE CH C/D





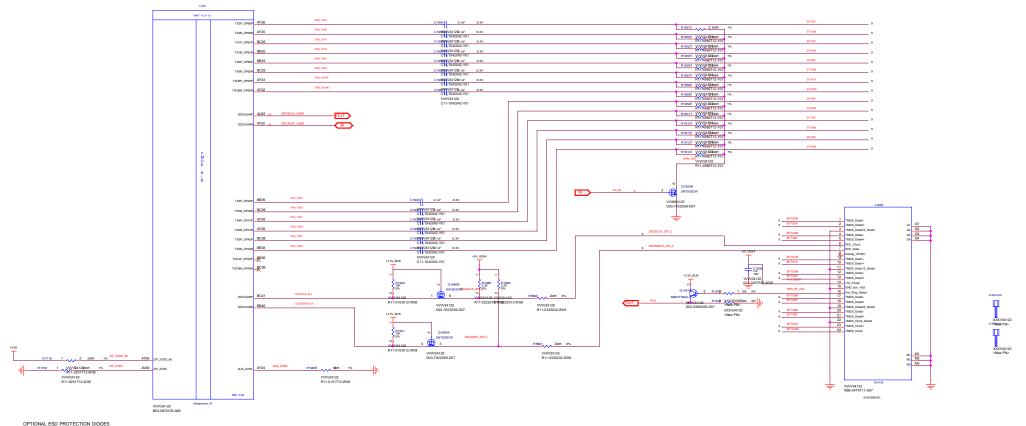




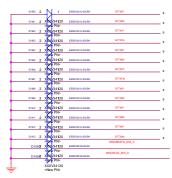




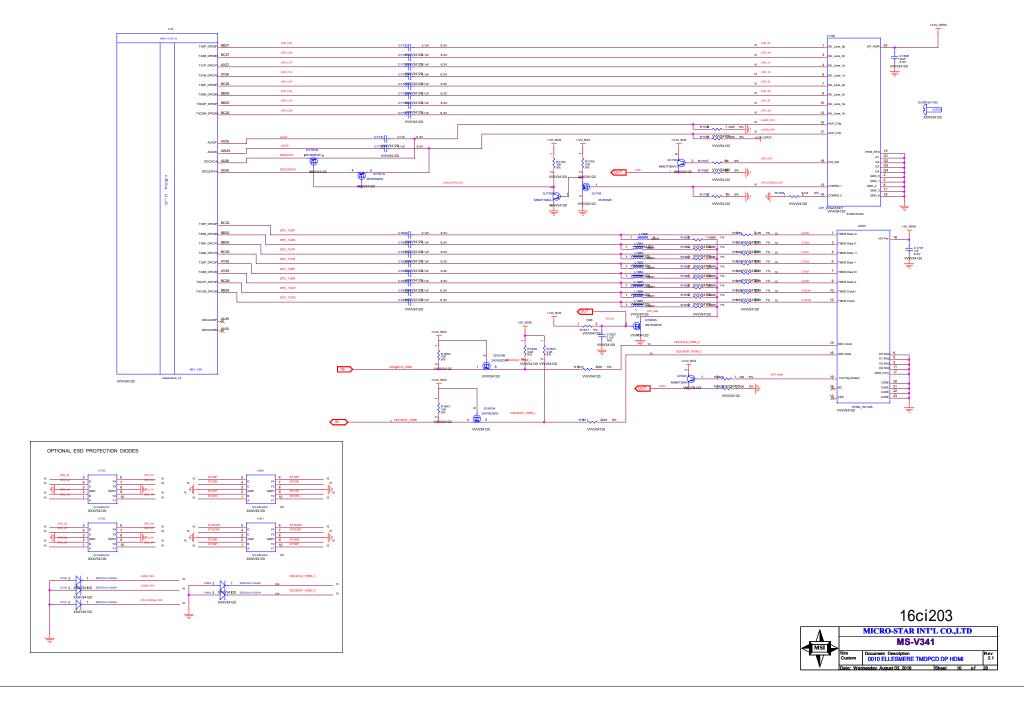
(9) ELLESMERE TMDP A/B



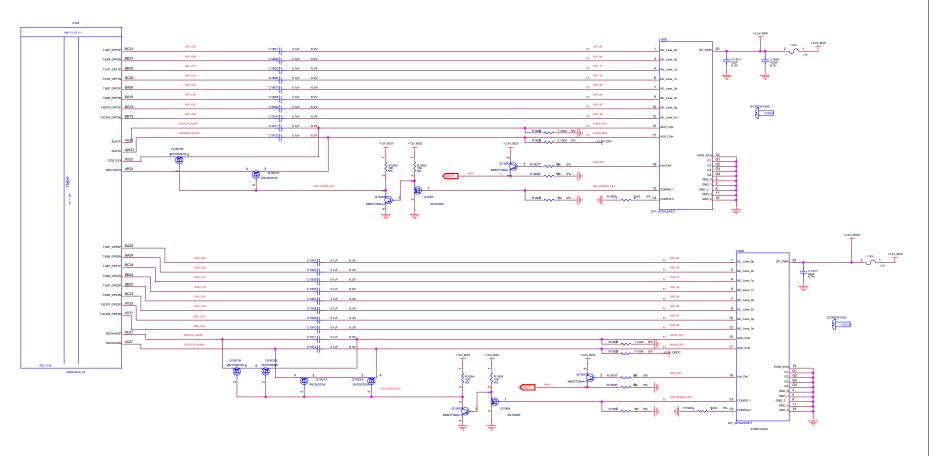




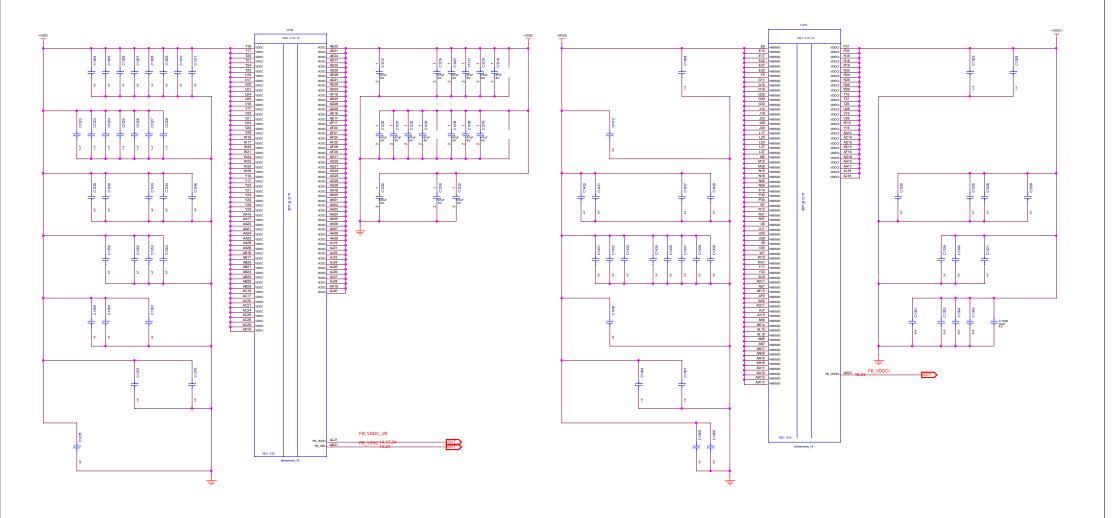




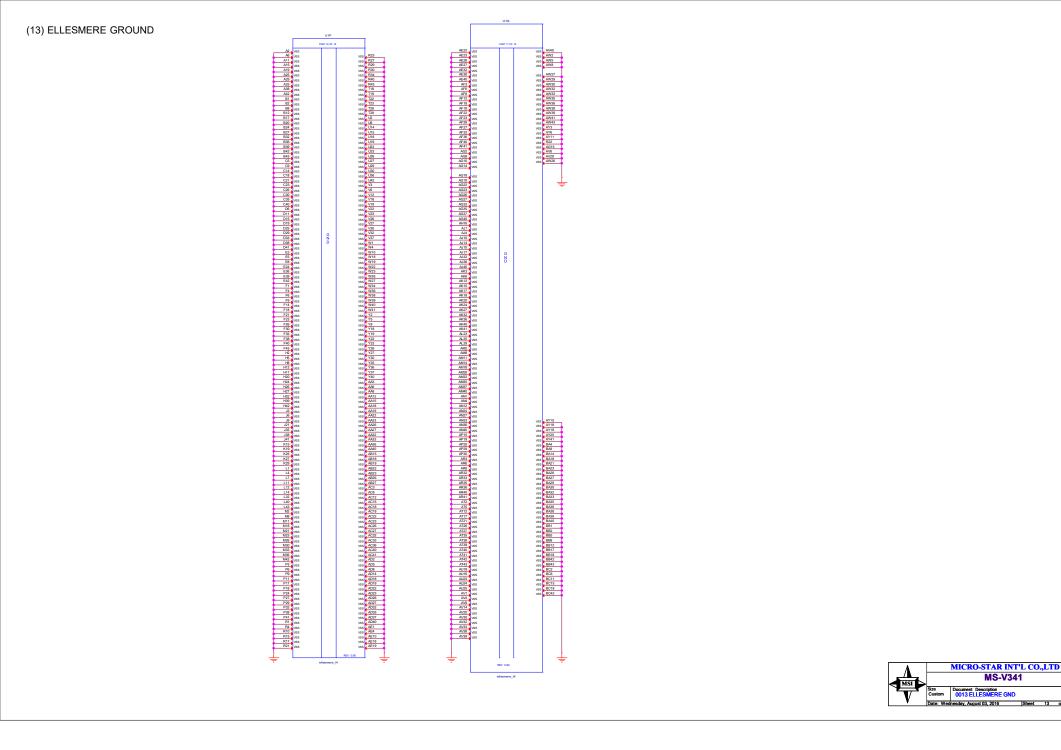
(11) ELLESMERE LVTMDP E/F



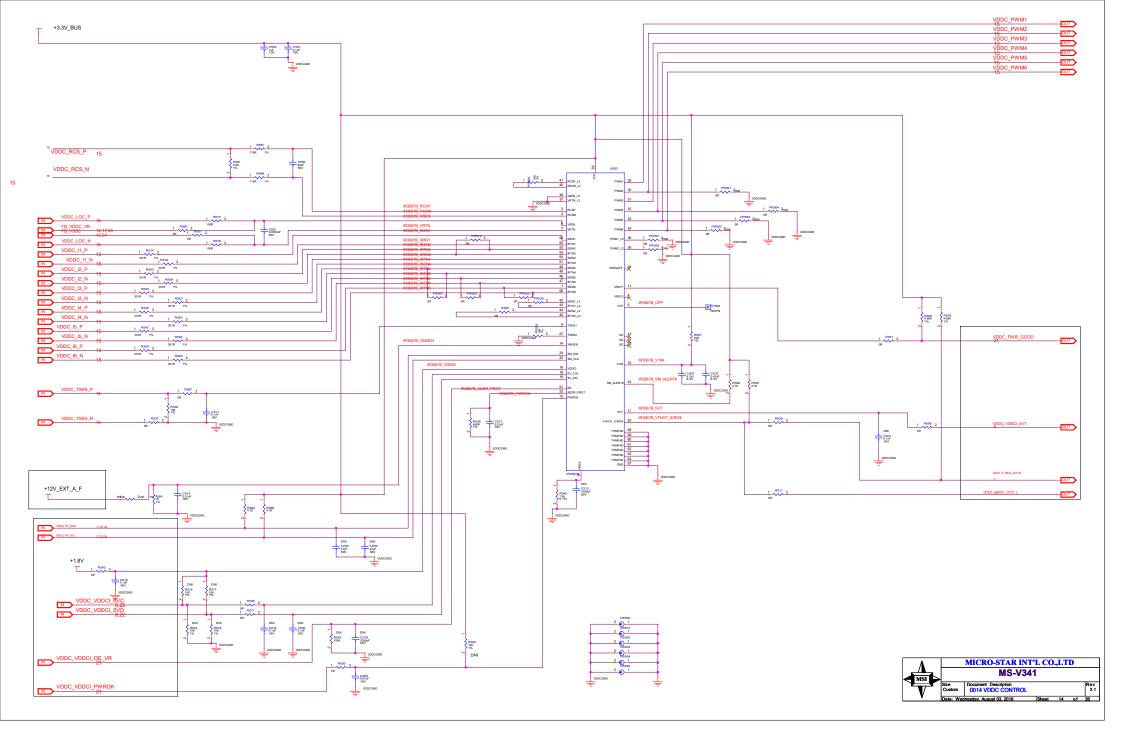


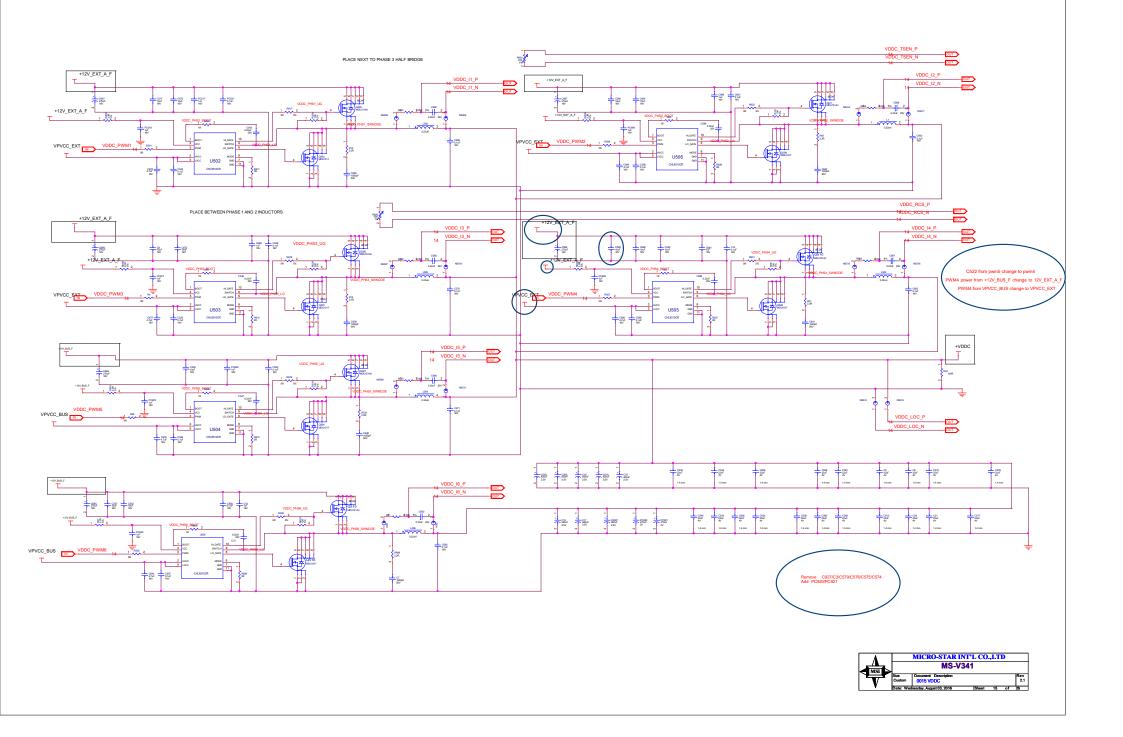


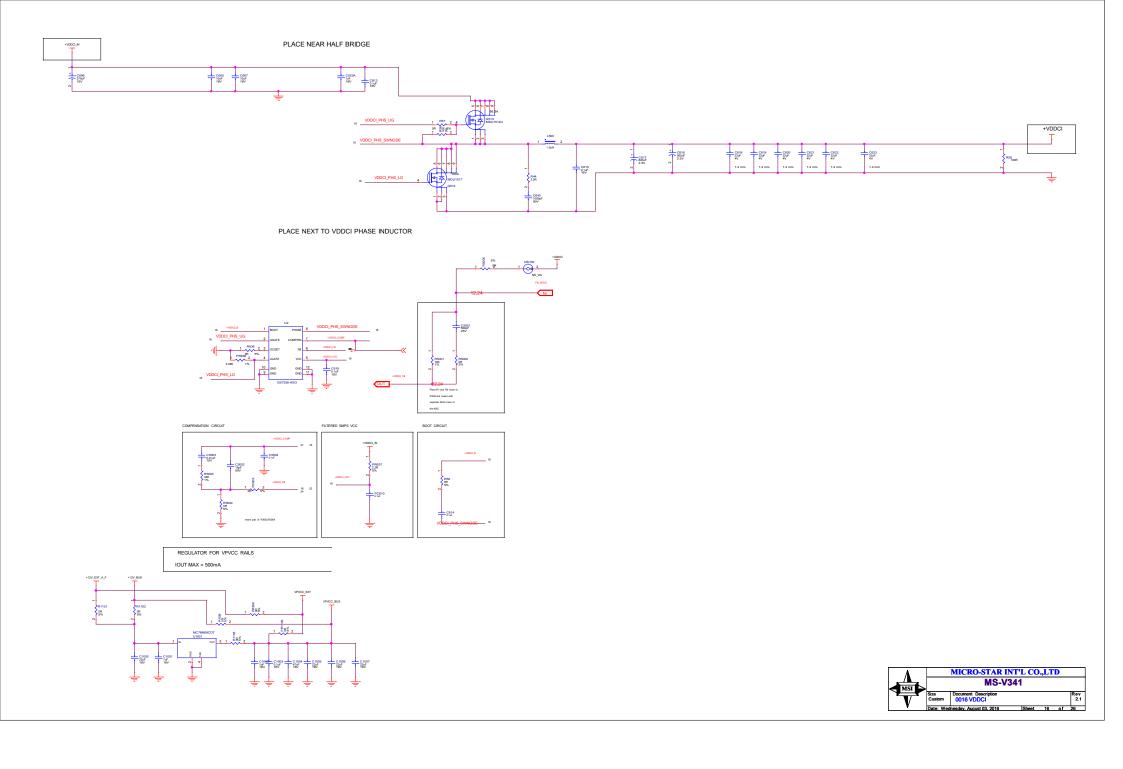


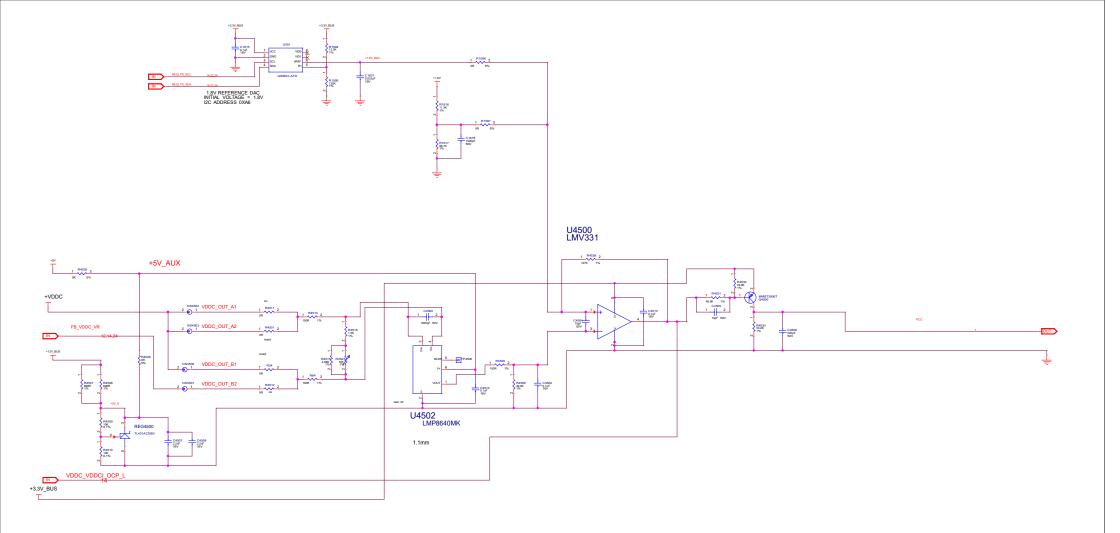


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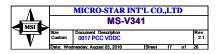


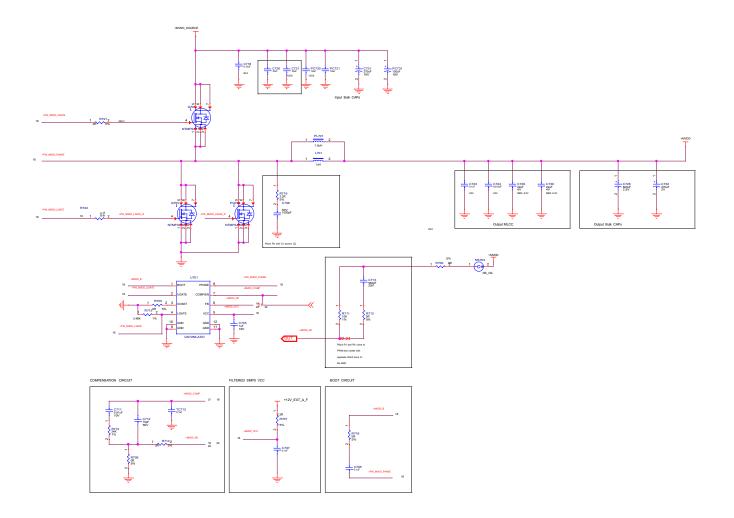




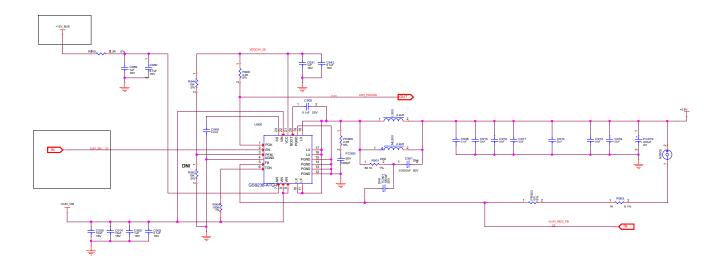














(18) SMALL RAIL REGULATORS



