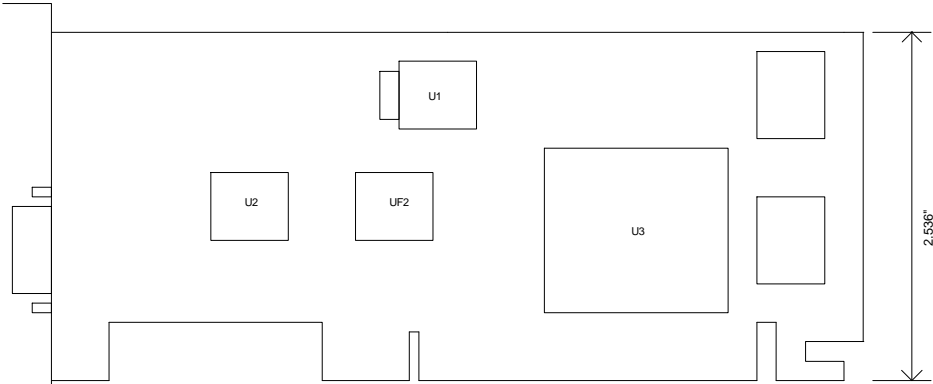


		MS-8814 Vanta LT AGP 8MB 1M*32 SGRAM		
	Size C	CAGE Code	DWG NO	Rev 2.0
Tuesday, November 28, 2000	Scale		Sheet	4 of 5

1. Use dual footprint for U1 TO-263/Power Flex
2. Use universal footprint for 4.7 uF capacitors, should accept tantalum A and B size, or 1206/1210 ceramic
3. Do not make a second key in AGP connector  
It will prevent use of this board in 1.5V-only systems.
4. Use standard low cost bracket before designing the PCB  
Position P1 to match the bracket.



APPROXIMATE LOCATIONS OF MAIN COMPONENTS

P25 History:

Rev A00

1. Design is based off P17-C00 with 1Mx16 SDRAM replaced by 1Mx32 SDRAM.
2. Fixed 13 mils card length error along VGA connector edge from P17-C00.
3. Fixed AGP gold finger height error from P17-C00.
4. Added 8 VDDCLAMP pins for 3.3V I/O

Rev B00

1. Add C128 and C129 0.1uF and C9/C12 470uF capacitors to improve NVVDD filtering
2. Correct AGP connector goldfinger space violation
3. Changed value of C74 from 220pF to 22pF

PCB Stackup:  
LAYER 1: TOP ROUTE WITH GND POUR  
LAYER 2: SOLID GND WITH ANNELO CIRCUITS  
LAYER 3: VDDQ, NVVDD, 3.3V, 5V  
LAYER 4: BOTTOM ROUTE WITH GND POUR

	MS-8814 Vanta LT AGP 8MB 1M*32 SGRAM			
	Size C	CAGE Code	DWG NO	Rev 2.0
Tuesday, November 28, 2000	Scale			Sheet 5 of 5