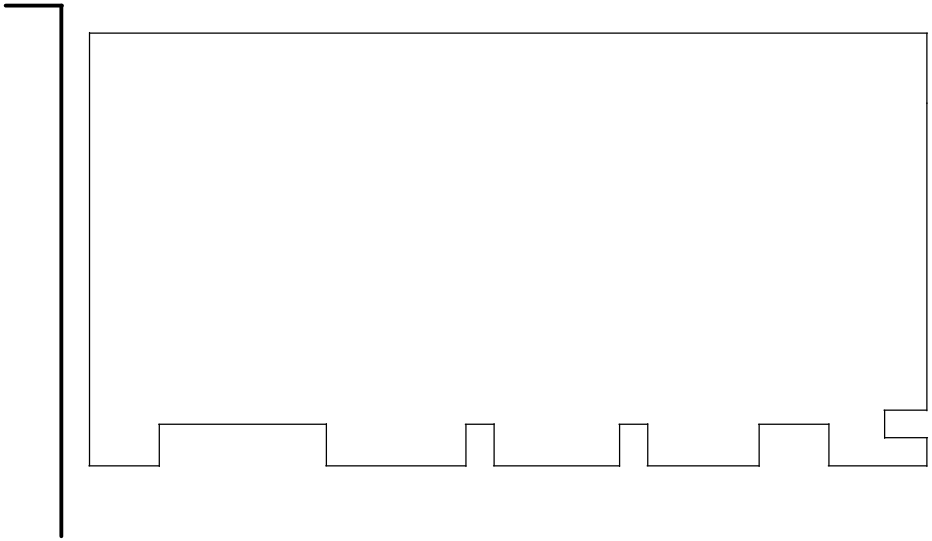
NV11, 2MX32 SDR, RGB, INTERNAL DVI-I, AGP.

PCI DEVICE ID 0X0=0X110 FOR NV11.

STRAPS: AGP4X, SIDEBAND DIS, FAST WRITE ENA, ADP BIOS, NORMAL PCIAD, 14.318MHZ
STRAP_RAM_TYPE [5:2]=0000-2MX32 SDR SDRAM



HISTORY REV. C00:

1) C00: Implemented Separate DFP_VREF and DFP_RSET for each TMDS channel.
2) C00: Added RC networks for FBDQS and FBCLK.
3) C00: Fixed 64 bit memory interface.
4) C00: Added more bulk decoupling caps to 3.3V rail.
5) C00: Added alternative footprint for D800 and C808.
6) C00: Changed HSYNC/VSYNC caps from 220pf to 22pf.
7) C00: Recalculated DFP hi frequency caps to have RSF from 65mhz to 108mhz.

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	MS-8817 based on P39C00	
Size B	Document Number HISTORY	Rev 1
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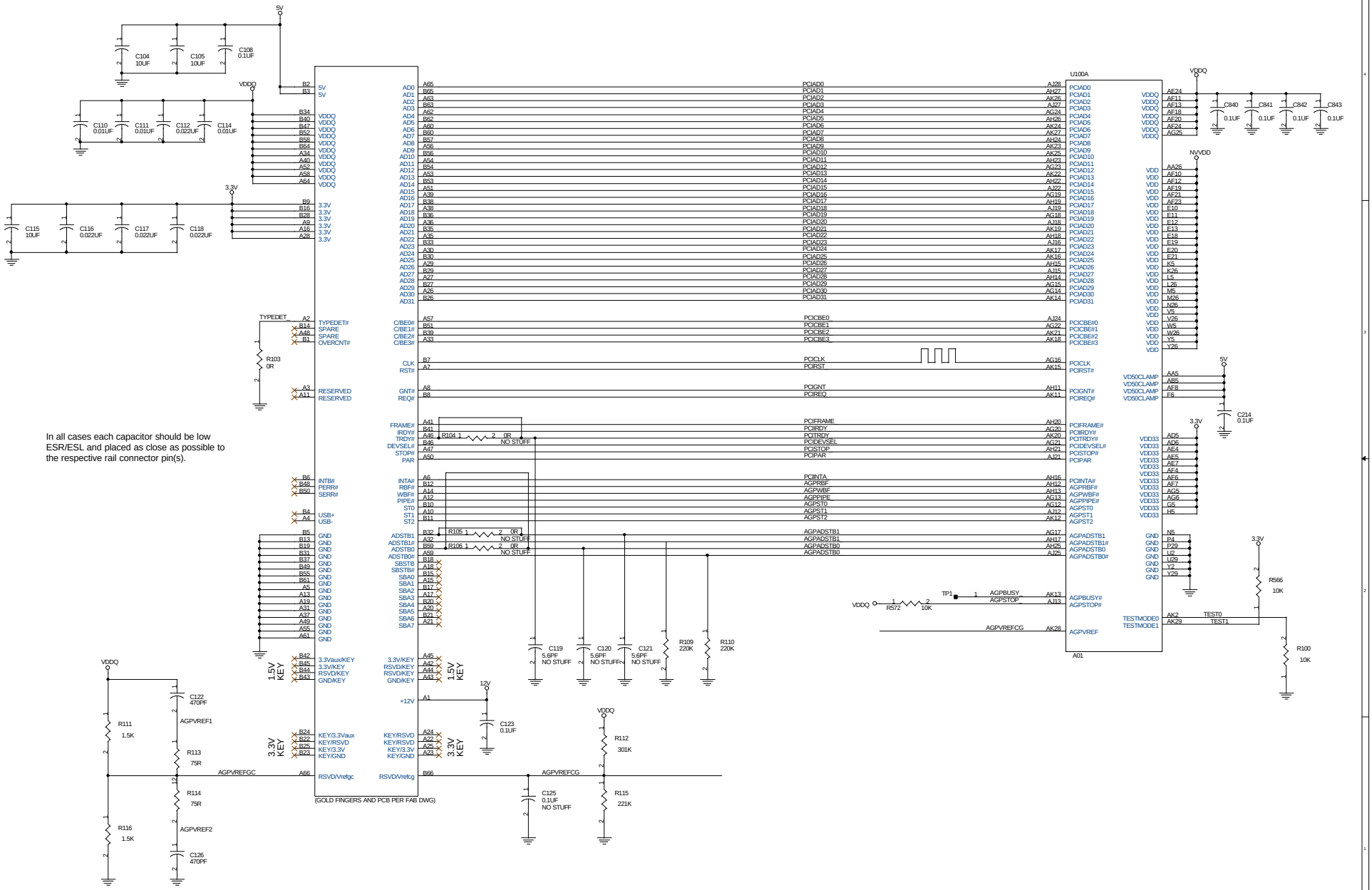

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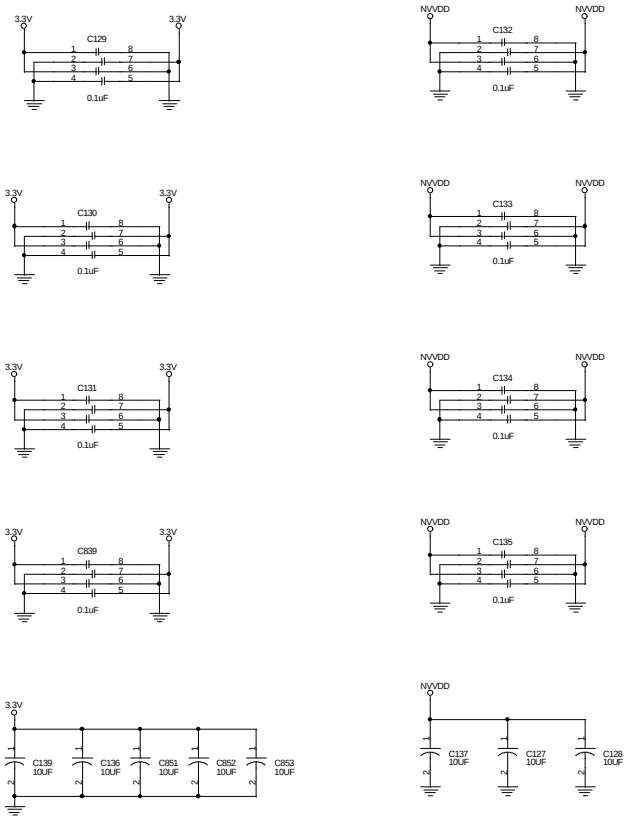
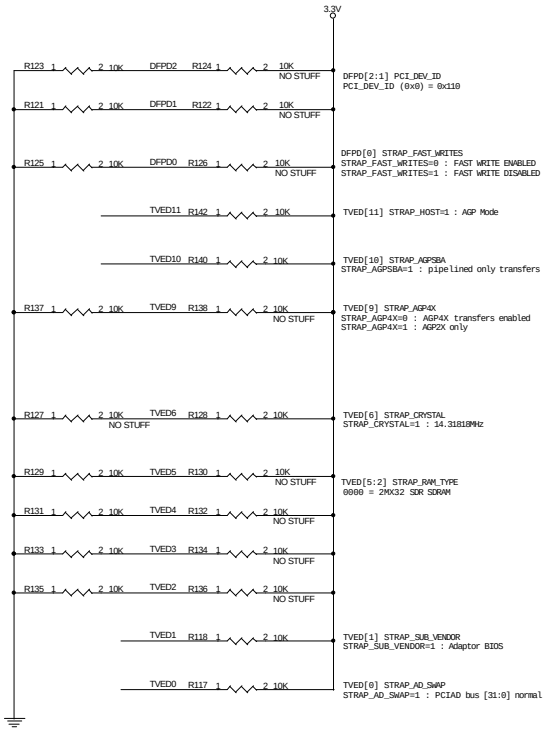
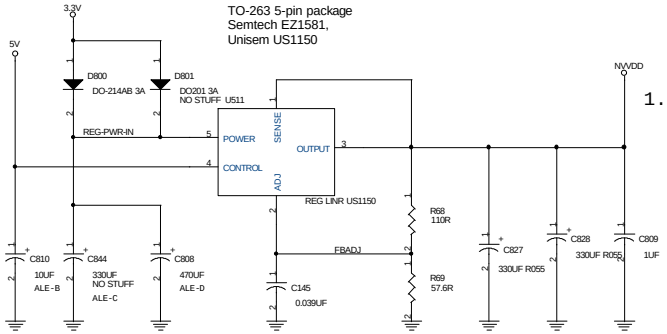




TVED[11:0]
VIPD[7:0]
DFPD[11:0]

TO-263 5-pin package
Semtech EZ1581,
Unisem US1150



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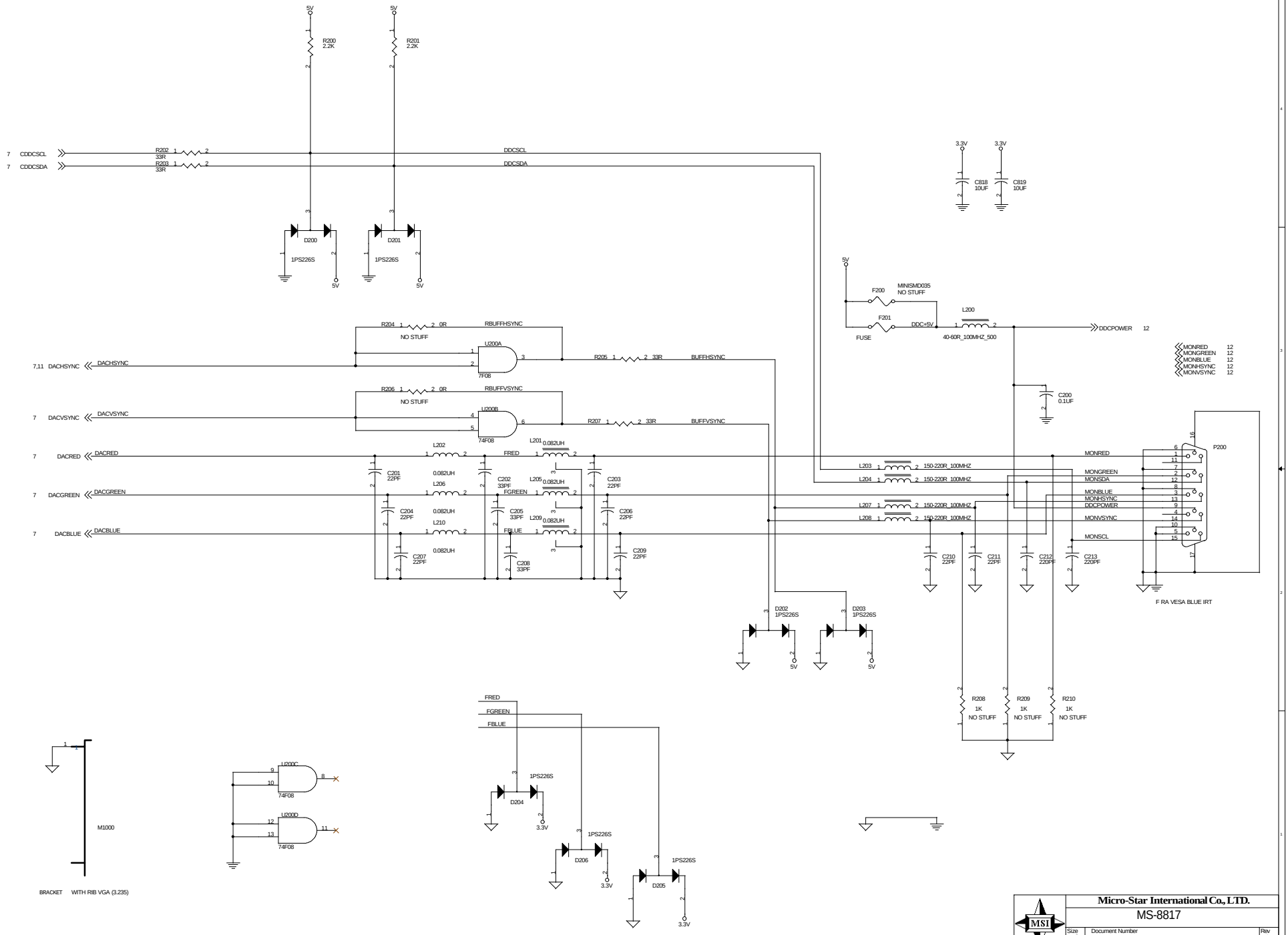

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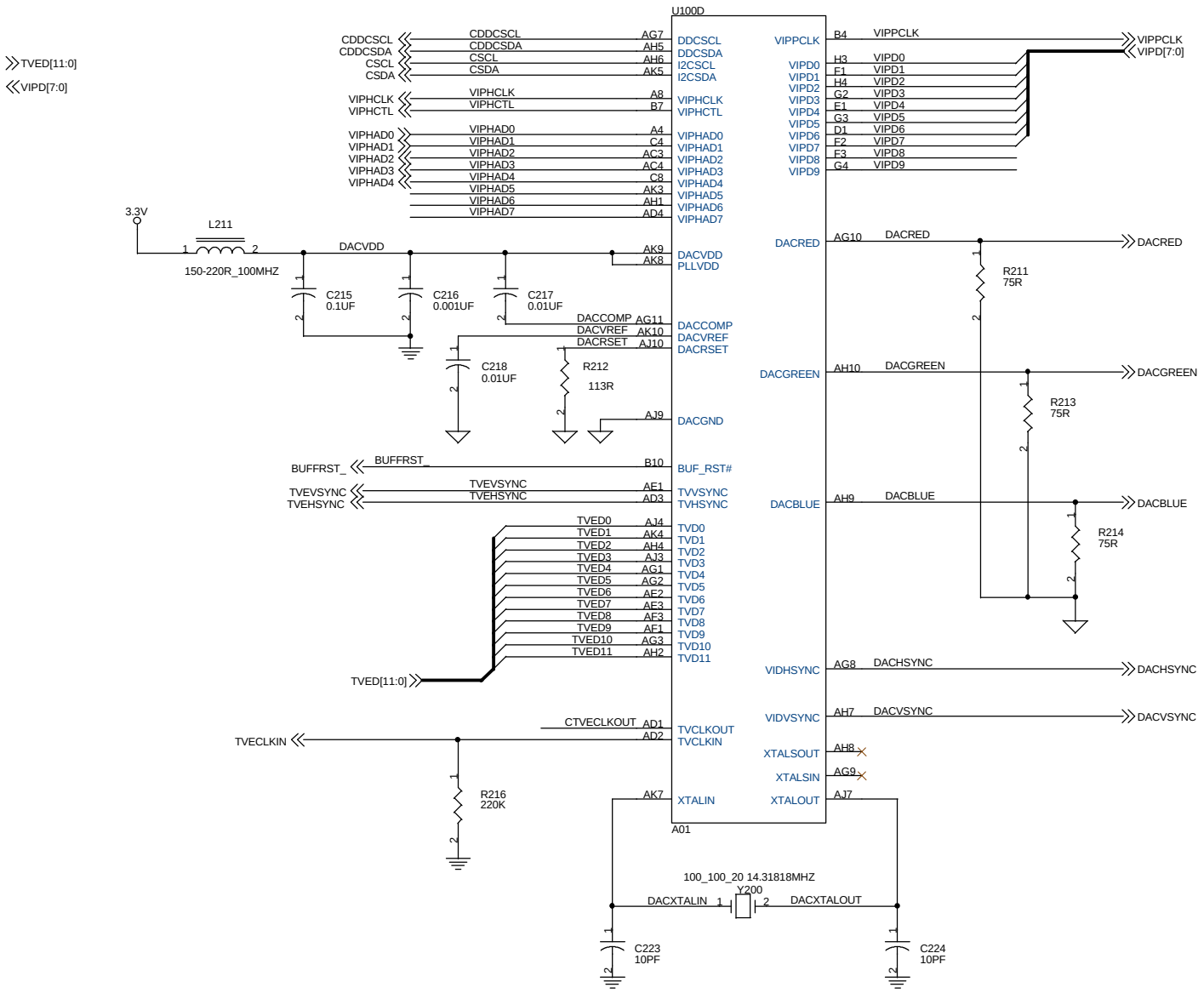

Use thick (non-impedance controlled) traces on XTALIN/OUT

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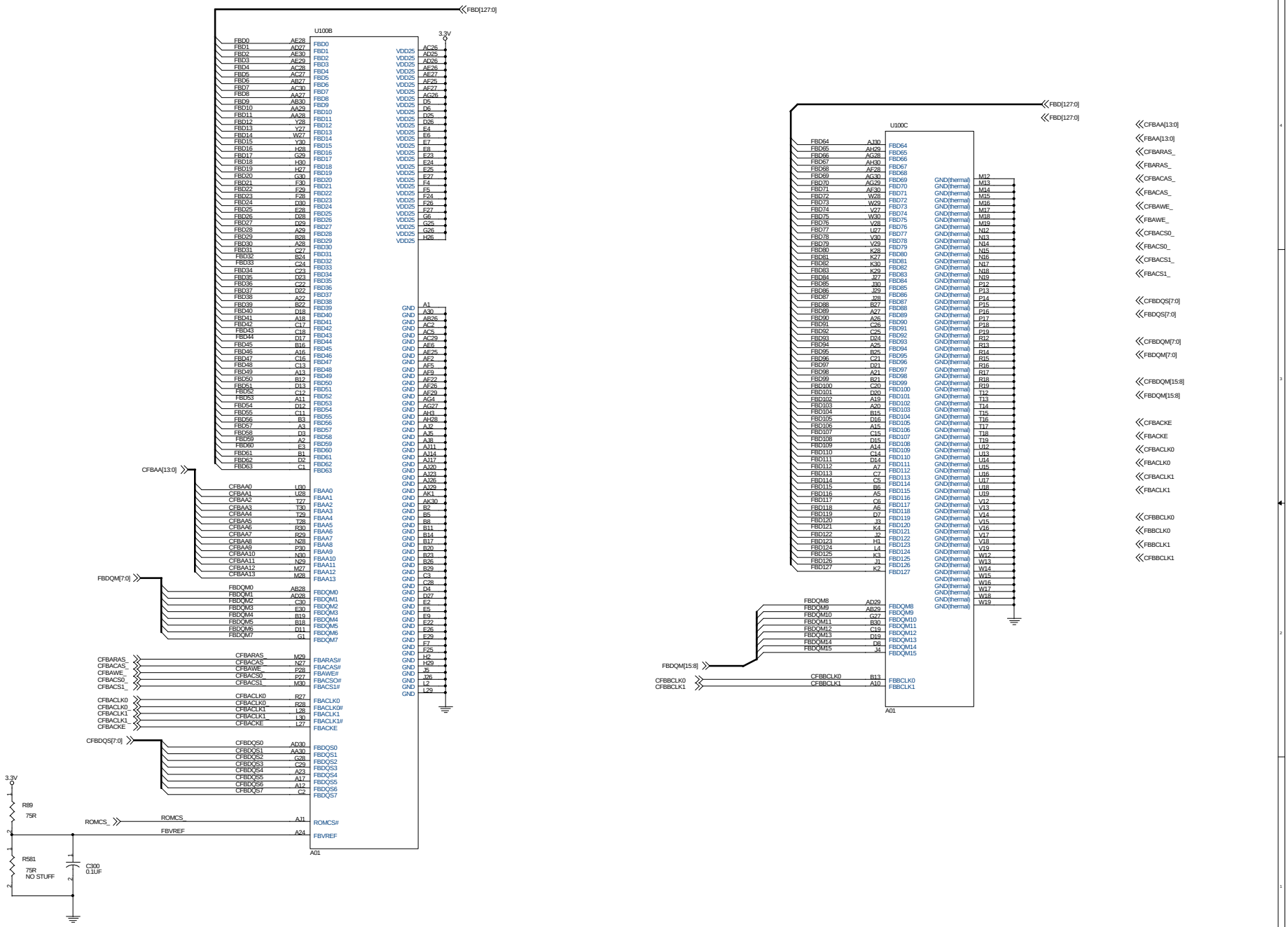

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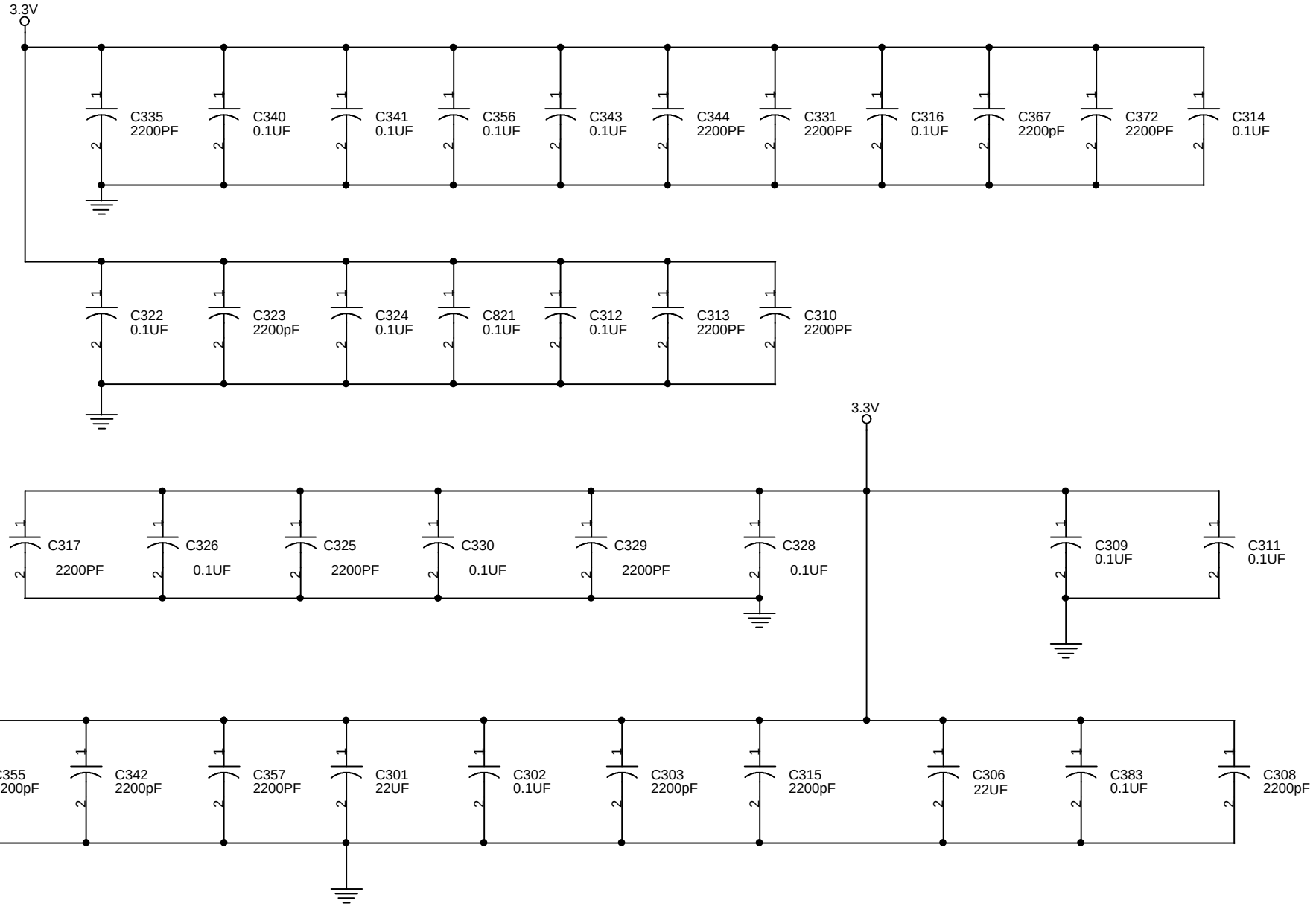

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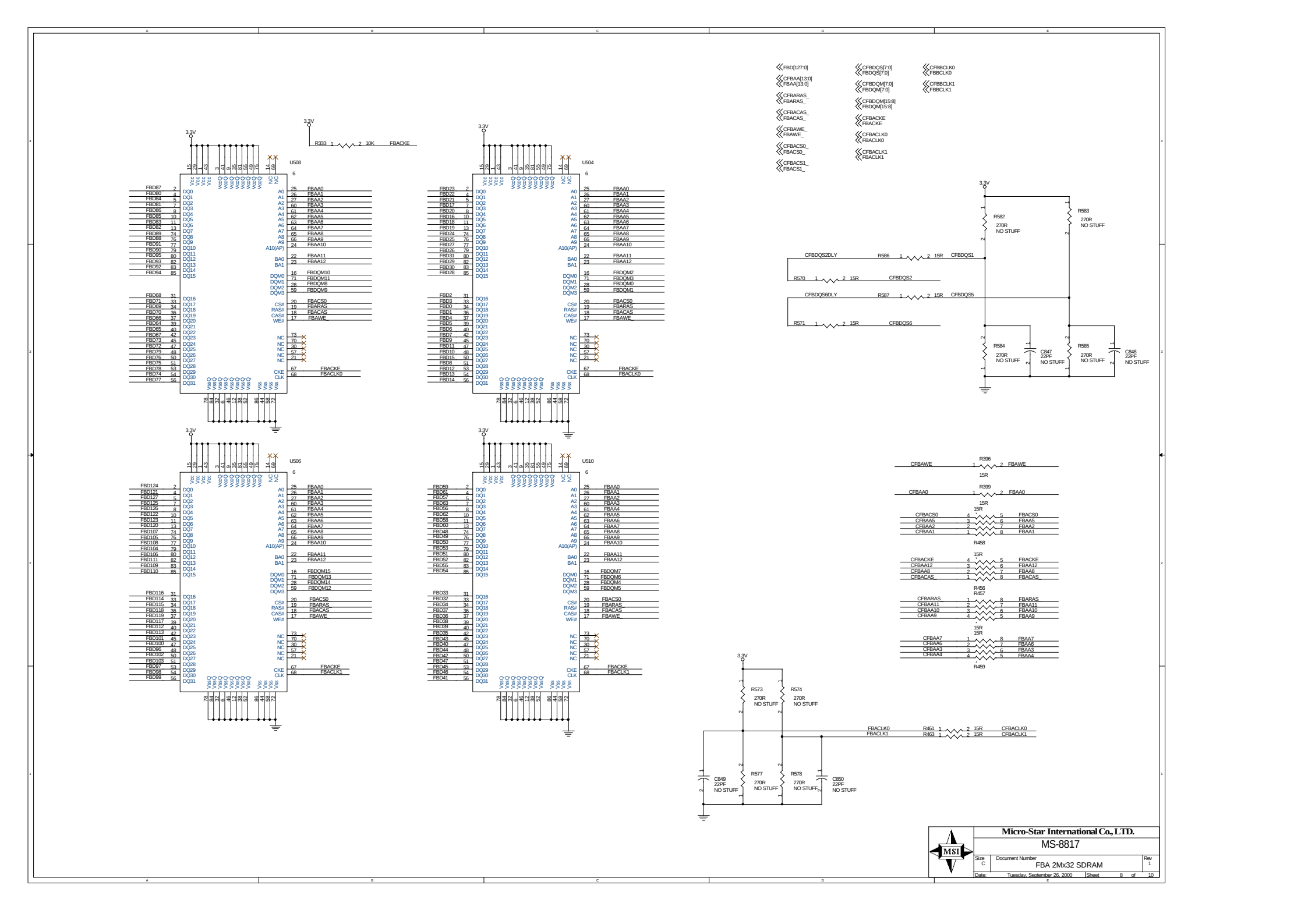

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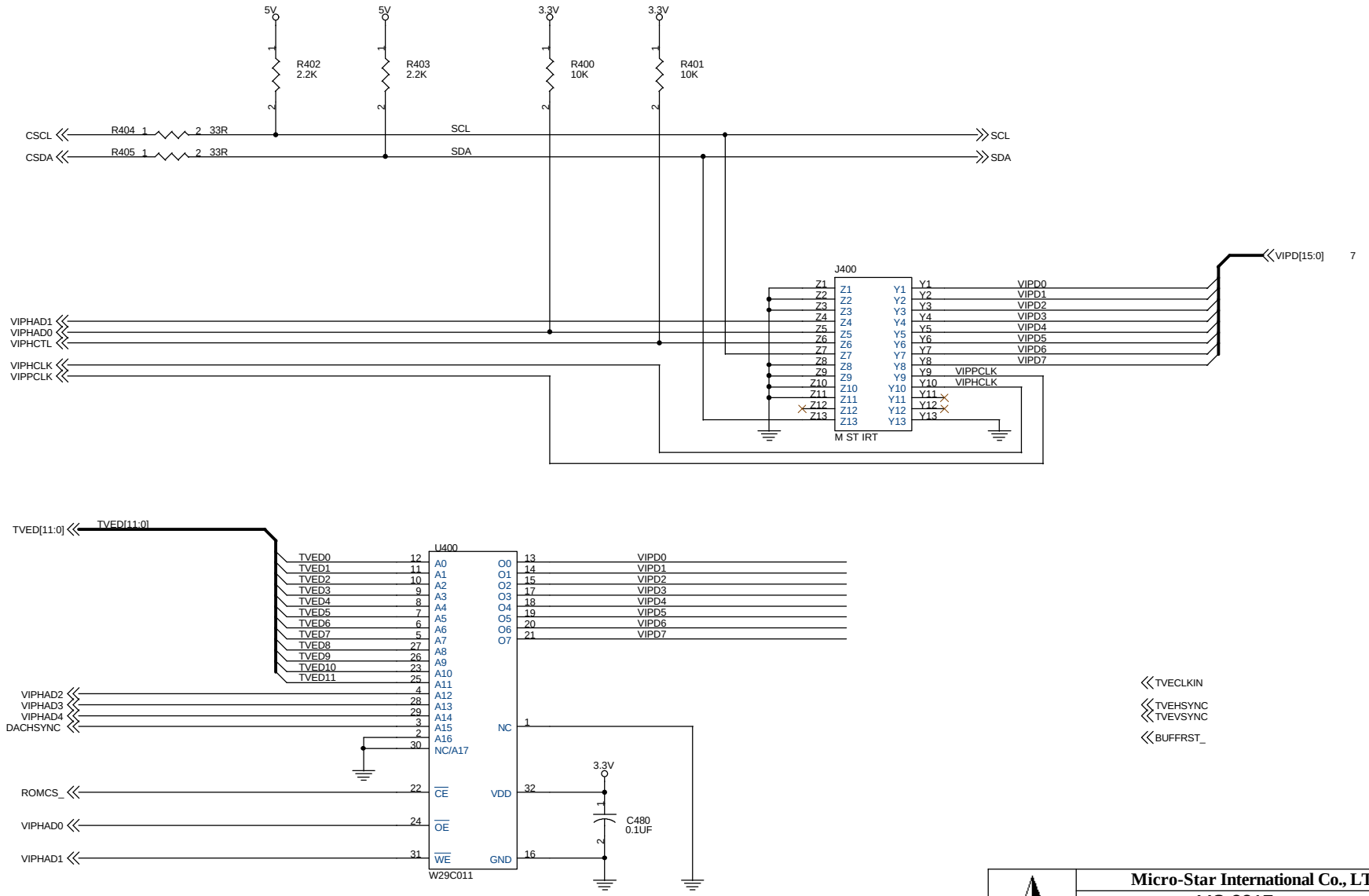

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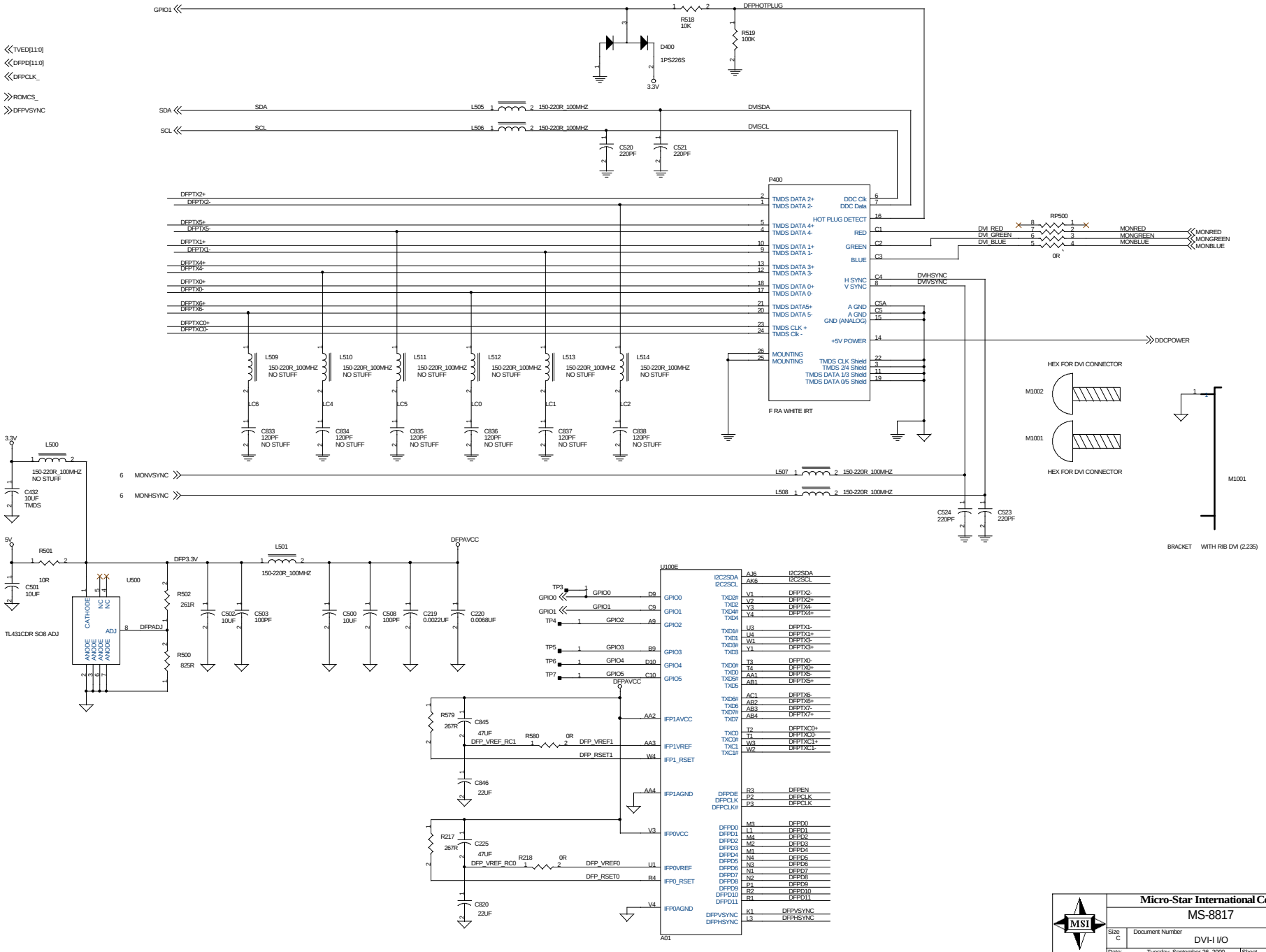

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