

G84/G86, 128bit DDR2, VGA, DVI-I, HDTV,HDMI

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V131-0A

BASE ON V092-10 modify

1.GPU change NB8P-GS G84-600-A2

2.Change 2X3 pin definition


3.change Power soulation

REV HISTORY

Base on V092-0A modify

1.page 3: R574 footprint change to 0805 :
2.C20 C31 C36 C45 footprint change to 15000P_6_3V :
3.page 19: G2D0 5 /G2D0 6 for NVVDD :
4.page 2: GPU pin T13 add 508K R626 connect toNVVDD :
5.page 6: add FRCND 11 for C1E need 10K pull low
6.page 17: MIOBQV+add R77 pull down to GND :
7.page 17: R572 R568 change to pull down to GND :
8.page 17: add pull up R627 + and connect to MIOB_R570C(G1.AF3) .

REV	VARIANT	NPVN	ASSEMBLY
0	0000	600-10501-0000-100	G75 400/500MHz 256MB 128bit DDR2 16MX16 DV+H/VGA+DTV/CUT
1	0001	600-10501-0001-100	G75V 375/500MHz 256MB 128bit DDR2 16MX16 DV+H/VGA+DTV/CUT
2	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
3	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
4	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
5	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
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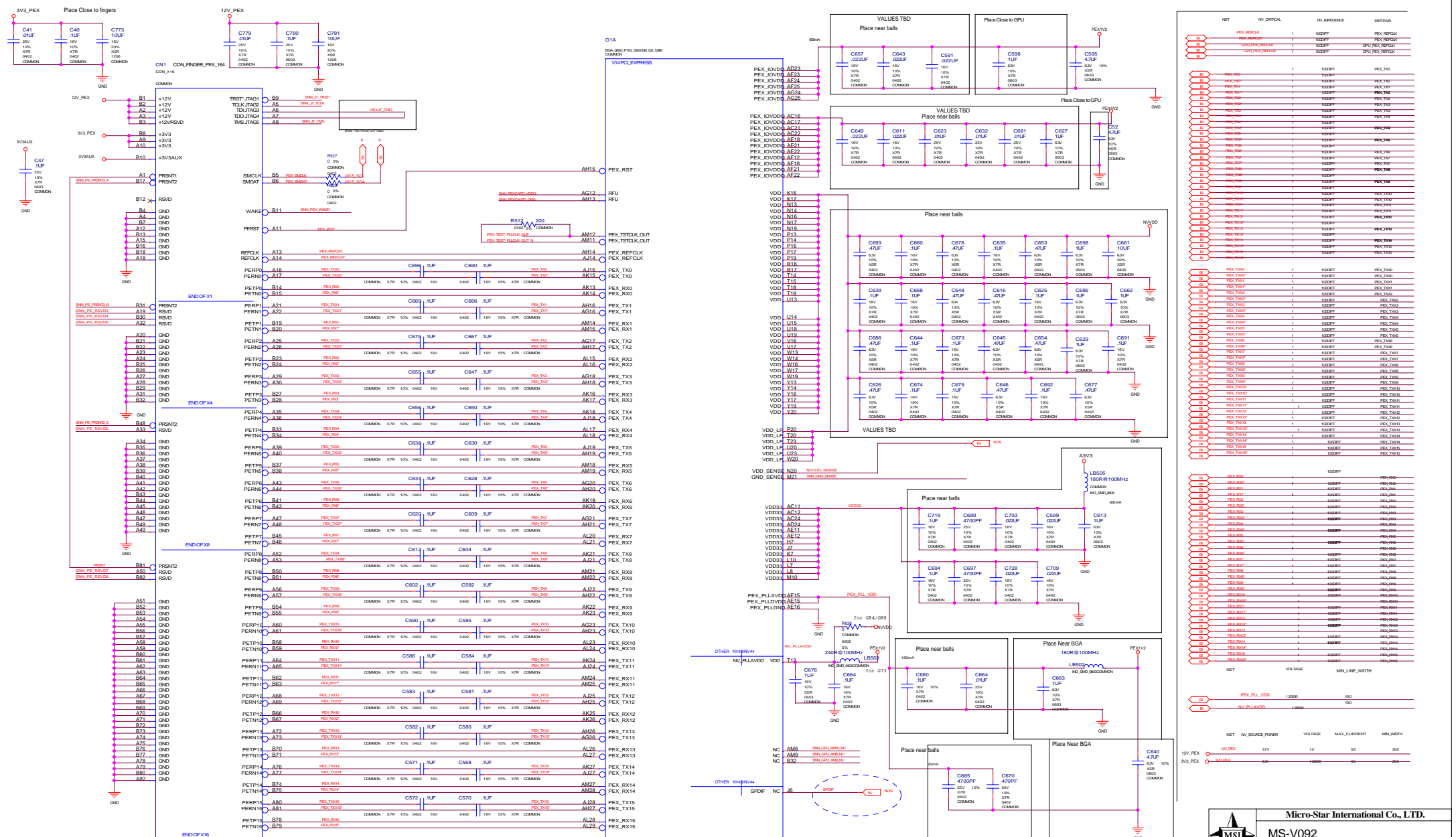
Micro-Star International Co., LTD.

MS-V092

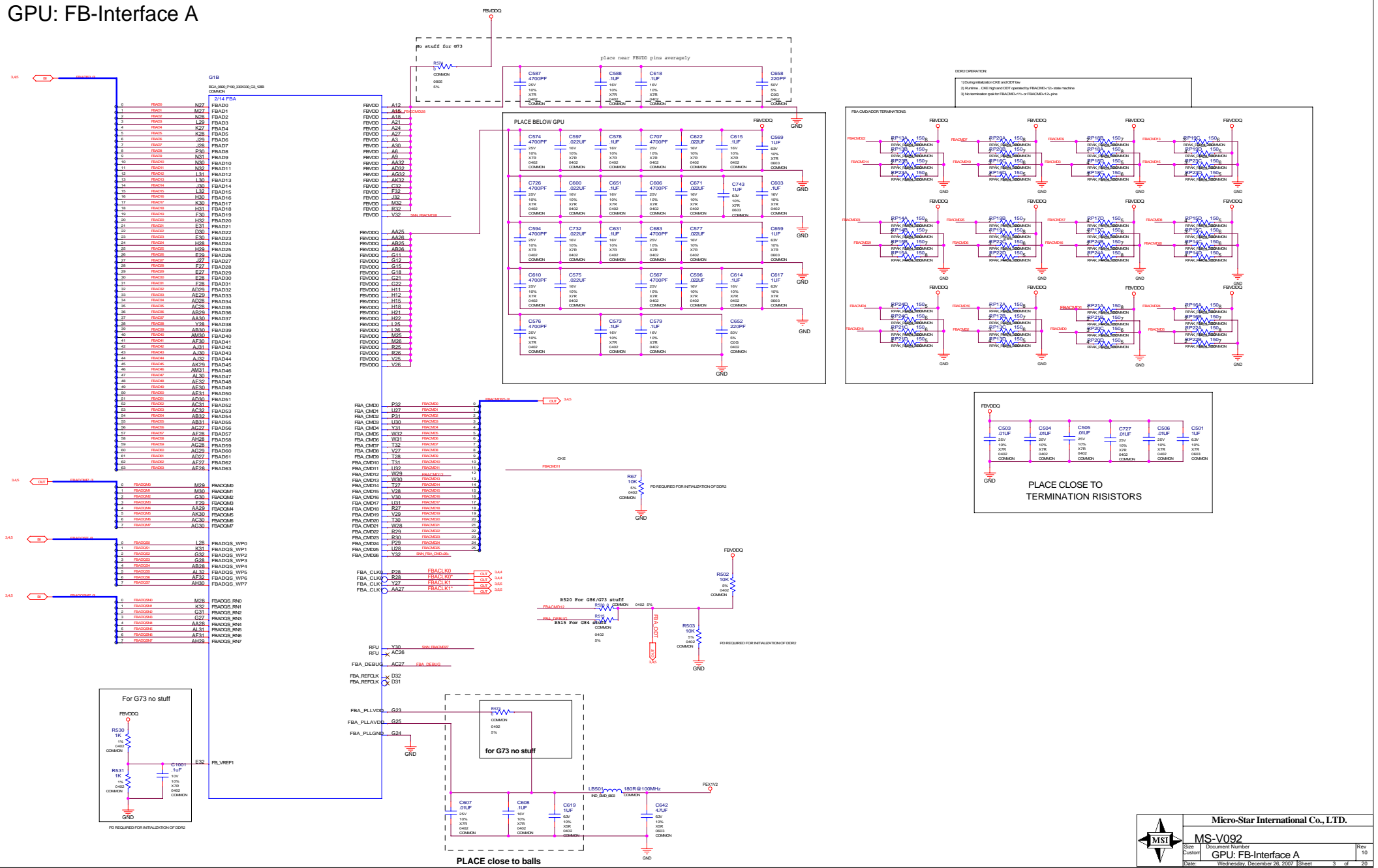
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16X PCIe Interface

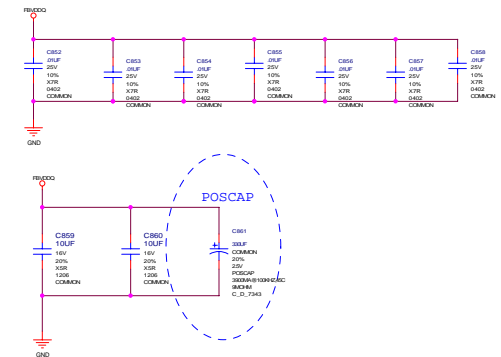
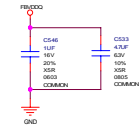
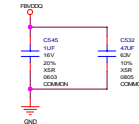
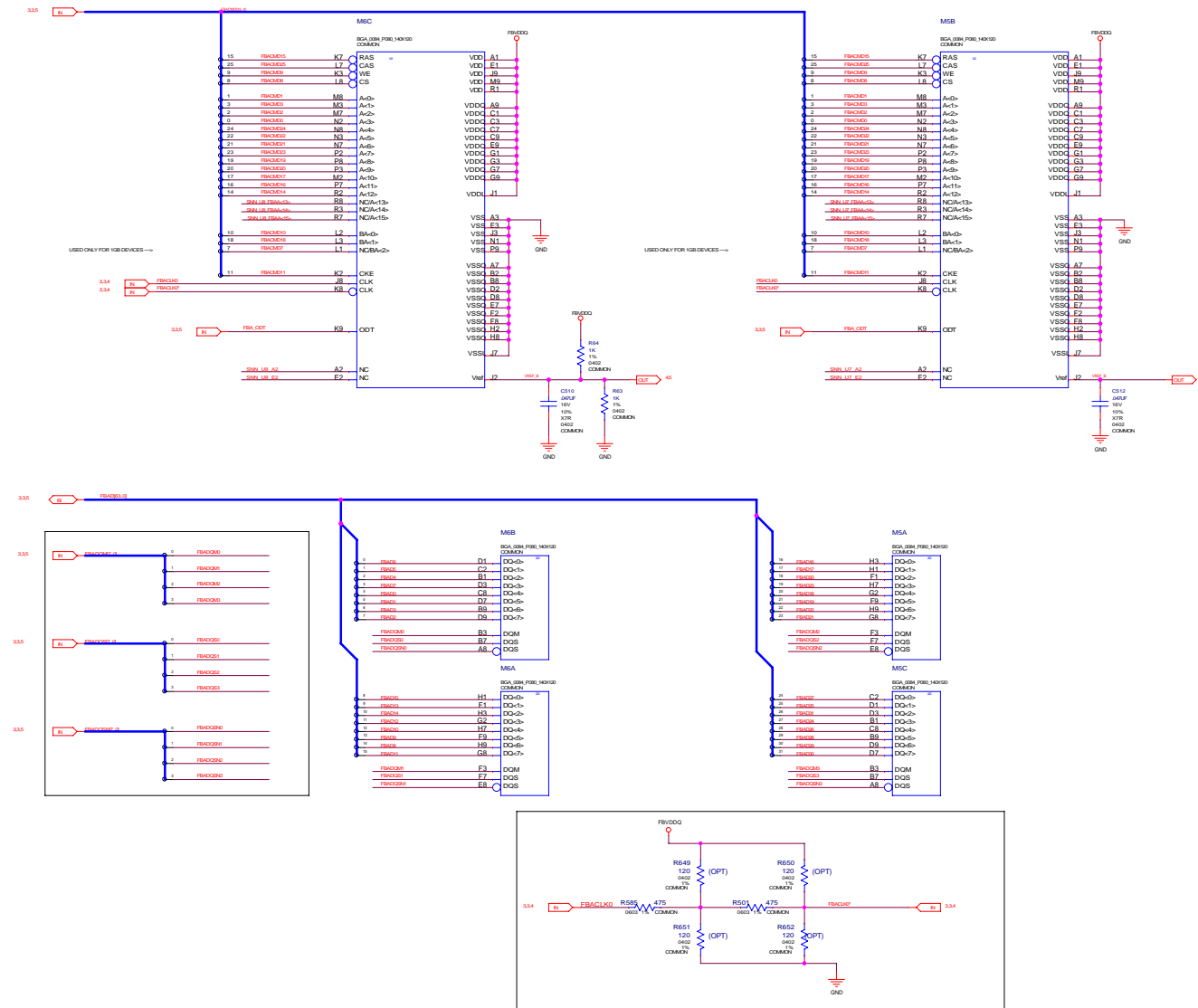


GPU: FB-Interface A



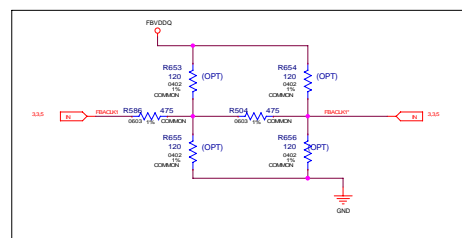
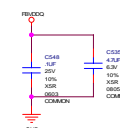
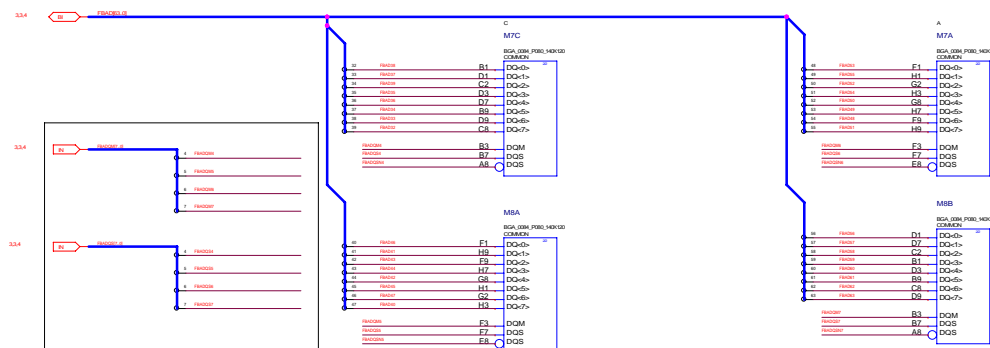
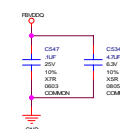
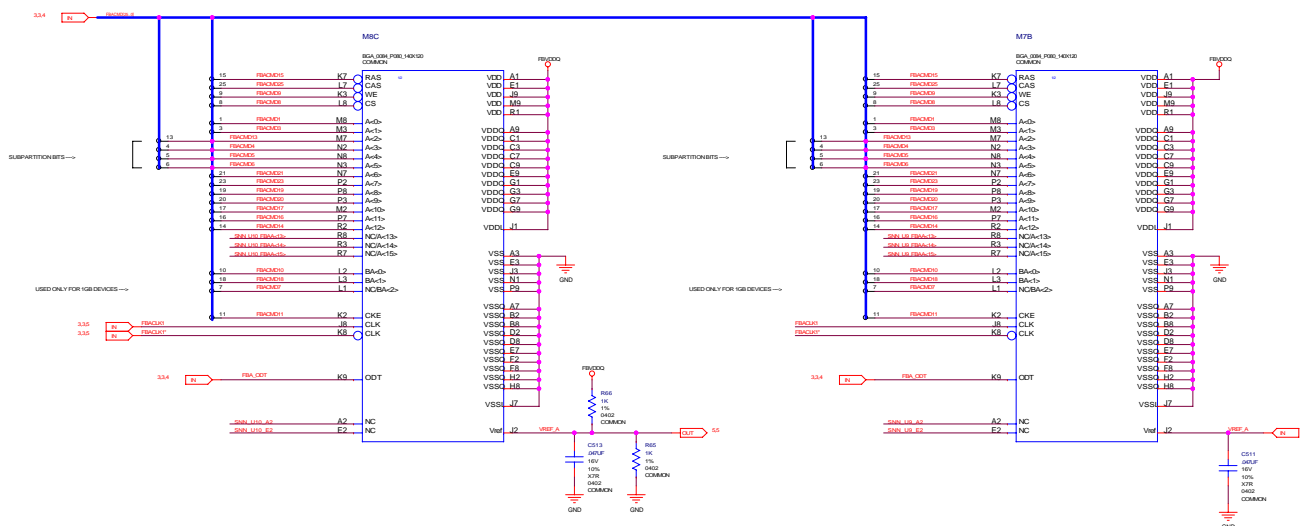
FBA MEMORY 1st bank 0..31

PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY

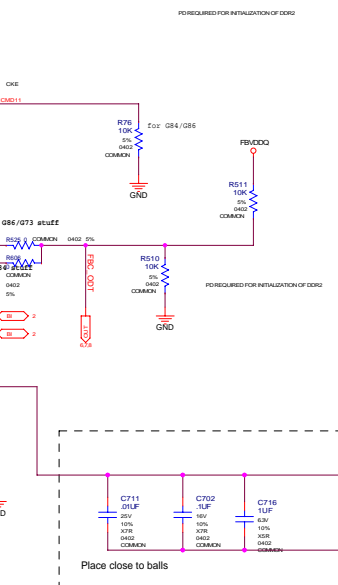
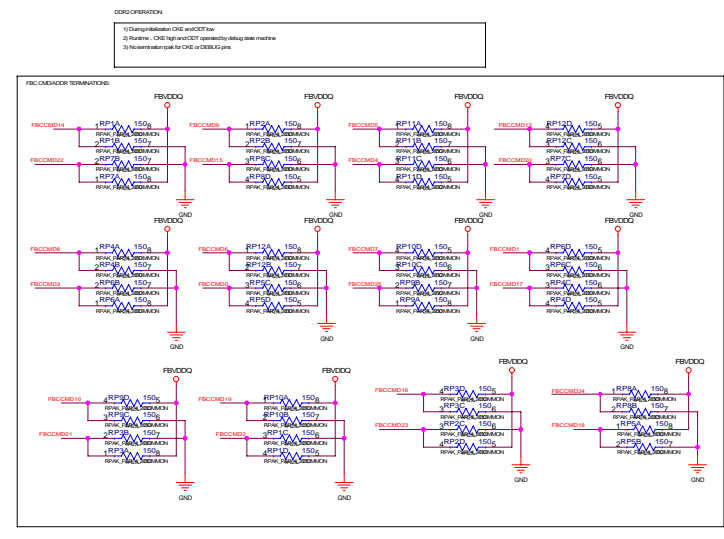
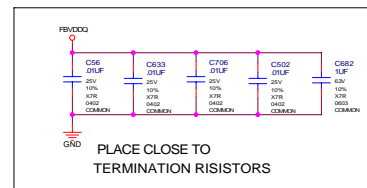
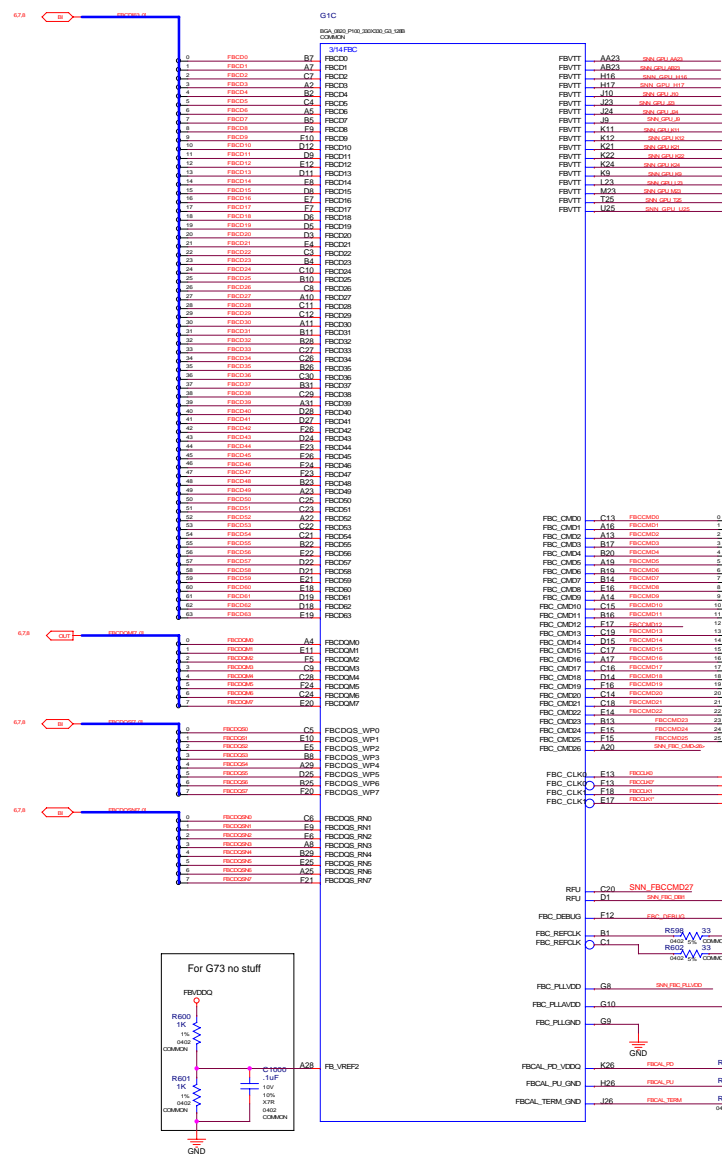


FBA MEMORY 1st bank 32..63

PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY



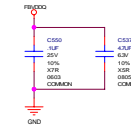
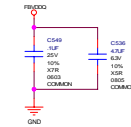
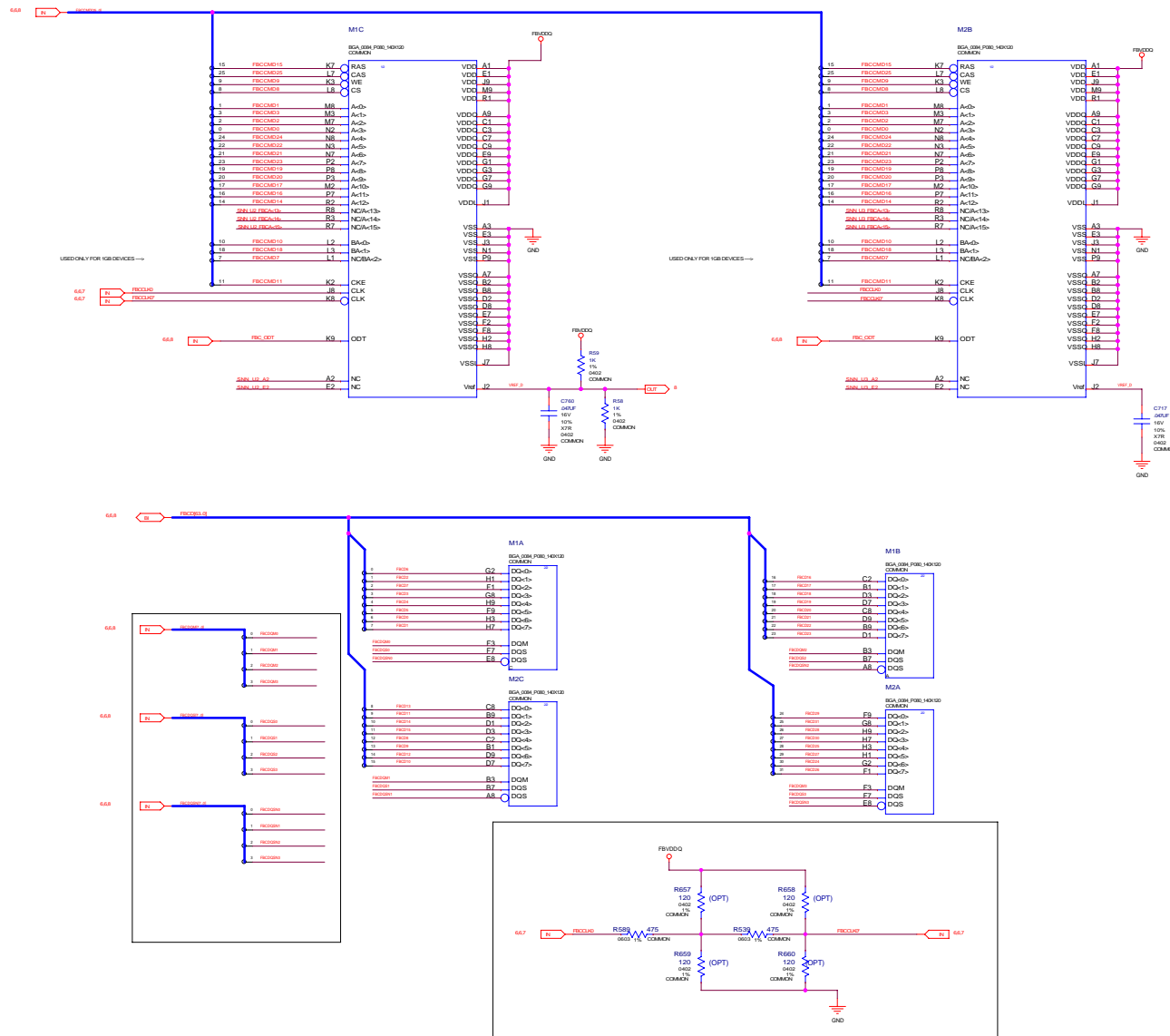
GPU: FB-Interface C



	NET	DIFFPAIR	MIN_LINE_WIDTH	WAVELENGTH	WAVELENGTH	WAVELENGTH
6.77	1	TRC1010	TRC1010	1000	1000	1
6.77	2	TRC1011	TRC1011	1000	1000	1
6.8	3	TRC1012	TRC1012	1000	1000	1
6.8	4	TRC1013	TRC1013	1000	1000	1
6.8	5	TRC1014	TRC1014	1000	1000	1
6.8	6	TRC1015	TRC1015	1000	1000	1
6.8	7	TRC1016	TRC1016	1000	1000	1
6.8	8	TRC1017	TRC1017	1000	1000	1
6.8	9	TRC1018	TRC1018	1000	1000	1
6.8	10	TRC1019	TRC1019	1000	1000	1
6.8	11	TRC1020	TRC1020	1000	1000	1
6.8	12	TRC1021	TRC1021	1000	1000	1
6.8	13	TRC1022	TRC1022	1000	1000	1
6.8	14	TRC1023	TRC1023	1000	1000	1
6.8	15	TRC1024	TRC1024	1000	1000	1
6.8	16	TRC1025	TRC1025	1000	1000	1
6.8	17	TRC1026	TRC1026	1000	1000	1
6.8	18	TRC1027	TRC1027	1000	1000	1
6.8	19	TRC1028	TRC1028	1000	1000	1
6.8	20	TRC1029	TRC1029	1000	1000	1
6.8	21	TRC1030	TRC1030	1000	1000	1
6.8	22	TRC1031	TRC1031	1000	1000	1
6.8	23	TRC1032	TRC1032	1000	1000	1
6.8	24	TRC1033	TRC1033	1000	1000	1
6.8	25	TRC1034	TRC1034	1000	1000	1
6.8	26	TRC1035	TRC1035	1000	1000	1
6.8	27	TRC1036	TRC1036	1000	1000	1
6.8	28	TRC1037	TRC1037	1000	1000	1
6.8	29	TRC1038	TRC1038	1000	1000	1
6.8	30	TRC1039	TRC1039	1000	1000	1
6.8	31	TRC1040	TRC1040	1000	1000	1
6.8	32	TRC1041	TRC1041	1000	1000	1
6.8	33	TRC1042	TRC1042	1000	1000	1
6.8	34	TRC1043	TRC1043	1000	1000	1
6.8	35	TRC1044	TRC1044	1000	1000	1
6.8	36	TRC1045	TRC1045	1000	1000	1
6.8	37	TRC1046	TRC1046	1000	1000	1
6.8	38	TRC1047	TRC1047	1000	1000	1
6.8	39	TRC1048	TRC1048	1000	1000	1
6.8	40	TRC1049	TRC1049	1000	1000	1
6.8	41	TRC1050	TRC1050	1000	1000	1
6.8	42	TRC1051	TRC1051	1000	1000	1
6.8	43	TRC1052	TRC1052	1000	1000	1
6.8	44	TRC1053	TRC1053	1000	1000	1
6.8	45	TRC1054	TRC1054	1000	1000	1
6.8	46	TRC1055	TRC1055	1000	1000	1
6.8	47	TRC1056	TRC1056	1000	1000	1
6.8	48	TRC1057	TRC1057	1000	1000	1
6.8	49	TRC1058	TRC1058	1000	1000	1
6.8	50	TRC1059	TRC1059	1000	1000	1
6.8	51	TRC1060	TRC1060	1000	1000	1
6.8	52	TRC1061	TRC1061	1000	1000	1
6.8	53	TRC1062	TRC1062	1000	1000	1
6.8	54	TRC1063	TRC1063	1000	1000	1
6.8	55	TRC1064	TRC1064	1000	1000	1
6.8	56	TRC1065	TRC1065	1000	1000	1
6.8	57	TRC1066	TRC1066	1000	1000	1
6.8	58	TRC1067	TRC1067	1000	1000	1
6.8	59	TRC1068	TRC1068	1000	1000	1
6.8	60	TRC1069	TRC1069	1000	1000	1
6.8	61	TRC1070	TRC1070	1000	1000	1
6.8	62	TRC1071	TRC1071	1000	1000	1
6.8	63	TRC1072	TRC1072	1000	1000	1
6.8	64	TRC1073	TRC1073	1000	1000	1
6.8	65	TRC1074	TRC1074	1000	1000	1
6.8	66	TRC1075	TRC1075	1000	1000	1
6.8	67	TRC1076	TRC1076	1000	1000	1
6.8	68	TRC1077	TRC1077	1000	1000	1
6.8	69	TRC1078	TRC1078	1000	1000	1
6.8	70	TRC1079	TRC1079	1000	1000	1
6.8	71	TRC1080	TRC1080	1000	1000	1
6.8	72	TRC1081	TRC1081	1000	1000	1
6.8	73	TRC1082	TRC1082	1000	1000	1
6.8	74	TRC1083	TRC1083	1000	1000	1
6.8	75	TRC1084	TRC1084	1000	1000	1
6.8	76	TRC1085	TRC1085	1000	1000	1
6.8	77	TRC1086	TRC1086	1000	1000	1
6.8	78	TRC1087	TRC1087	1000	1000	1
6.8	79	TRC1088	TRC1088	1000	1000	1
6.8	80	TRC1089	TRC1089	1000	1000	1
6.8	81	TRC1090	TRC1090	1000	1000	1
6.8	82	TRC1091	TRC1091	1000	1000	1
6.8	83	TRC1092	TRC1092	1000	1000	1
6.8	84	TRC1093	TRC1093	1000	1000	1
6.8	85	TRC1094	TRC1094	1000	1000	1
6.8	86	TRC1095	TRC1095	1000	1000	1
6.8	87	TRC1096	TRC1096	1000	1000	1
6.8	88	TRC1097	TRC1097	1000	1000	1
6.8	89	TRC1098	TRC1098	1000	1000	1
6.8	90	TRC1099	TRC1099	1000	1000	1
6.8	91	TRC1100	TRC1100	1000	1000	1
6.8	92	TRC1101	TRC1101	1000	1000	1
6.8	93	TRC1102	TRC1102	1000	1000	1
6.8	94	TRC1103	TRC1103	1000	1000	1
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6.8	97	TRC1106	TRC1106	1000	1000	1
6.8	98	TRC1107	TRC1107	1000	1000	1
6.8	99	TRC1108	TRC1108	1000	1000	1
6.8	100	TRC1109	TRC1109	1000	1000	1
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6.8	102	TRC1111	TRC1111	1000	1000	1
6.8	103	TRC1112	TRC1112	1000	1000	1
6.8	104	TRC1113	TRC1113	1000	1000	1
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6.8	106	TRC1115	TRC1115	1000	1000	1
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6.8	108	TRC1117	TRC1117	1000	1000	1
6.8	109	TRC1118	TRC1118	1000	1000	1
6.8	110	TRC1119	TRC1119	1000	1000	1
6.8	111	TRC1120	TRC1120	1000	1000	1
6.8	112	TRC1121	TRC1121	1000	1000	1
6.8	113	TRC1122	TRC1122	1000	1000	1
6.8	114	TRC1123	TRC1123	1000	1000	1
6.8	115	TRC1124	TRC1124	1000	1000	1
6.8	116	TRC1125	TRC1125	1000	1000	1
6.8	117	TRC1126	TRC1126	1000	1000	1
6.8	118	TRC1127	TRC1127	1000	1000	1
6.8	119	TRC1128	TRC1128	1000	1000	1
6.8	120	TRC1129	TRC1129	1000	1000	1
6.8	121	TRC1130	TRC1130	1000	1000	1
6.8	122	TRC1131	TRC1131	1000	1000	1
6.8	123	TRC1132	TRC1132	1000	1000	1
6.8	124	TRC1133	TRC1133	1000	1000	1
6.8	125	TRC1134	TRC1134	1000	1000	1
6.8	126	TRC1135	TRC1135	1000	1000	1
6.8	127	TRC1136	TRC1136	1000	1000	1
6.8	128	TRC1137	TRC1137	1000	1000	1
6.8	129	TRC1138	TRC1138	1000	1000	1
6.8	130	TRC1139	TRC1139	1000	1000	1
6.8	131	TRC1140	TRC1140	1000	1000	1
6.8	132	TRC1141	TRC1141	1000	1000	1
6.8	133	TRC1142	TRC1142	1000	1000	1
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6.8	136	TRC1145	TRC1145	1000	1000	1
6.8	137	TRC1146	TRC1146	1000	1000	1
6.8	138	TRC1147	TRC1147	1000	1000	1
6.8	139	TRC1148	TRC1148	1000	1000	1
6.8	140	TRC1149	TRC1149	1000	1000	1
6.8	141	TRC1150	TRC1150	1000	1000	1
6.8	142	TRC1151	TRC1151	1000	1000	1
6.8	143	TRC1152	TRC1152	1000	1000	1
6.8	144	TRC1153	TRC1153	1000	1000	1
6.8	145	TRC1154	TRC1154	1000	1000	1
6.8	146	TRC1155	TRC1155	1000	1000	1
6.8	147	TRC1156	TRC1156	1000	1000	1
6.8	148	TRC1157	TRC1157	1000	1000	1
6.8	149	TRC1158	TRC1158	1000	1000	1
6.8	150	TRC1159	TRC1159	1000	1000	1
6.8	151	TRC1160	TRC1160	1000	1000	1
6.8	152	TRC1161	TRC1161	1000	1000	1
6.8	153	TRC1162	TRC1162	1000	1000	1
6.8	154	TRC1163	TRC1163	1000	1000	1
6.8	155	TRC1164	TRC1164	1000	1000	1
6.8	156	TRC1165	TRC1165	1000	1000	1
6.8	157	TRC1166	TRC1166	1000	1000	1
6.8	158	TRC1167	TRC1167	1000	1000	1
6.8	159	TRC1168	TRC1168	1000	1000	1
6.8	160	TRC1169	TRC1169	1000	1000	1
6.8	161	TRC1170	TRC1170	1000	1000	1
6.8	162	TRC1171	TRC1171	1000	1000	1
6.8	163	TRC1172	TRC1172	1000	1000	1
6.8	164	TRC1173	TRC1173	1000	1000	1
6.8	165	TRC1174	TRC1174	1000	1000	1
6.8	166	TRC1175	TRC1175	1000	1000	1
6.8	167	TRC1176	TRC1176	1000	1000	1
6.8	168	TRC1177	TRC1177	1000	1000	1
6.8	169	TRC1178	TRC1178	1000	1000	1
6.8	170	TRC1179	TRC1179	1000	1000	1
6.8	171	TRC1180	TRC1180	1000	1000	1
6.8	172	TRC1181	TRC1181	1000	1000	1
6.8	173	TRC1182	TRC1182	1000	1000	1
6.8	174	TRC1183	TRC1183	1000	1000	1
6.8	175	TRC1184	TRC1184	1000	1000	1
6.8	176	TRC1185	TRC1185	1000	1000	1
6.8	177	TRC1186	TRC1186	1000	1000	1
6.8	178	TRC1187	TRC1187	1000	1000	1
6.8	179	TRC1188	TRC1188	1000	1000	1
6.8	180	TRC1189	TRC1189	1000	1000	1
6.8	181	TRC1190	TRC1190	1000	1000	1
6.8	182	TRC1191	TRC1191	1000	1000	1
6.8	183	TRC1192	TRC1192	1000	1000	1
6.8	184	TRC1193	TRC1193	1000	1000	1
6.8	185	TRC1194	TRC1194	1000	1000	1
6.8	186	TRC1195	TRC1195	1000	1000	1
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6.8	189	TRC1198</				

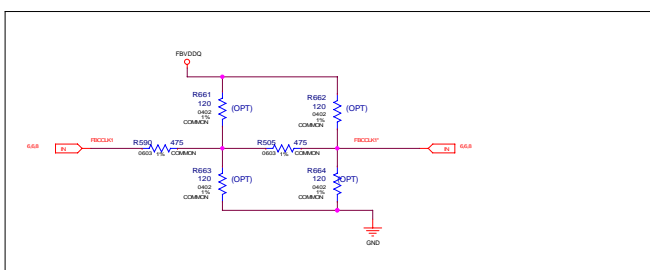
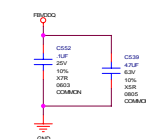
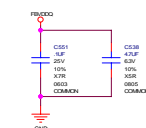
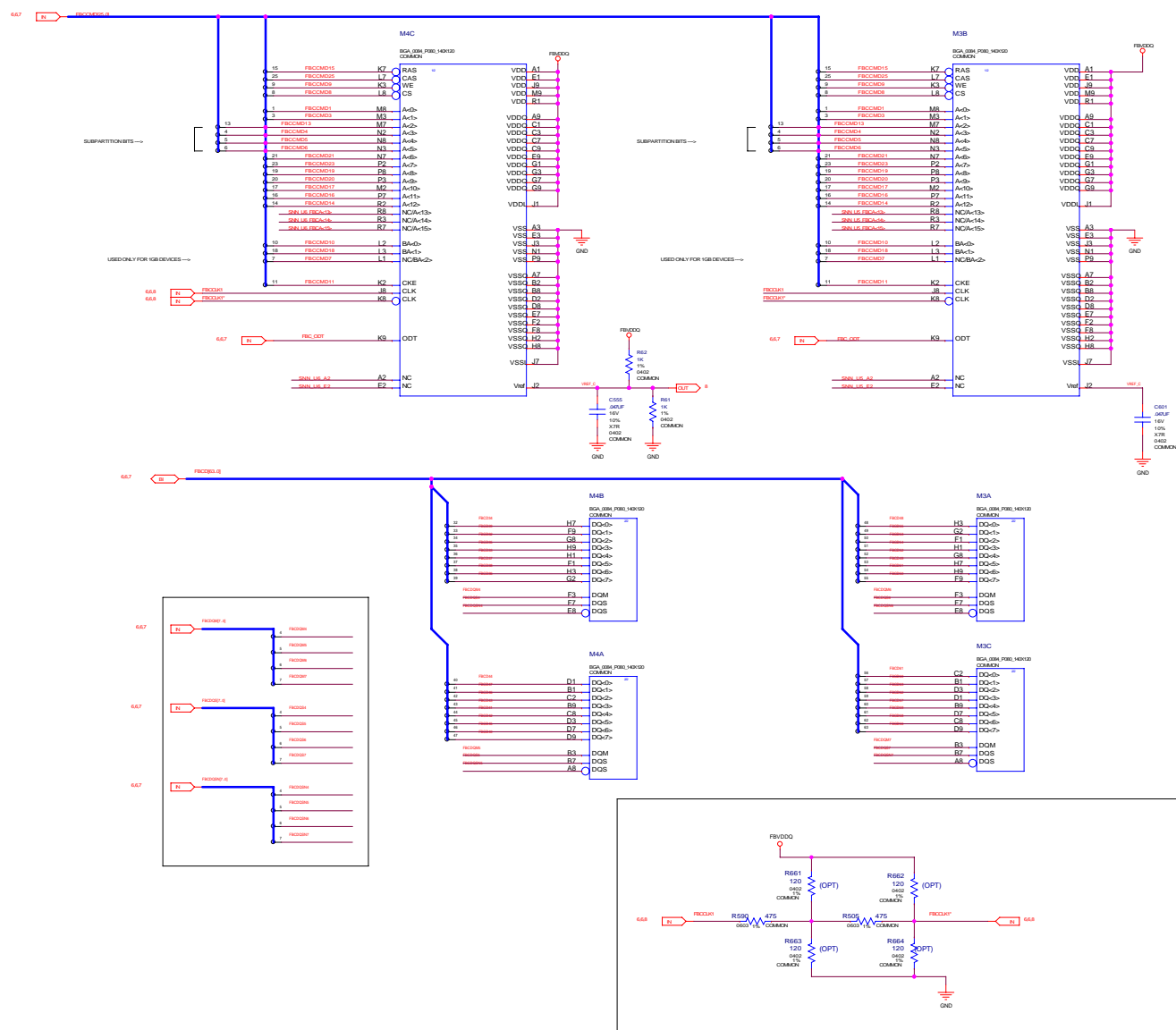
FBC MEMORY 2nd bank 0..31

PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY



FBC MEMORY 2nd bank 32..63

PLACE ALL DISCRETE COMPONENTS AS NEAR AS POSSIBLE TO MEMORY

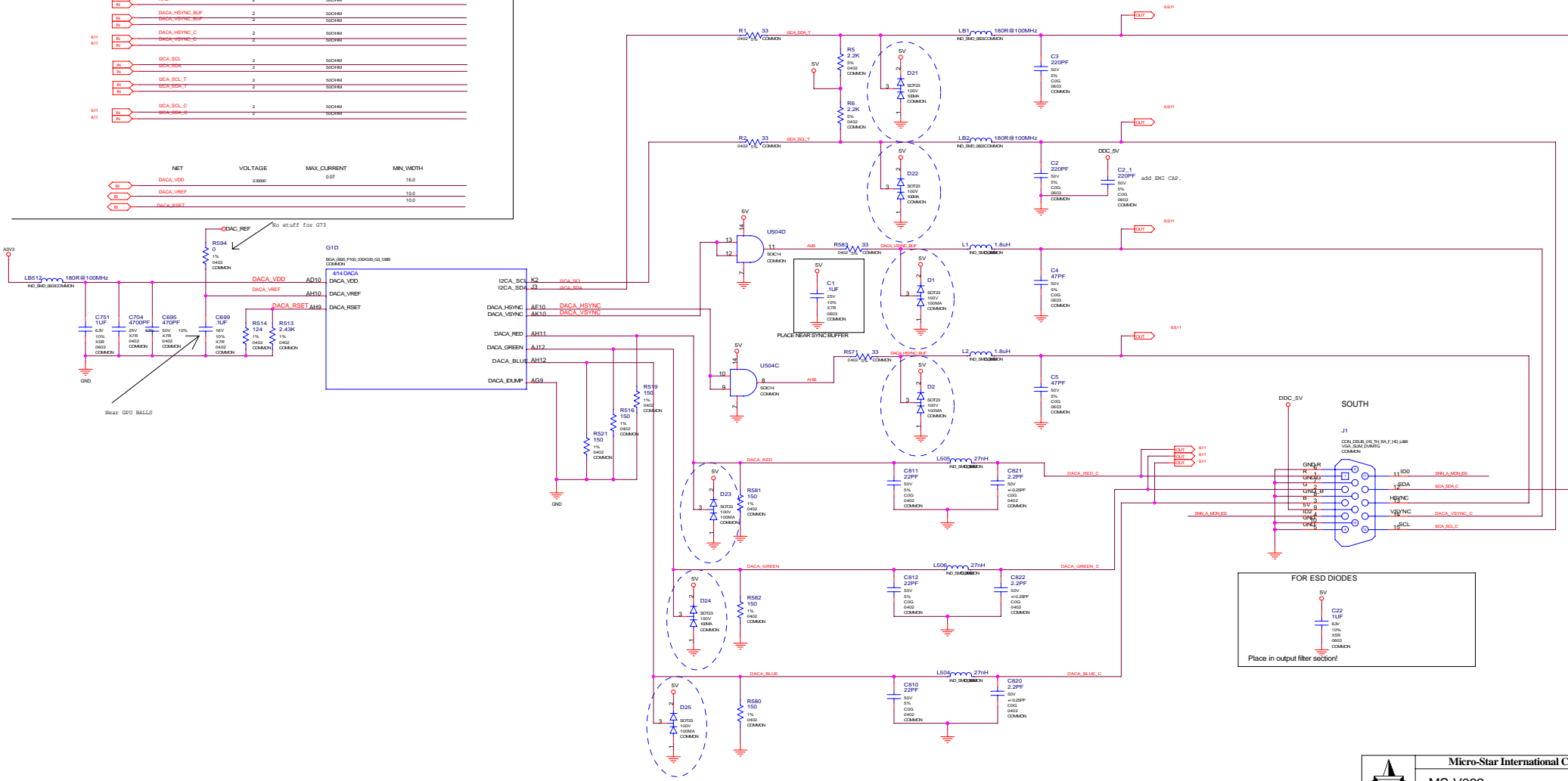


Primary Display (DACA), Slim DB15

DACA NET RULES

NET	NO. CRITICAL	NO. IMPEDANCE	DIFFPAR
DACA_RED	1	50OHM	
DACA_GREEN	1	50OHM	
DACA_BLUE	1	50OHM	
DACA_RED_C	1	50OHM	
DACA_GREEN_C	1	50OHM	
DACA_BLUE_C	1	50OHM	
DACA_HSYNC	2	50OHM	
DACA_VSYNC	2	50OHM	
AVS	2	50OHM	
AVH	2	50OHM	
DACA_HSYNC_BUF	2	50OHM	
DACA_VSYNC_BUF	2	50OHM	
DACA_HSYNC_C	2	50OHM	
DACA_VSYNC_C	2	50OHM	
DACA_SCL	2	50OHM	
DACA_SDA	2	50OHM	
DACA_SCL_T	2	50OHM	
DACA_SDA_T	2	50OHM	
DACA_SCL_C	2	50OHM	
DACA_SDA_C	2	50OHM	
DACA_VDD	33000	0.07	
DACA_VREF		10.0	
DACA_RESET		10.0	

DACA RGB-FILTER



Secondary Display (DACC), DB15

DACC NET RULES

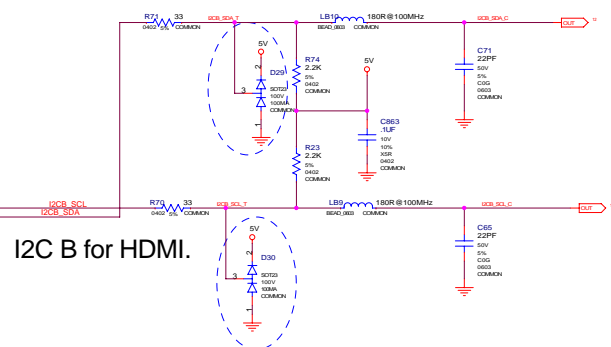
NET	NV_CRITICAL	NV_IMPEDANCE	DIFFPAIR
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IN	OUT	DATA	STATUS
IN	DACC_RED	1	SDRM
IN	DACC_GREEN	1	SDRM
IN	DACC_BLUE	1	SDRM
IN	DACC_RED_C	1	SDRM
IN	DACC_GREEN_C	1	SDRM
IN	DACC_BLUE_C	1	SDRM

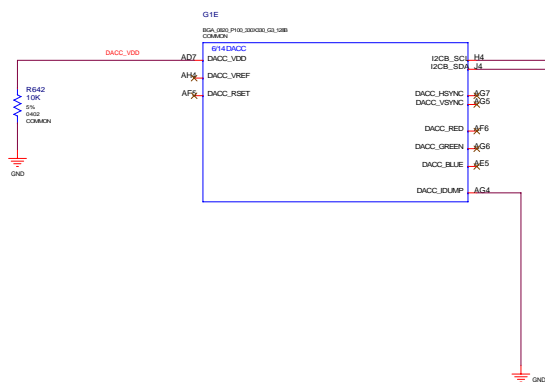
IN	DACC_HEYINC	2	SDRAM
IN	SDACC_VSYN0	2	SDCERR
IN	CYB	2	SDCERR
IN	CDB	2	SDCERR
IN	DACC_HEYINC_BUF	2	SDRAM
IN	SDACC_VSYN0_BUF	2	SDCERR
IN	DACC_HEYINC_C	2	SDRAM
IN	SDACC_VSYN0_C	2	SDCERR

IN	GCB_SCL	2	SDRAM
IN	GCB_SDA	2	SDRAM
IN	GCB_SCL_T	2	SDRAM
IN	GCB_SDA_T	2	SDRAM
IN	GCB_SCL_C	2	SDRAM
IN	GCB_SDA_C	2	SDRAM

	NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
U10	DACC_VDD	3.30000	0.54	16.0
U10	DACC_VREF			10.0
U10	DACC_RSET			10.0



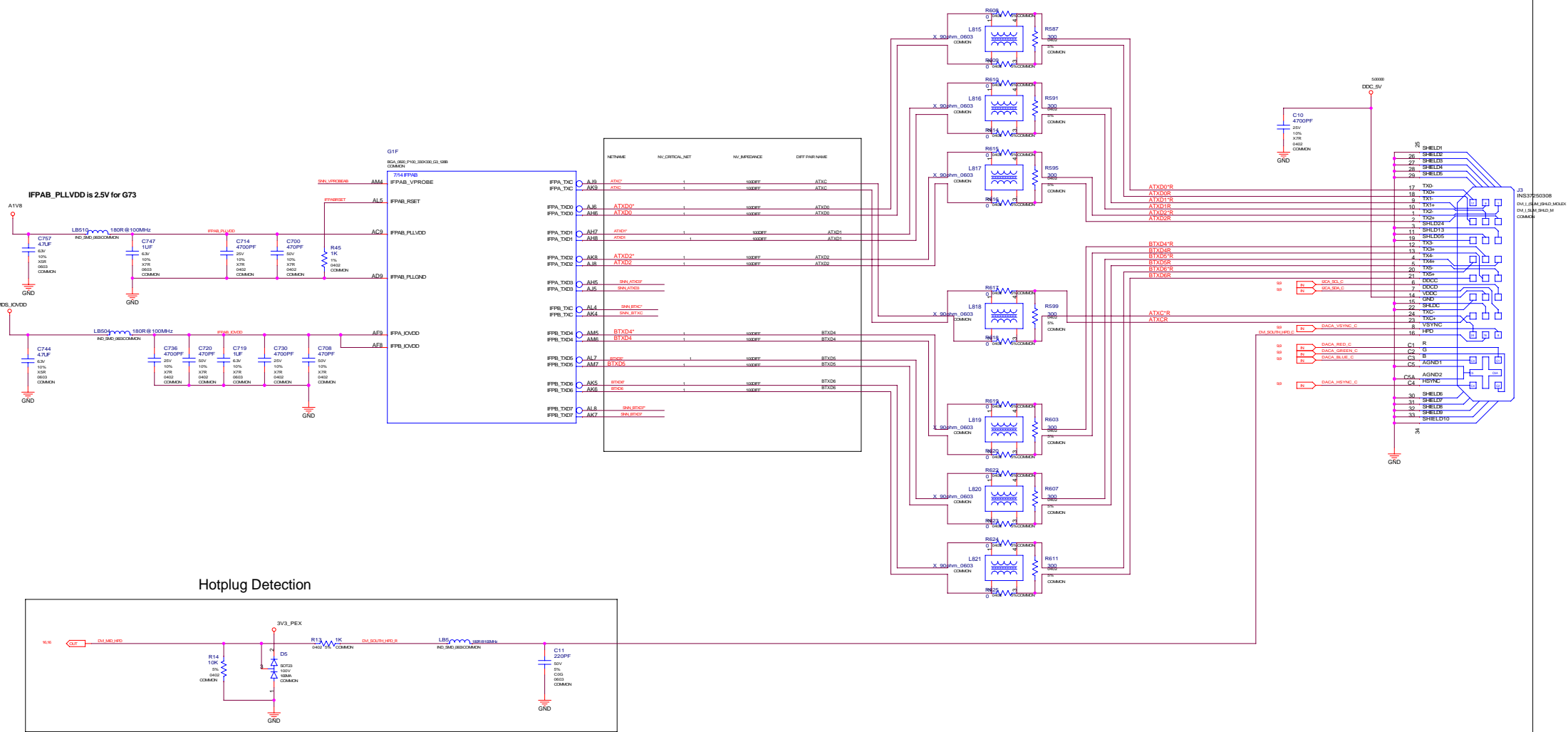
I2C B for HDMI.



INTERNAL TMDS .. LINK A & B

IFPAB NET RULES

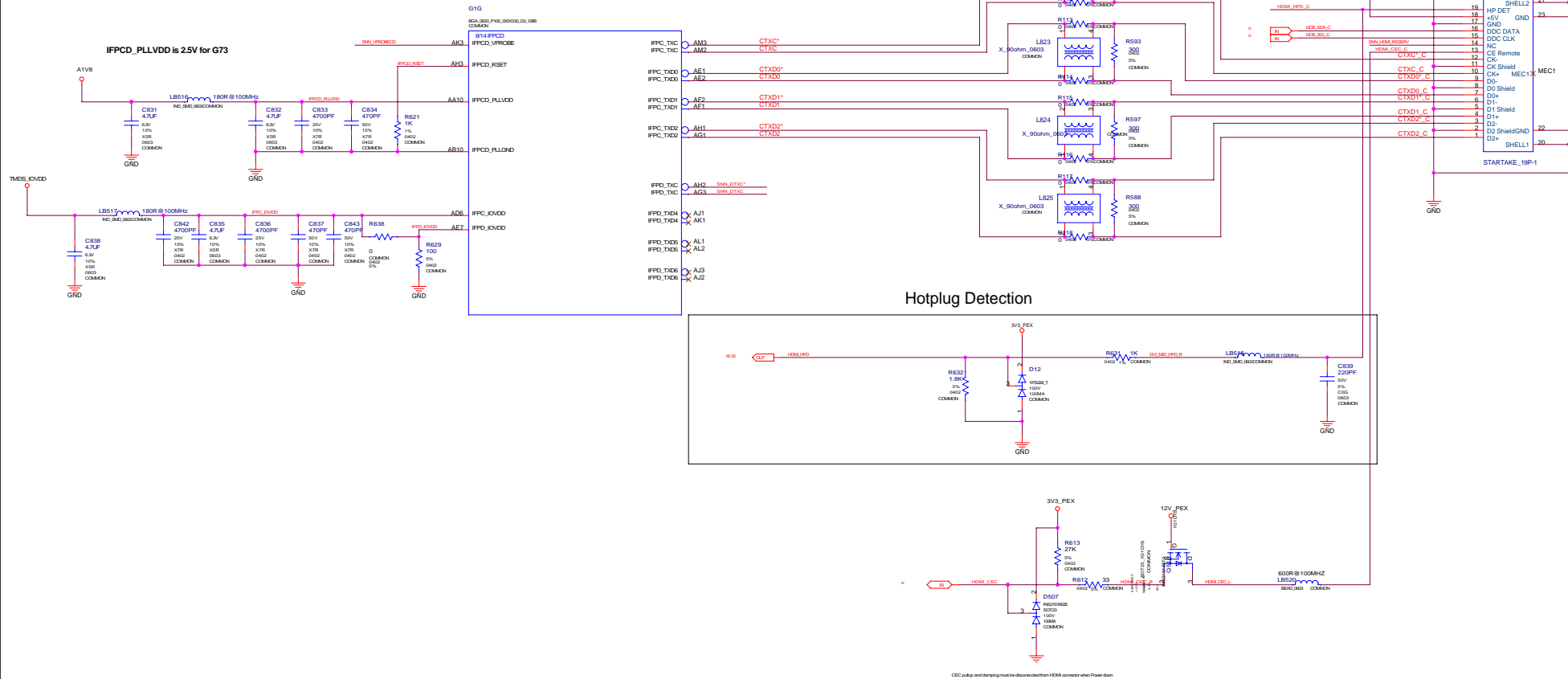
	NET	IV_CRITICAL	IV_IMPEDANCE	DIFF_PAIR
	NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
#1	IFPAB_PU10D	0.0000	0.0	0.0
#2	IFPAB_PU0D	0.0000	0.24	16.0
#3	IFPABNET			12.0
#4	DIV_SOUTH_HPO_C	1	500HM	
#5	SW_SOUTH_HPO_C	1	500HM	



INTERNAL TMDS .. LINK C

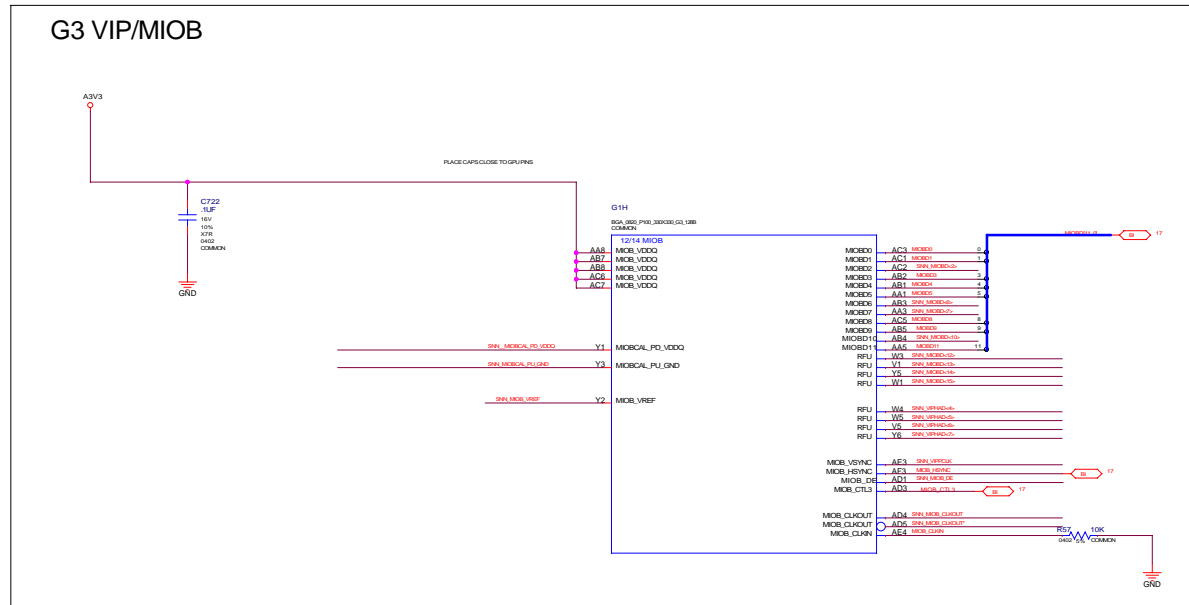
IFPAB NET RULES

	NET	NO_CRITICAL	NO_IMPEDANCE	DIFFPAIR
	IFPC0_RSET		0.03M	
	DIV_0M_HFPO_C	1	0.03M	
	DIV_0M_HFPO_R	1	0.03M	
	NET			
	IFPC0_PU10D	3.0000	0.04	10.0
	IFPC0_PU10D	3.0000	0.04	10.0
	IFPC0_PU10D	10.0000	0	10.0

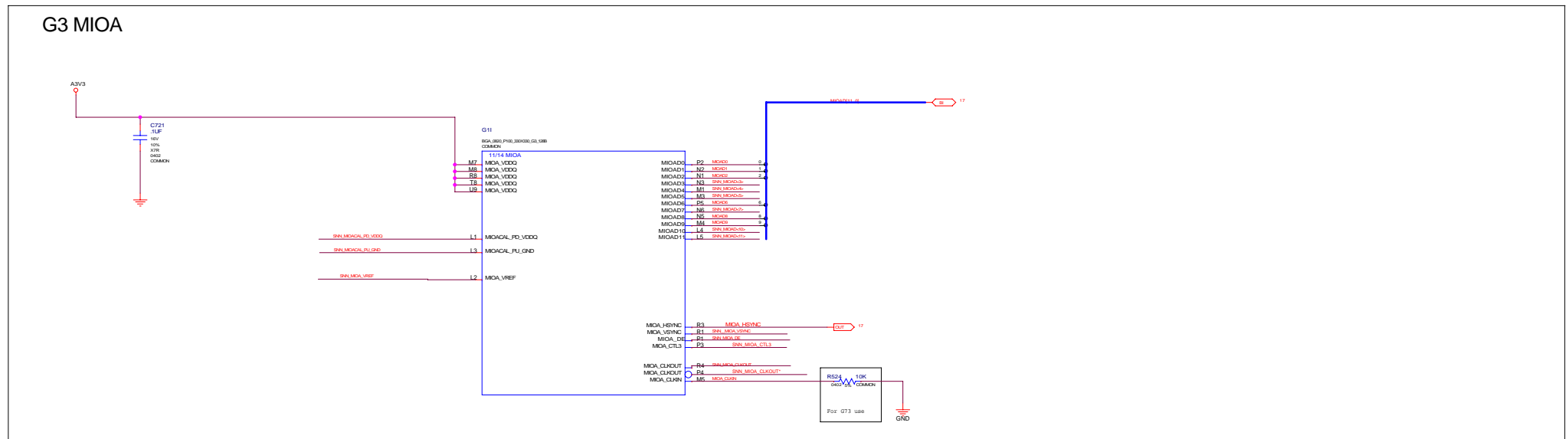


G3 VIP/MIOB/MIOA

G3 VIP/MIOB



G3 MIOA



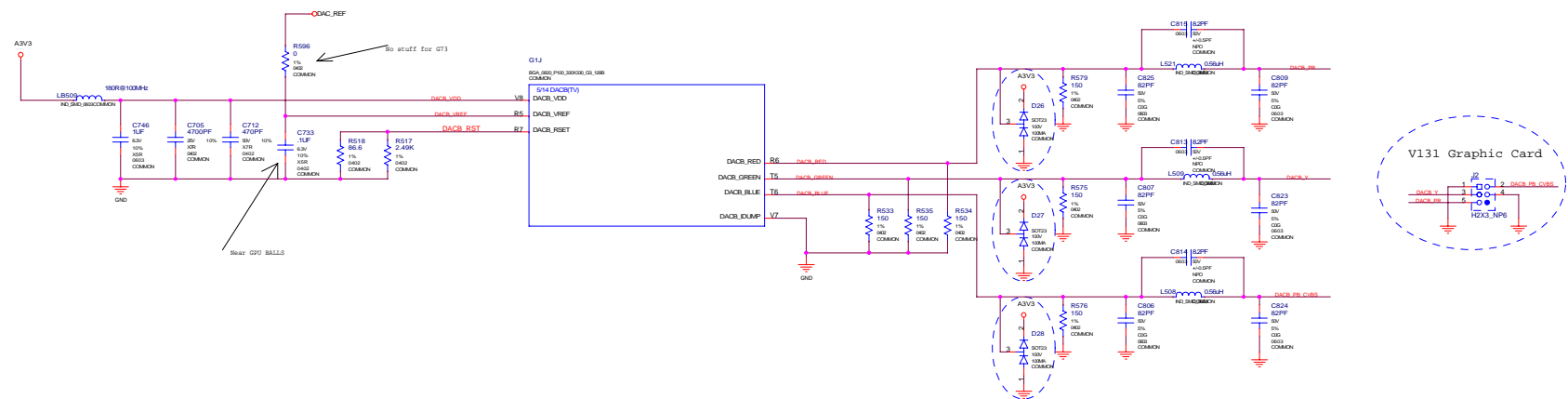
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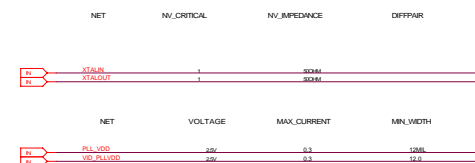
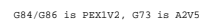
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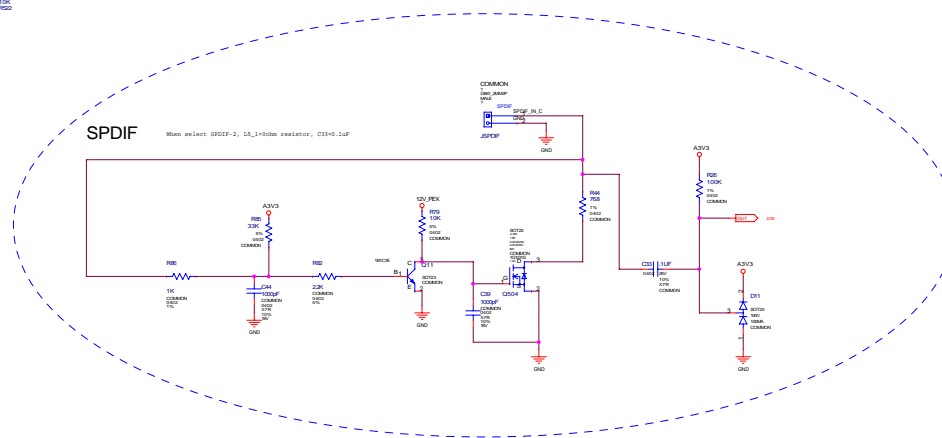
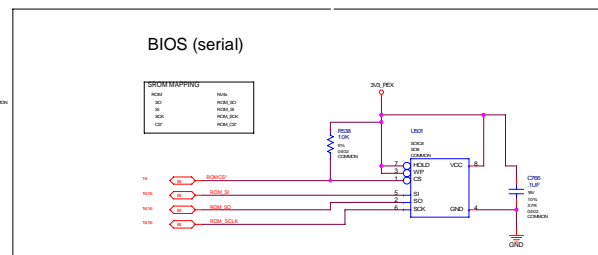
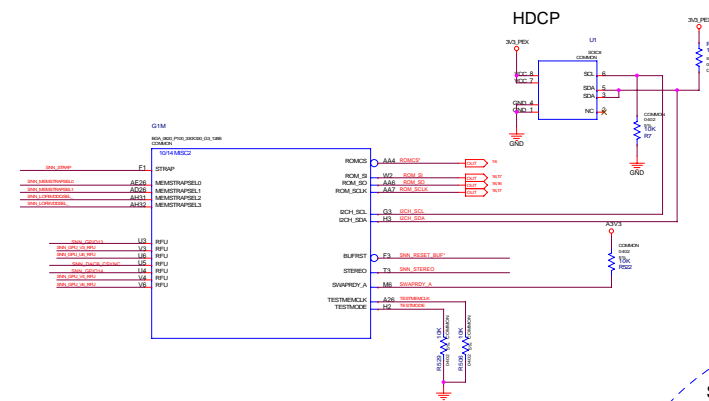
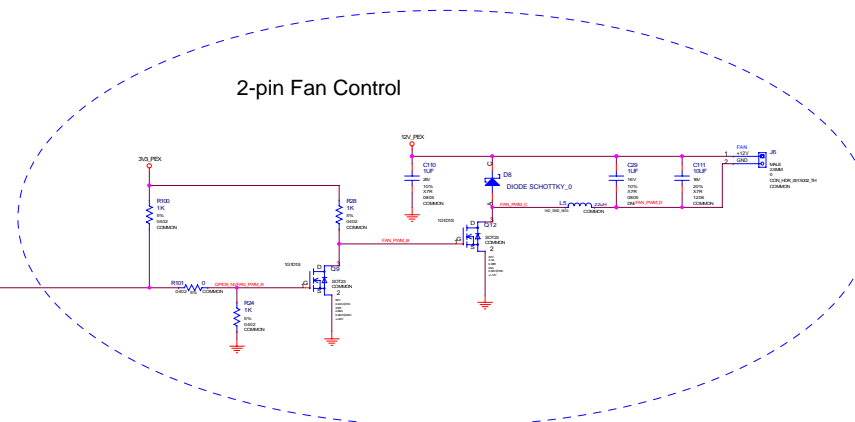
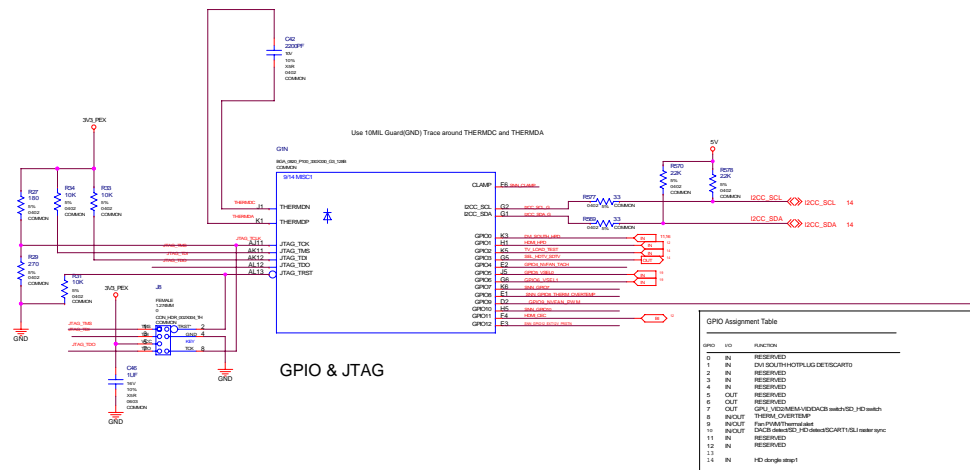
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DACB .. MiniDIN VIDEO OUT CONNECTOR



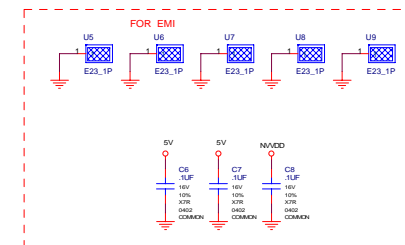


GPIO / JTAG / HDCP / BIOS / SPDIF



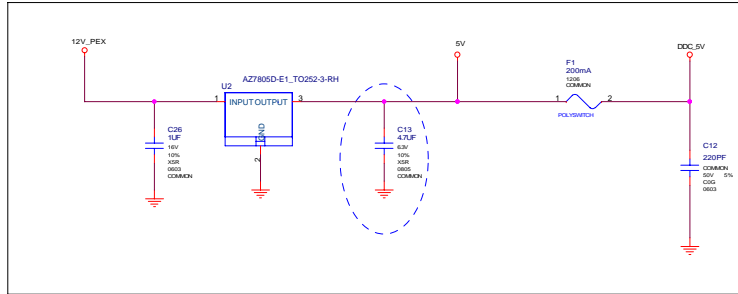


Bit Signal	VALUE_ID	VALUES
17 FIDC_PLL_EN_TERM[0]		0 (default - internal term on)
12 3G0G_PADCFG_LUT_ADDR[0]		
13 3G0G_PADCFG_LUT_ADDR[1]		
14 3G0G_PADCFG_LUT_ADDR[2]		
3G0G_PADCFG_LUT_ADDR[3]		
0 FDC_LOAD[0]	0	0 (enable)
0 FDC_LOAD[1]	1	1 (enable (default))
0 FDC_LOAD[2]	0	0 (enable (default))
0 FDC_LOAD[3]	1	1 (enable)
0 FDC_LOAD[4]	0	0 (enable (default))
0 FDC_LOAD[5]	1	1 (enable)
0 FDC_LOAD[6]	0	0 (enable (default))
0 FDC_LOAD[7]	1	1 (enable)
0 FDC_LOAD[8]	0	0 (enable (default))
0 FDC_LOAD[9]	1	1 (enable)
0 FDC_LOAD[10]	0	0 (enable (default))
0 FDC_LOAD[11]	1	1 (enable)
0 FDC_LOAD[12]	0	0 (enable (default))
0 FDC_LOAD[13]	1	1 (enable)
0 FDC_LOAD[14]	0	0 (enable (default))
0 FDC_LOAD[15]	1	1 (enable)
0 FDC_LOAD[16]	0	0 (enable (default))
0 FDC_LOAD[17]	1	1 (enable)
0 FDC_LOAD[18]	0	0 (enable (default))
0 FDC_LOAD[19]	1	1 (enable)
0 FDC_LOAD[20]	0	0 (enable (default))
0 FDC_LOAD[21]	1	1 (enable)
0 FDC_LOAD[22]	0	0 (enable (default))
0 FDC_LOAD[23]	1	1 (enable)
0 FDC_LOAD[24]	0	0 (enable (default))
0 FDC_LOAD[25]	1	1 (enable)
0 FDC_LOAD[26]	0	0 (enable (default))
0 FDC_LOAD[27]	1	1 (enable)
0 FDC_LOAD[28]	0	0 (enable (default))
0 FDC_LOAD[29]	1	1 (enable)
0 FDC_LOAD[30]	0	0 (enable (default))
0 FDC_LOAD[31]	1	1 (enable)
0 FDC_LOAD[32]	0	0 (enable (default))
0 FDC_LOAD[33]	1	1 (enable)
0 FDC_LOAD[34]	0	0 (enable (default))
0 FDC_LOAD[35]	1	1 (enable)
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0 FDC_LOAD[40]	0	0 (enable (default))
0 FDC_LOAD[41]	1	1 (enable)
0 FDC_LOAD[42]	0	0 (enable (default))
0 FDC_LOAD[43]	1	1 (enable)
0 FDC_LOAD[44]	0	0 (enable (default))
0 FDC_LOAD[45]	1	1 (enable)
0 FDC_LOAD[46]	0	0 (enable (default))
0 FDC_LOAD[47]	1	1 (enable)
0 FDC_LOAD[48]	0	0 (enable (default))
0 FDC_LOAD[49]	1	1 (enable)
0 FDC_LOAD[50]	0	0 (enable (default))
0 FDC_LOAD[51]	1	1 (enable)
0 FDC_LOAD[52]	0	0 (enable (default))
0 FDC_LOAD[53]	1	1 (enable)
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0 FDC_LOAD[56]	0	0 (enable (default))
0 FDC_LOAD[57]	1	1 (enable)
0 FDC_LOAD[58]	0	0 (enable (default))
0 FDC_LOAD[59]	1	1 (enable)
0 FDC_LOAD[60]	0	0 (enable (default))
0 FDC_LOAD[61]	1	1 (enable)
0 FDC_LOAD[62]	0	0 (enable (default))
0 FDC_LOAD[63]	1	1 (enable)
0 FDC_LOAD[64]	0	0 (enable (default))
0 FDC_LOAD[65]	1	1 (enable)
0 FDC_LOAD[66]	0	0 (enable (default))
0 FDC_LOAD[67]	1	1 (enable)
0 FDC_LOAD[68]	0	0 (enable (default))
0 FDC_LOAD[69]	1	1 (enable)
0 FDC_LOAD[70]	0	0 (enable (default))
0 FDC_LOAD[71]	1	1 (enable)
0 FDC_LOAD[72]	0	0 (enable (default))
0 FDC_LOAD[73]	1	1 (enable)
0 FDC_LOAD[74]	0	0 (enable (default))
0 FDC_LOAD[75]	1	1 (enable)
0 FDC_LOAD[76]	0	0 (enable (default))
0 FDC_LOAD[77]	1	1 (enable)
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0 FDC_LOAD[79]	1	1 (enable)
0 FDC_LOAD[80]	0	0 (enable (default))
0 FDC_LOAD[81]	1	1 (enable)
0 FDC_LOAD[82]	0	0 (enable (default))
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0 FDC_LOAD[90]	0	0 (enable (default))
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0 FDC_LOAD[103]	1	1 (enable)
0 FDC_LOAD[104]	0	0 (enable (default))
0 FDC_LOAD[105]	1	1 (enable)
0 FDC_LOAD[106]	0	0 (enable (default))
0 FDC_LOAD[107]	1	1 (enable)
0 FDC_LOAD[108]	0	0 (enable (default))
0 FDC_LOAD[109]	1	1 (enable)
0 FDC_LOAD[110]	0	0 (enable (default))
0 FDC_LOAD[111]	1	1 (enable)
0 FDC_LOAD[112]	0	0 (enable (default))
0 FDC_LOAD[113]	1	1 (enable)
0 FDC_LOAD[114]	0	0 (enable (default))
0 FDC_LOAD[115]	1	1

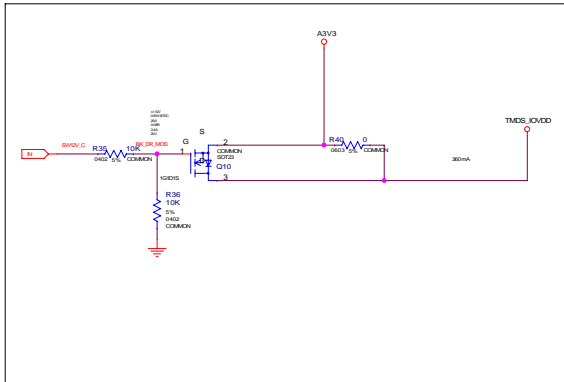


Power Supply:TMDS_IOVDD/A3V3/5V/DAC_REF

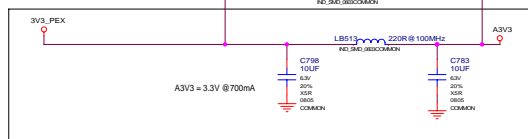
DDC 5V



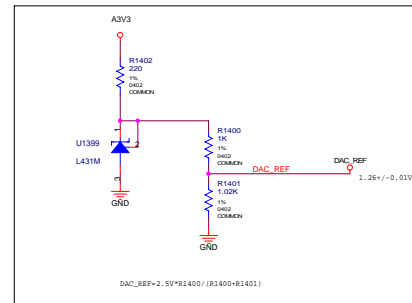
TMDS IO SUPPLY WITH BACKDRIVE PROTECTION



A3V3 Power Supply



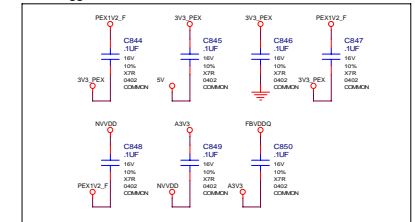
DAC_REF



NETNAME	MAX_CURRENT	MIN_LINE_WIDTH	VOLTAGE
DDC_5V0	0.1	50.0	5000000
A2V0	0.06	25.0	2500000
TIME_0VDD	0.04	20.0	3300000
A3V0	1	20.0	1400000
GND		30.0	0000000



EMC suggestion reserve

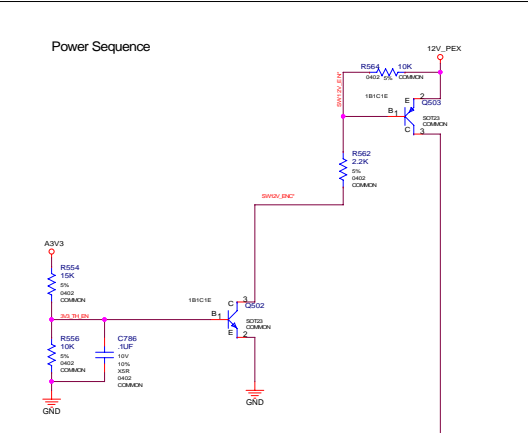


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PowerSupply: NVVDD, A1V8



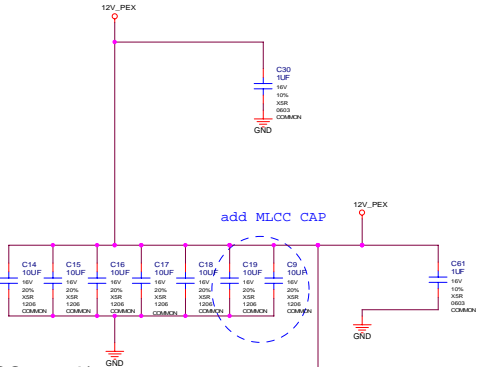
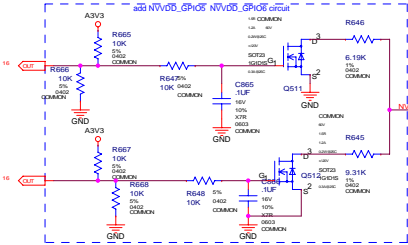
A1V8

$V_{out} = V_{Ref} * (1 + R_{top}/R_{bot})$

$2.48V = 0.8V * (1 + (3.32k/1.07k))$ (ISL6549)

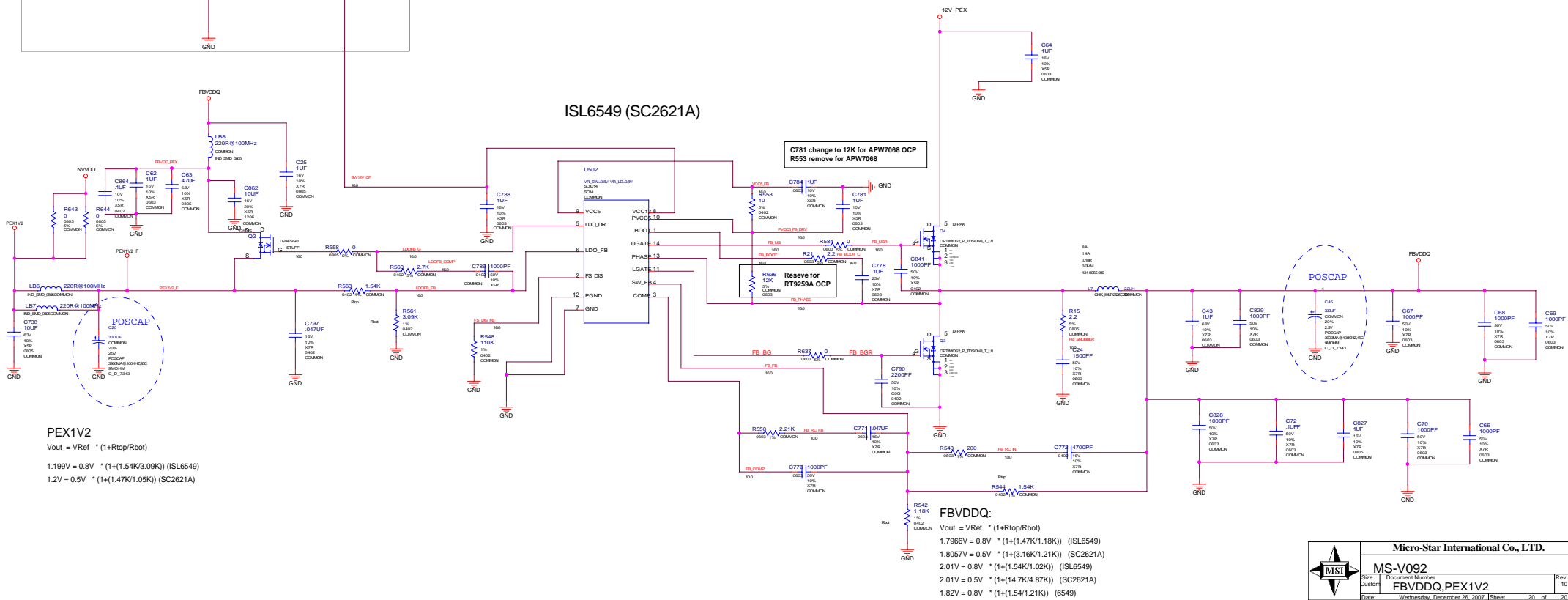
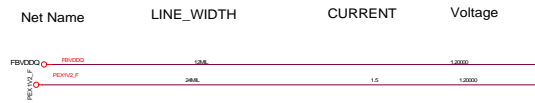
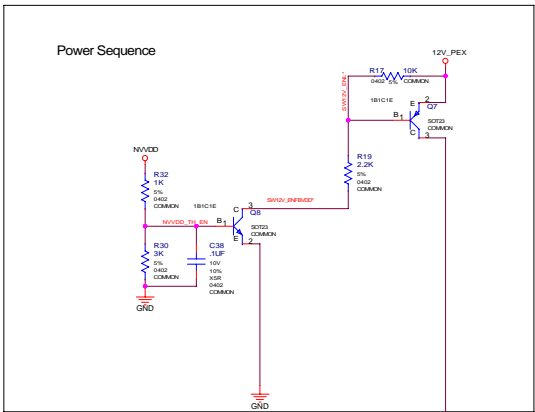
$2.5V = 0.5V * (1 + (4.53k/1.13k))$ (SC2621A)

$1.8V = 0.8V * (1 + (1.27k/1k))$ (ISL6549)



Net Name	LINE_WIDTH	Current	Voltage
12V_PEX	30MIL	3	12.000V
NVVDD	10MIL	0.5	1.2000V
PEX1V8	30MIL	2	1.0000V
NVVDD_SENSE	10MIL	0.5	1.2V
NVVDD_SENSE_M	10MIL	0.5	1.2V

PowerSupplyIII: FBVDDQ,PEX1V2



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Size	Document Number
Custom	FBVDDQ,PEX1V2

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