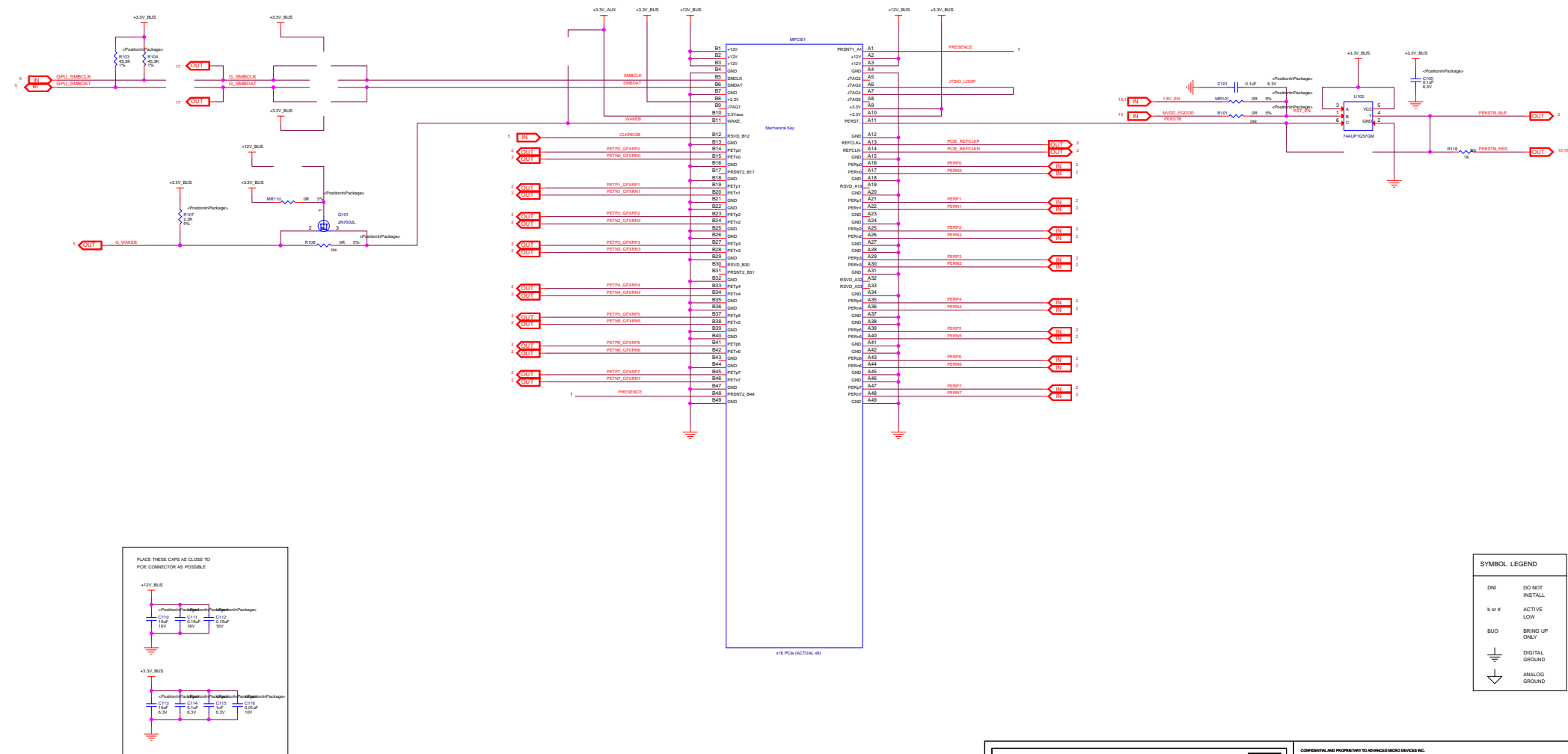

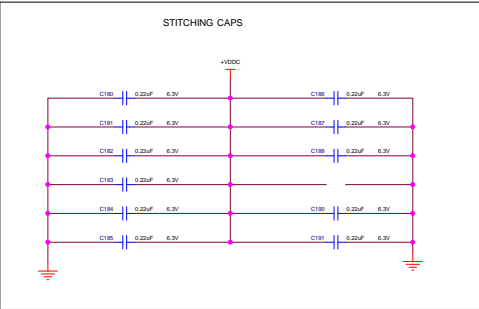
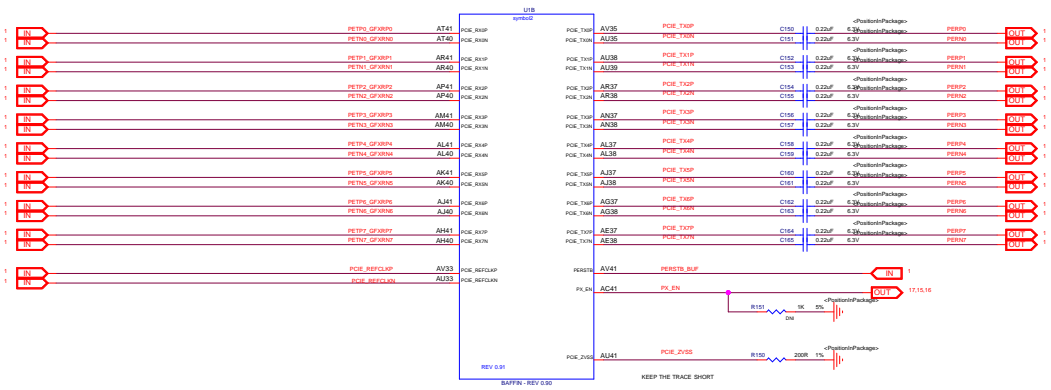


(1) PCIE EDGE CONNECTOR




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<p>SHEET: PCIe EDGE CONNECTOR</p>		<p>REV: 1.0</p>		<p>TITLE:</p>	
<p>DATE: Wed Jun 01 06:00:28 2016</p>				<p>TITLE</p>	
<p>SHEET NUMBER: 1 OF 19</p>					
<p>DOCUMENT NUMBER: 105_CXX000_00A</p>					

(2) BAFFIN PCIE



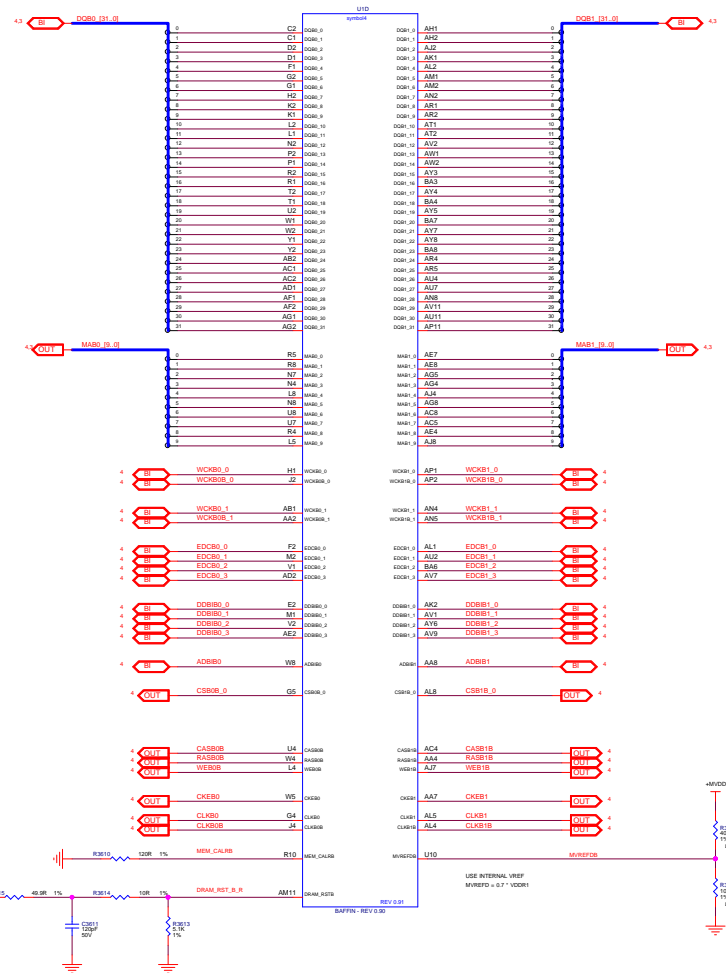
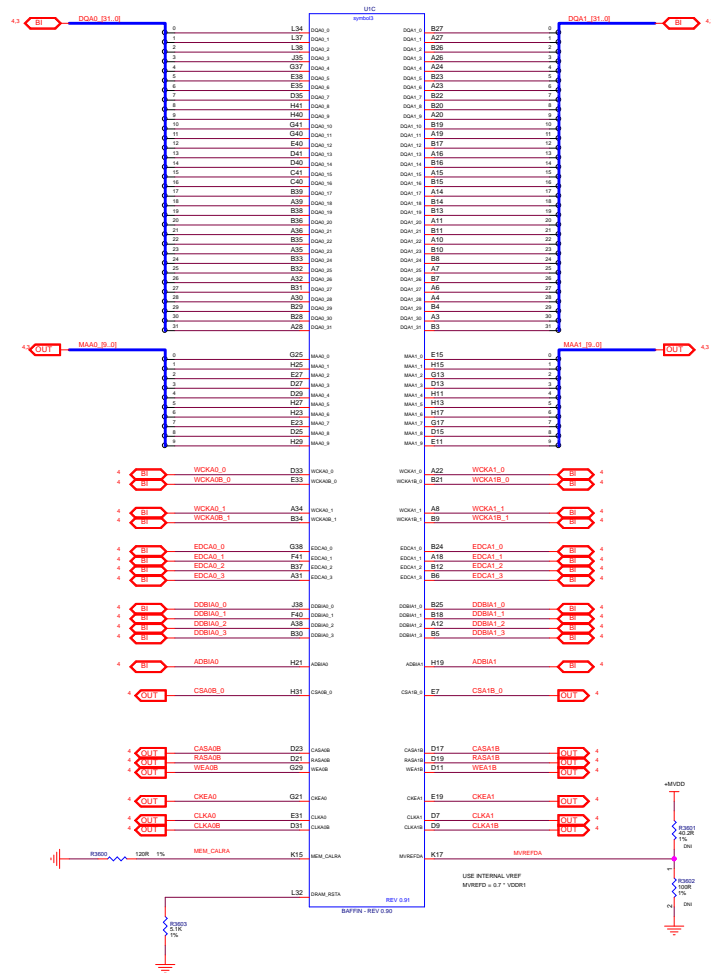
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SHANGHAI, CHINA 201203



SHEET: BAFFIN PCIE	
DATE: Wed Jun 01 06:00:28 2016	REV: 1.0
SHEET NUMBER: 2 OF 19	TITLE:
DOCUMENT NUMBER: 106_CXXX00_00A	TITLE

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(3) RACERX MEMORY INTERFACE



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SHEET: BAFFIN MEMORY

DATE: Wed Jun 01 06:00:28 2016

REV: 1.0

SHEET NUMBER: 3 OF 11

TITLE:	TITLE
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8

7

6

5

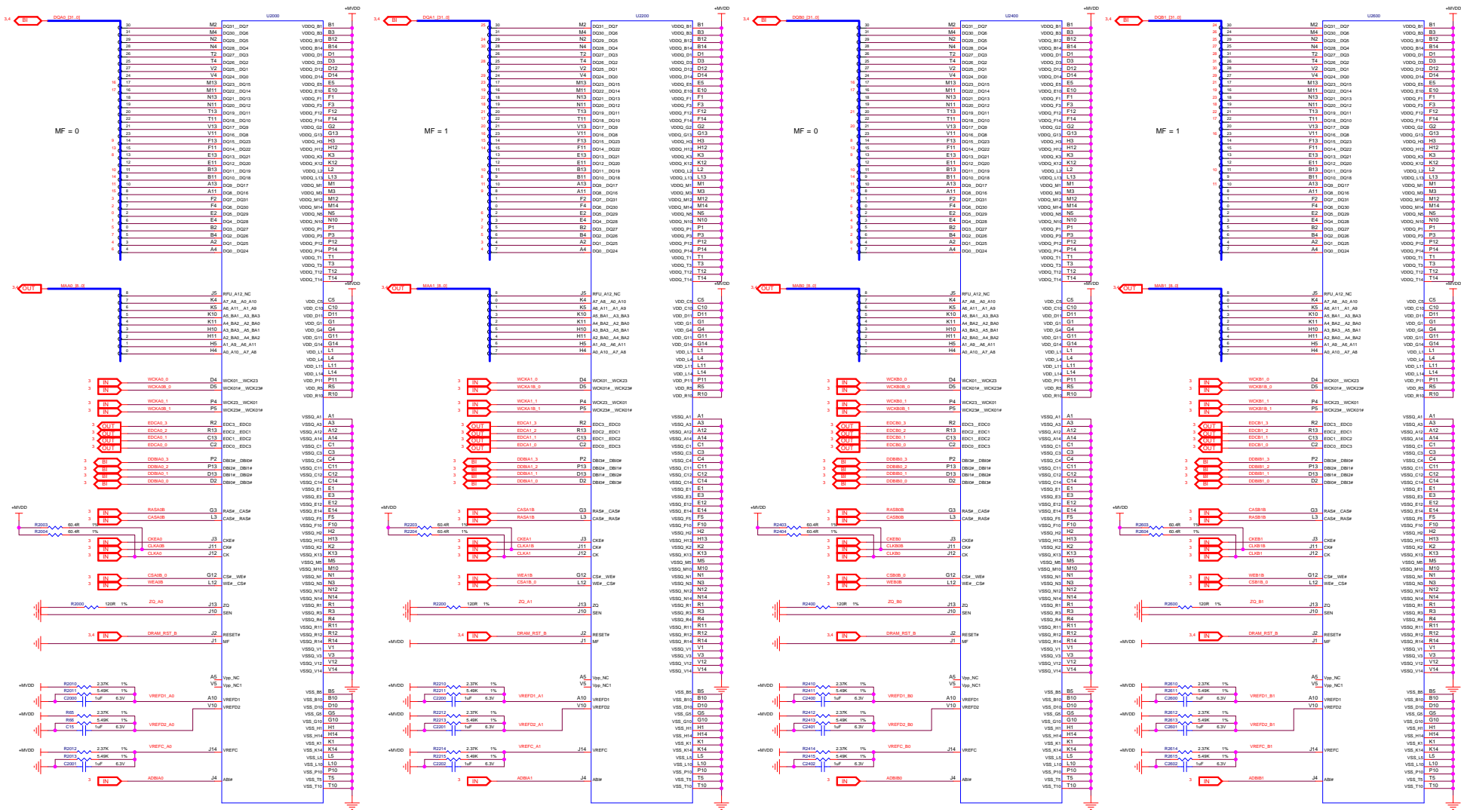
4

3

:

1

(4) GDDR5 CHAB x32



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SHEET: GDDR5 x32 CHAB

DATE: Wed Jun 01 08:00:29 2016

SHEET NUMBER: 4 OF 19

DOCUMENT NUMBER: 106_CXX00_00A

REV: 1.0

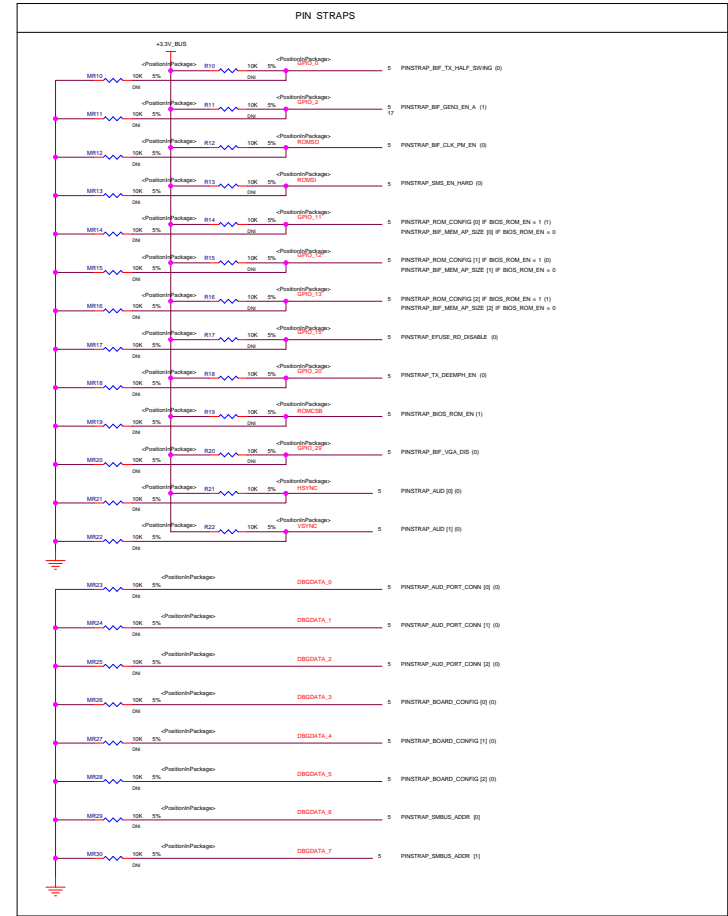
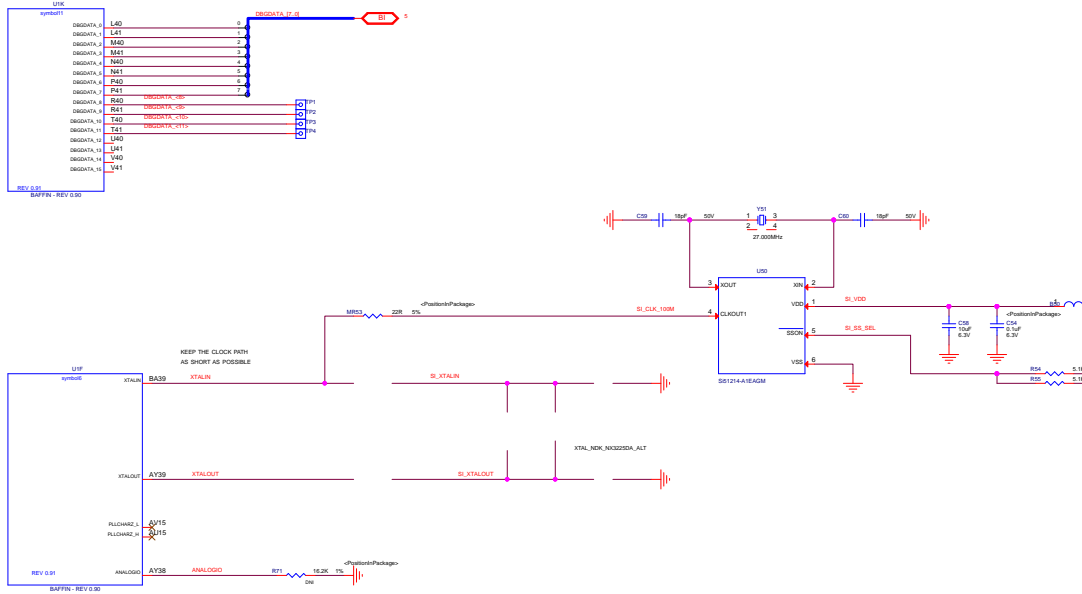
TITLE:

TITLE

(5) BAFFIN GPIO STRAPS CLK

DEVICE	ADDRESS
AD5274	0x5C
RT9403	0x62
VTMM II	

DEVICE	ADDRESS
LM9603	0x98



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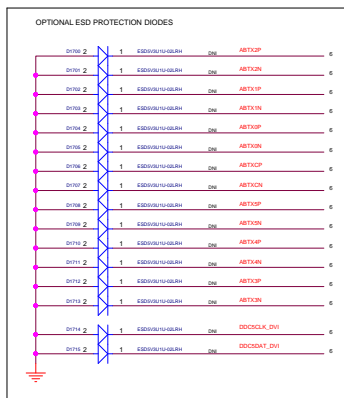
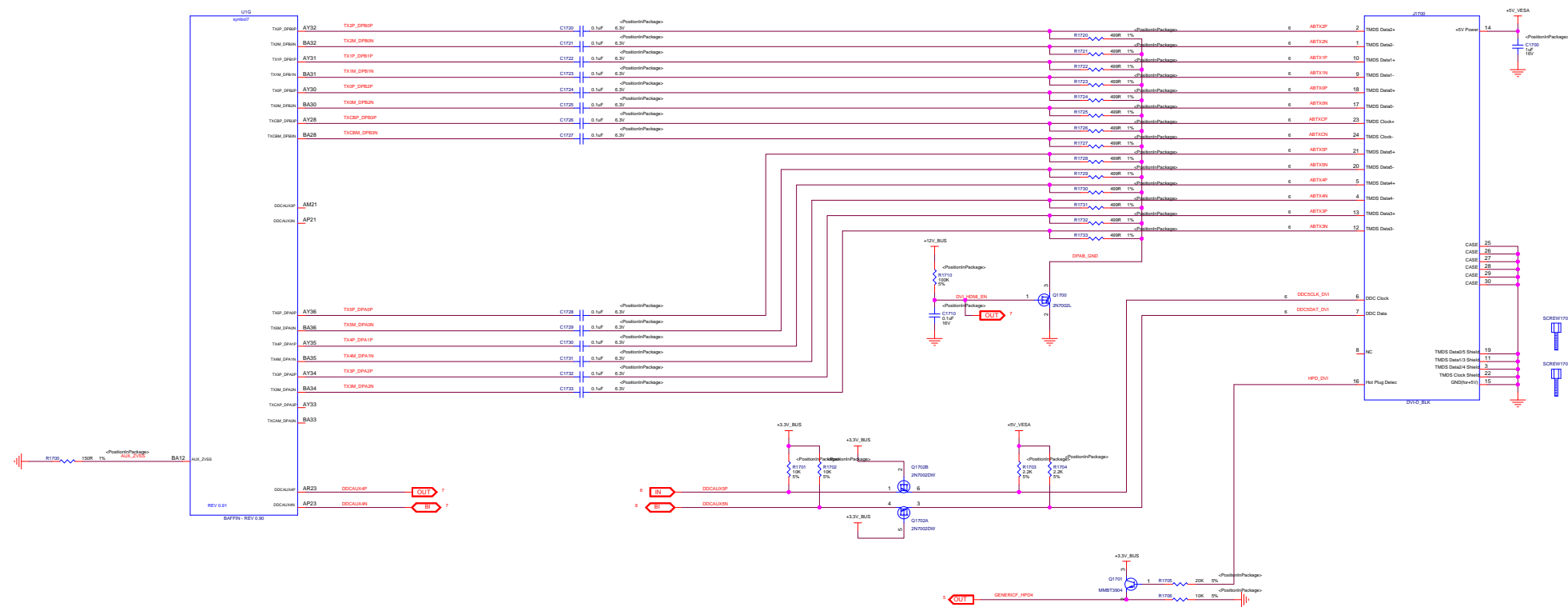
SHEET: BAFFIN GPIO STRAPS CLK

DATE: Wed Jun 01 06:00:29 2016

SHEET NUMBER: 5 OF

TITLE:	TITLE
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(6) BAFFIN TMDPAB - dDVI



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SHEET: BAFFIN TMDPAB - dJVI

DATE: Wed Jun 01 06:00:29 2016

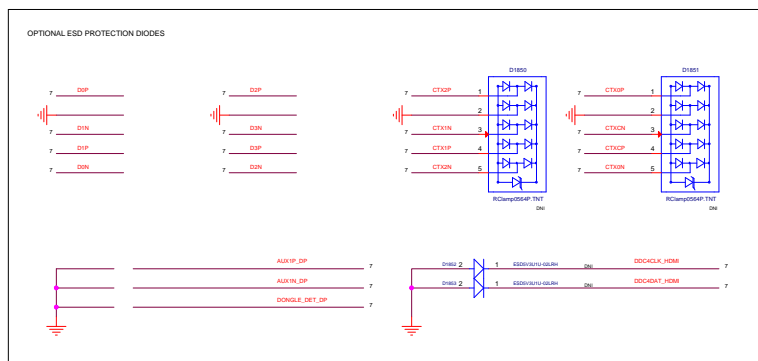
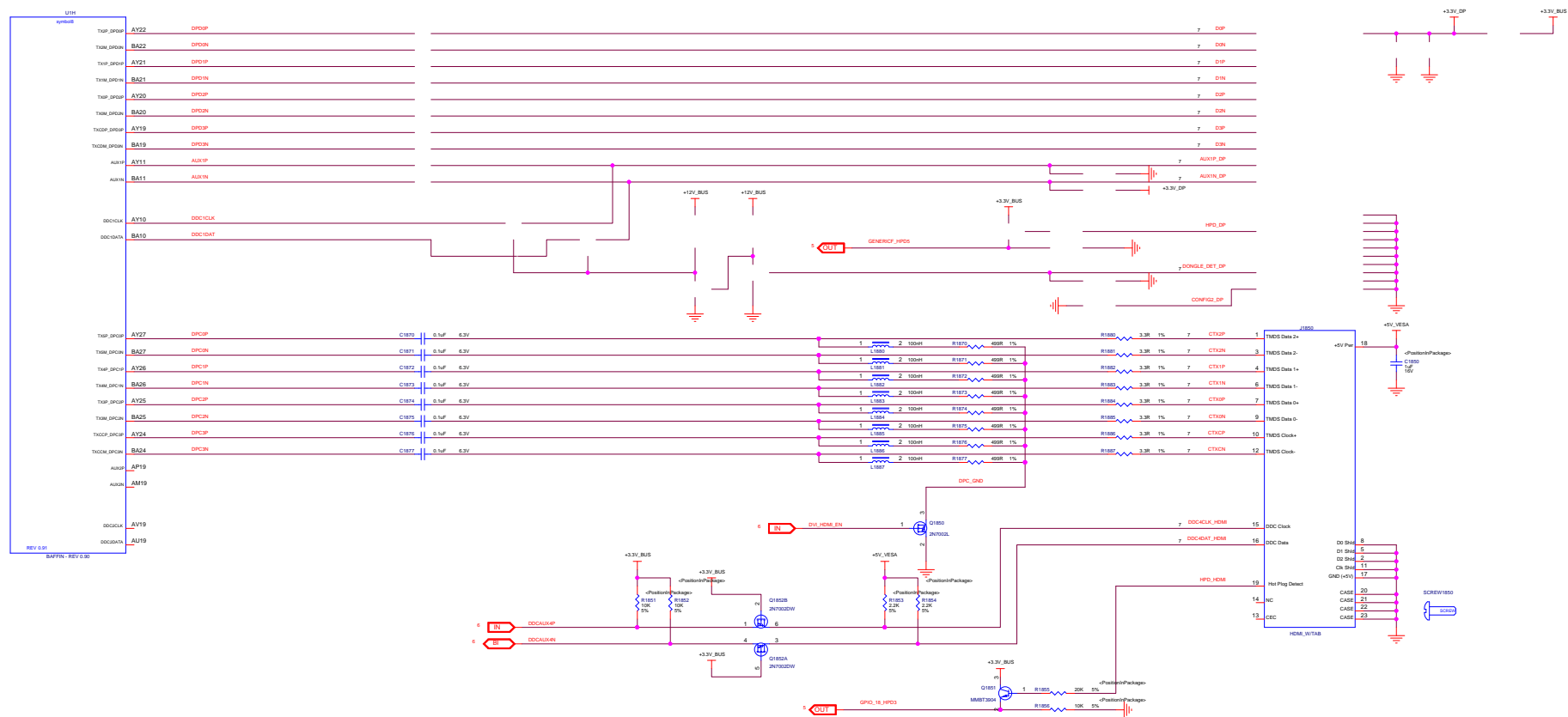
REV: 1.0

SHEET NUMBER: 6 OF 11

TITLE:	
TITLE	

DOCUMENT NUMBER: 105_CXXX00_00

(7) BAFFIN TMDPCD - HDMI DP



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SHEET: BAFFIN TMDPCD - HDMI DP

DATE: Wed Jun 01 06:00:29 2016

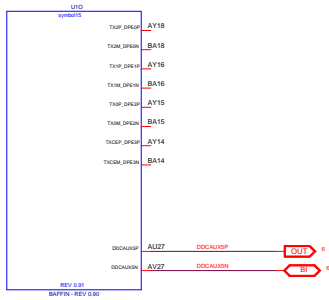
REV: 1.0


SHEET NUMBER: 7 OF 11

TITLE:	TITLE
--------	-------

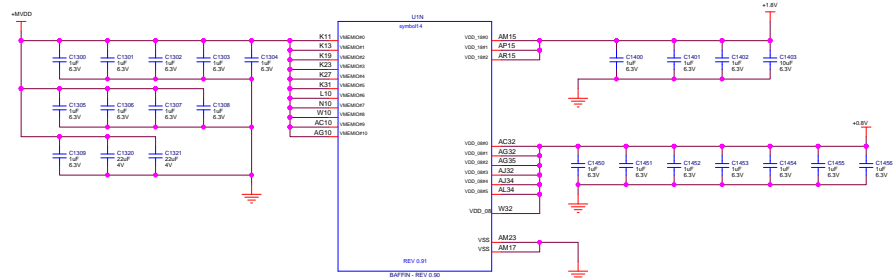
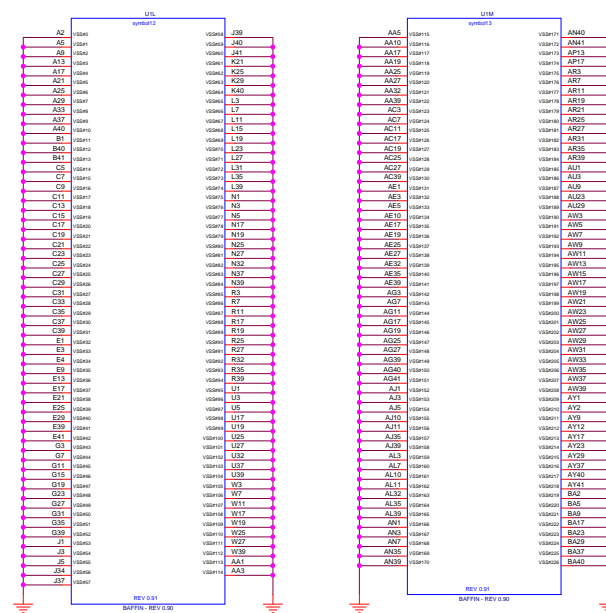
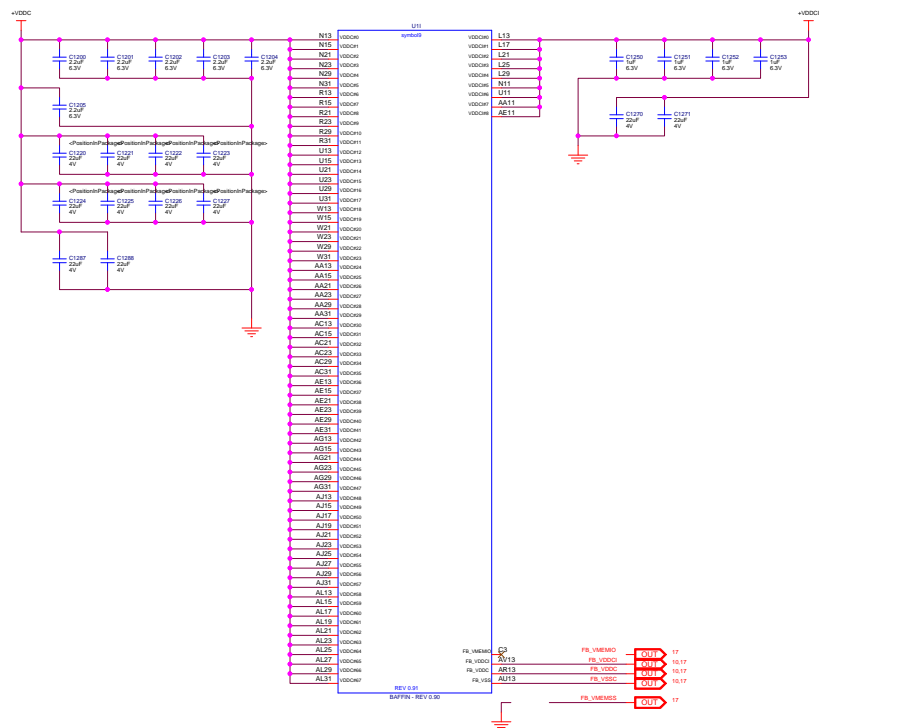
DOCUMENT NUMBER: 105_CXXX00_00

(8) BAFFIN TMDPE



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SHEET: BAFFIN TMDPE - NC			
DATE: Wed Jun 01 08:00:29 2016	REV: 1.0		
SHEET NUMBER: 8 OF 19		TITLE:	
DOCUMENT NUMBER: 106_CXXX00_00A		TITLE	

(9) BAFFIN POWER GND



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SHEET: BAFFIN POWER GND

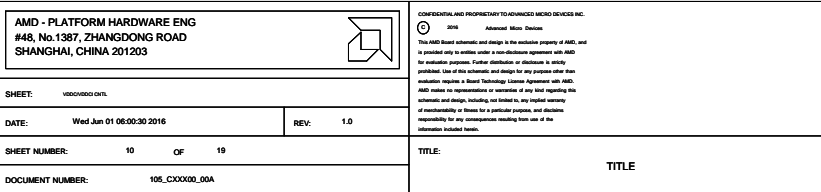
DATE: Wed Jun 01 06:00:30 2016

REV: 1.0

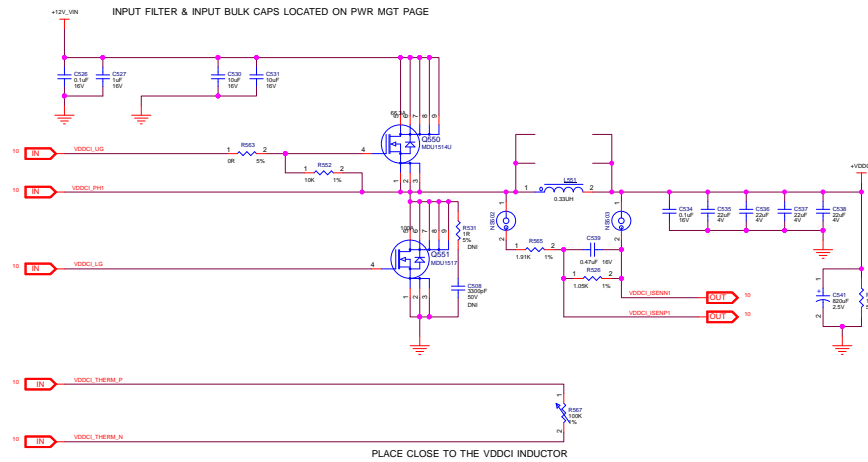
SHEET NUMBER: 9 OF 19

TITLE: _____

DOCUMENT NUMBER: 105_CXXX00_00



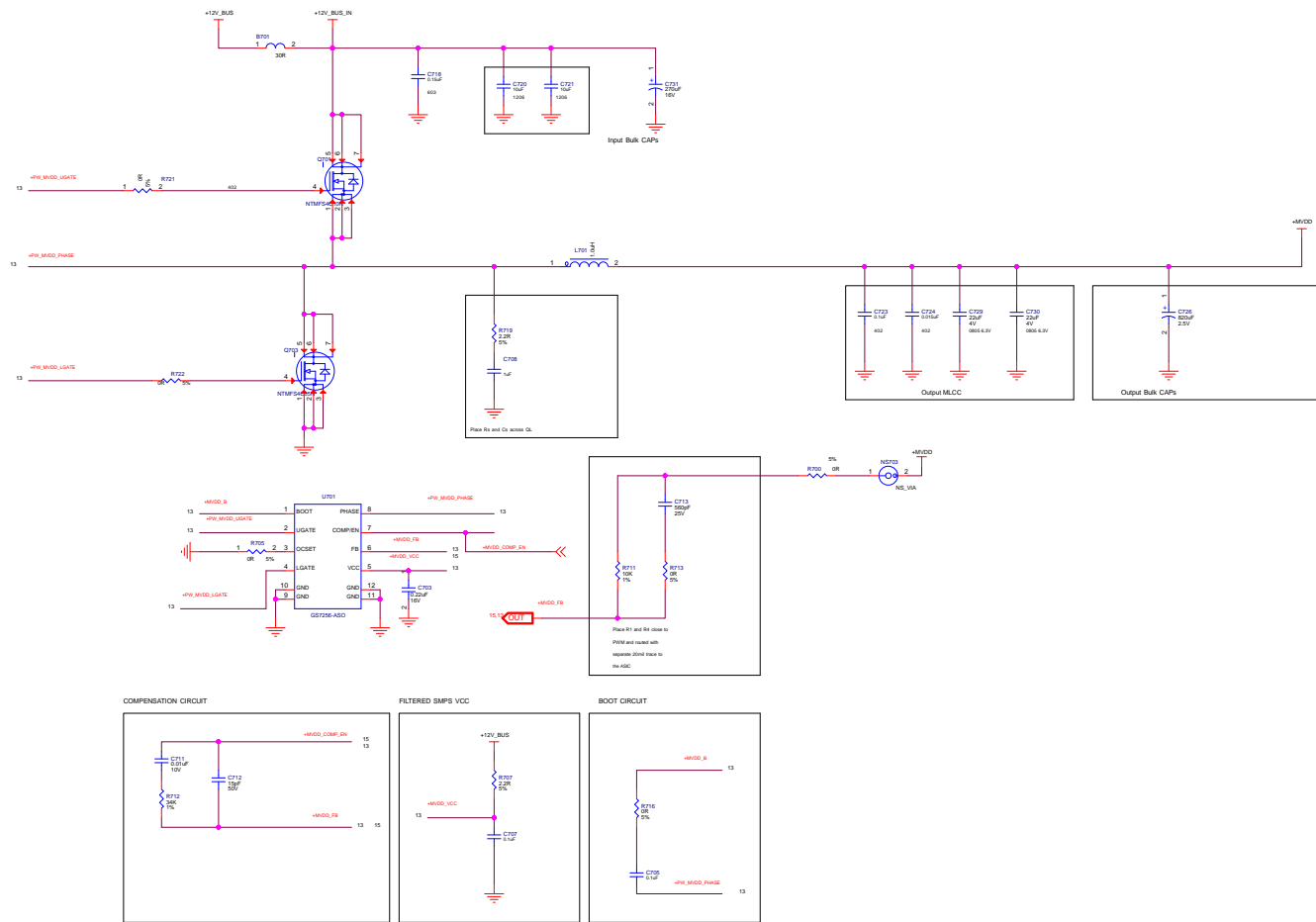
(11) VDDCI




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SHEET: VDDCI		
DATE: Wed Jun 01 08:00:30 2016	REV: 1.0	
SHEET NUMBER: 11 OF 19		
DOCUMENT NUMBER: 106_CXXX00_00A		

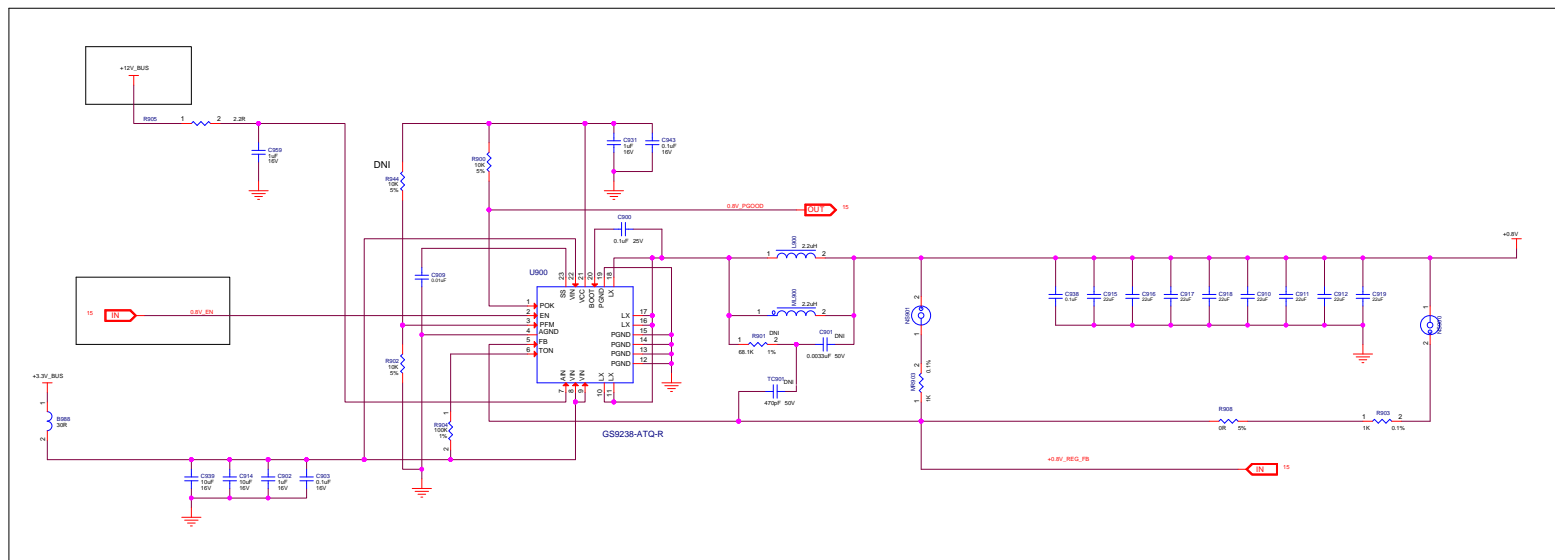
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(18) MVDD



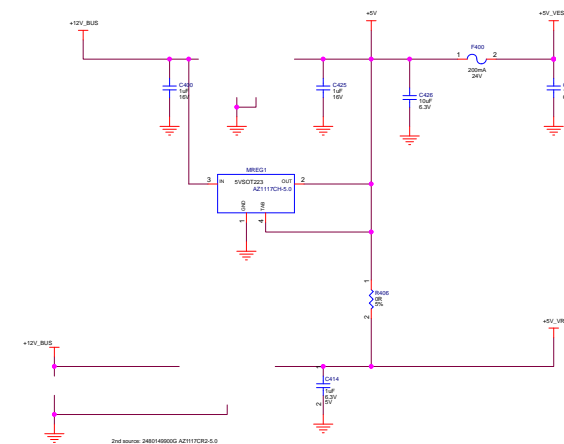
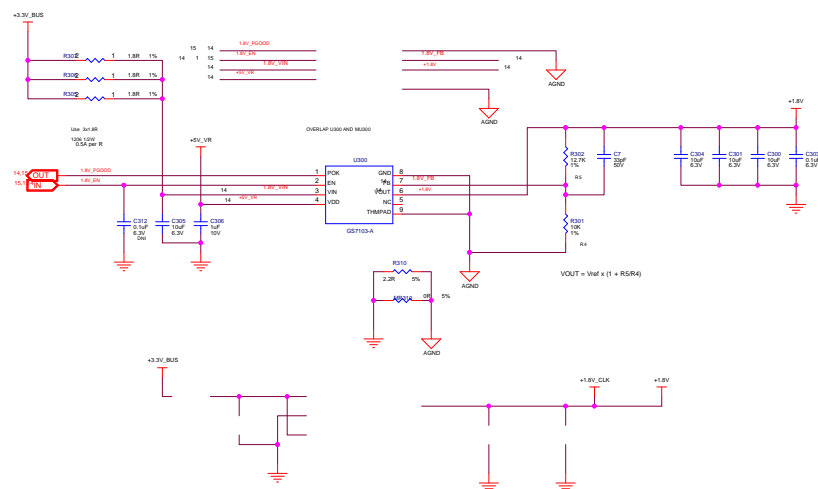
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SHEET: MVDD		DATE: Wed Jun 01 06:00:30 2016		REV: 1.0	
SHEET NUMBER: 13		OF 19		TITLE: TITLE	
DOCUMENT NUMBER: 105_CXXX00_00A					


(19) 0.8V



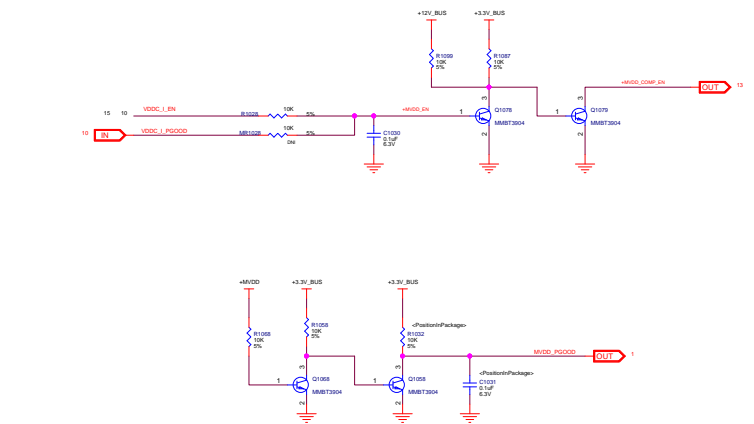
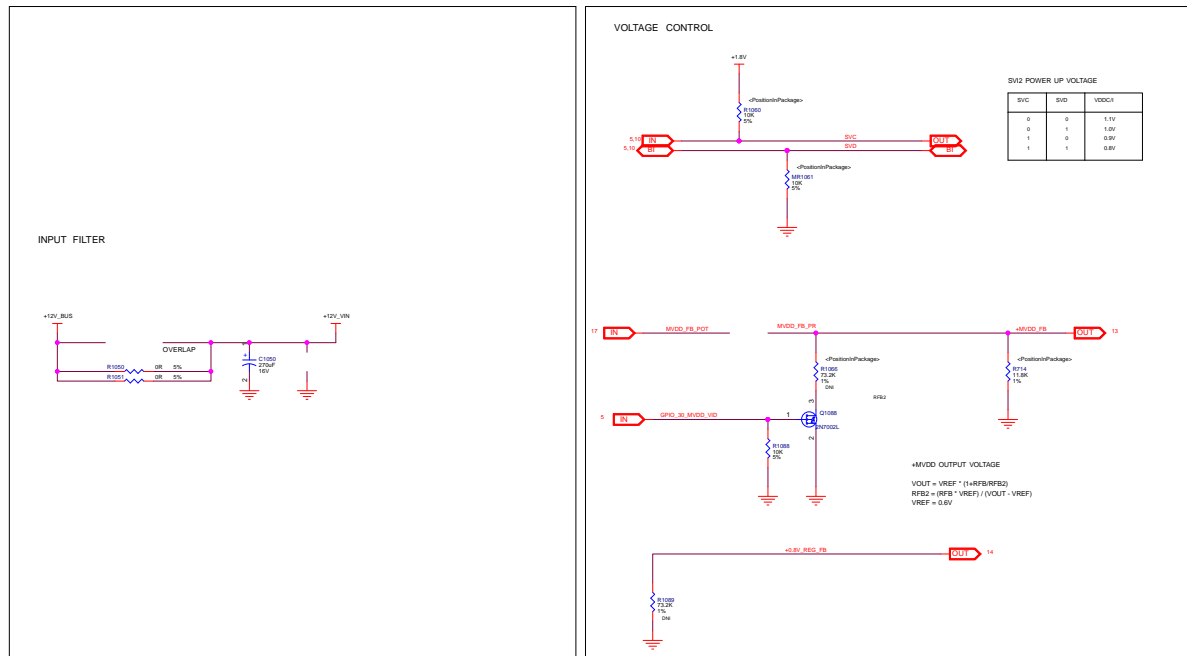
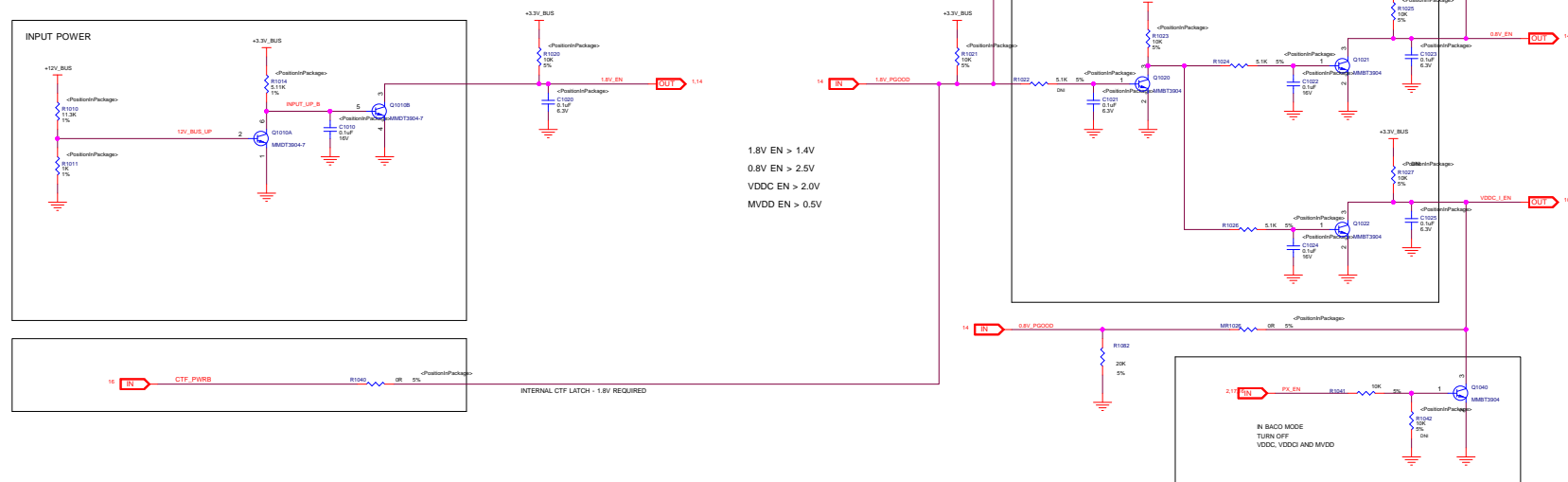
REGULATOR FOR +5V RAILS

LDO #1: VIN = 3.0V TO 3.6V MAX VOUT = +1.8V +/- 2% IOUT = 1.3A RMS MAX
PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING



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<p>SHEET: 0.8V REG</p>	
<p>DATE: Wed Jun 01 06:00:30 2016</p>	<p>REV: 1.0</p>
<p>SHEET NUMBER: 14 OF 19</p>	<p>TITLE:</p> <p style="text-align: center; font-weight: bold; font-size: 1.2em;">TITLE</p>
<p>DOCUMENT NUMBER: 105_CXX000_00A</p>	
<p>NOTES: NOTE</p>	

(16) POWER MANAGEMENT

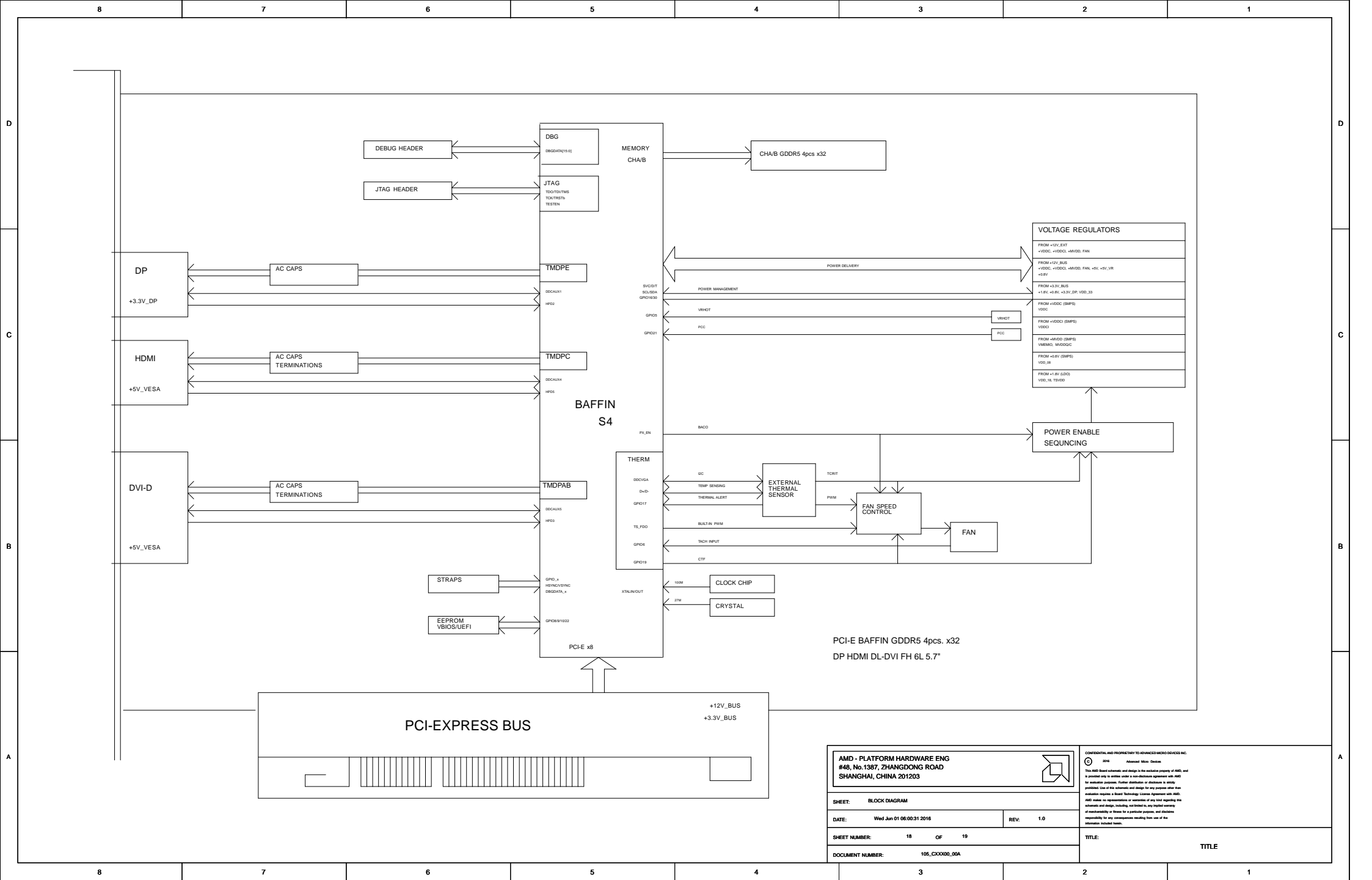


SV1C	SV1D	VDDC/I
0	0	1.1V
0	1	1.5V
1	0	0.9V
1	1	0.8V

$$V_{OUT} = V_{REF} * (1 + R_{FB1}/R_{FB2})$$

$$R_{FB2} = (R_{FB1} * V_{REF}) / (V_{OUT} - V_{REF})$$

$$V_{REF} = 0.6V$$



8	7	6	5	4	3	2	1				
AMD 		TITLE:		DOCUMENT NUMBER: 106_C00000_00A		DATE: Wed Jun 01 06:00:31 2016		SHEET NUMBER: 19 OF 19		REV: 1.0	
REVISION HISTORY		ENGINEER: XXXX		NOTES: NOTE		<div>CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC. This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representation or warranty of any kind regarding this schematic and design, including, not limited to, any explicit warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information included herein.</div> <div>C 2016 Advanced Micro Devices</div>		AMD - PLATFORM HARDWARE ENG #48, No.1367, ZHANGDONG ROAD SHANGHAI, CHINA 201203			
SCH Rev	PCB Rev	Date	REVISION DESCRIPTION								
0	00A	1810202015	INITIAL RELEASE								
1	00B	1802202015	AMD 1.0V1.0202015 CLK 0200 AMD 100MHz CLOCK speed Change 0.02 power good pin up								

D

D

C

C

B

B

A

A