EAH6750 (C099PI) Repair Guide

May/13/2011
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CSC-GRMA

ASUS Model name	EAH6750 FML/DI/1GD5
ASUS P/N	80-C1CR30-L0A01
Graphics Engine	ATI Radeon HD 6750
GPU P/N	02G050004401
Bus Standard	PCI Express 2.1
Video Memory	GDDR5 1GB
Engine Clock	700 MHz
Memory Clock	4000 MHz (1 GHz GDDR5)
RAMDAC	400 MHz
Memory Interface	128-bit

Page1. Basic Information about the Model

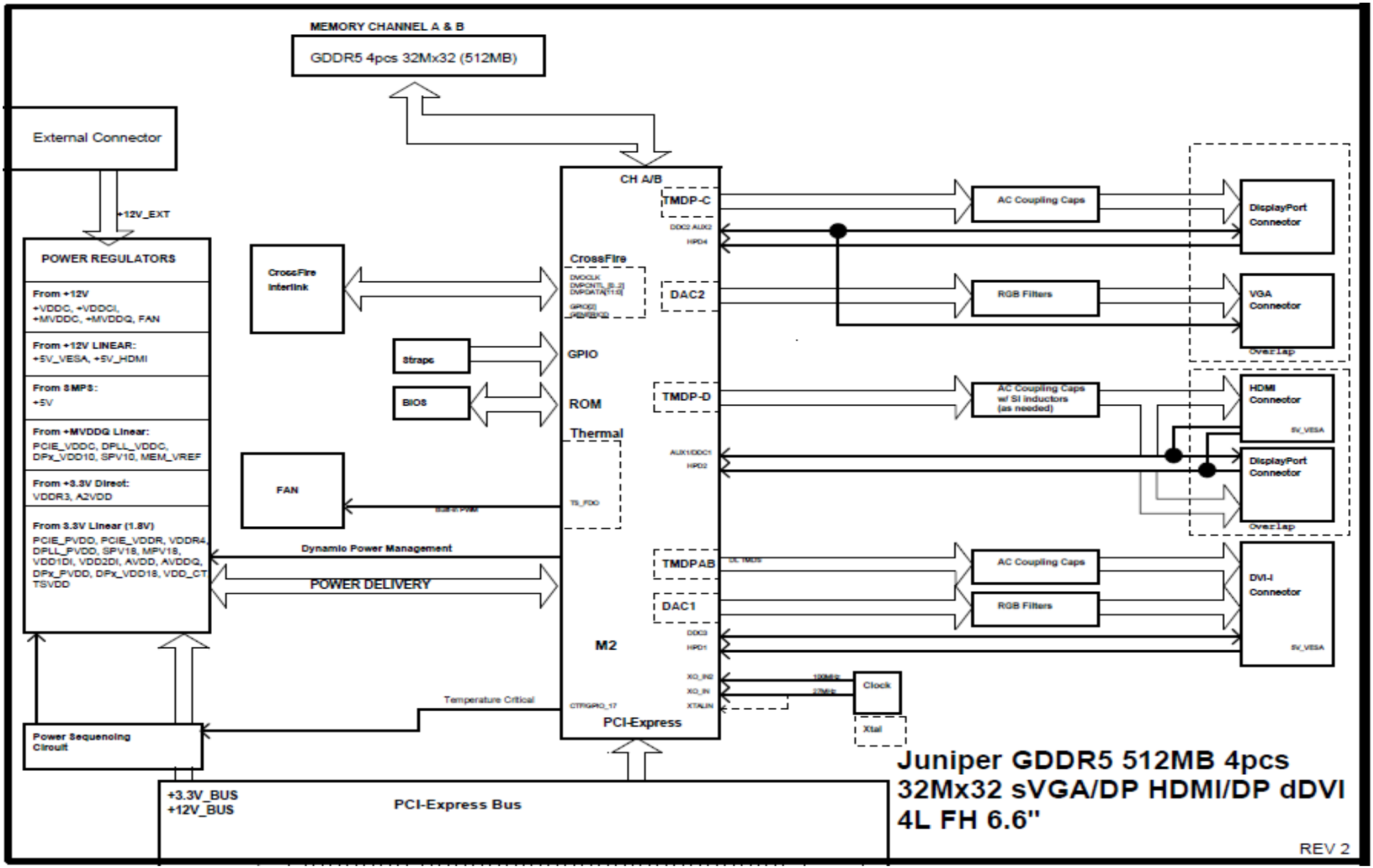
Page2. Block Diagram

Page3. Power Sequence

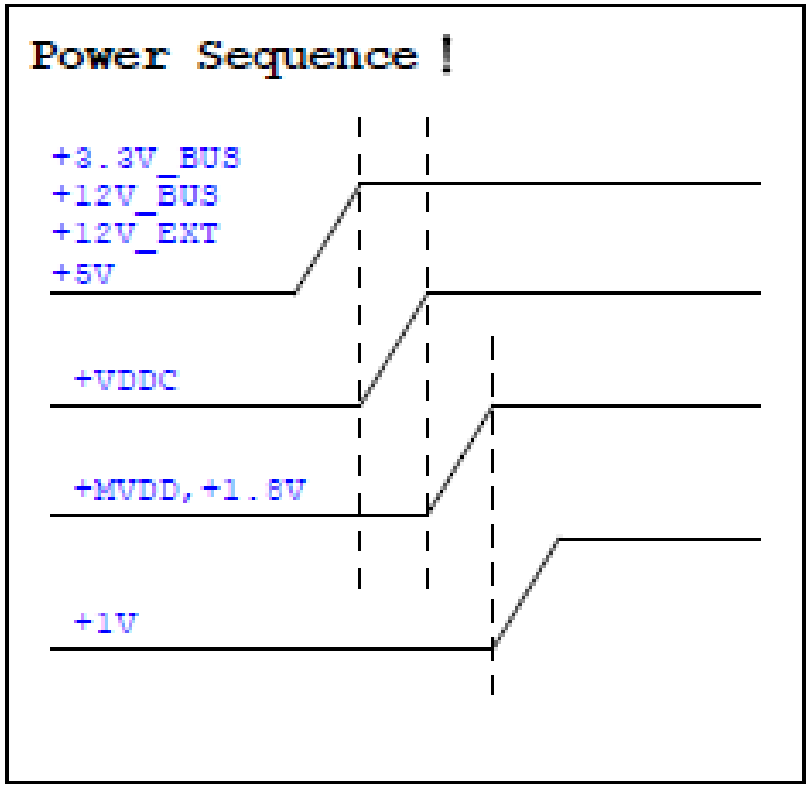
Page4. Power Flow

Page5. Important Signal map (Top side)

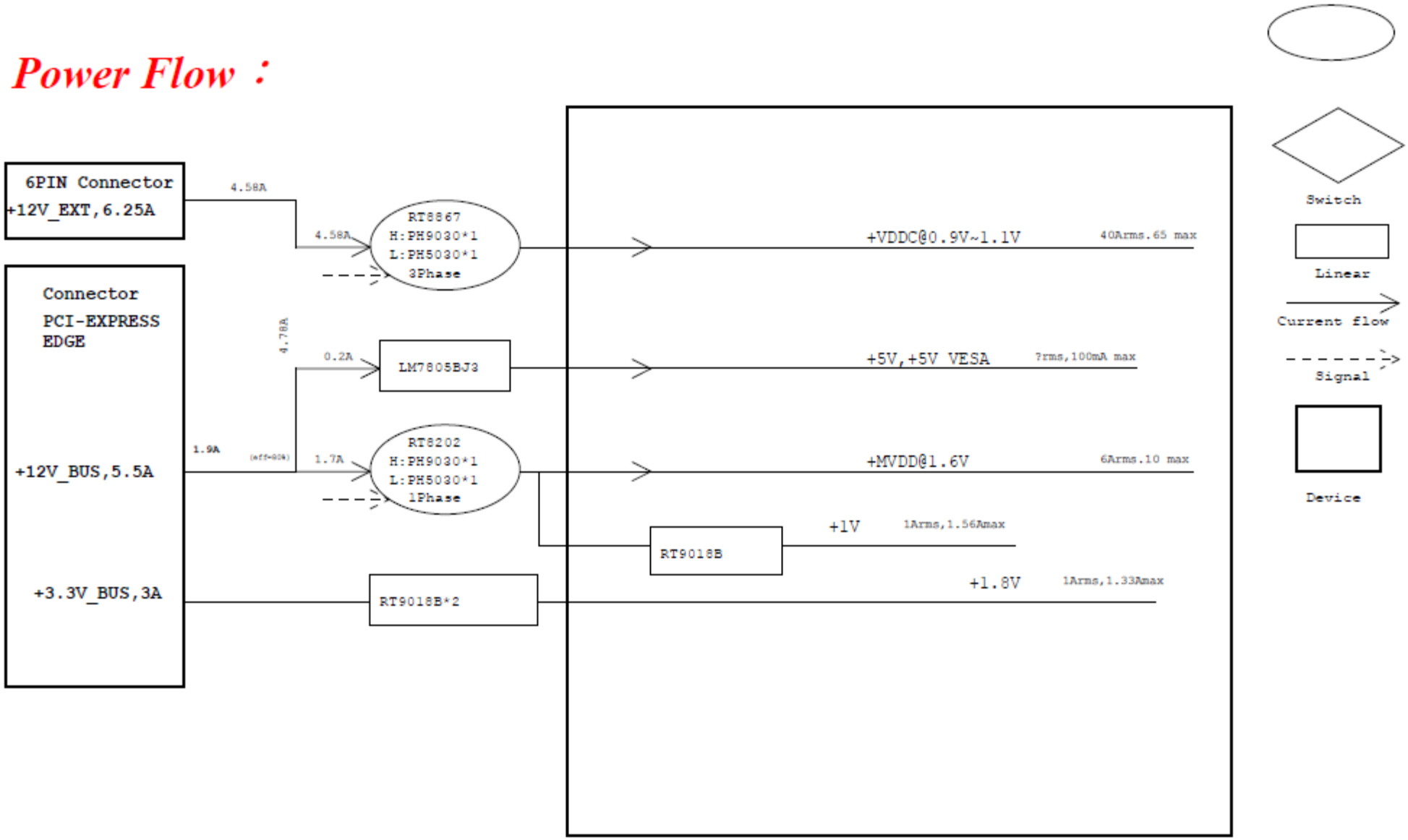
Page6. Important Signal map (Bottom side)

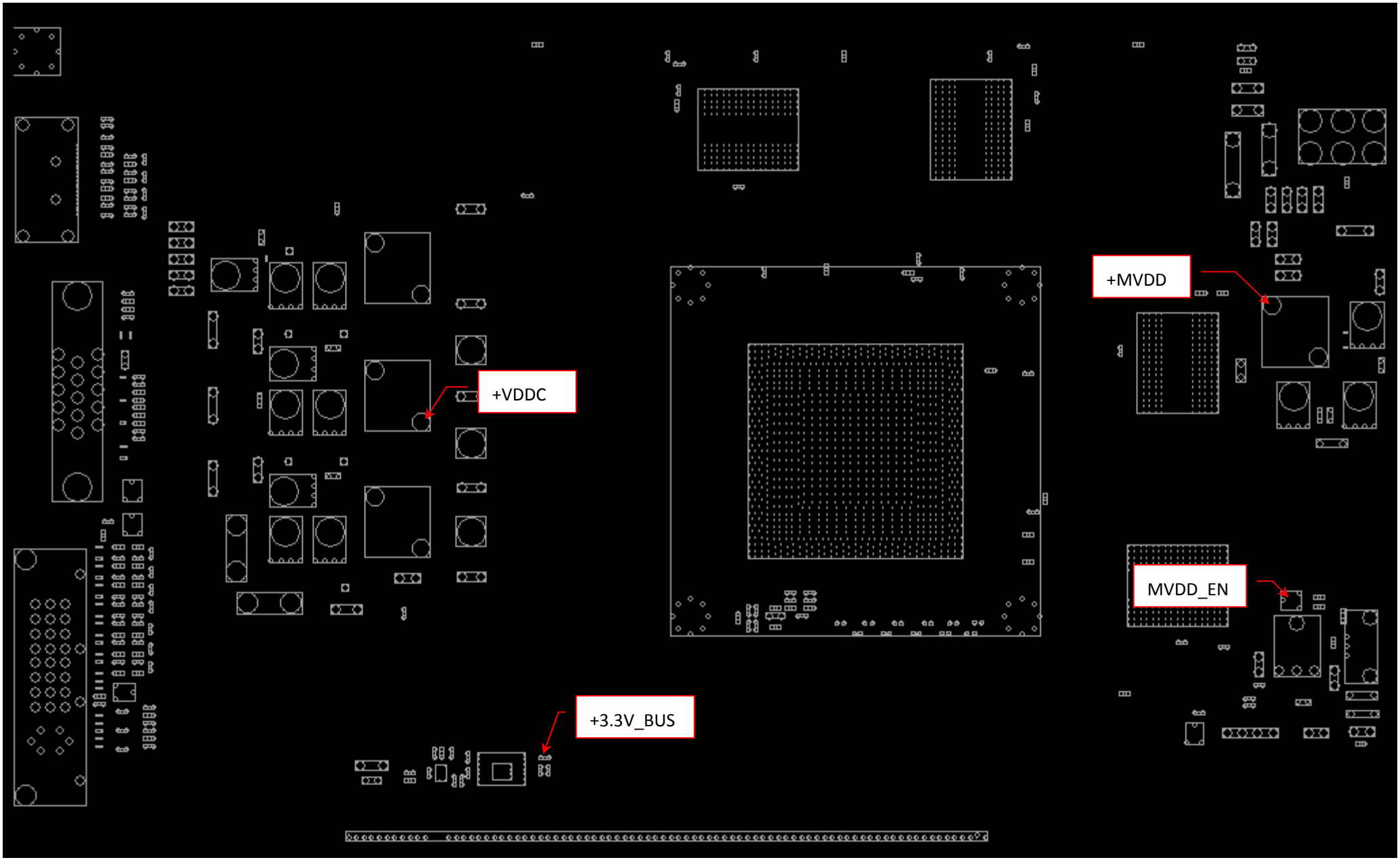

MEMORY CHANNEL A & B
GDDR5 4pcs 32Mx32 (512MB)
External Connector
+12V_EXT
POWER REGULATORS
From +12V
+VDDC, +VDDCI,
+MVDDC, +MVDDQ, FAN
From +12V LINEAR:
+5V_VESA, +5V_HDMI
From SMPS:
+5V
From +MVDDQ Linear:
PCIE_VDDC, DPLL_VDDC,
DPx_VDD10, SPV10, MEM_VREF
From +3.3V Direct:
VDDR3, A2VDD
From 3.3V Linear (1.8V)
PCIE_PVDD, PCIE_VDDR, VDDR4,
DPLL_PVDD, SPV18, MPV18,
VDD1DI, VDD2DI, AVDD, AVDDQ,
DPx_PVDD, DPx_VDD18, VDD_CT
TSVDD
Power Sequencing
Circuit
CrossFire
Interlink
Straps
BIOS
FAN
Dynamic Power Management
POWER DELIVERY
Temperature Critical
CH A/B
TMDP-C
CrossFire
DAC2
GPIO
ROM
TMDP-D
Thermal
TMDPAB
DAC1
M2
PCI-Express
Clock
Xtal
AC Coupling Caps
RGB Filters
AC Coupling Caps
w/ SI inductors
(as needed)
AC Coupling Caps
RGB Filters
DisplayPort
Connector
VGA
Connector
Overlap
HDMI
Connector
5V_VESA
DisplayPort
Connector
Overlap
DVI-I
Connector
5V_VESA
+3.3V_BUS
+12V_BUS
PCI-Express Bus
Juniper GDDR5 512MB 4pcs
32Mx32 sVGA/DP HDMI/DP dDVI
4L FH 6.6"
REV 2

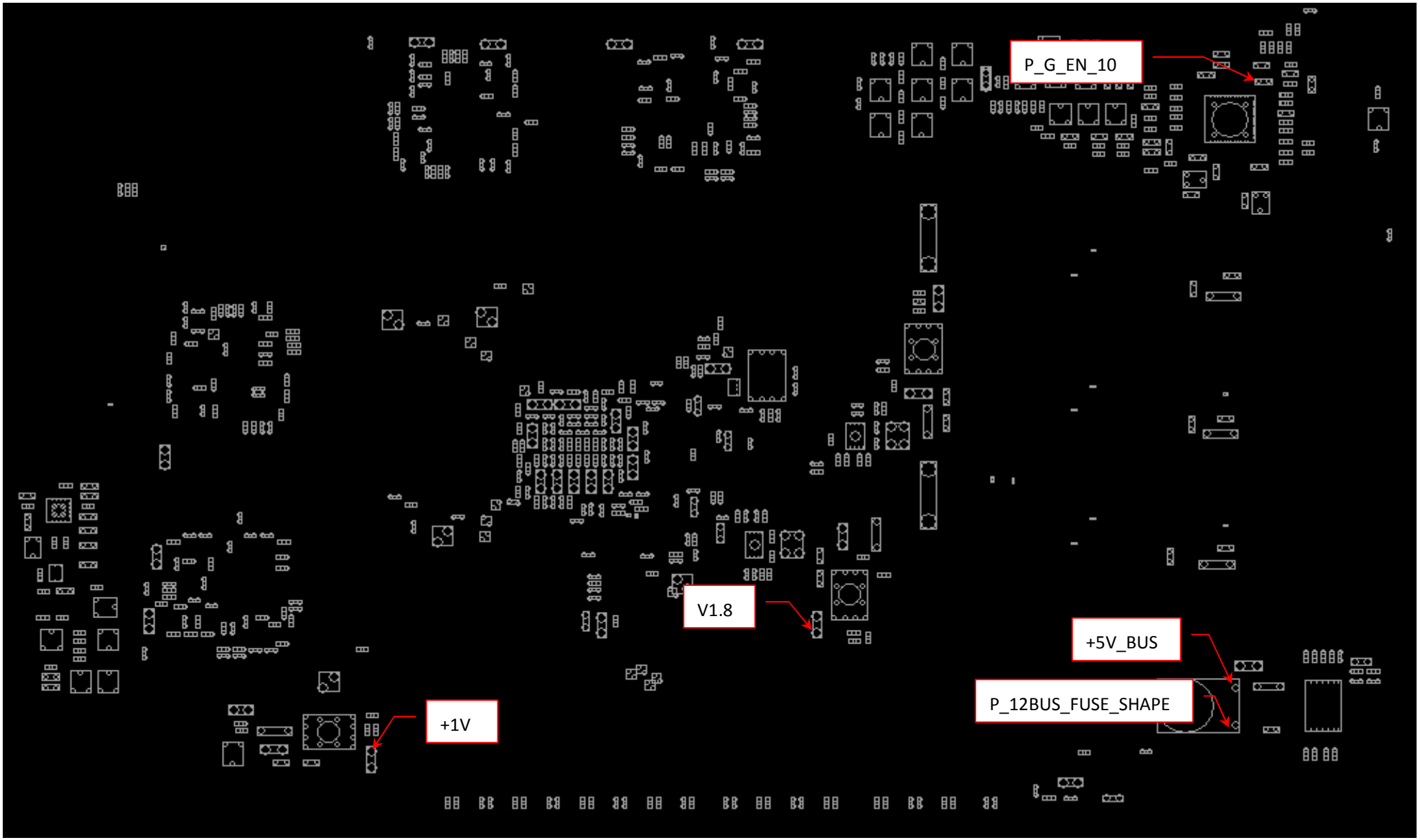
Power Sequence ：



Power Flow :


6PIN Connector
+12V_EXT,6.25A
4.58A
4.58A
RT8867
H:PH9030*1
L:PH5030*1
3Phase
+VDDC@0.9V~1.1V
40Arms.65 max
Connector
PCI-EXPRESS
EDGE
4.78A
0.2A
LM7805BJ3
+5V,+5V VESA
?rms,100mA max
1.9A
(eff=80%)
+12V_BUS,5.5A
1.7A
RT8202
H:PH9030*1
L:PH5030*1
1Phase
+MVDD@1.6V
6Arms.10 max
+1V
1Arms,1.56Amax
RT9018B
+3.3V_BUS,3A
RT9018B*2
+1.8V
1Arms,1.33Amax
Switch
Linear
Current flow
Signal
Device


+VDDC
+MVDD
MVDD_EN
+3.3V_BUS


P_G_EN_10
V1.8
+5V_BUS
P_12BUS_FUSE_SHAPE
+1V