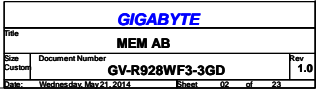
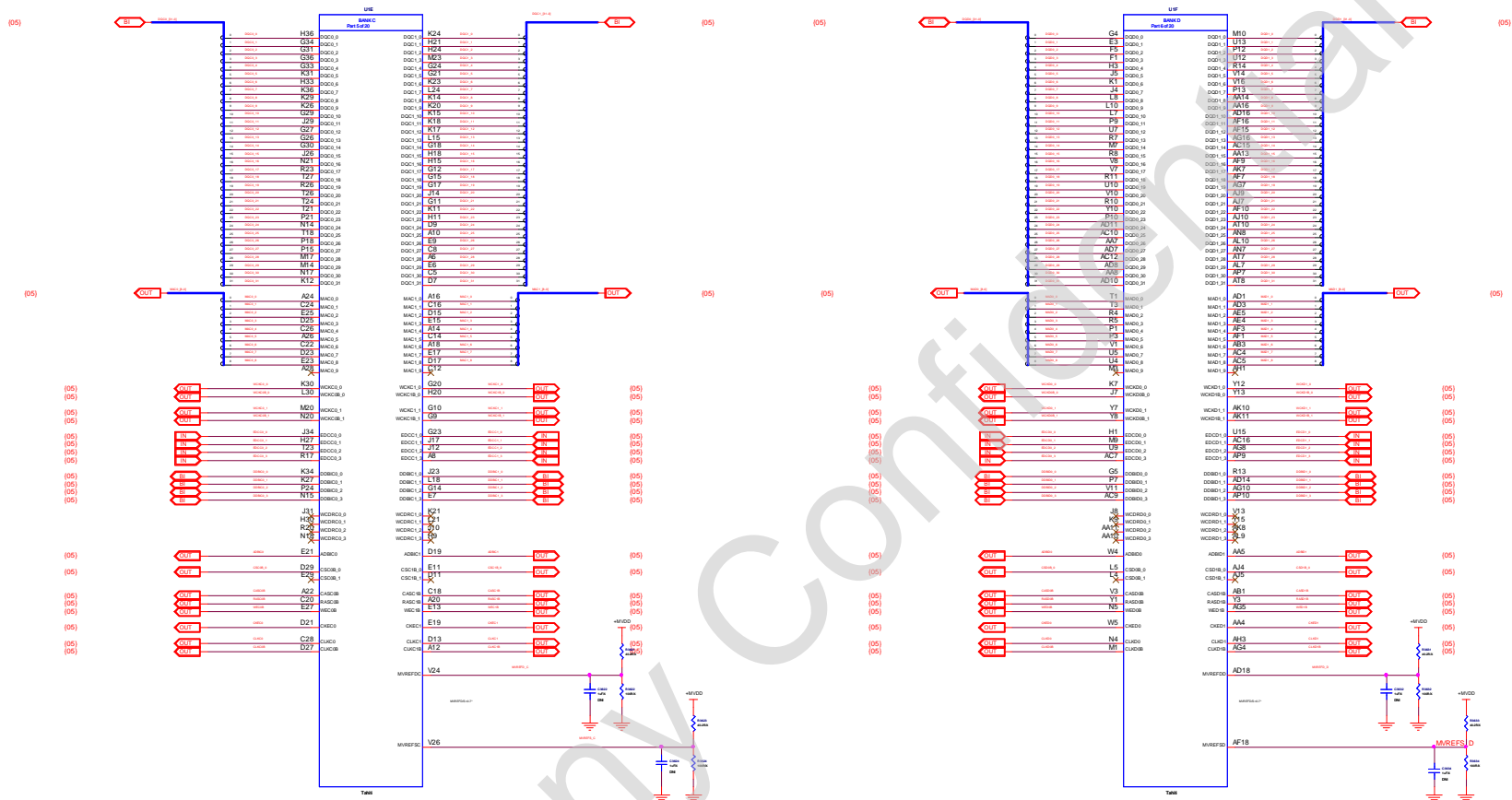
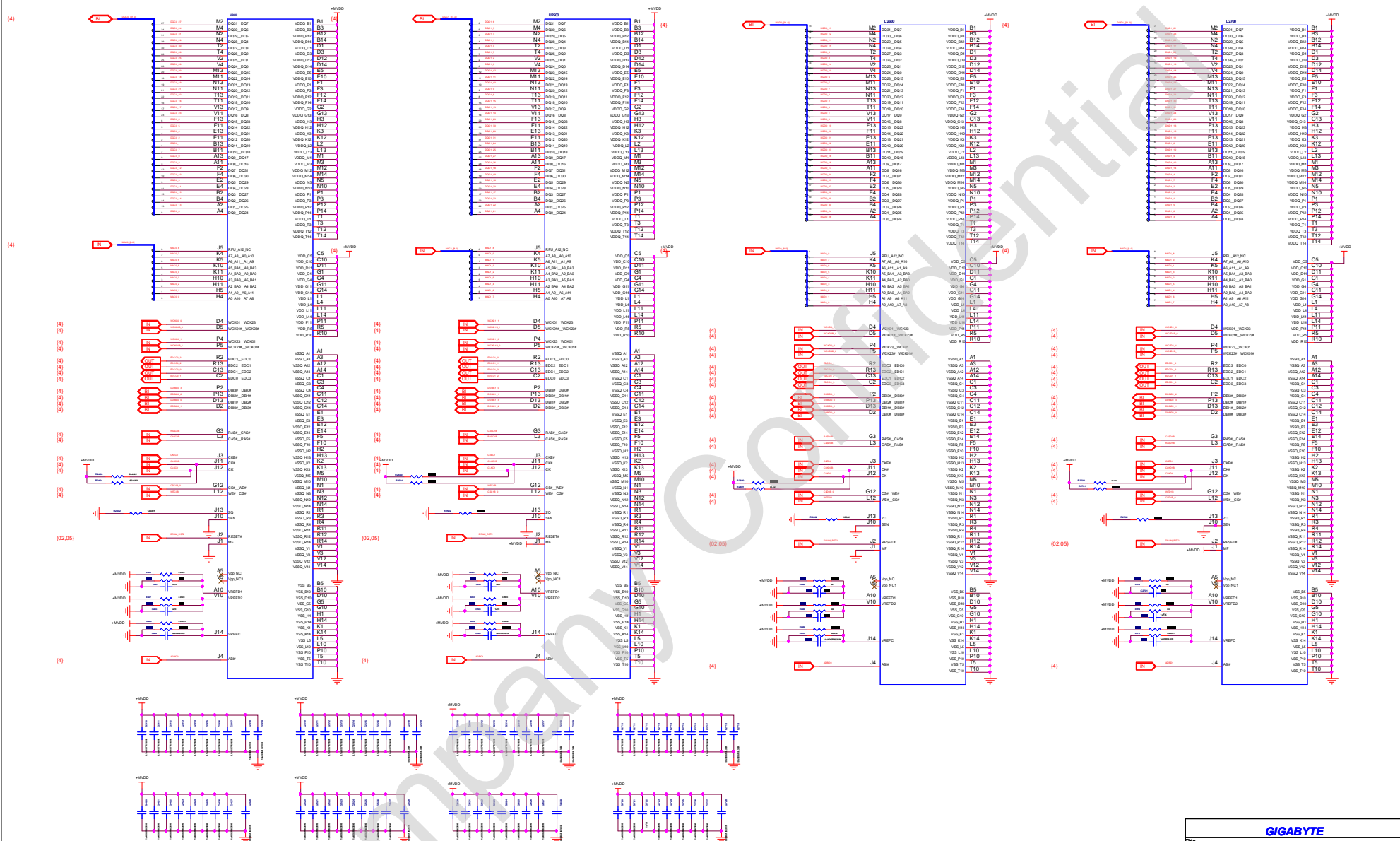
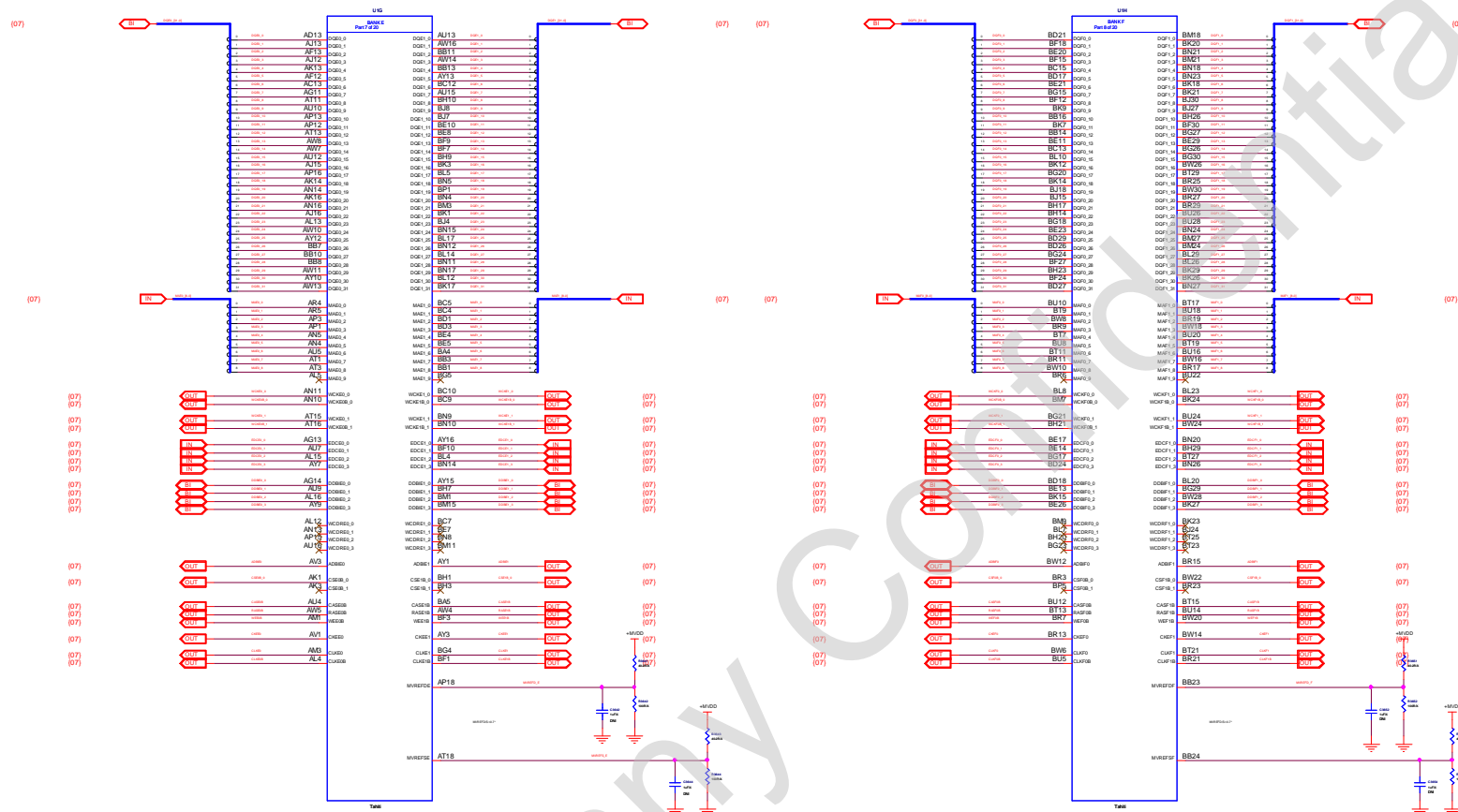


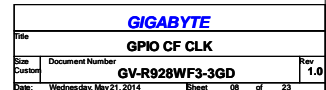
FinePrint Software, LLC
16 Napier Lane
San Francisco, CA 94133
Tel: 415-989-2722
Fax: 209-821-7869
www.fineprint.com

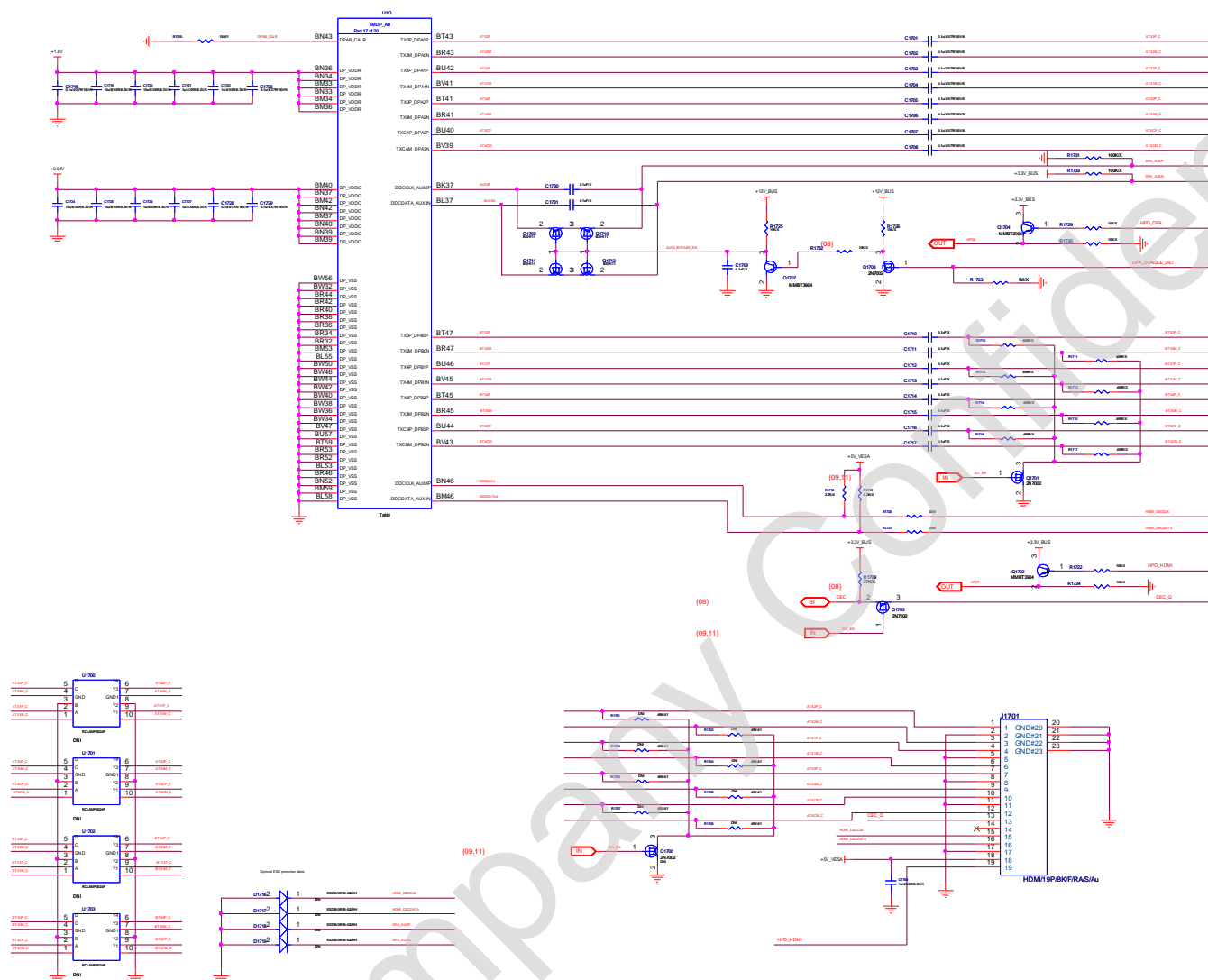


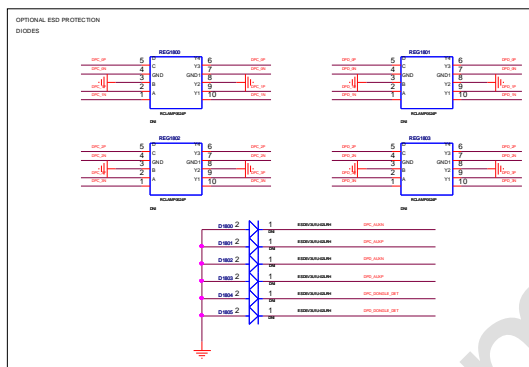
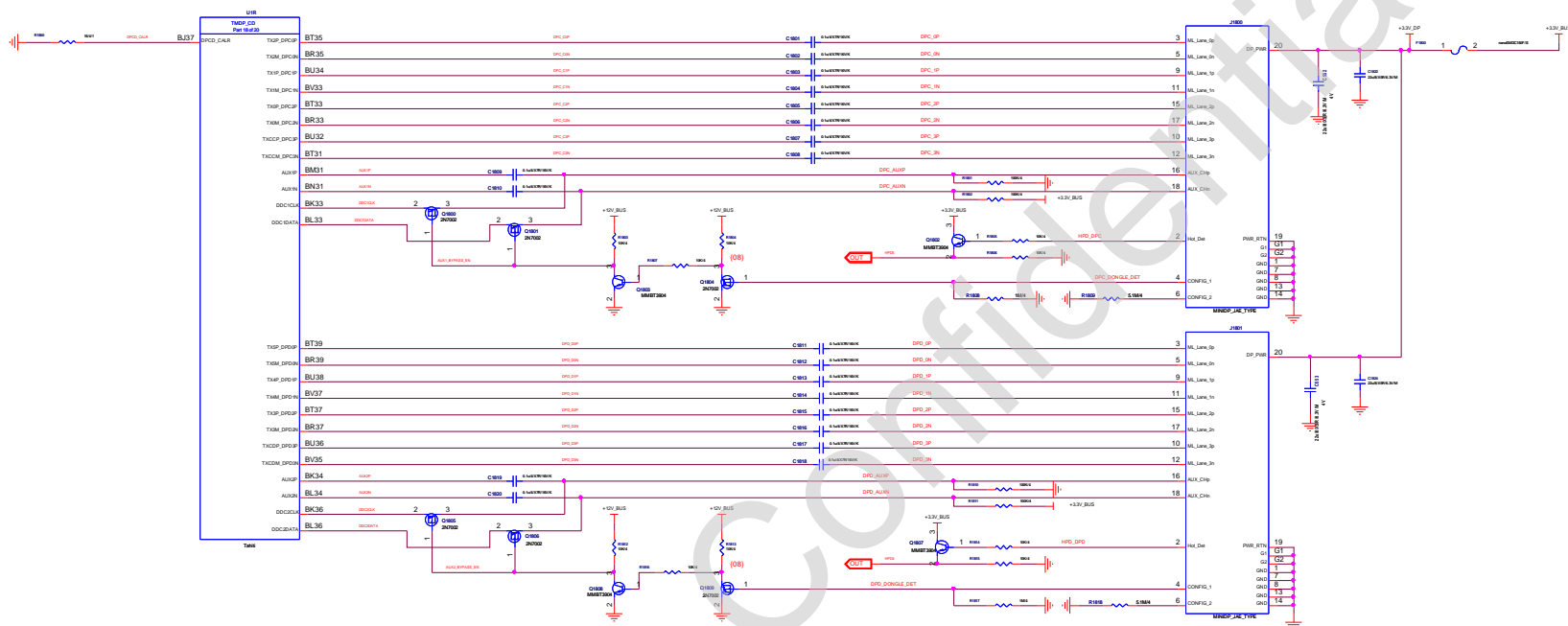


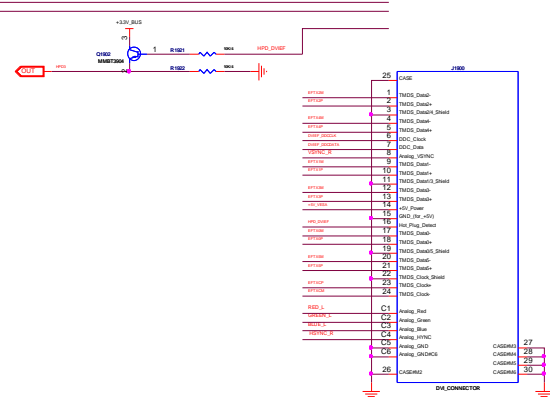
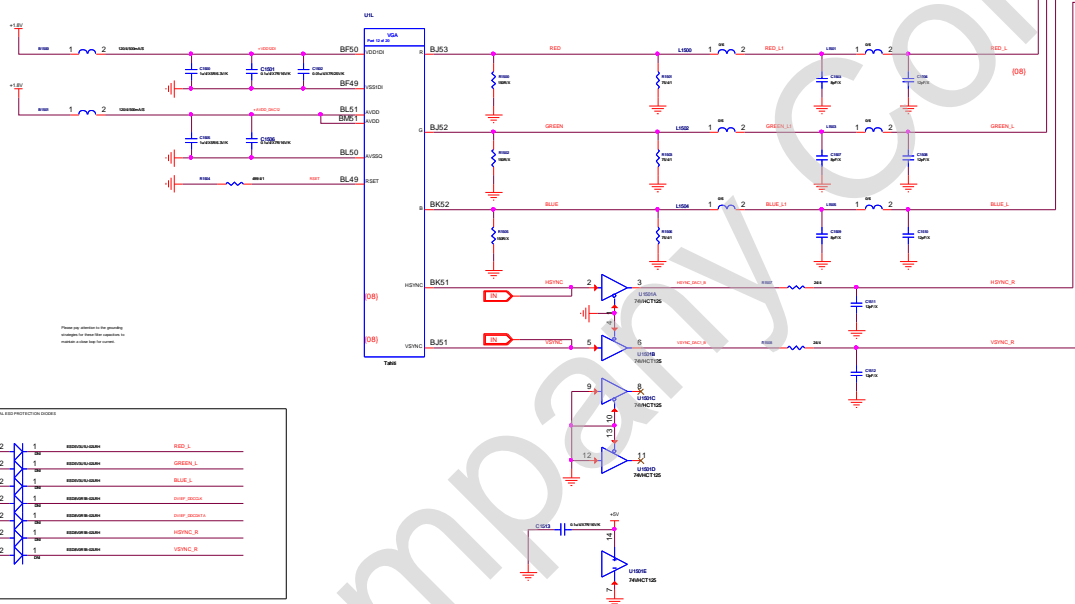
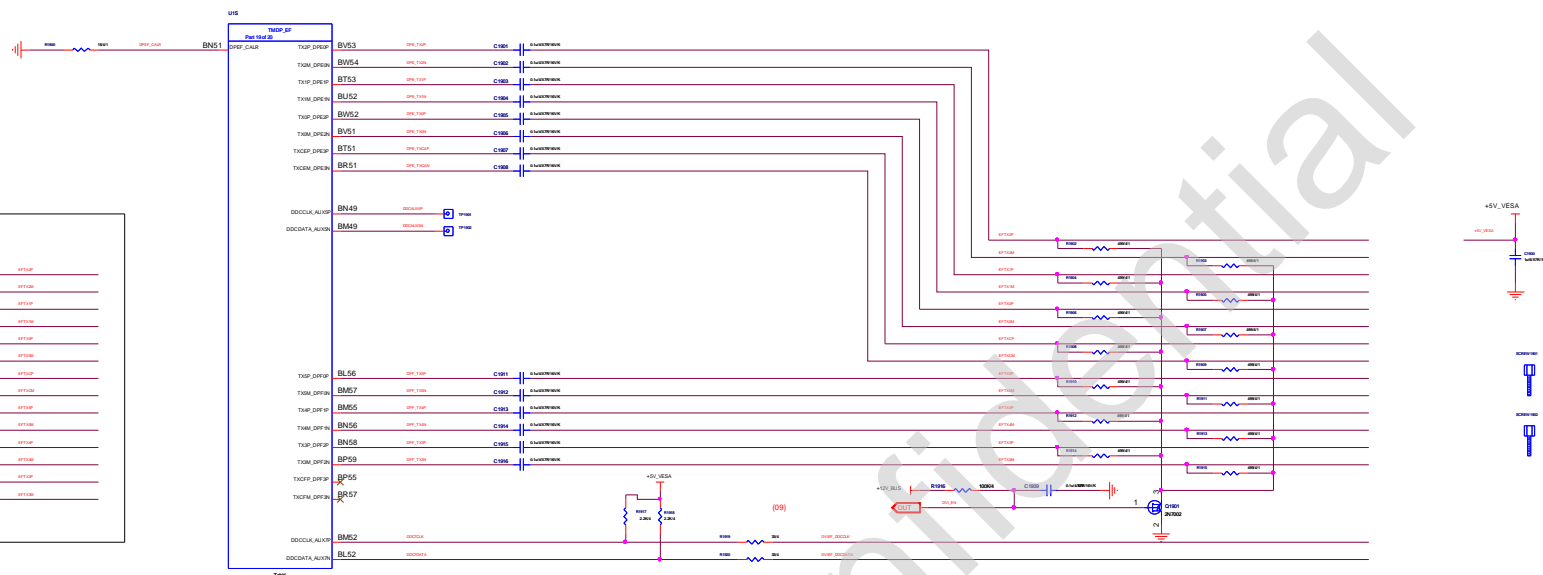
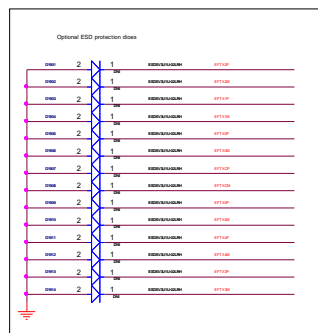




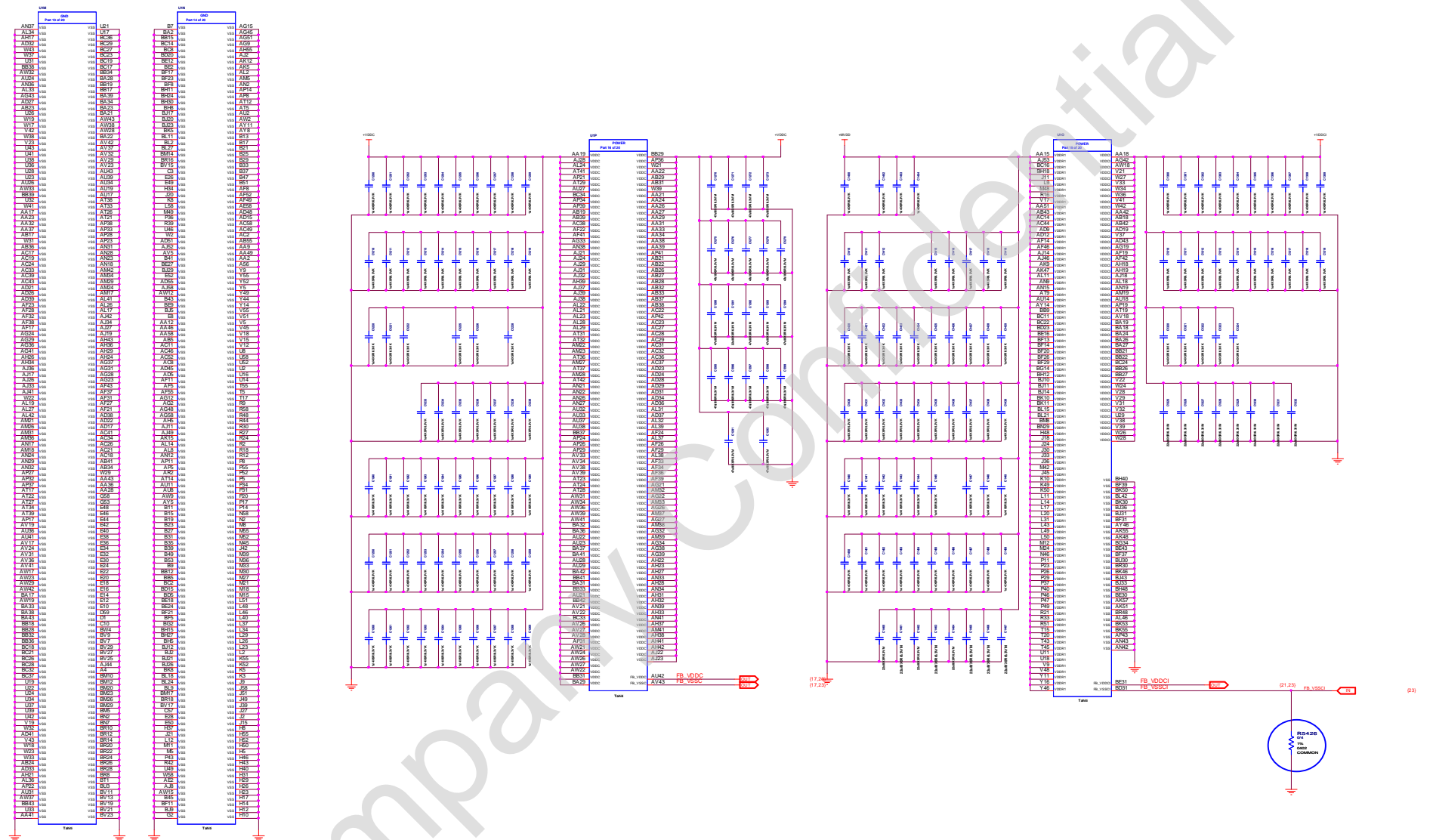


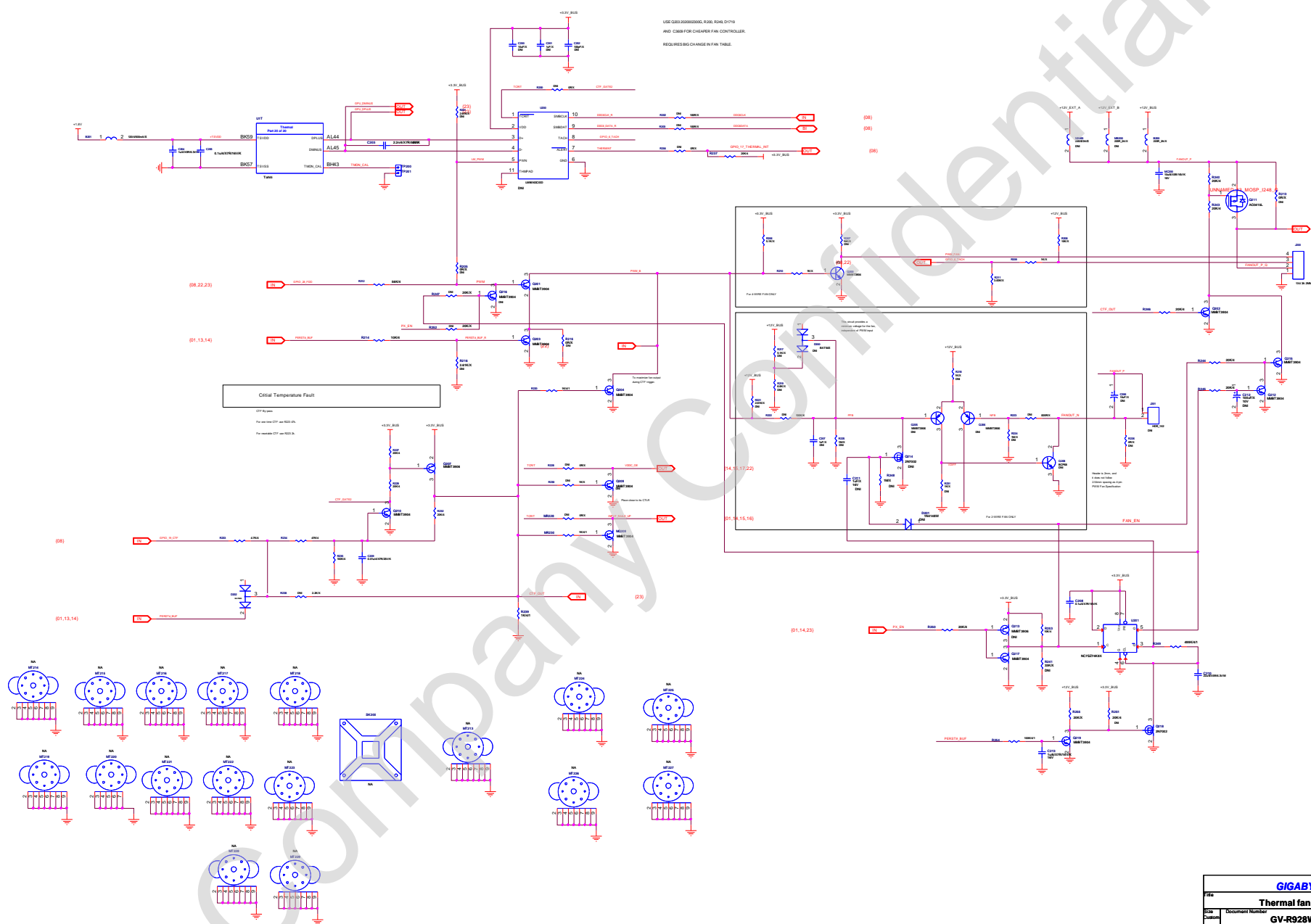




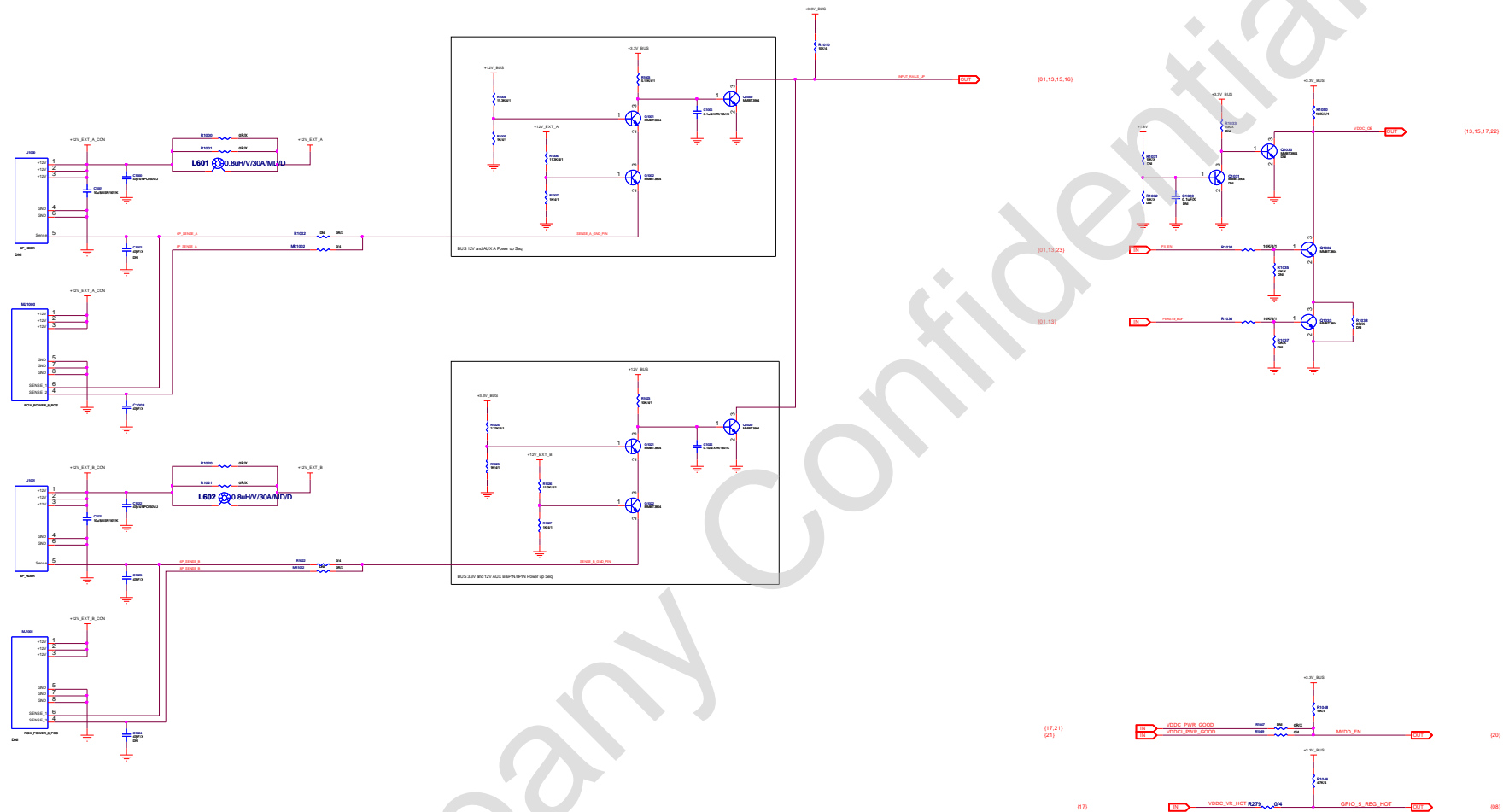


GIGABYTE			
Title			
DW EF DAC			
Size	Document Number		Rev
Custom	GV-R928WF3-3GD		1.0
Date:	Wednesday, May 21, 2014	Sheet	11 of 23

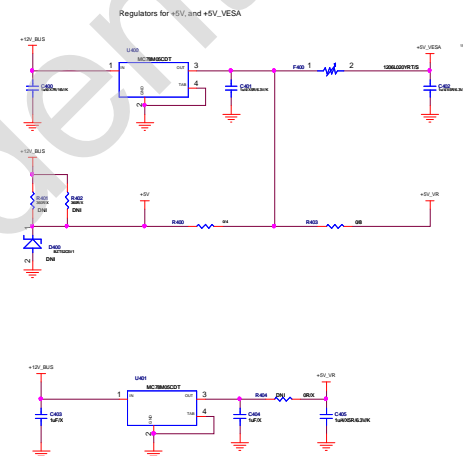




(14) POWER MGMNT

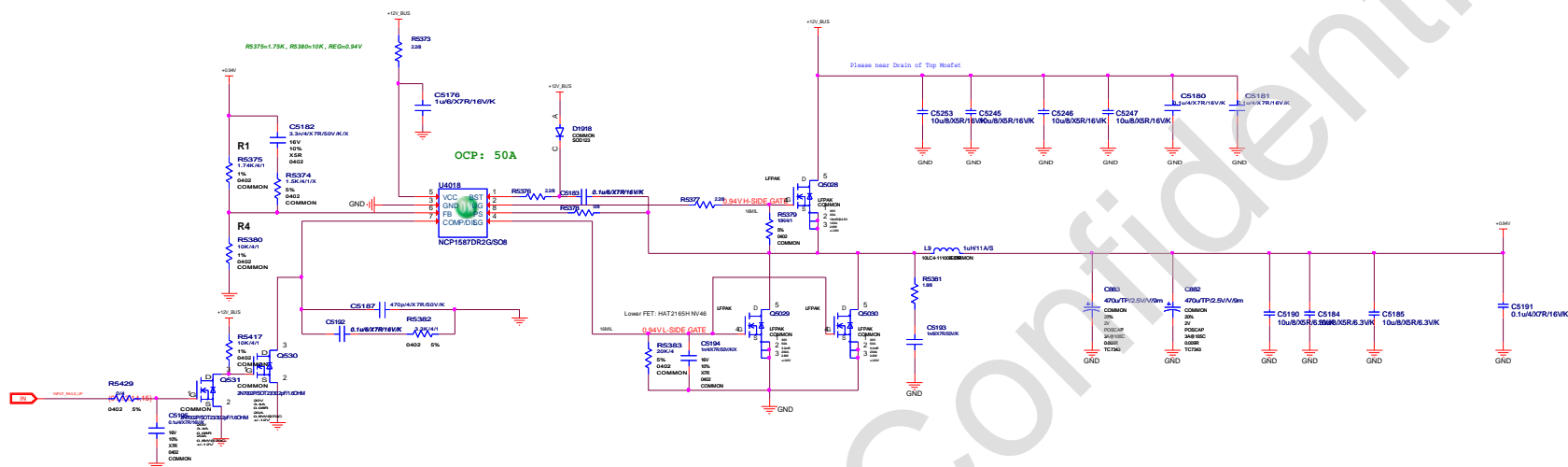


(15) BIF, 5V, 1.8V REG



GIGABYTE			
Title			
5V,1.8V REG			
Size	Document Number		Rev
Custom	GV-R928WF3-3GD		1.0
Date:	Wednesdav, May 21, 2014	Sheet	16 of 23

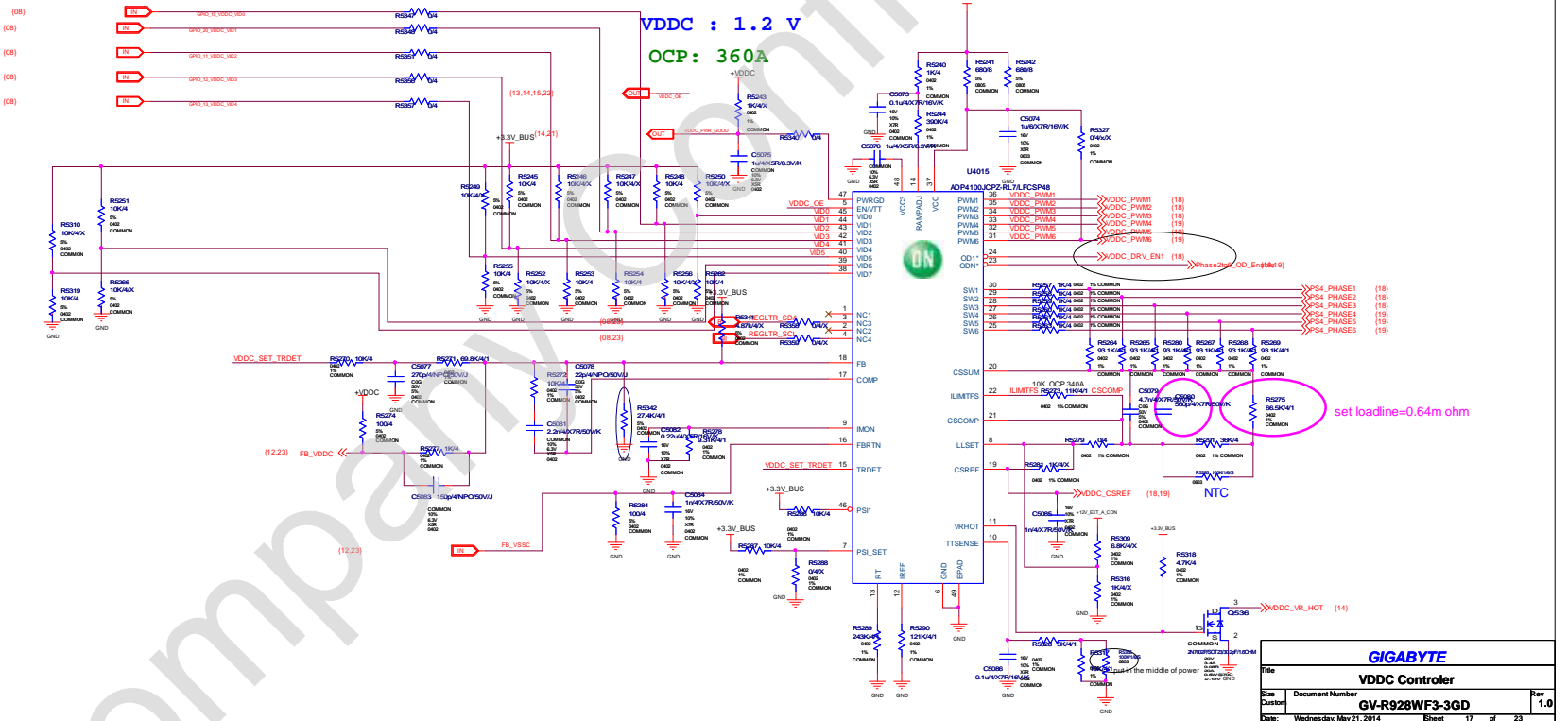
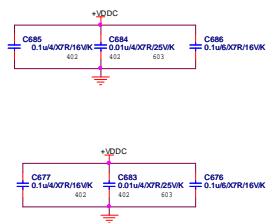
(16) 0.94V REG



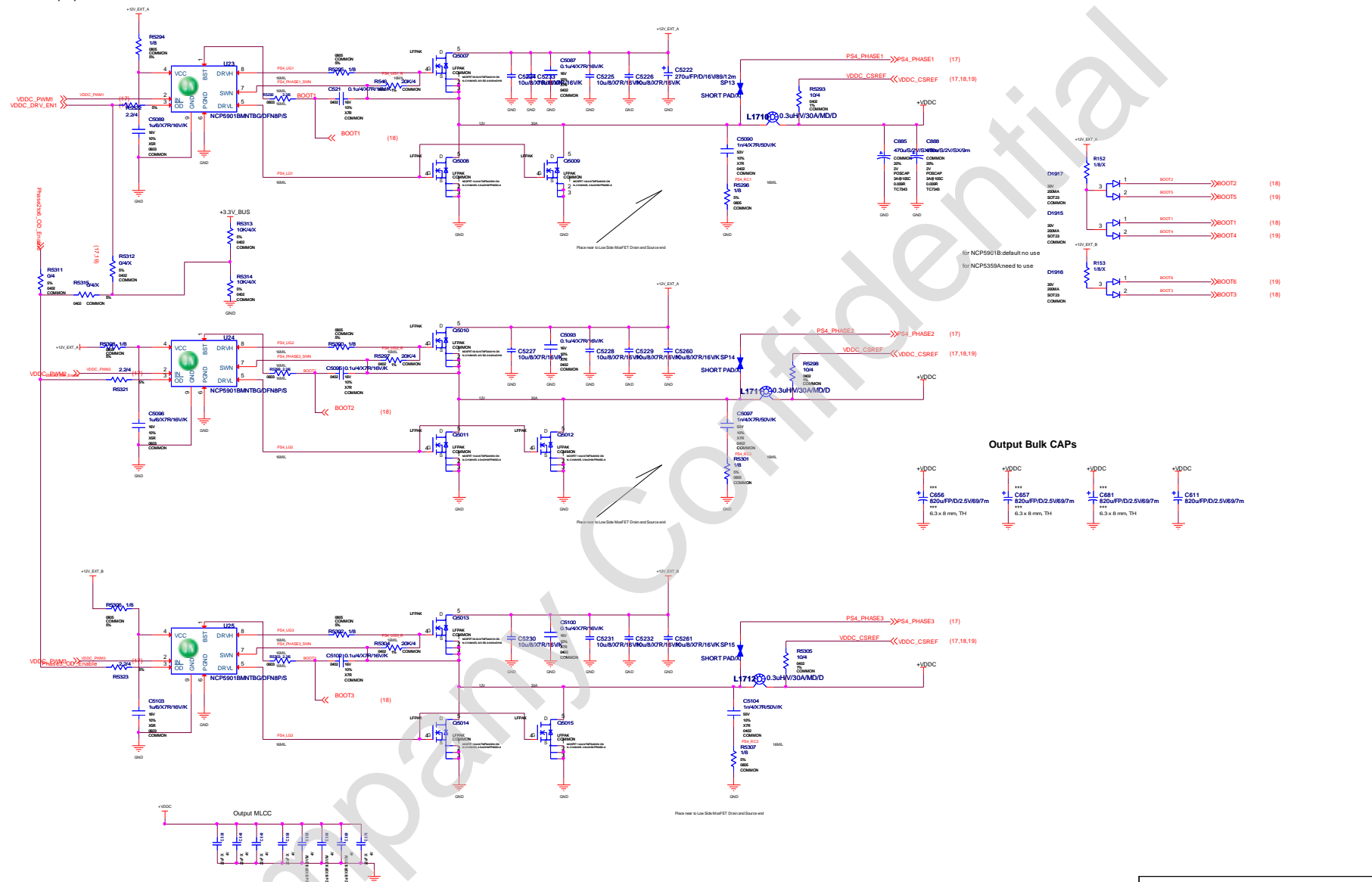
For UPI329 (2481023002), voltage with following resistors:

VIO2: 1.2 * R322/R302
VIO1: 1.2 * R325/R302
VIO2: 1.2 * R320/R302
VIO3: 1.2 * R315/R302

(17) VDDC

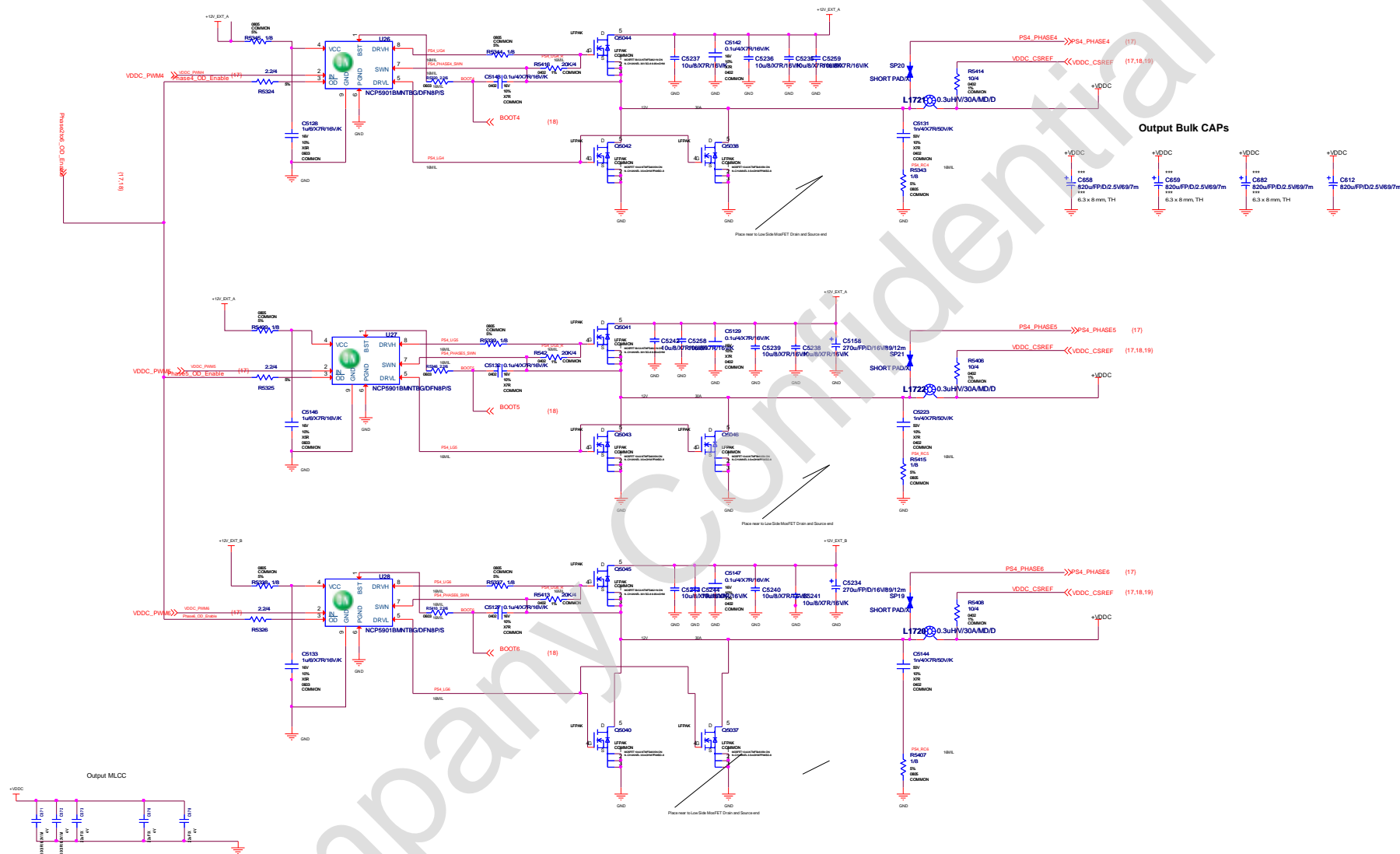


(18) VDDC Phase 1,2,3

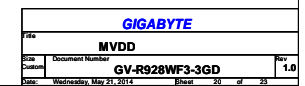


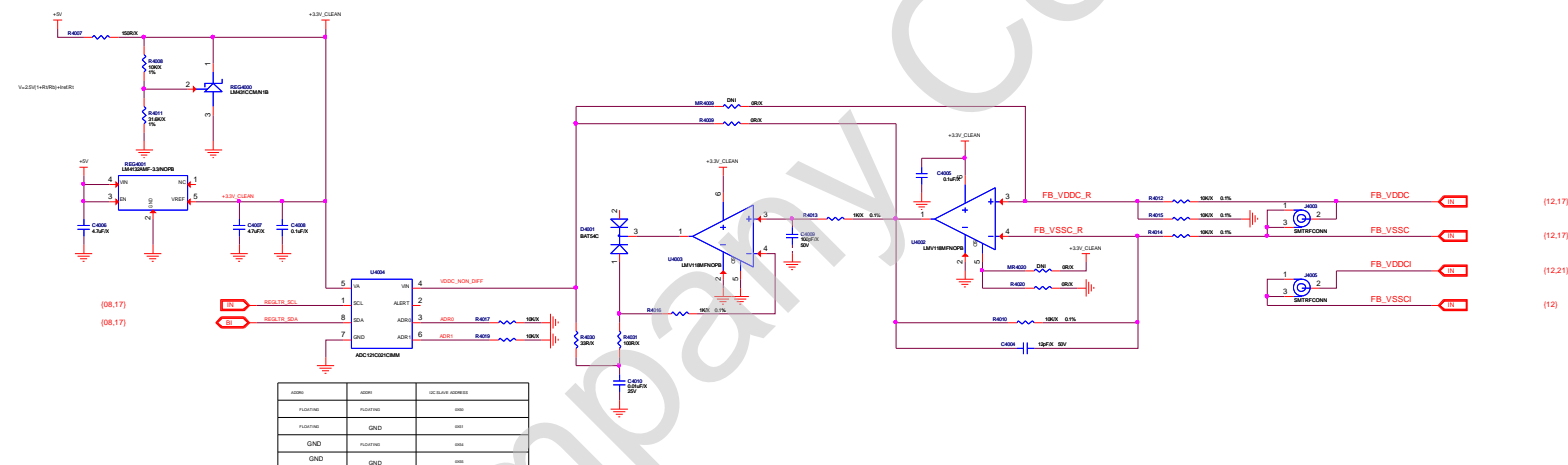
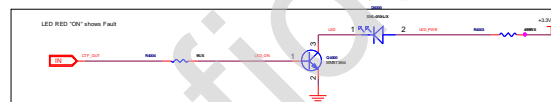
GIGABYTE			
Title VDDC Phase 1,2,3			
Size	Document Number		Rev
Custom	GV-R928WF3-3GD		1

(19) VDDC Phase 4,5,6



GIGABYTE			
Title VDDC Phase 4,5,6			
Size	Document Number		
Custom	GV-R928WF3-3GD		
Date:	Wednesday, May 01, 2002 4:00 PM	Sheet:	10 of 55





ADDRESS	ADDRESS	USE SLAVE ADDRESS
FLOATING	FLOATING	000
FLOATING	GND	001
GND	FLOATING	004
GND	GND	005

GIGABYTE			
Title DEBUG Circuit			
Size Custom	Document Number GV-R928WF3-3GD		Rev 1.
Date: Wednesday, May 24, 2006 10:54 AM Page 93 of 93			