P2091-A00 GK107, MXM V3. 0/ V3. 1 TYPE A 2048MB 128BIT sDDR3 LVDS, QUAD DP, DVI, VGA TABLE OF CONTENTS Page Description Table of Contents PCI EXPRESS Interface Frame Buffer A GPU Interface Frame Buffer B GPU Interface Frame Buffer Partition A Frame Buffer Partition B Memory Decoupling Caps DACA, PLL, CRYSTAL DP LINKS C, D, E, F MXM Connector, IO-Section 11 GPIOs. JTAG, Thermal Sensor, Info ROM 12 LVDS, VBI 0S GPU VDD\DCPLNG\GND 13 NVVDD controller, NVDD phase 1 and 2 driver Stages 14 15 FBVDDQ PEXVDD Power Supplies 16 STRAPS and MOUNTING HOLES NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA NV_PN 600-52091-0501-000 ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED 'AS IS'. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS































