

PG301 A02

Comanche 192b GDDR5, <150W, 2-way SLI
Tall DVI-I + DP + DP + DP/HDMI + DP

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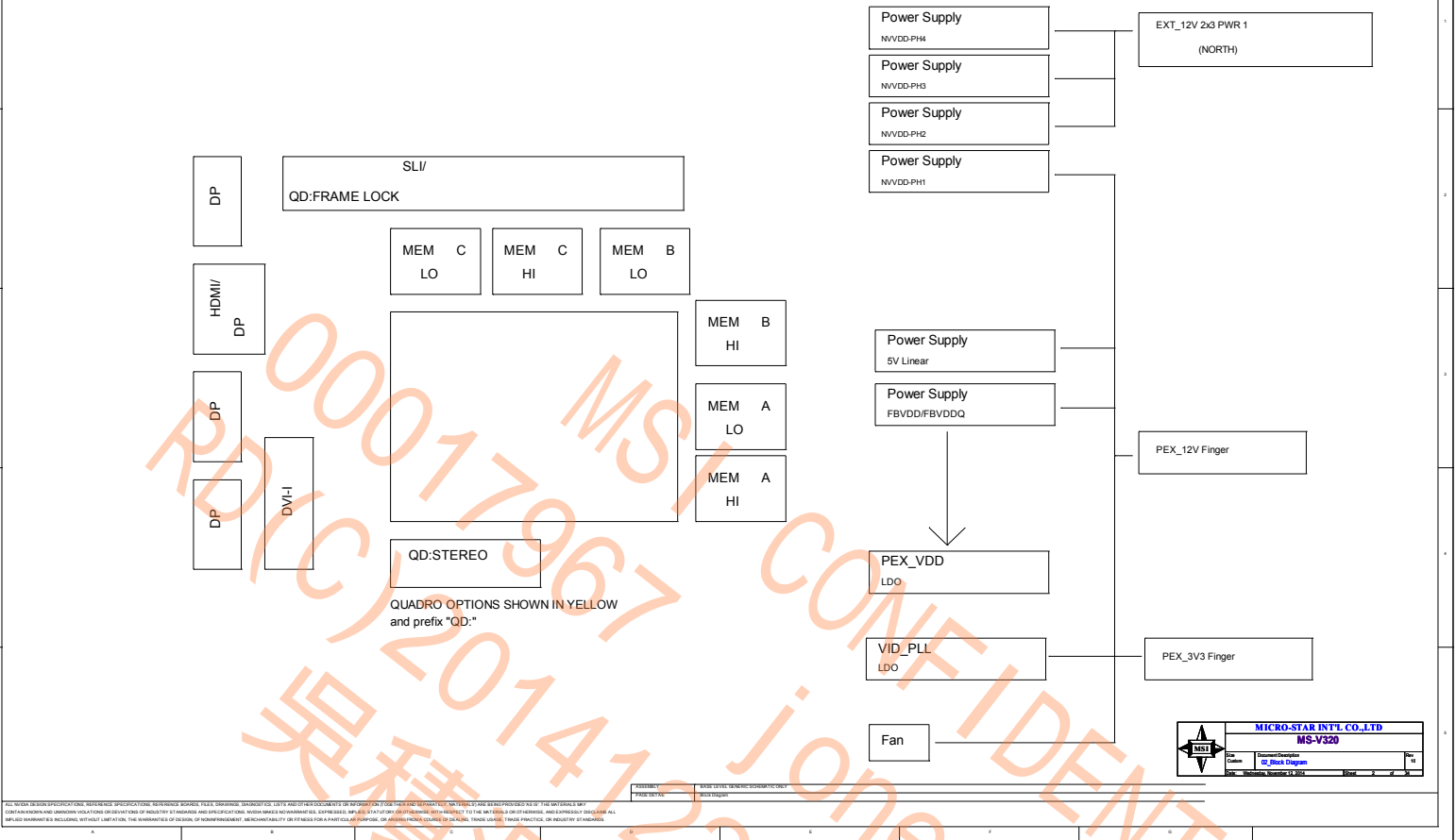
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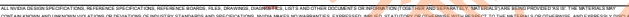
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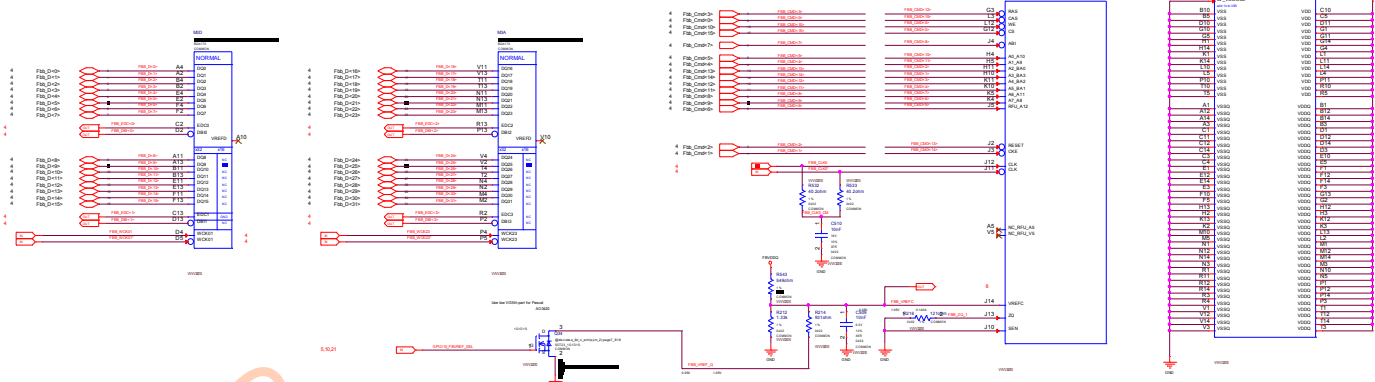


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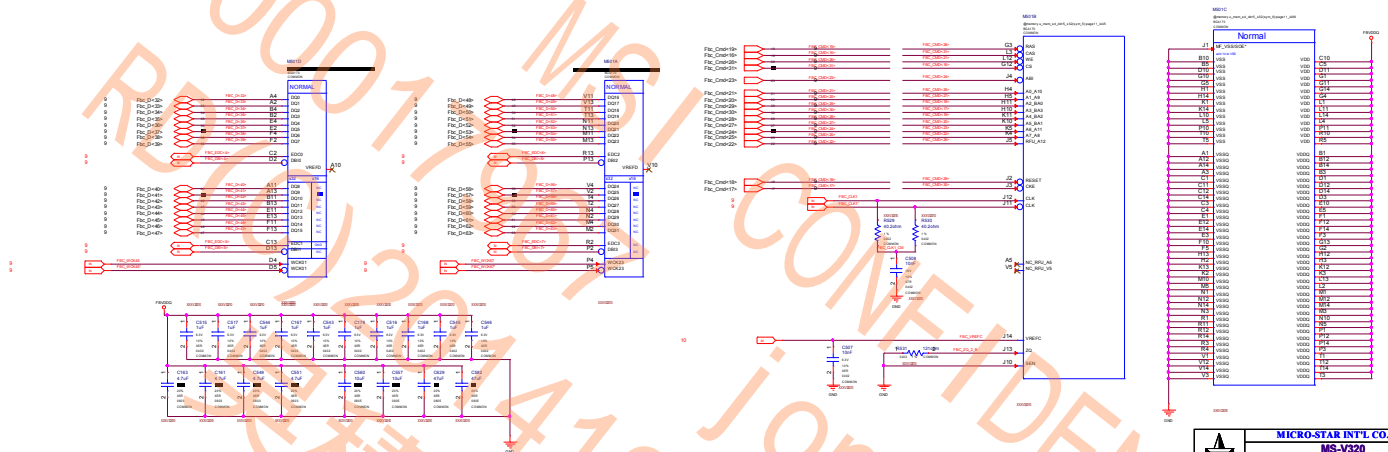


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MS-V320	
Doc No.	MS-V320-01
Rev.	1.0
Issue Date	2014/12/29
Issue By	W. J. J.
Issue For	W. J. J.







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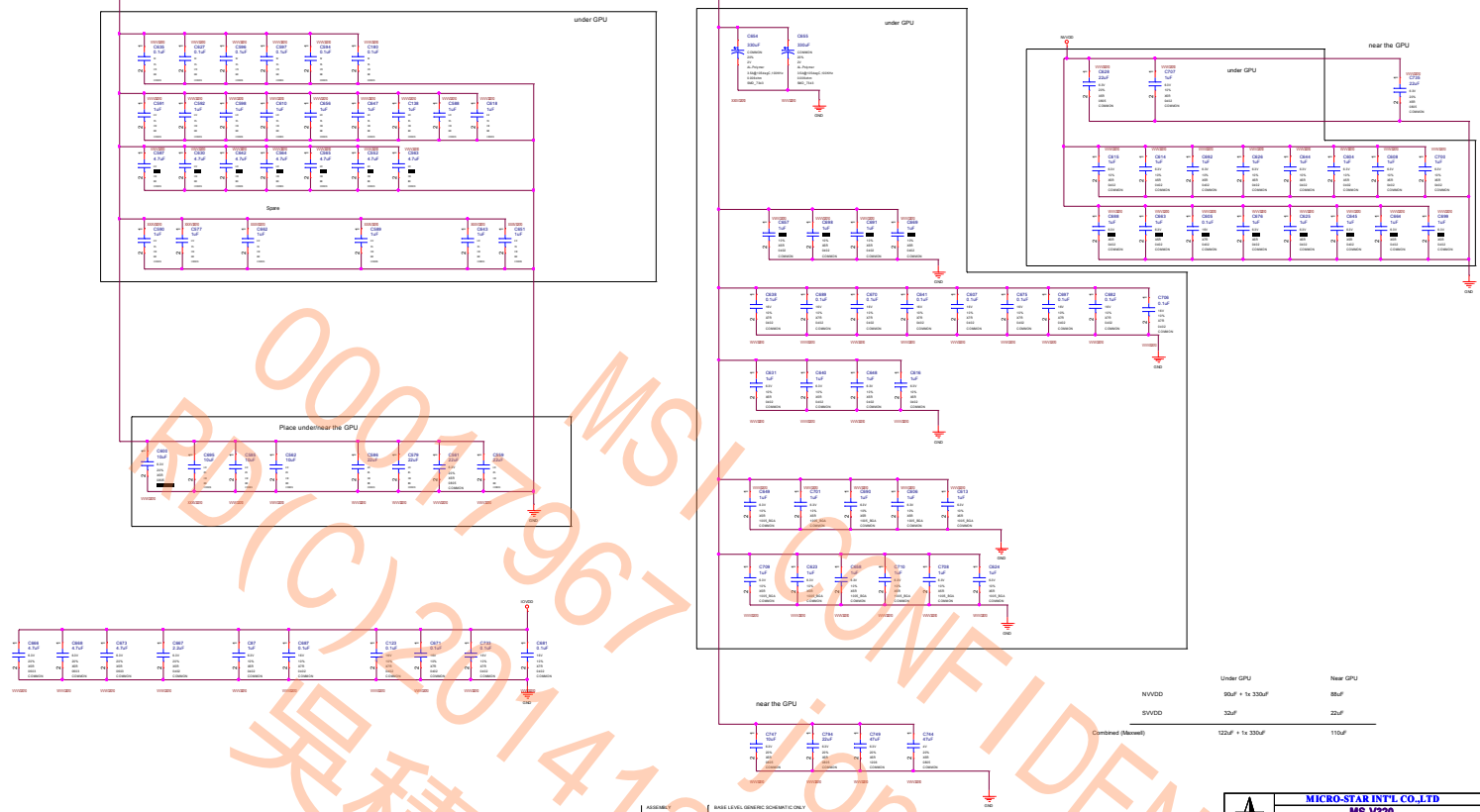
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GPU Decoupling

FBVDDQ

NVDD



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ASSEMBLY
PAGE 01/01

BASE LEVEL SCHEMATIC ONLY
GPU Decoupling



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MS-V320
Date: 2014/12/22
Rev: 1.0

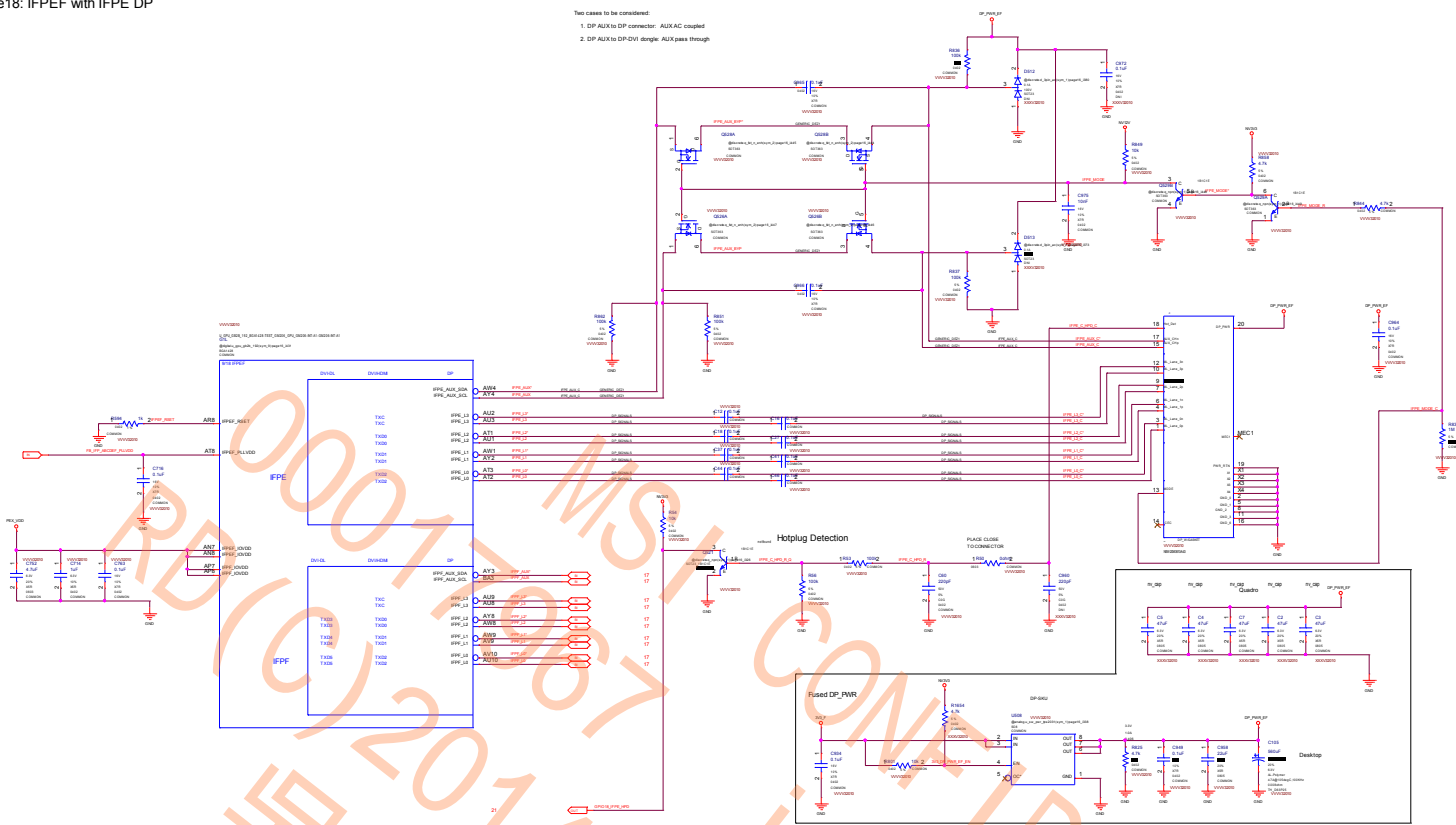


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Two cases to be considered:

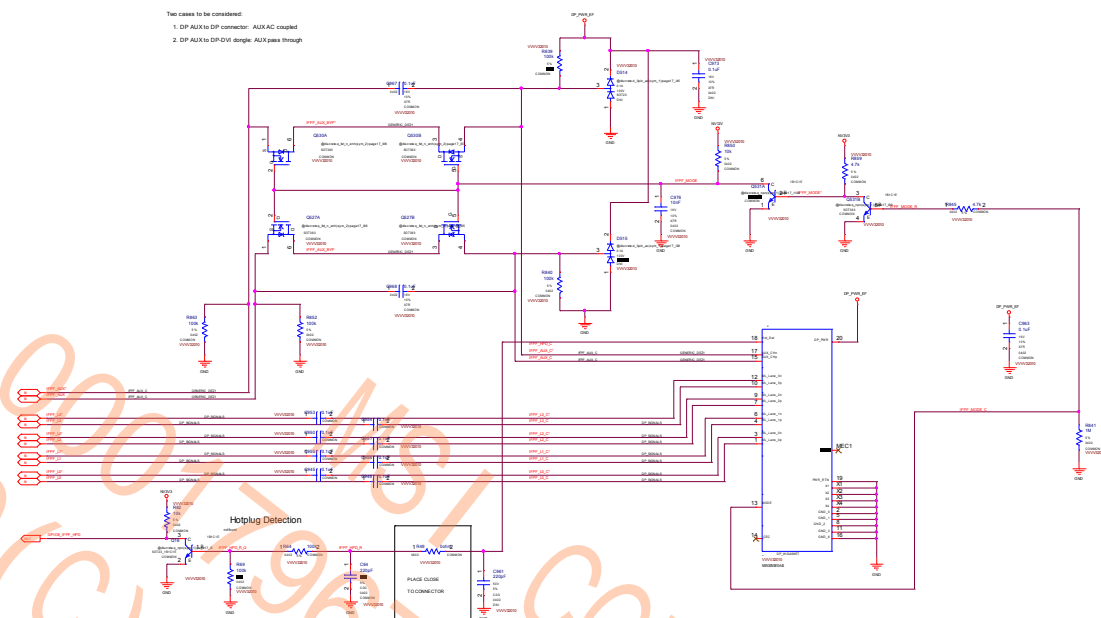
1. DP AUX to DP connector: AUXAC coupled
2. DP AUX to DP-DVI dongle: AUX pass through



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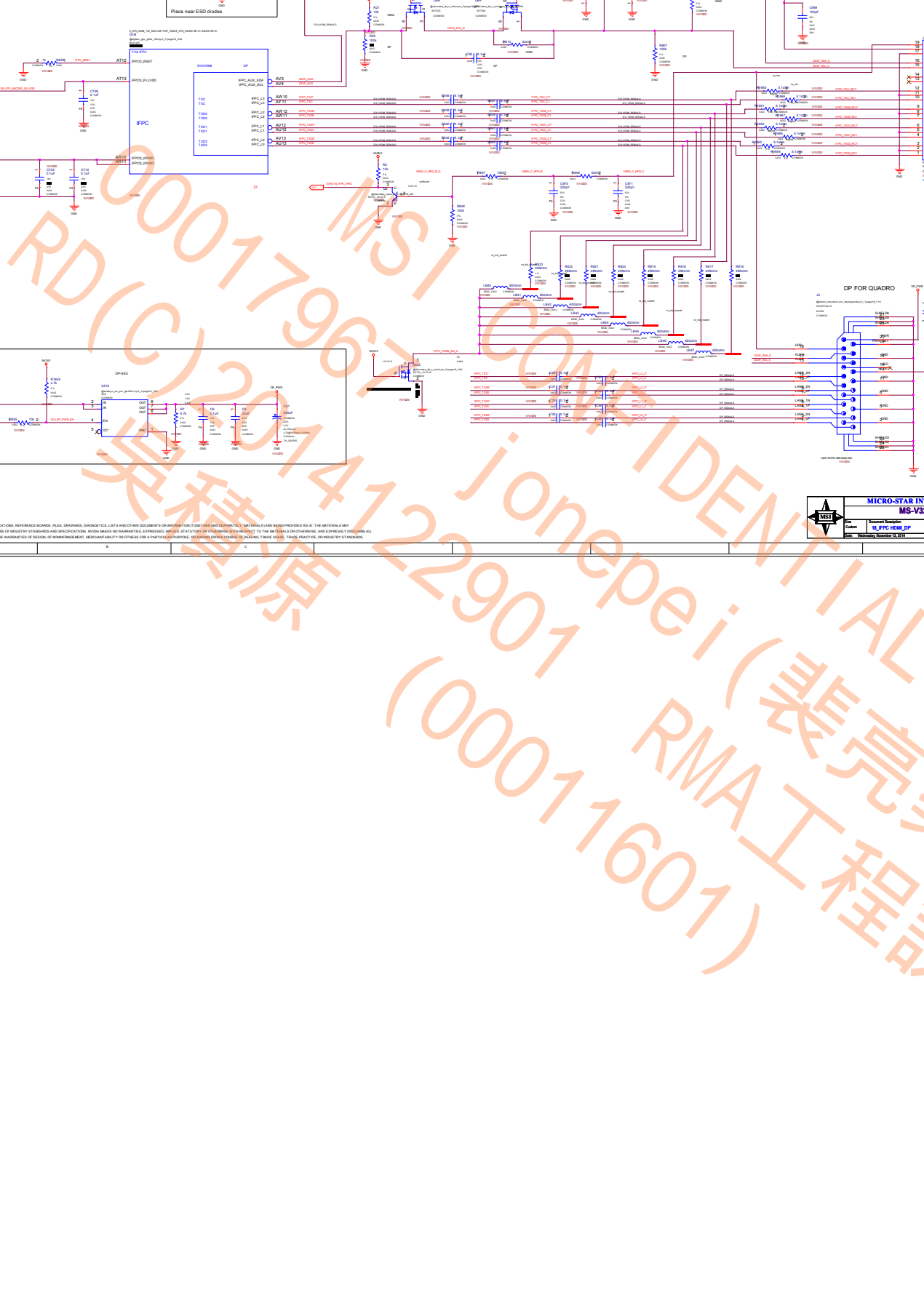


- Two cases to be considered:
1. DP AUX to DP connector: AUXKAC capped
 2. DP AUX to DP-DVI single: AUX pass through

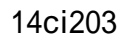


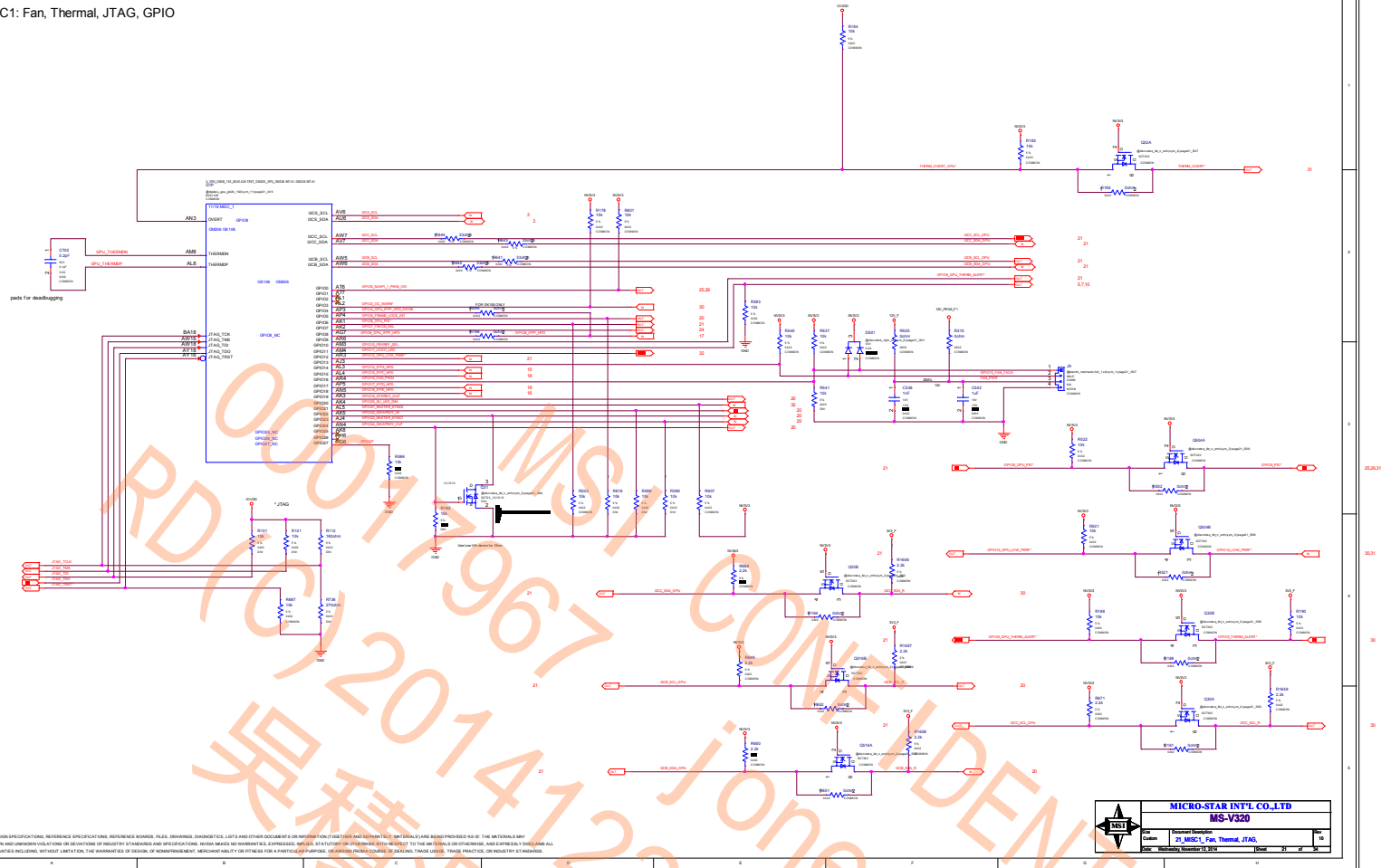
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MS-V320	
Rev	1.0
Doc No	17-099 DP
Rev	1.0



1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through





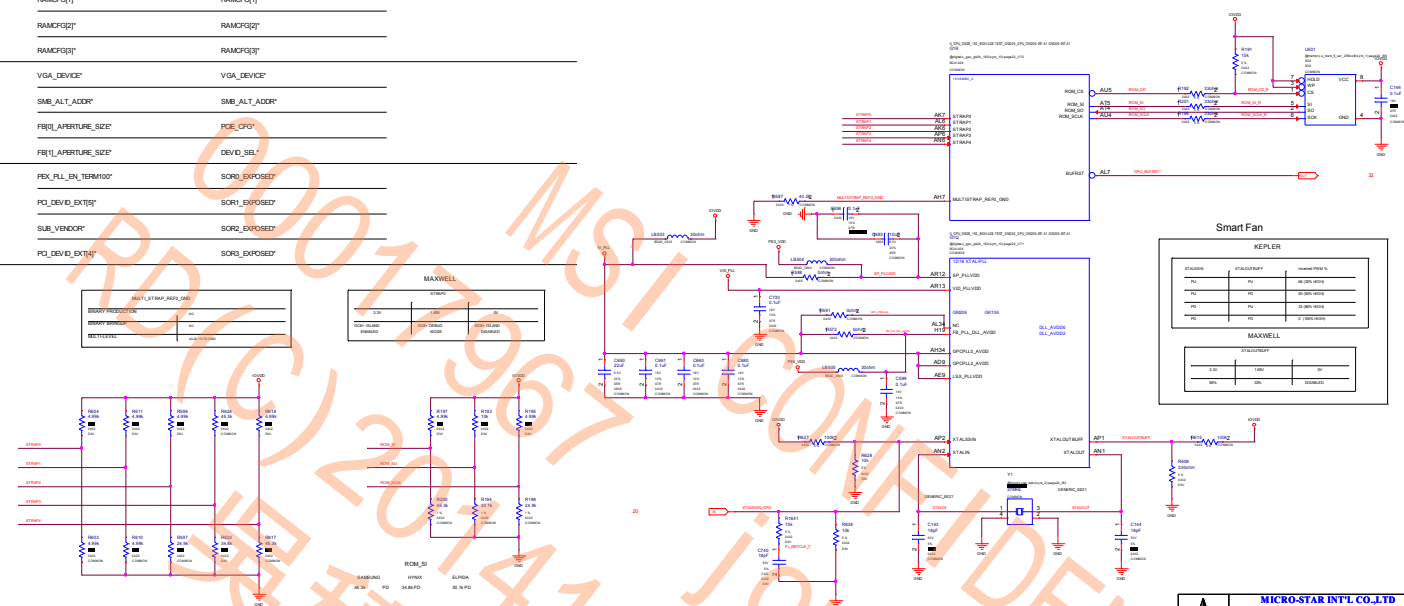
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MS-V320	
21_MISC1_Fan_Thermal_JTAG	
Rev. 1.0	

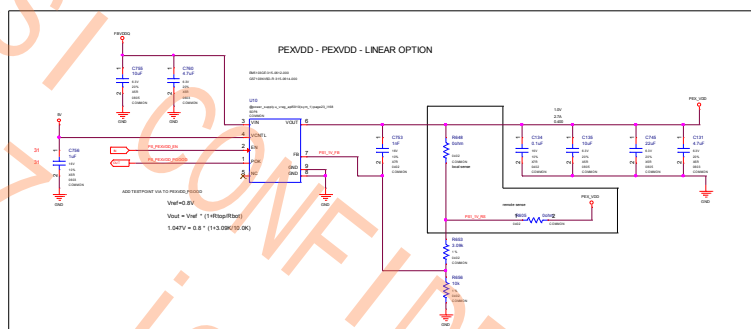
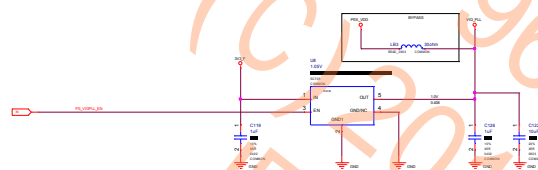
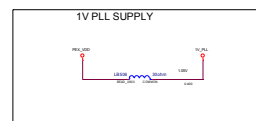
MISC2: ROM, XTAL, Straps

	KEPLER	MAXWELL
STRAP0	UBER_INT[3:0]'	
STRAP1	3G0_PADCFG1_UT_ADDR'	
STRAP2	PC1_DEVID[3:0]'	
STRAP3	SOR1_EXPOSED[3:0]'	
STRAP4	DP_FLT_VDDO_33V"	
	PEX_MAX_SPEED'	
	PEX_SRP_CHANGE_GEN0"	
	*	
	RAMCFG[0]'	RAMCFG[0]'
	RAMCFG[1]'	RAMCFG[1]'
	RAMCFG[2]'	RAMCFG[2]'
	RAMCFG[3]'	RAMCFG[3]'
ROM_S1	VGA_DEVICE"	VGA_DEVICE"
	SMB1_AL1_ADDR"	SMB1_AL1_ADDR"
	FB[0]_APERTURE_SIZE"	PCIE_CPO"
	FB[1]_APERTURE_SIZE"	DEVID_SBL'
	PEX_FLT_EN_TERM100"	SOR0_EXPOSED'
	PC1_DEVID_EXT[0]'	SOR1_EXPOSED'
	SUB_VENDOR'	SOR2_EXPOSED'
	PC1_DEVID_EXT[4]'	SOR3_EXPOSED'
ROM_SCLK		

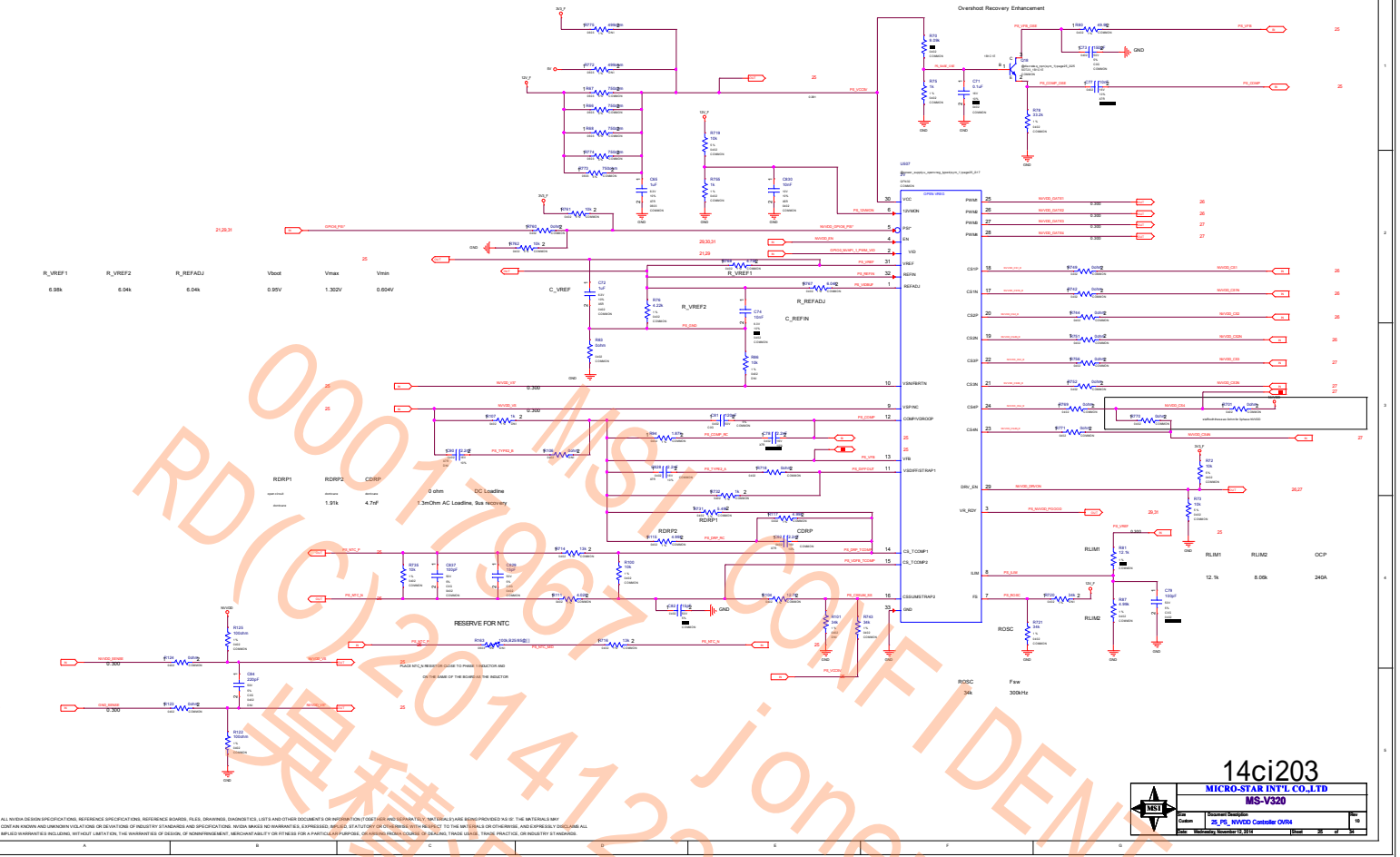
	GND	3V3
5k	0000	1000
10k	0001	1001
15k	0010	1010
20k	0011	1011
25k	0100	1100
30k	0101	1101
35k	0110	1110
45k	0111	1111



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	MS-V320		
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Order: _____ Date: _____			

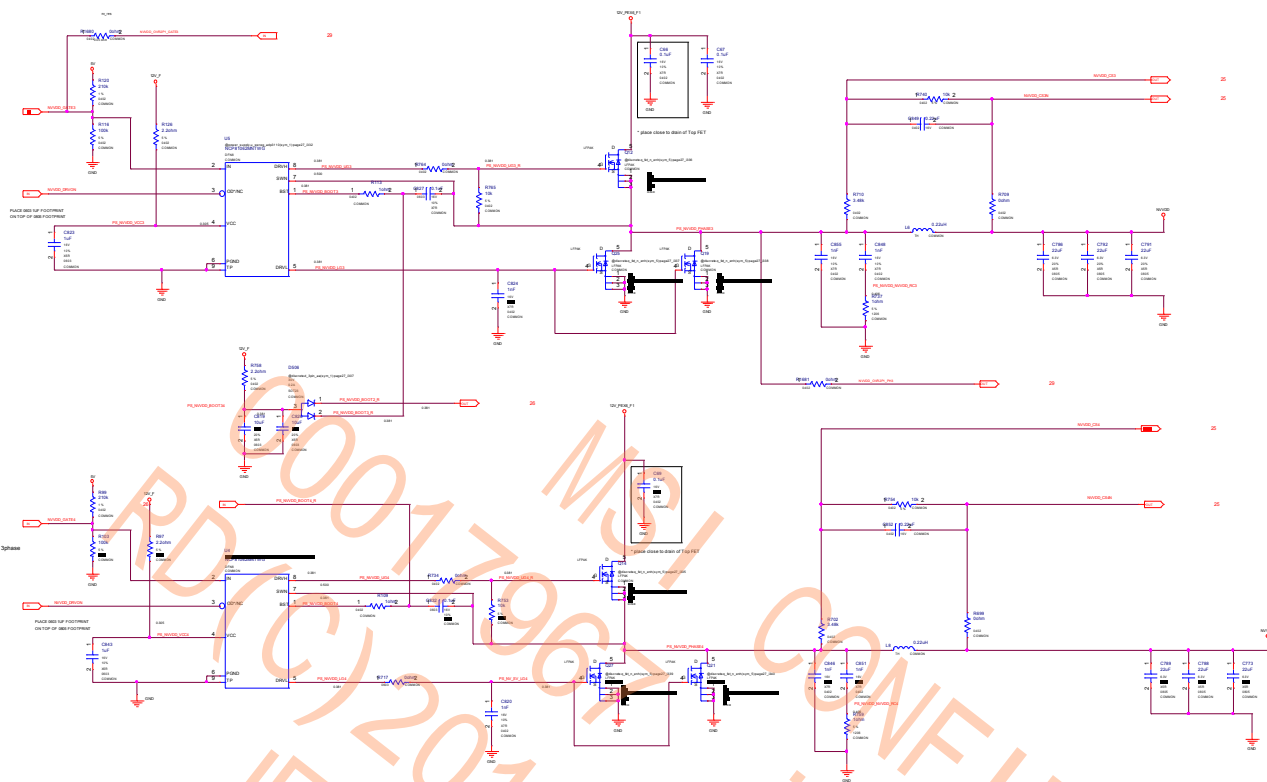


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14ci203
MICRO-STAR INT'L CO., LTD.
MS-V320

Part Number	14ci203
Manufacturer	MICRO-STAR INT'L CO., LTD.
Package	MS-V320
Pin Count	28
Operating Voltage	1.2V to 1.8V
Operating Current	10mA
Operating Temperature	-40°C to 85°C





14ci203

MICRO-STAR INT'L CO., LTD.

MS-V320

Rev	1
Date	27 Jul 2003
Drawn	27 Jul 2003
Check	27 Jul 2003
Appr	27 Jul 2003

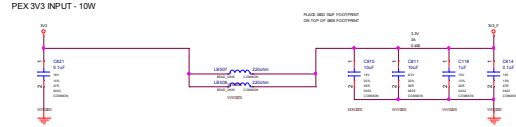
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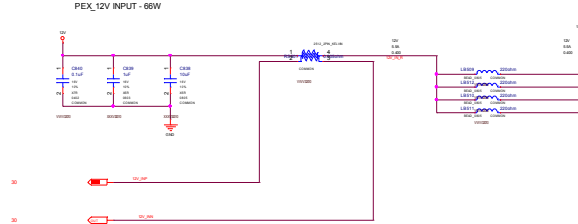
MICRO-STAR INT'L CO., LTD.	
MS-V320	
Rev	01
Issue	01
Date: 2014.11.20	



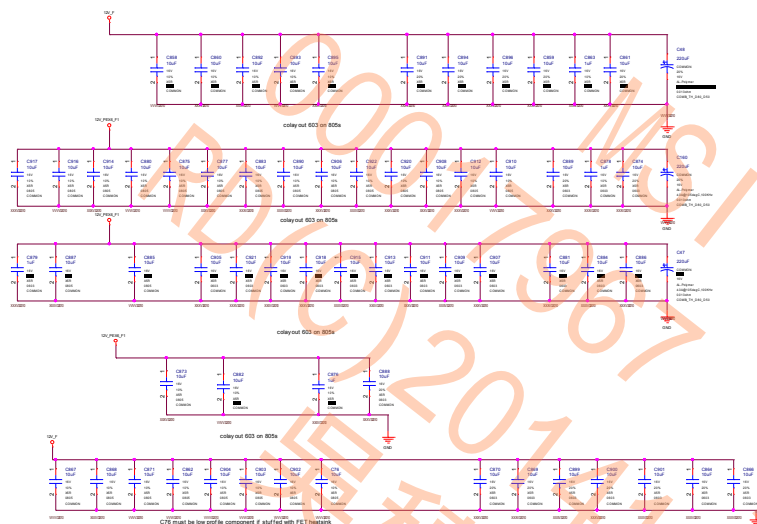
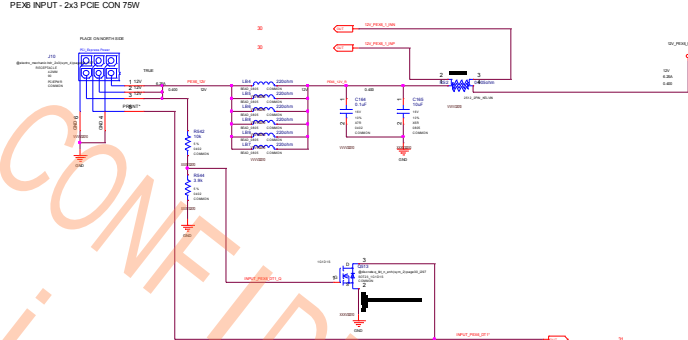
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PEX 12V INPUT - 66W

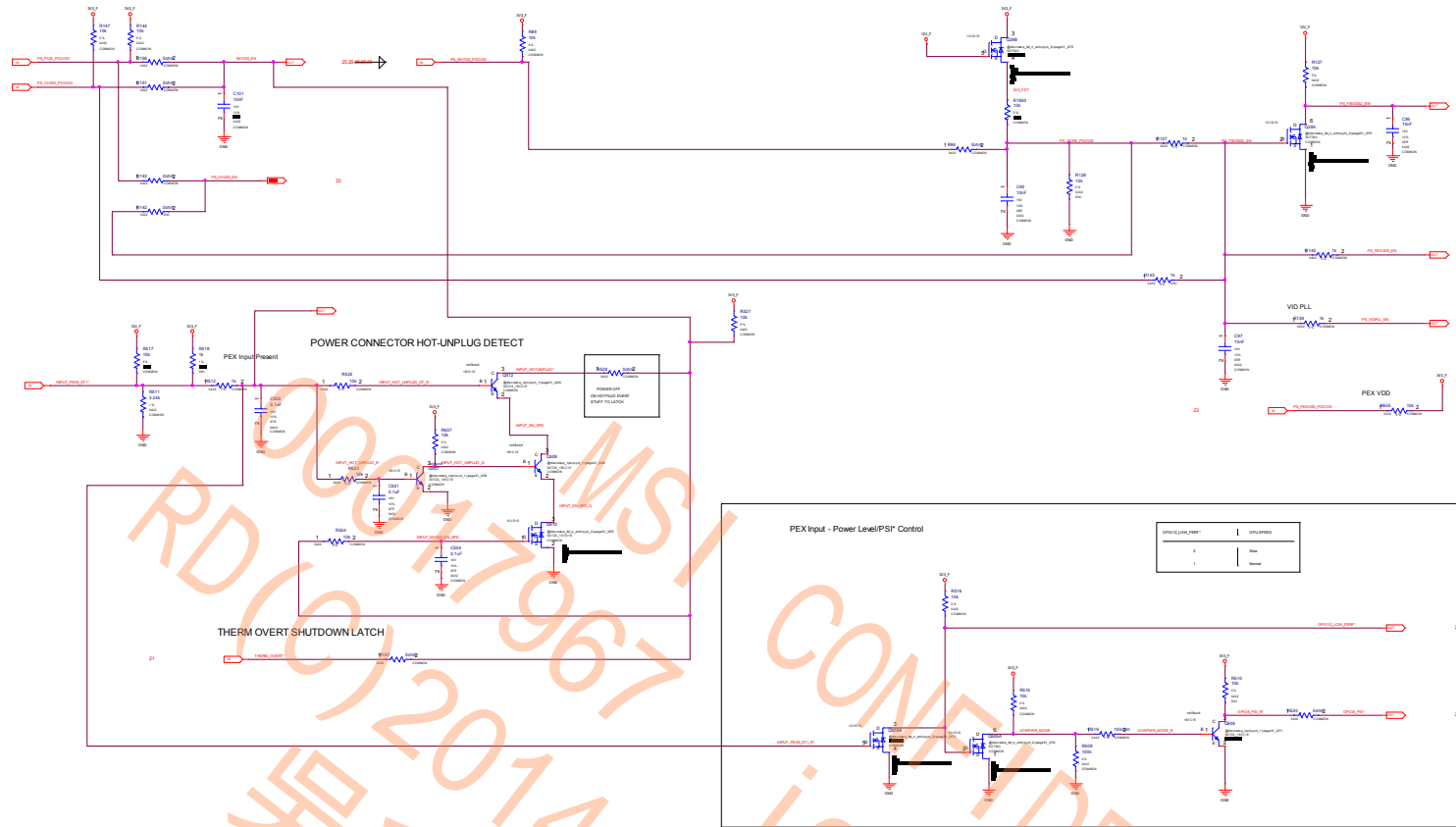


PEX8 INPUT - 2x3 PCIE CON 75W



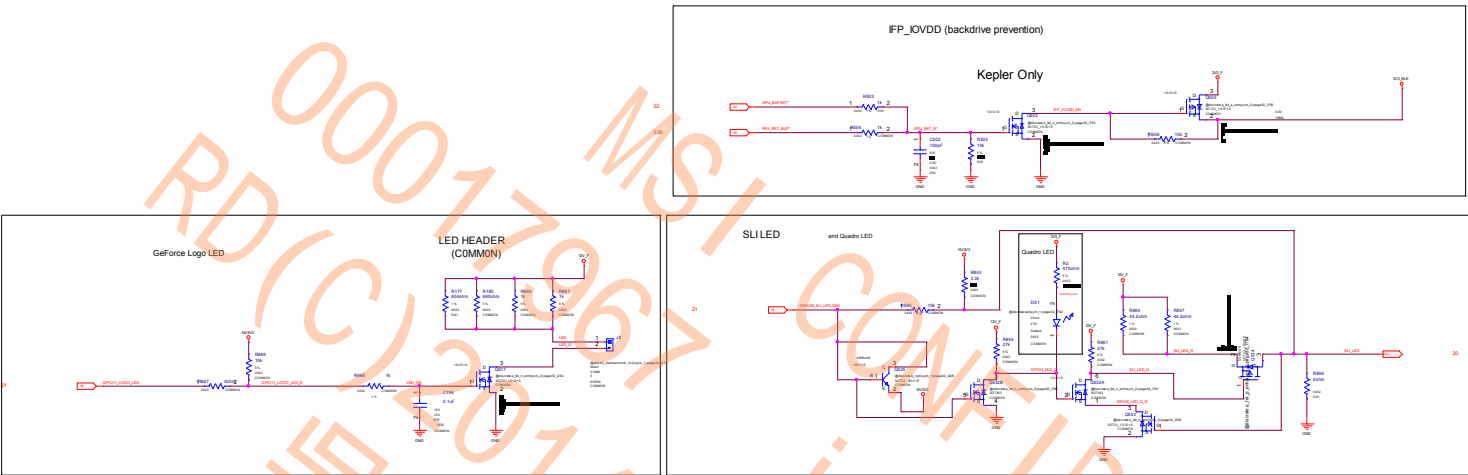
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PS: Sequence and Shutdown

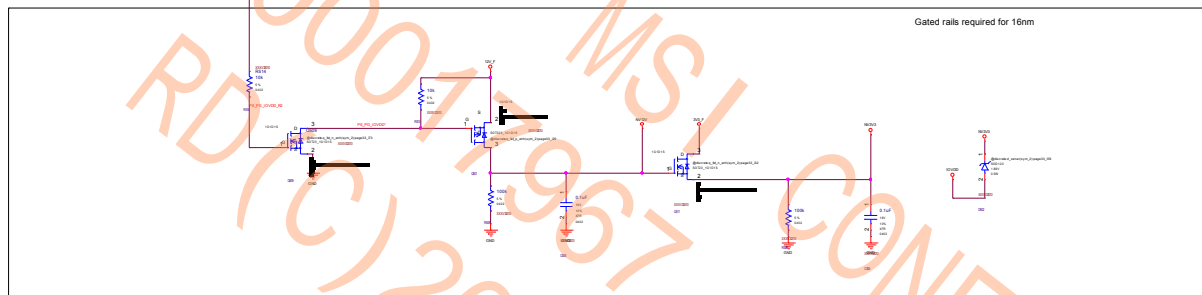
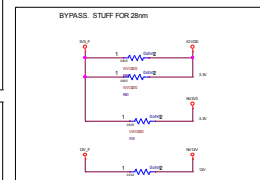


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Rev. 1.0	
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Appr: [Signature]	



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