G94-P562-A00 - GDDR3, DVI/VGA + DVI/VGA

sku	VARIANT	NVPN	ASSEMBLY
B	BASE	600-10562-base-000	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	sku0000	600-10562-0000-000	G94-400 625MHz/900MHz 512MB 16Mx32 BGA136 GDDR3, DVI-I-DL+DVI-I-DL
2	<UNDEFINED	<UNDEFINED>	<UNDEFINED
3	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
4	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
5	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
6	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
7	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
8	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
9	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
10	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
11	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
12	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
13	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
14	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
15	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>

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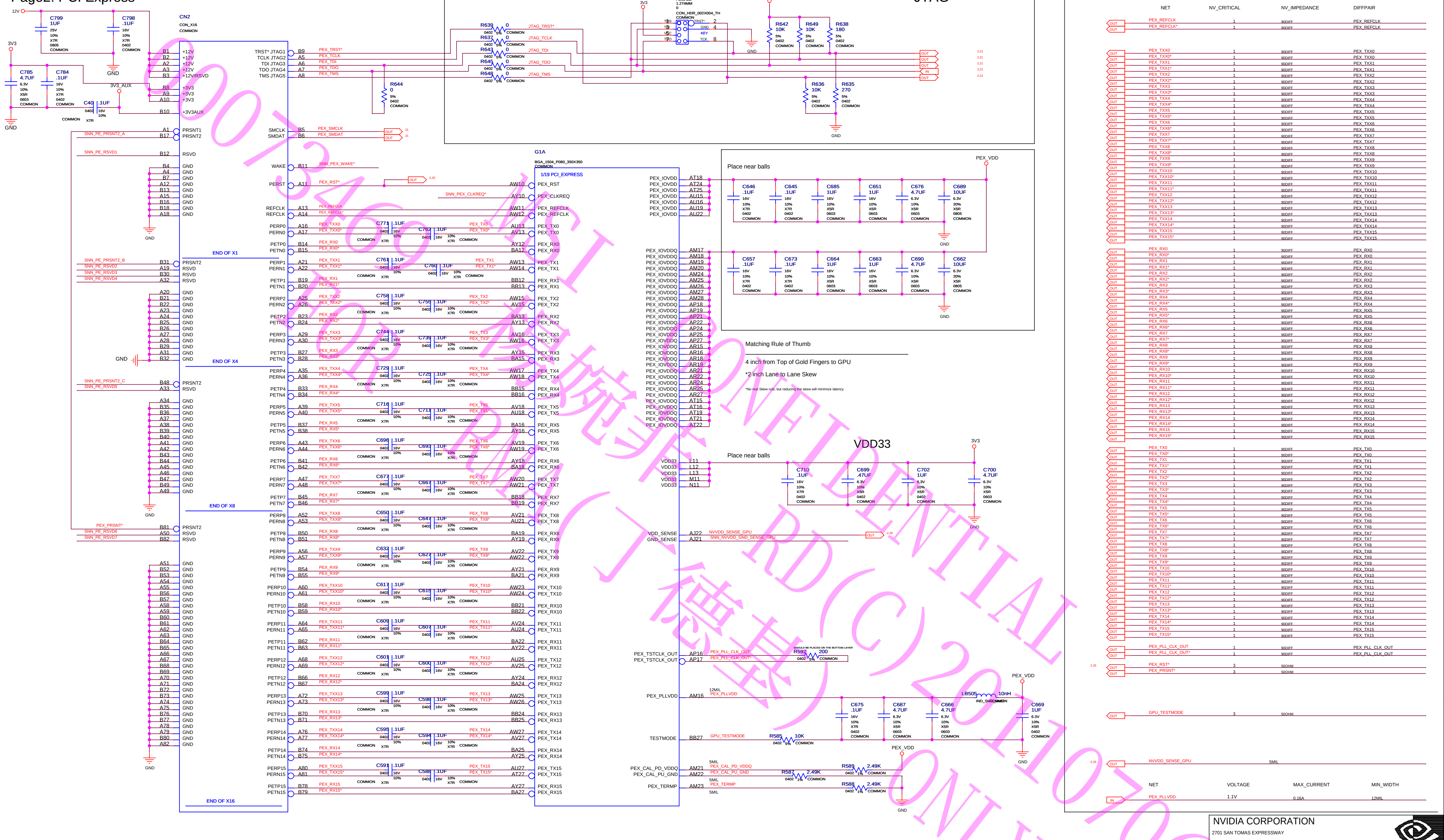
Page2: PCI Express



ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PCI Express

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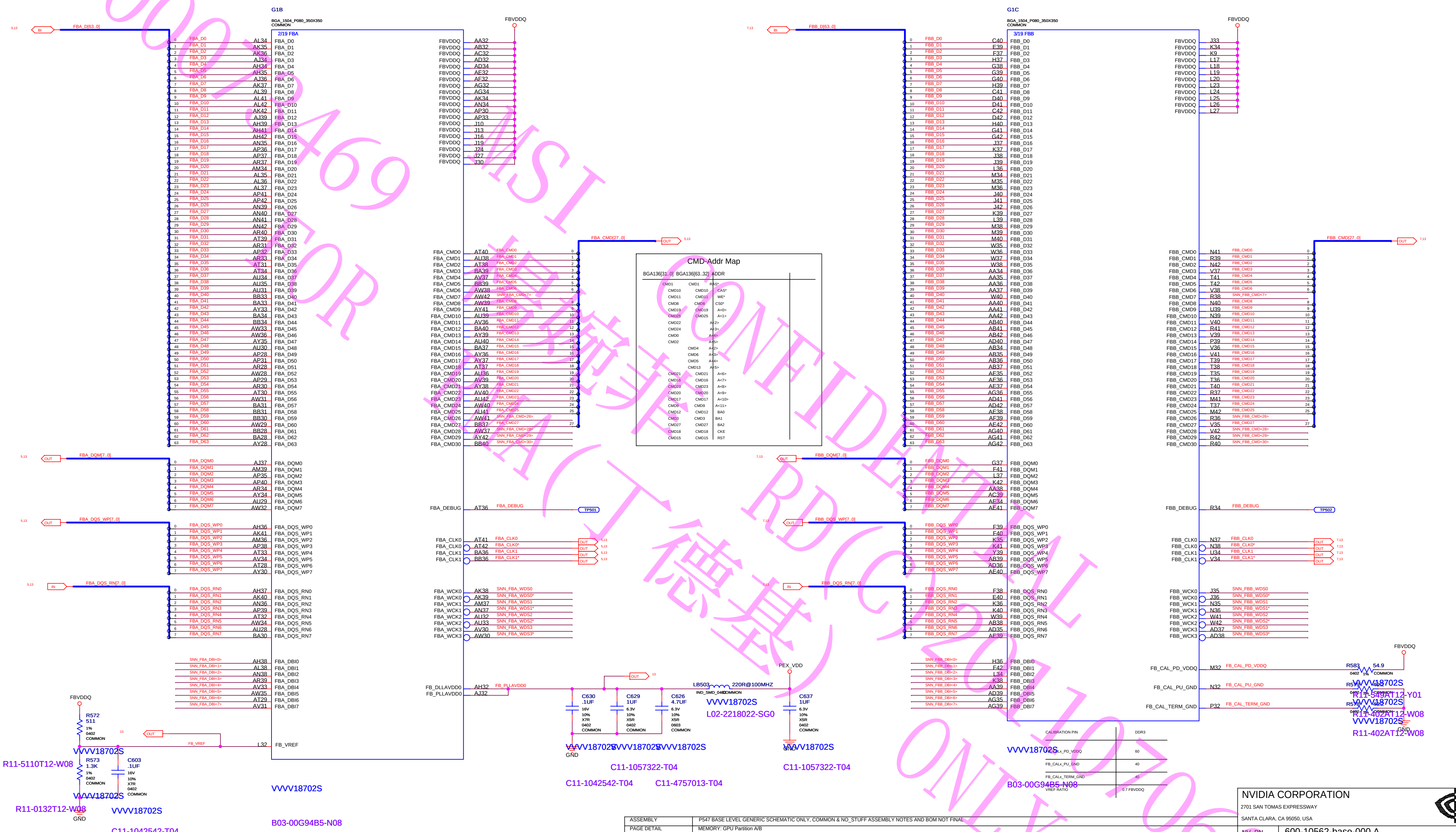
Page3: MEMORY: GPU Partition A/B



ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	MEMORY: GPU Partition A/B

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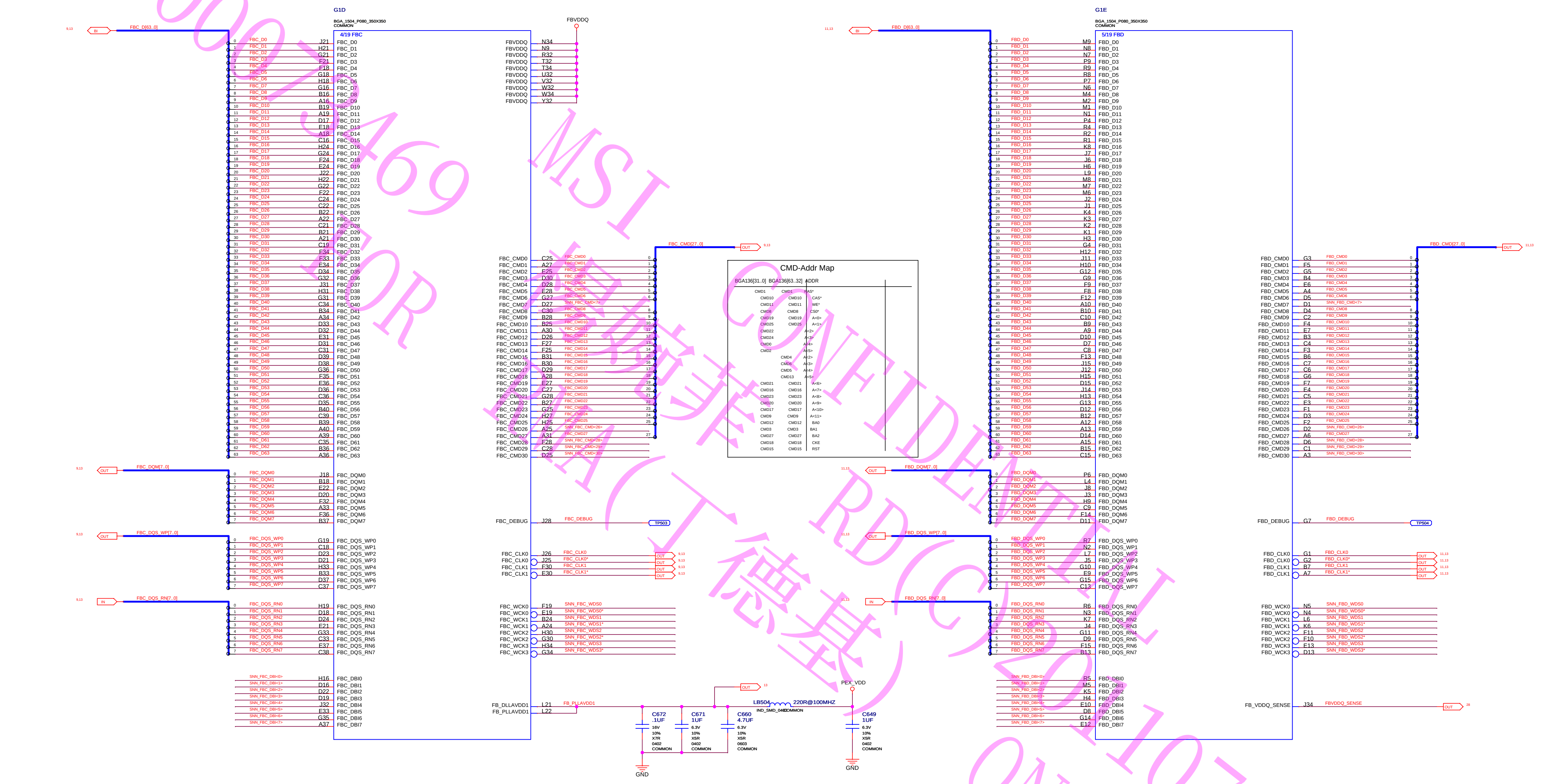
Page4: MEMORY: GPU Partition C/D



ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	MEMORY: GPU Partition C/D

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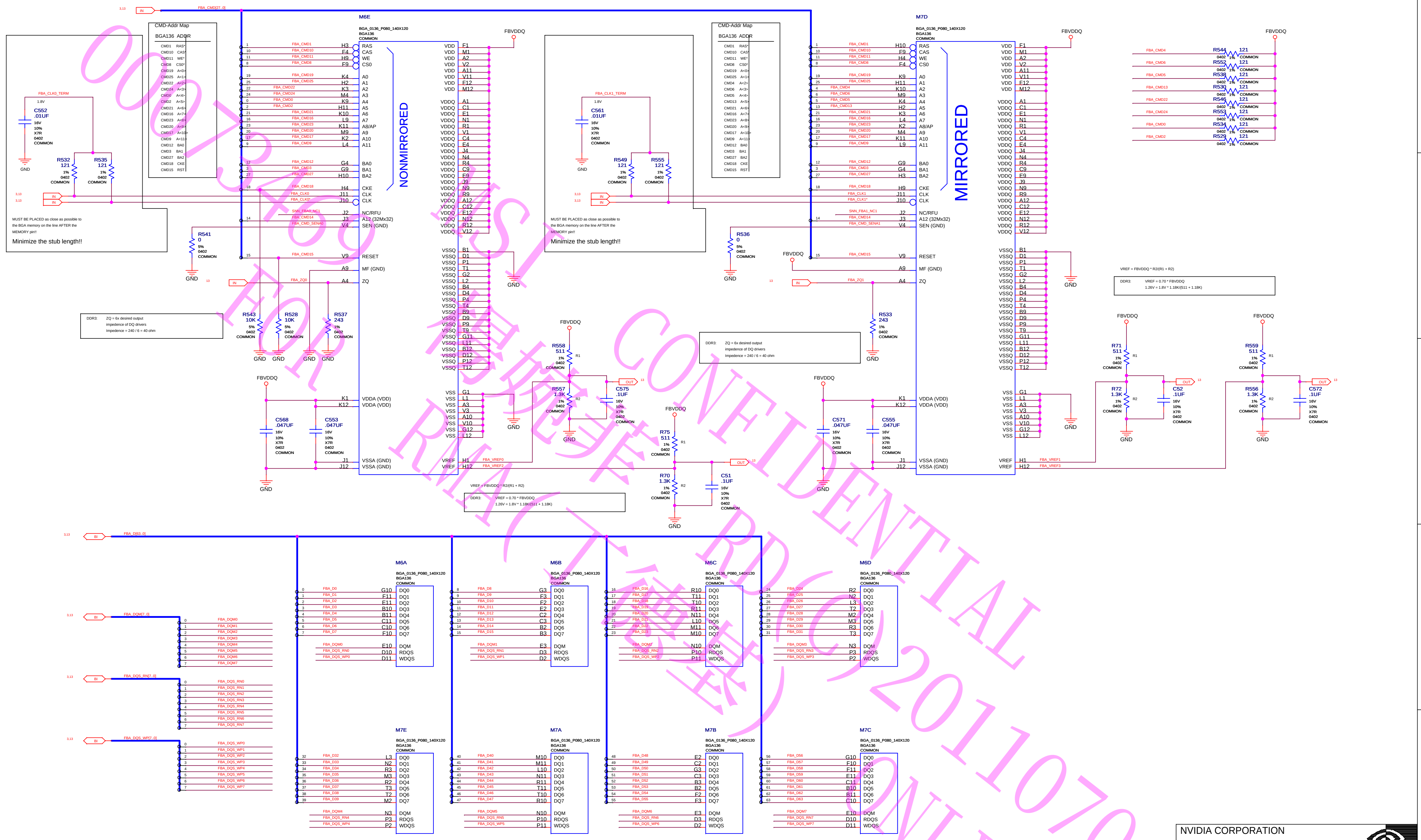
Page5: FBA Partition



ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	FBA Partition

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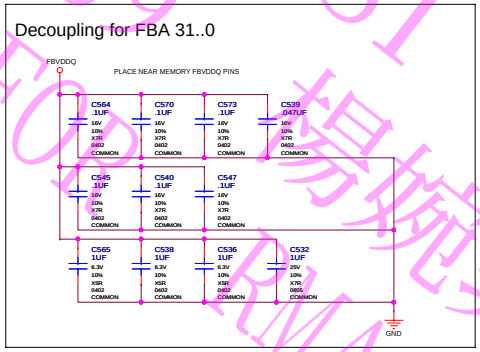
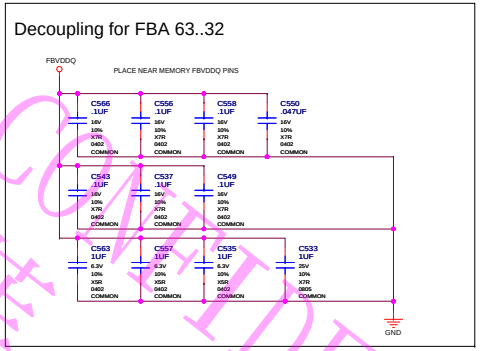
Page6: FBA Partition Decoupling





ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	FBA Partition Decoupling

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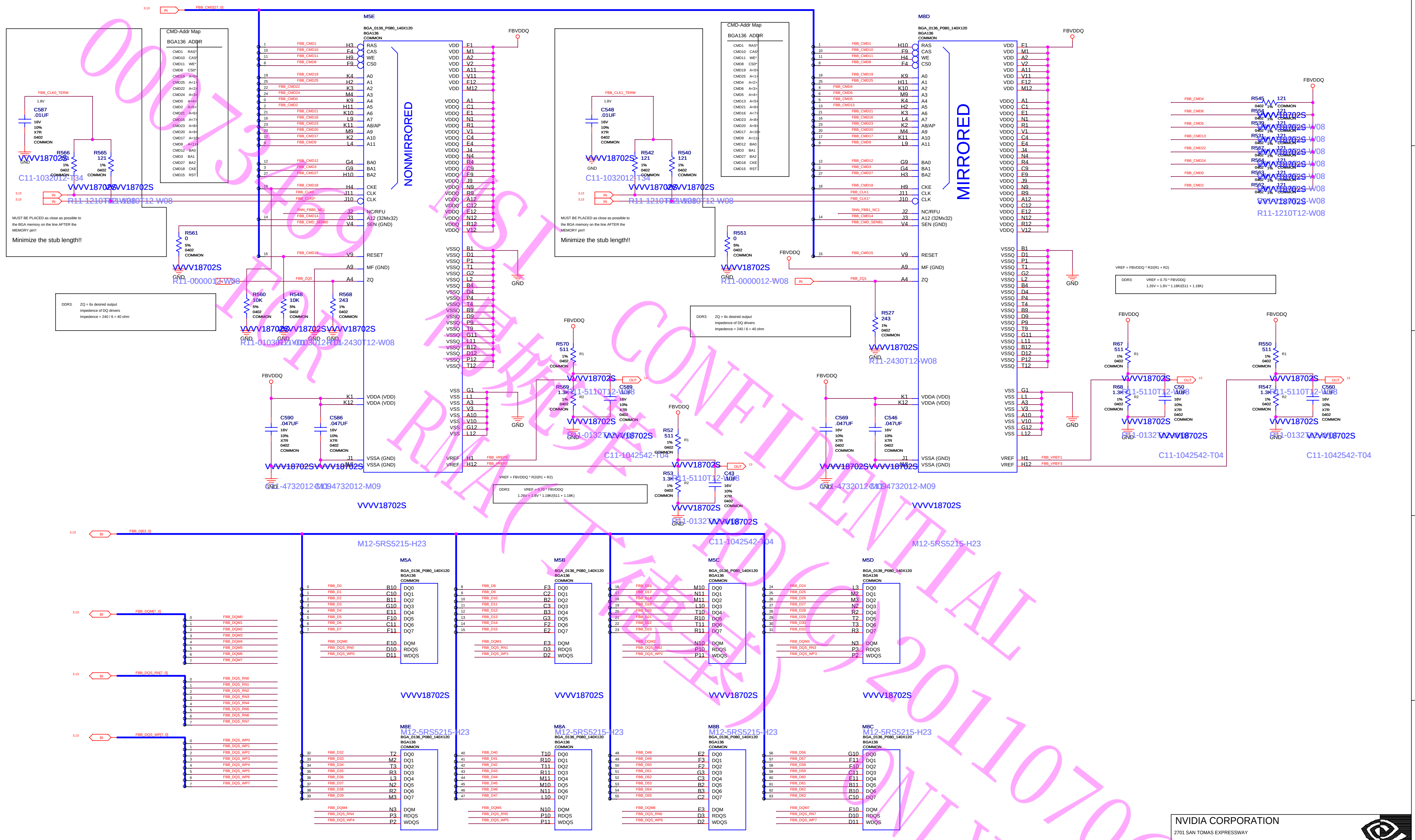
Page7: FBB Partition



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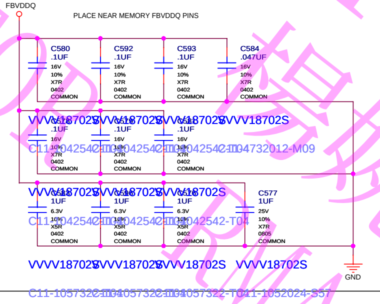
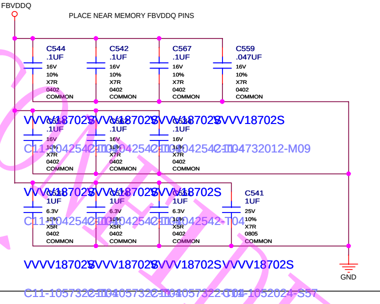
Page8: FBB Partition Decoupling

Decoupling for FBB 31..0



Decoupling for FBB 63..32



ASSEMBLY: P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL

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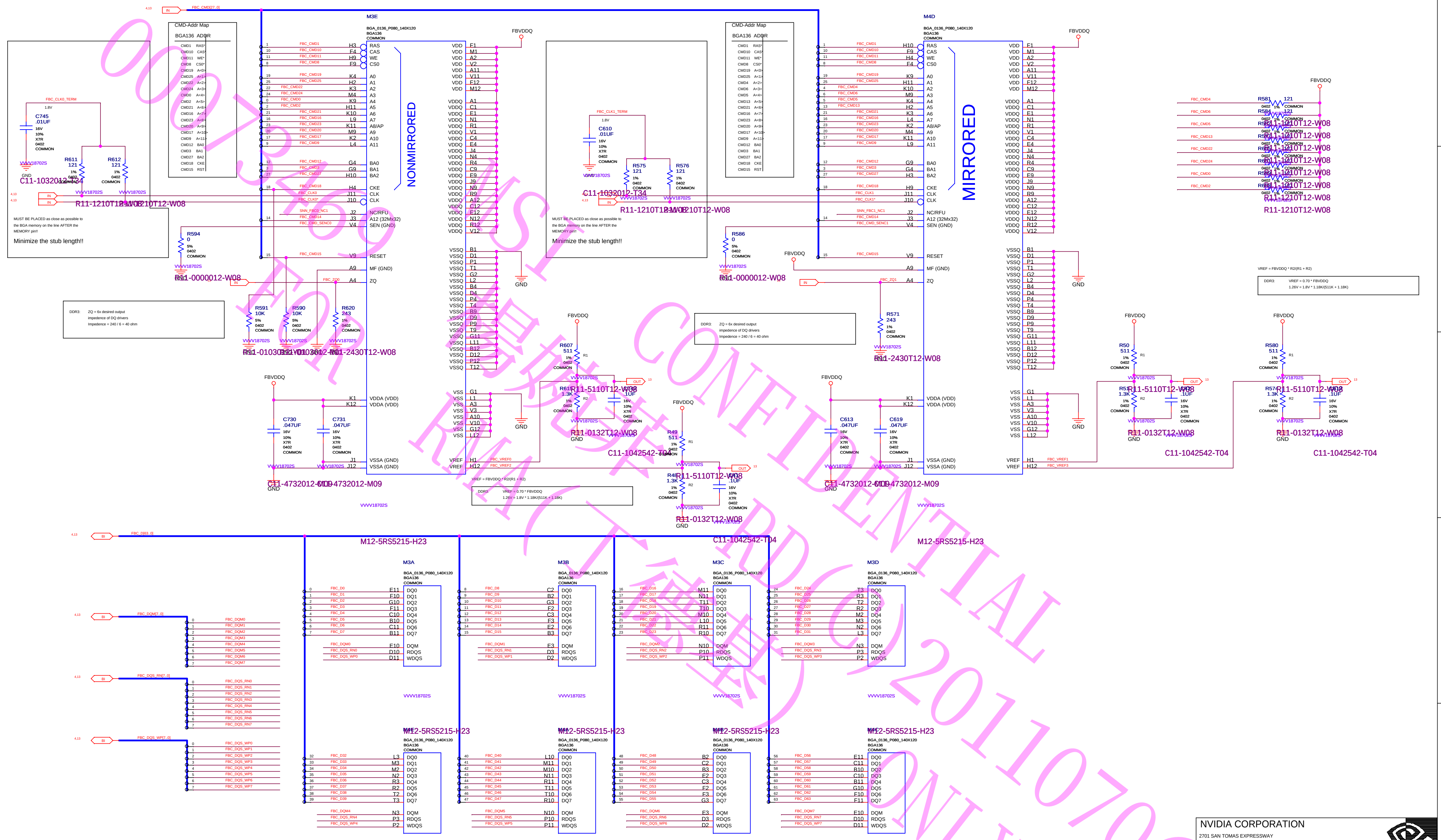
Page9: FBC Partition



ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	FBC Partition

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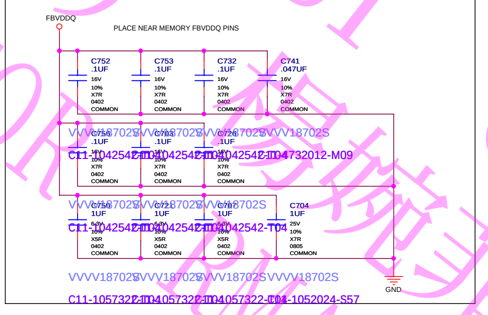
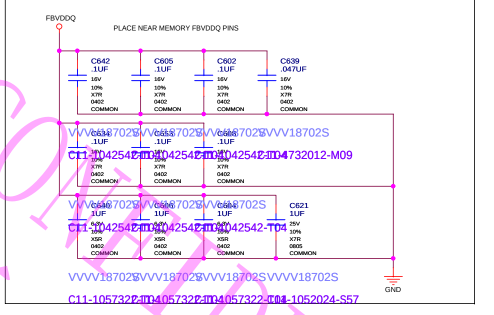
Page10: FBC Partition

Decoupling for FBC 31..0



Decoupling for FBC 63..32



ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	FBC Partition Decoupling

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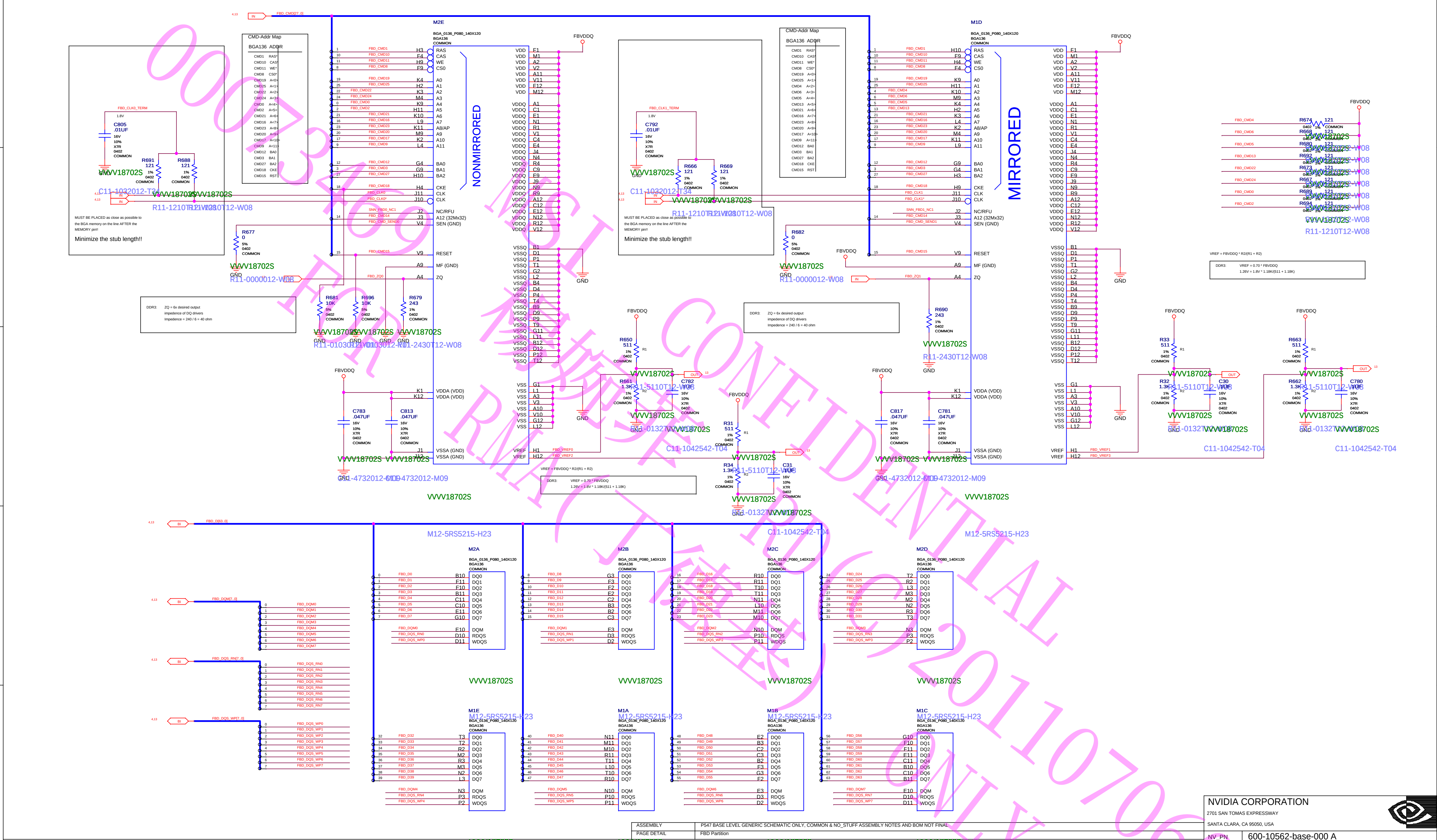
Page11: FBD Partition



ASSEMBLY: P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL

PAGE DETAIL: FBD Partition

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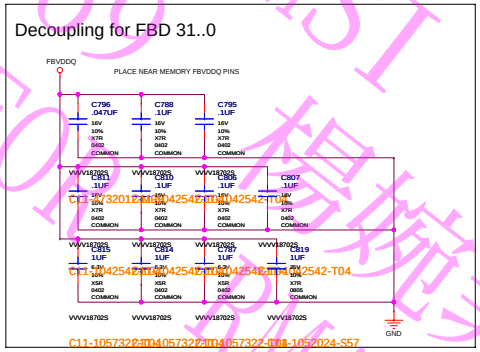
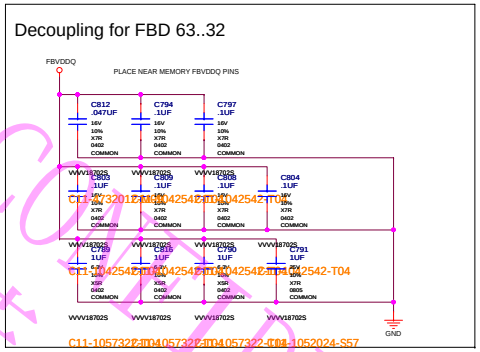
Page12: FBD Partition Decoupling





ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	FBD Partition Decoupling

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Page13: FB Net Properties

NET RULES for FBA and FBB

Ref	Port	NET	NV_CRITICAL_NET	IMPEDANCE	DIFFPAIR
3,5	OUT	FBA_CLK0	1	80DIFF	FBA_CLK0
3,5	OUT	FBA_CLK0*	1	80DIFF	FBA_CLK0
3,5	OUT	FBA_CLK1	1	80DIFF	FBA_CLK1
3,5	OUT	FBA_CLK1*	1	80DIFF	FBA_CLK1

Ref	Port	NET	NV_CRITICAL_NET	IMPEDANCE
3,5	OUT	FBA_CMD[27..0]	1	40OHM
3,5	OUT	FBA_DQS_WP[7..0]	1	40OHM
3,5	IN	FBA_DQS_RN[7..0]	1	40OHM
3,5	OUT	FBA_DQM[7..0]	1	40OHM
3,5	BI	FBA_D[63..0]	1	40OHM

Ref	Port	NET	NV_CRITICAL_NET	IMPEDANCE	DIFFPAIR
3,7	OUT	FBB_CLK0	1	80DIFF	FBB_CLK0
3,7	OUT	FBB_CLK0*	1	80DIFF	FBB_CLK0
3,7	OUT	FBB_CLK1	1	80DIFF	FBB_CLK1
3,7	OUT	FBB_CLK1*	1	80DIFF	FBB_CLK1

Ref	Port	NET	NV_CRITICAL_NET	IMPEDANCE
3,7	OUT	FBB_CMD[27..0]	1	40OHM
3,7	OUT	FBB_DQS_WP[7..0]	1	40OHM
3,7	IN	FBB_DQS_RN[7..0]	1	40OHM
3,7	OUT	FBB_DQM[7..0]	1	40OHM
3,7	BI	FBB_D[63..0]	1	40OHM

Ref	Port	NET	MIN_LINE_WIDTH	VOLTAGE	NV_NET_MAX_CURRENT
3	BI	FB_PLLAVDD0	12MIL	1.1V	0.04A
3	BI	FB_VREF	5MIL	1.26V	0.02A
5	BI	FBA_VREF0	12MIL	1.26V	0.02A
5	BI	FBA_VREF1	12MIL	1.26V	0.02A
5	BI	FBA_VREF2	12MIL	1.26V	0.02A
5	BI	FBA_VREF3	12MIL	1.26V	0.02A
5	BI	FBA_ZQ0	12MIL	1.26V	0.02A
5	BI	FBA_ZQ1	12MIL	1.26V	0.02A
7	BI	FBB_VREF0	12MIL	1.26V	0.02A
7	BI	FBB_VREF1	12MIL	1.26V	0.02A
7	BI	FBB_VREF2	12MIL	1.26V	0.02A
7	BI	FBB_VREF3	12MIL	1.26V	0.02A
7	BI	FBB_ZQ0	12MIL	1.26V	0.02A
7	BI	FBB_ZQ1	12MIL	1.26V	0.02A

NET RULES for FBC and FBD

Ref	Port	NET	NV_CRITICAL_NET	IMPEDANCE	DIFFPAIR
4,9	OUT	FBC_CLK0	1	80DIFF	FBC_CLK0
4,9	OUT	FBC_CLK0*	1	80DIFF	FBC_CLK0
4,9	OUT	FBC_CLK1	1	80DIFF	FBC_CLK1
4,9	OUT	FBC_CLK1*	1	80DIFF	FBC_CLK1

Ref	Port	NET	NV_CRITICAL_NET	IMPEDANCE
4,9	OUT	FBC_CMD[27..0]	1	40OHM
4,9	OUT	FBC_DQS_WP[7..0]	1	40OHM
4,9	IN	FBC_DQS_RN[7..0]	1	40OHM
4,9	OUT	FBC_DQM[7..0]	1	40OHM
4,9	BI	FBC_D[63..0]	1	40OHM

Ref	Port	NET	NV_CRITICAL_NET	IMPEDANCE	DIFFPAIR
4,11	OUT	FBD_CLK0	1	80DIFF	FBD_CLK0
4,11	OUT	FBD_CLK0*	1	80DIFF	FBD_CLK0
4,11	OUT	FBD_CLK1	1	80DIFF	FBD_CLK1
4,11	OUT	FBD_CLK1*	1	80DIFF	FBD_CLK1

Ref	Port	NET	NV_CRITICAL_NET	IMPEDANCE
4,11	OUT	FBD_CMD[27..0]	1	40OHM
4,11	OUT	FBD_DQS_WP[7..0]	1	40OHM
4,11	IN	FBD_DQS_RN[7..0]	1	40OHM
4,11	OUT	FBD_DQM[7..0]	1	40OHM
4,11	BI	FBD_D[63..0]	1	40OHM

Ref	Port	NET	MIN_LINE_WIDTH	VOLTAGE	NV_NET_MAX_CURRENT
4	BI	FB_PLLAVDD1	12MIL	1.1V	0.04A
9	BI	FBC_VREF0	12MIL	1.26V	0.02A
9	BI	FBC_VREF1	12MIL	1.26V	0.02A
9	BI	FBC_VREF2	12MIL	1.26V	0.02A
9	BI	FBC_VREF3	12MIL	1.26V	0.02A
9	BI	FBC_ZQ0	12MIL	1.26V	0.02A
9	BI	FBC_ZQ1	12MIL	1.26V	0.02A
11	BI	FBD_VREF0	12MIL	1.26V	0.02A
11	BI	FBD_VREF1	12MIL	1.26V	0.02A
11	BI	FBD_VREF2	12MIL	1.26V	0.02A
11	BI	FBD_VREF3	12MIL	1.26V	0.02A
11	BI	FBD_ZQ0	12MIL	1.26V	0.02A
11	BI	FBD_ZQ1	12MIL	1.26V	0.02A

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PAGE DETAIL	FB Net Properties

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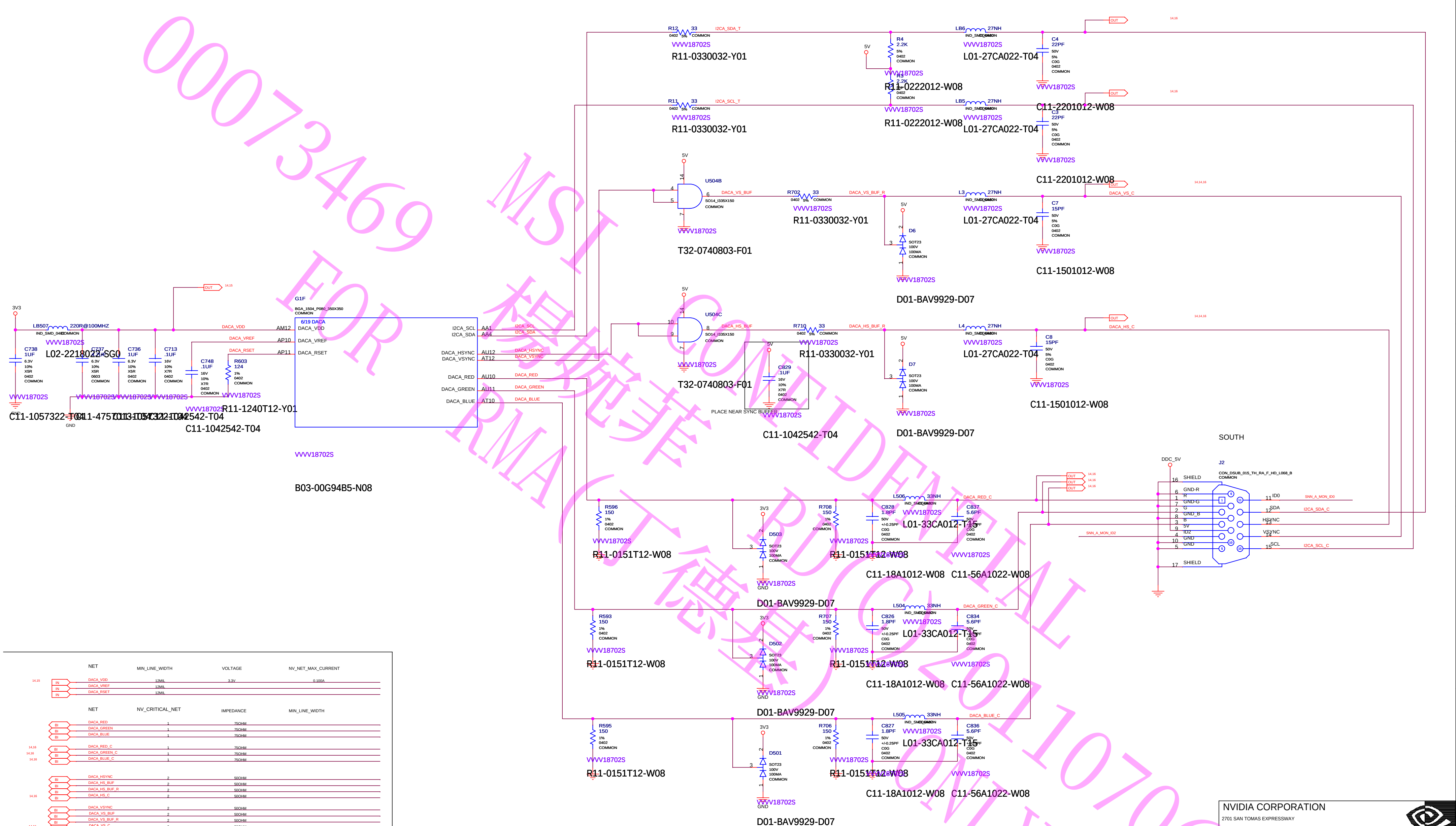
Page14: DACA Interface

DACA RGB-FILTER



NET	MIN_LINE_WIDTH	VOLTAGE	NV_NET_MAX_CURRENT
DACA_VDD	12MIL	3.3V	0.100A
DACA_VREF	12MIL		
DACA_RSET	12MIL		

NET	NV_CRITICAL_NET	IMPEDANCE	MIN_LINE_WIDTH
DACA_RED	1	75OHM	
DACA_GREEN	1	75OHM	
DACA_BLUE	1	75OHM	
DACA_RED_C	1	75OHM	
DACA_GREEN_C	1	75OHM	
DACA_BLUE_C	1	75OHM	
DACA_HSYNC	2	50OHM	
DACA_HS_BUF	2	50OHM	
DACA_HS_BUF_R	2	50OHM	
DACA_HS_C	2	50OHM	
DACA_VSYNC	2	50OHM	
DACA_VS_BUF	2	50OHM	
DACA_VS_BUF_R	2	50OHM	
DACA_VS_C	2	50OHM	

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PAGE DETAIL: DACA Interface

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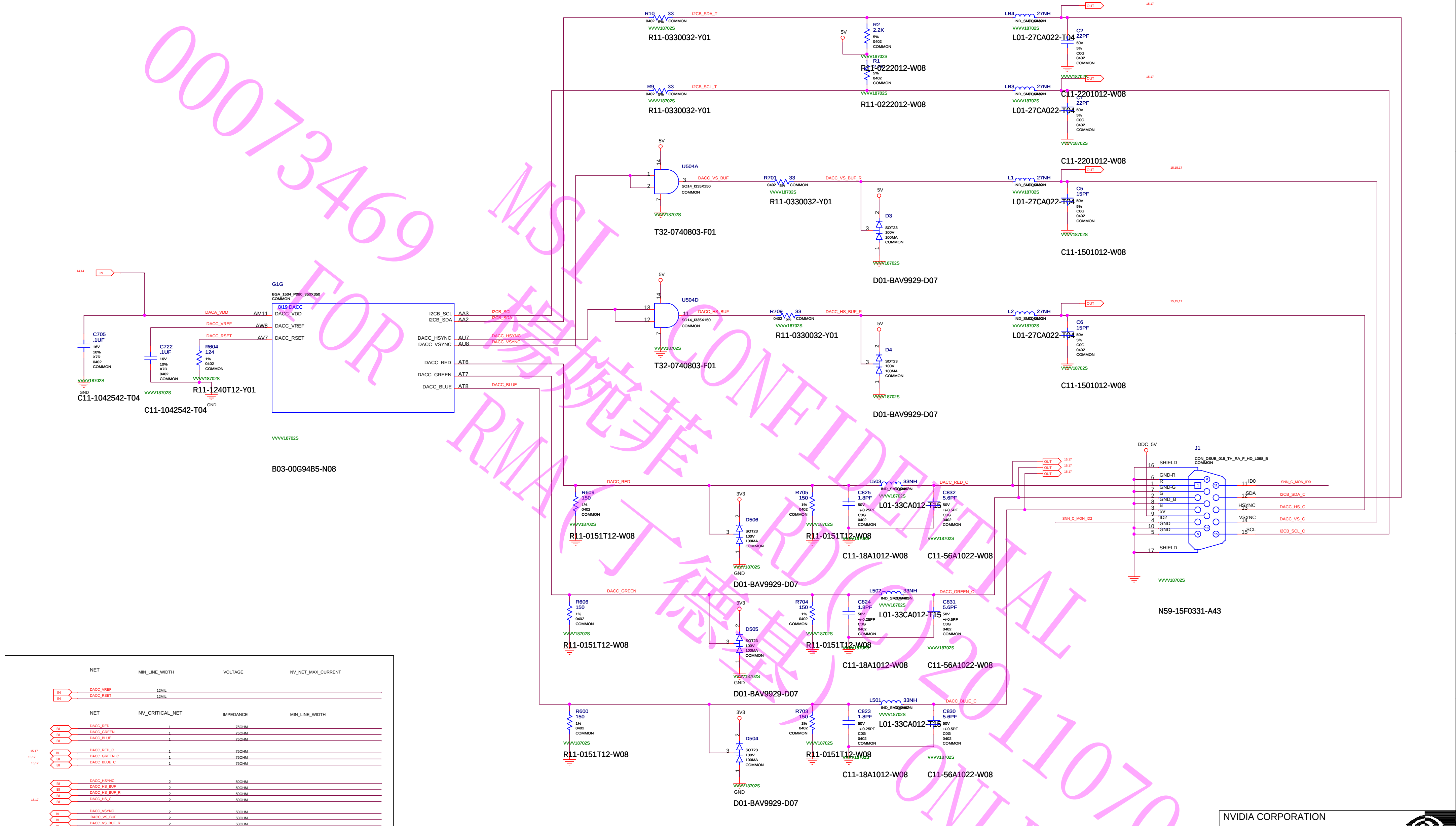
Page15: DACC Interface

DACC RGB-FILTER



NET	MIN_LINE_WIDTH	VOLTAGE	NV_NET_MAX_CURRENT
DACC_VREF	12MIL		
DACC_RSET	12MIL		

NET	NV_CRITICAL_NET	IMPEDANCE	MIN_LINE_WIDTH
DACC_RED	1	75OHM	
DACC_GREEN	1	75OHM	
DACC_BLUE	1	75OHM	
DACC_RED_C	1	75OHM	
DACC_GREEN_C	1	75OHM	
DACC_BLUE_C	1	75OHM	
DACC_HSYNC	2	50OHM	
DACC_HS_BUF	2	50OHM	
DACC_HS_BUF_R	2	50OHM	
DACC_HS_C	2	50OHM	
DACC_VSYNC	2	50OHM	
DACC_VS_BUF	2	50OHM	
DACC_VS_BUF_R	2	50OHM	
DACC_VS_C	2	50OHM	

ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACC Interface

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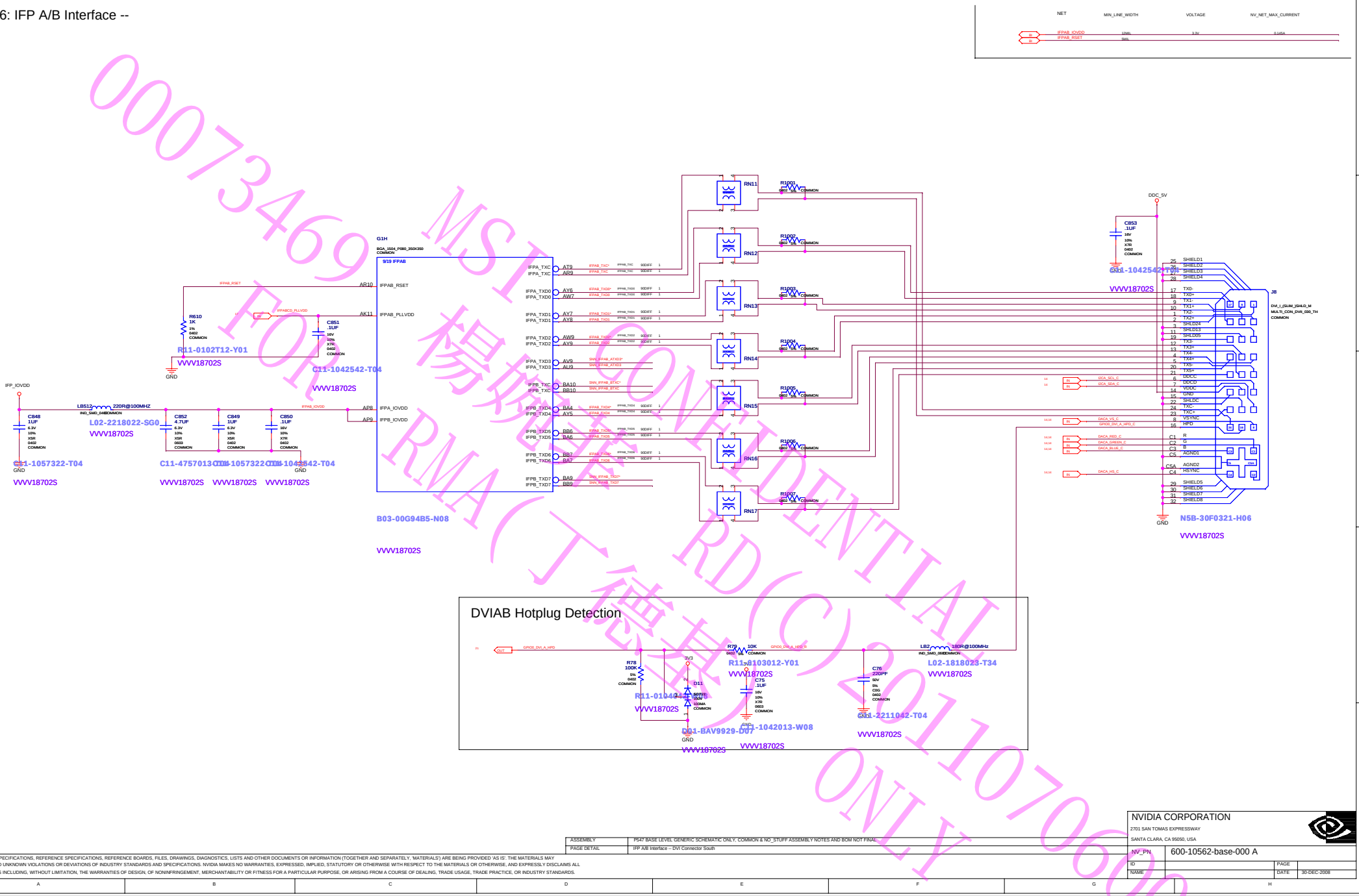
Page16: IFP A/B Interface --



ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	IFP A/B Interface -- DVI Connector South

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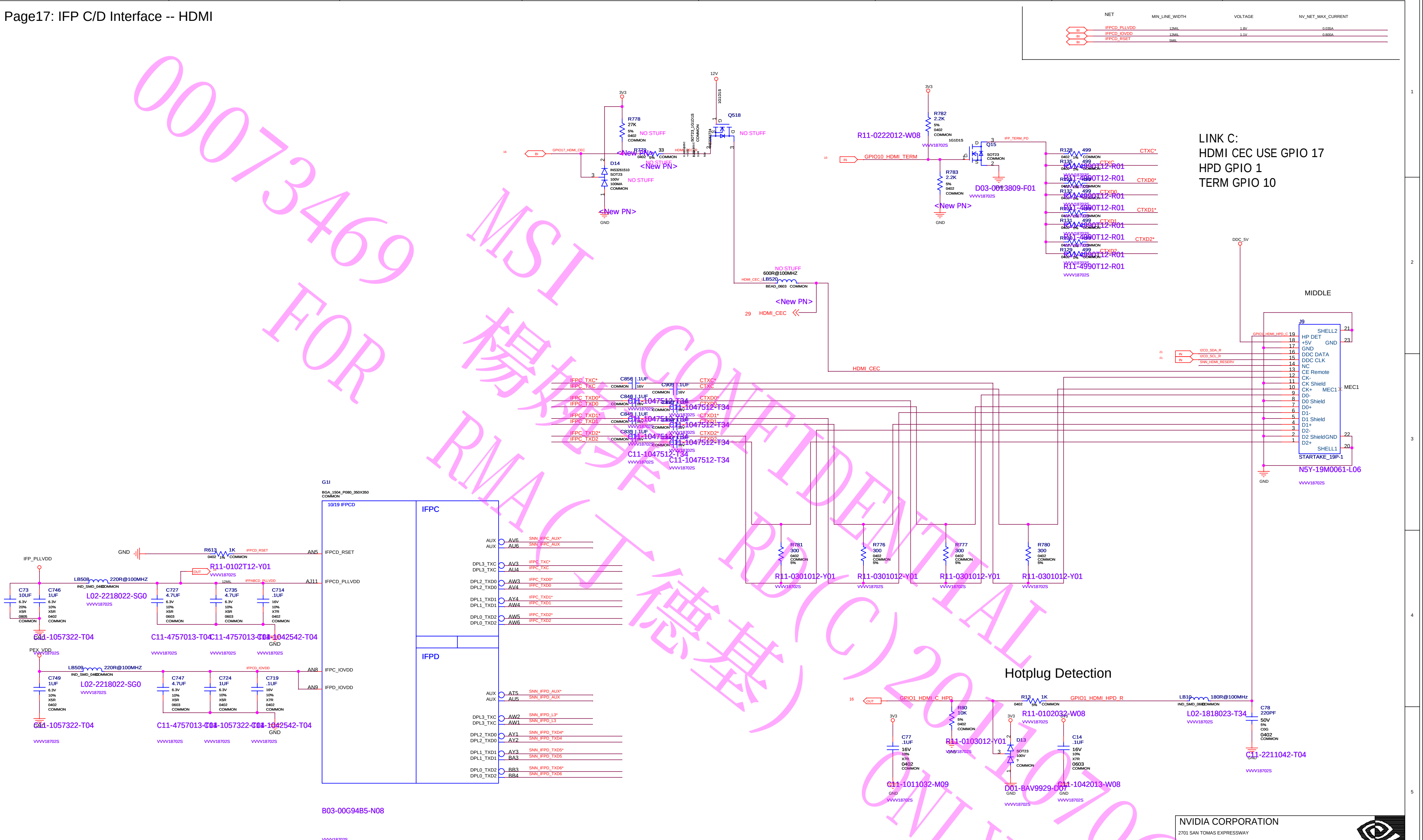
Page17: IFP C/D Interface -- HDMI



ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	IFP C/D Interface -- DVI Connector MID

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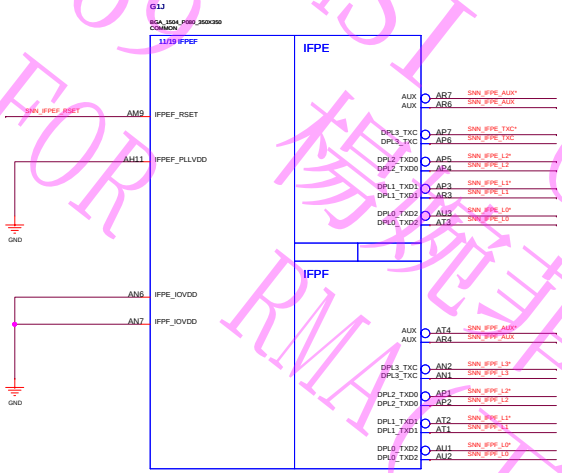
Page18: IFP E/F Interface -- HDMI



NET	MIN_LINE_WIDTH	VOLTAGE	NV_NET_MAX_CURRENT

ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	IFP E/F Interface -- Unused

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Page19: DACB Unused

ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
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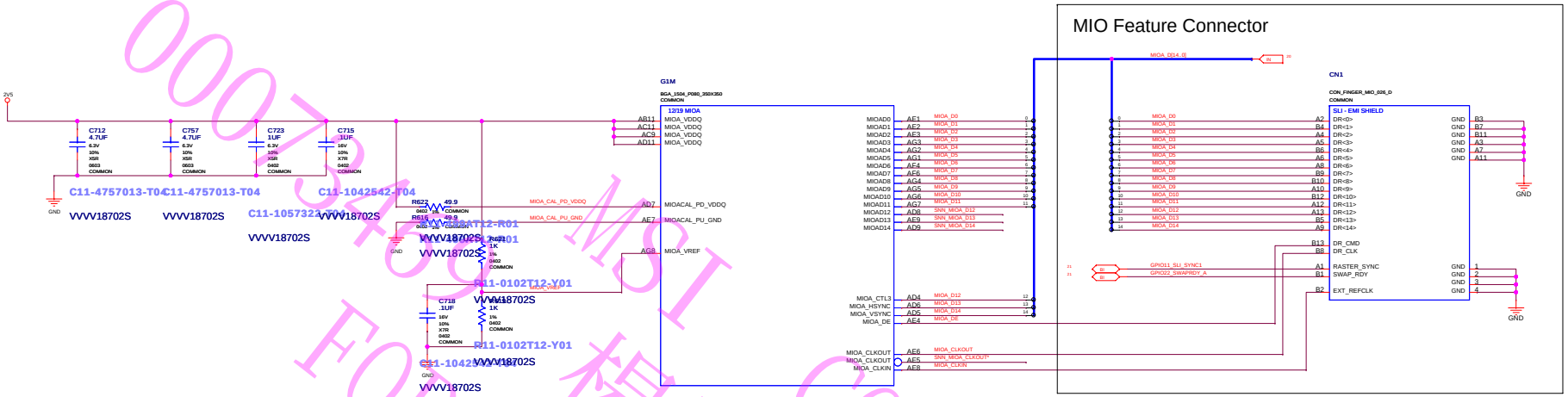
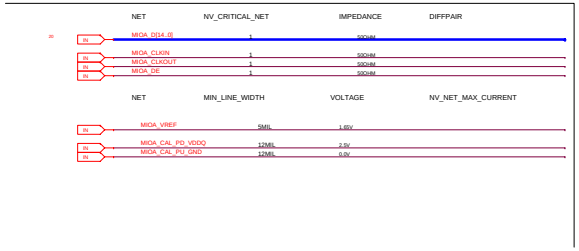
Page20: MIO A/B Interface





ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
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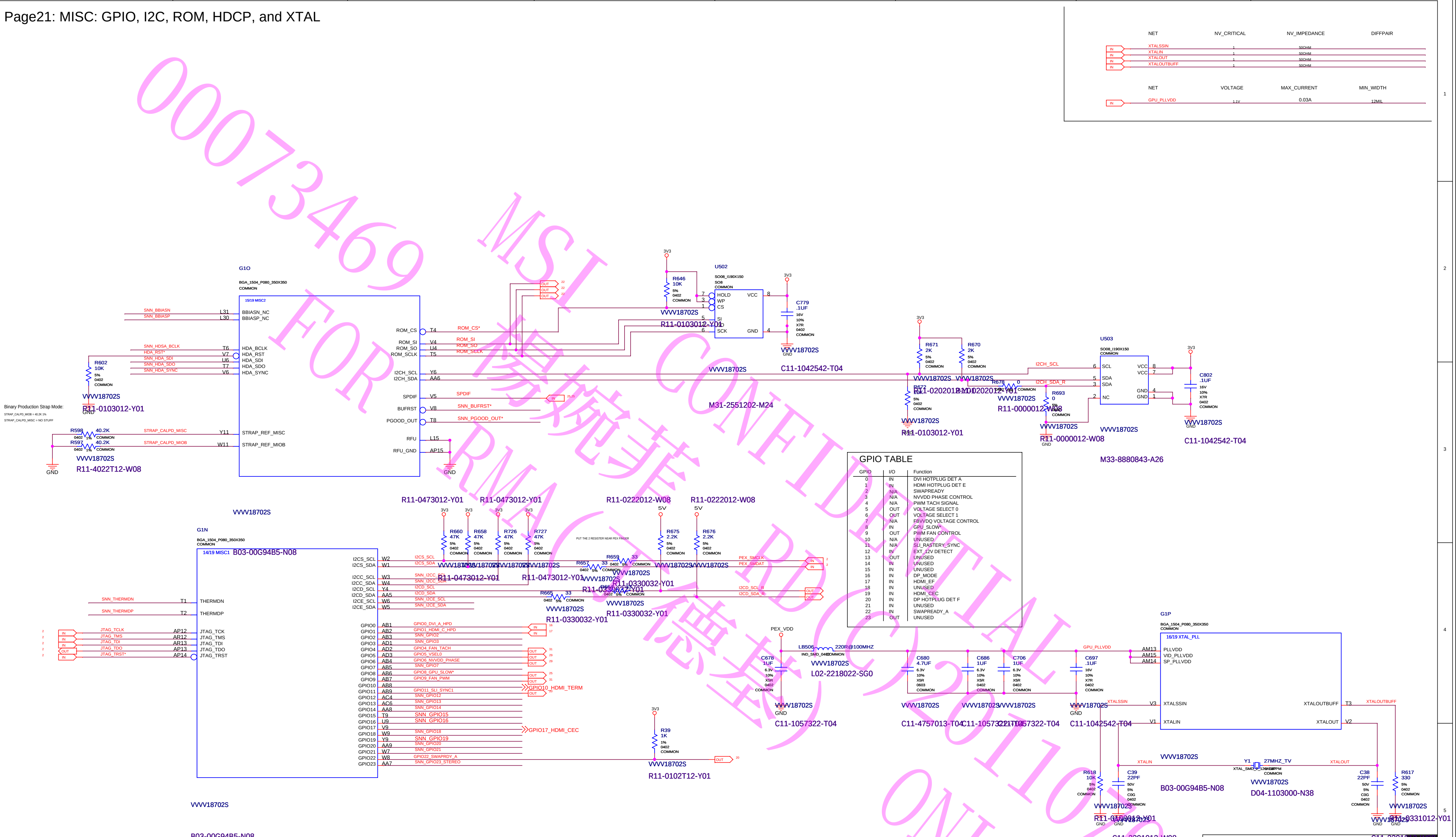
SANTA CLARA, CA 95050, USA

NV_PN	600-10562-base-000 A		
ID		PAGE	
NAME		DATE	30-DEC-2008

Page21: MISC: GPIO, I2C, ROM, HDCP, and XTAL



GPIO TABLE

GPIO	I/O	Function
0	IN	DVI HOTPLUG DET A
1	IN	HDMI HOTPLUG DET E
2	N/A	SWAPREADY
3	N/A	NVVDD PHASE CONTROL
4	N/A	PWM TACH SIGNAL
5	OUT	VOLTAGE SELECT 0
6	OUT	VOLTAGE SELECT 1
7	N/A	FBVVDQ VOLTAGE CONTROL
8	IN	GPU_SLOW*
9	OUT	PWM FAN CONTROL
10	N/A	UNUSED
11	N/A	SLI_RASTERY_SYNC
12	IN	EXT_12V DETECT
13	OUT	UNUSED
14	IN	UNUSED
15	IN	UNUSED
16	IN	DP_MODE
17	IN	HDMI_EF
18	IN	UNUSED
19	IN	HDMI_CEC
20	IN	DP HOTPLUG DET F
21	IN	UNUSED
22	IN	SWAPREADY_A
23	OUT	UNUSED

ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	MISC: GPIO, I2C, ROM, HDCP, and XTAL

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NV_PN	600-10562-base-000 A		
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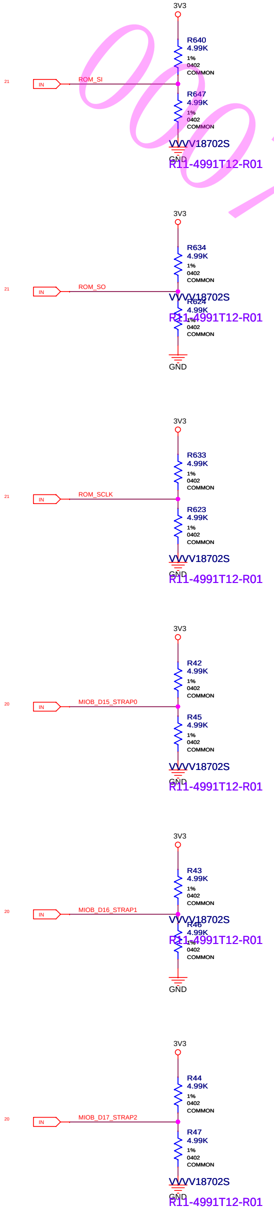
Page22: Strap Configuration



STRAP PIN	STRAP NAME
ROM_SI	PCI_DEVID_EXT

STRAP PIN	STRAP NAME
ROM_SO	SLOT_CLK_CFG

STRAP PIN	STRAP NAME
ROM_SCLK	PCI_DEVID[3]

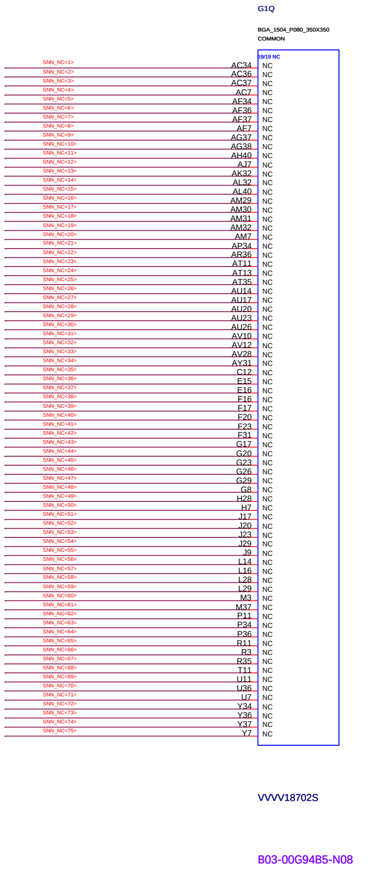
STRAP PIN	STRAP NAME
STRAP0	RAMCFG0
STRAP1	RAMCFG1
STRAP2	RAMCFG2

	3V3	GND
5K	1	0

* RAMCFG[2:0]

256MB (8Mx32)	512MB (16Mx32)	1024MB (32Mx32)
101 --- 256-bit Qimonda	001 --- 256-bit Qimonda	101 --- 256-bit Hynix
110 --- 256-bit Hynix	010 --- 256-bit Hynix	110 --- 256-bit Hynix
111 --- 256-bit Samsung	011 --- 256-bit Samsung	111 --- 256-bit Samsung

* VBIOS will be defined on a per SKU basis.



ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Strap Configuration

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Page23: PWR and GND Signals

ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PWR and GND Signals

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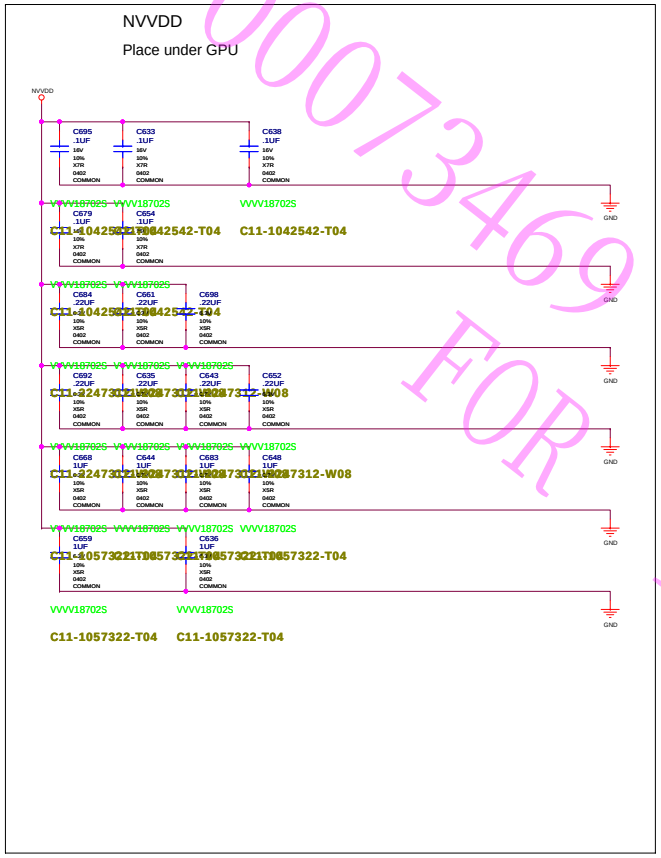
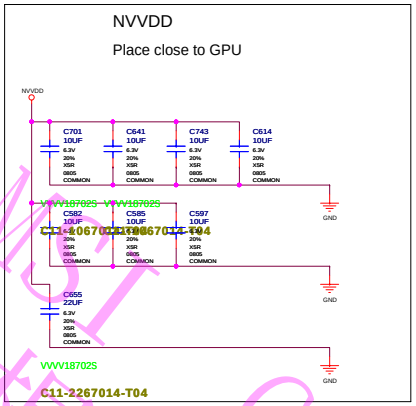
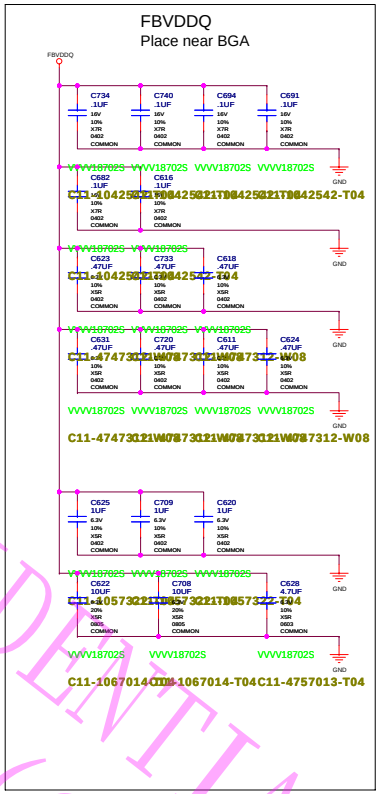
NV_PN	600-10562-base-000 A		
ID		PAGE	
NAME		DATE	30-DEC-2008

Page24: NVVDD and FBVDDQ Decoupling







ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	NVVDD and FBVDDQ Decoupling

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NV_PN	600-10562-base-000 A		
ID		PAGE	
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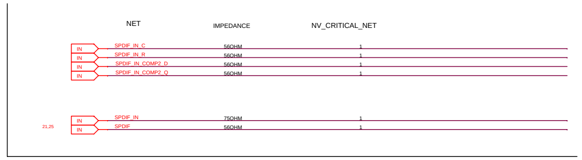
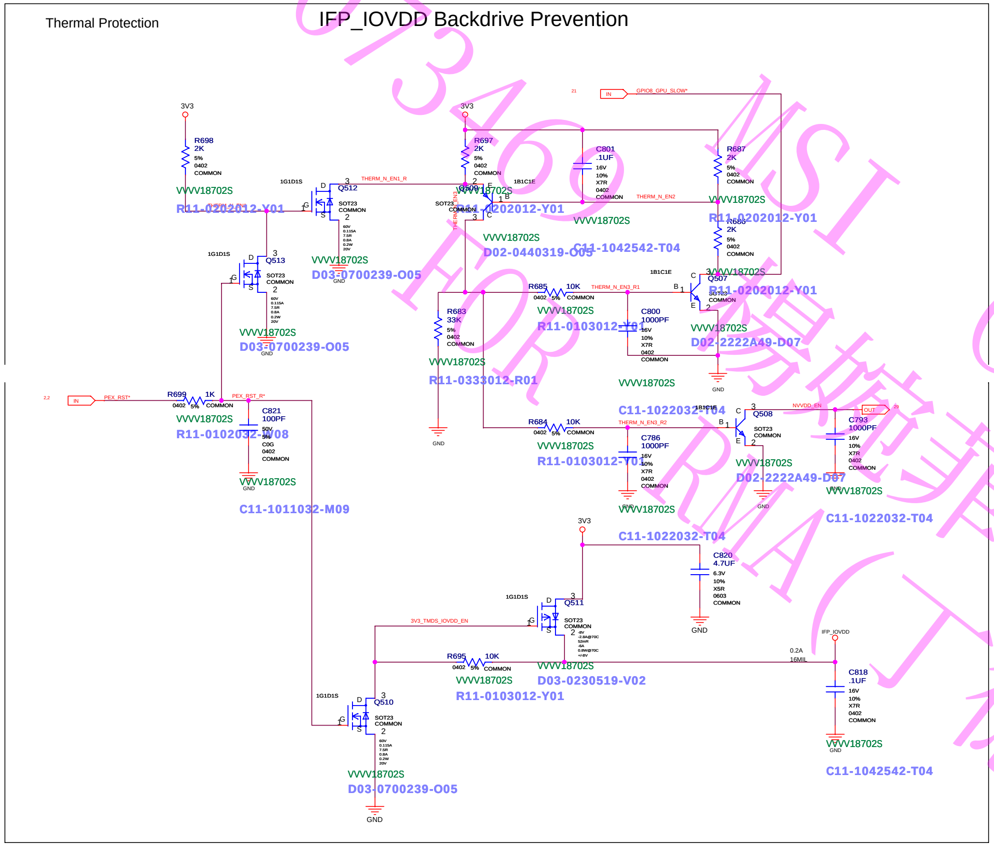
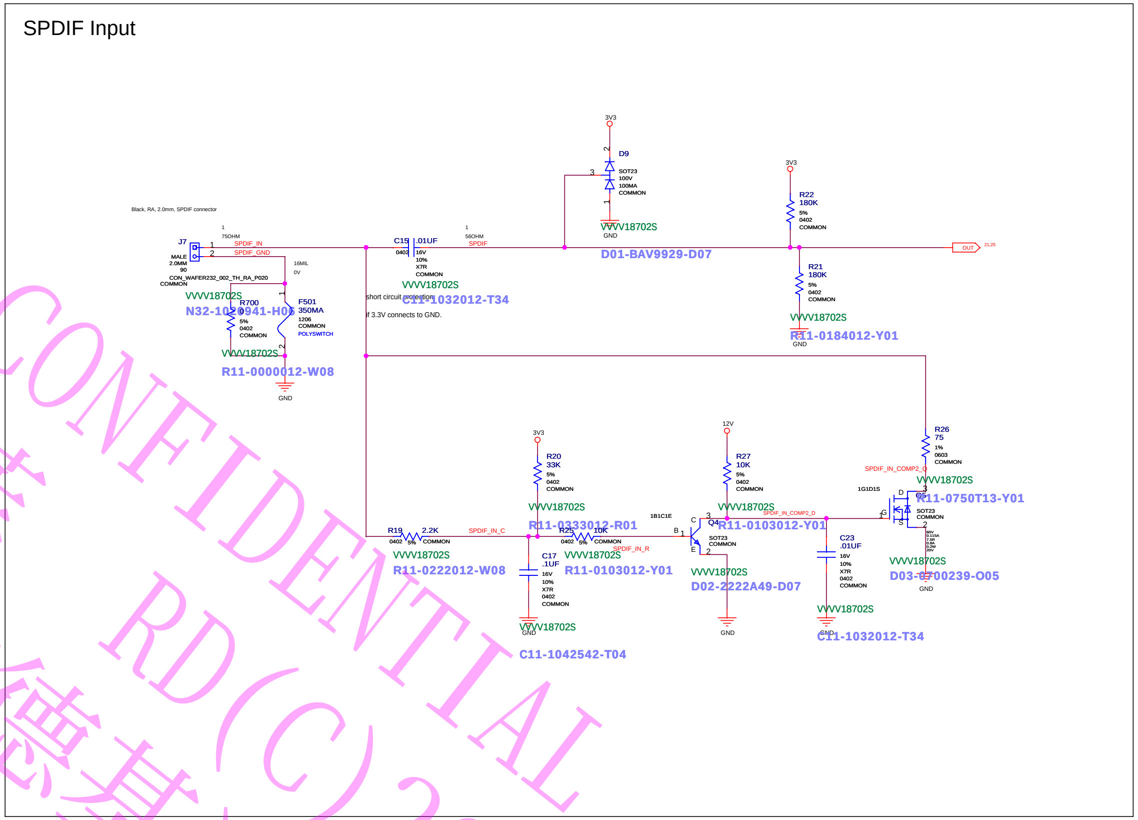
Page25: SPDIF Input, Thermal Protection, and IFP_IOVDD Power Supply







ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	SPDIF Input, Backdrive Protection, and IFP_IOVDD Power Supply

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NV_PN	600-10562-base-000 A		
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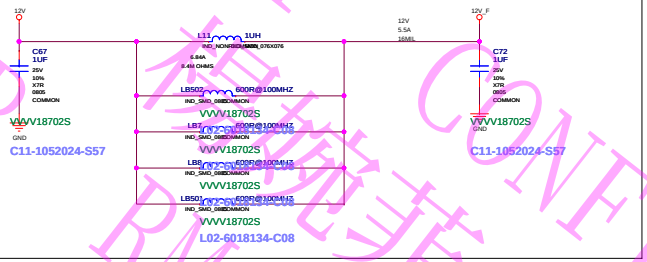
Page26: PS I: 3V3, 12V, and 12V_EXT Power Supply Filter

NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
12V	12V	5.5A	16MIL
GND	0V	28A	16MIL

12V Power Supply Filter

12V_F = 12V @ 5.5A



ASSEMBLY	PS47 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PS I: 3V3, 12V, and 12V_EXT Power Supply Filter

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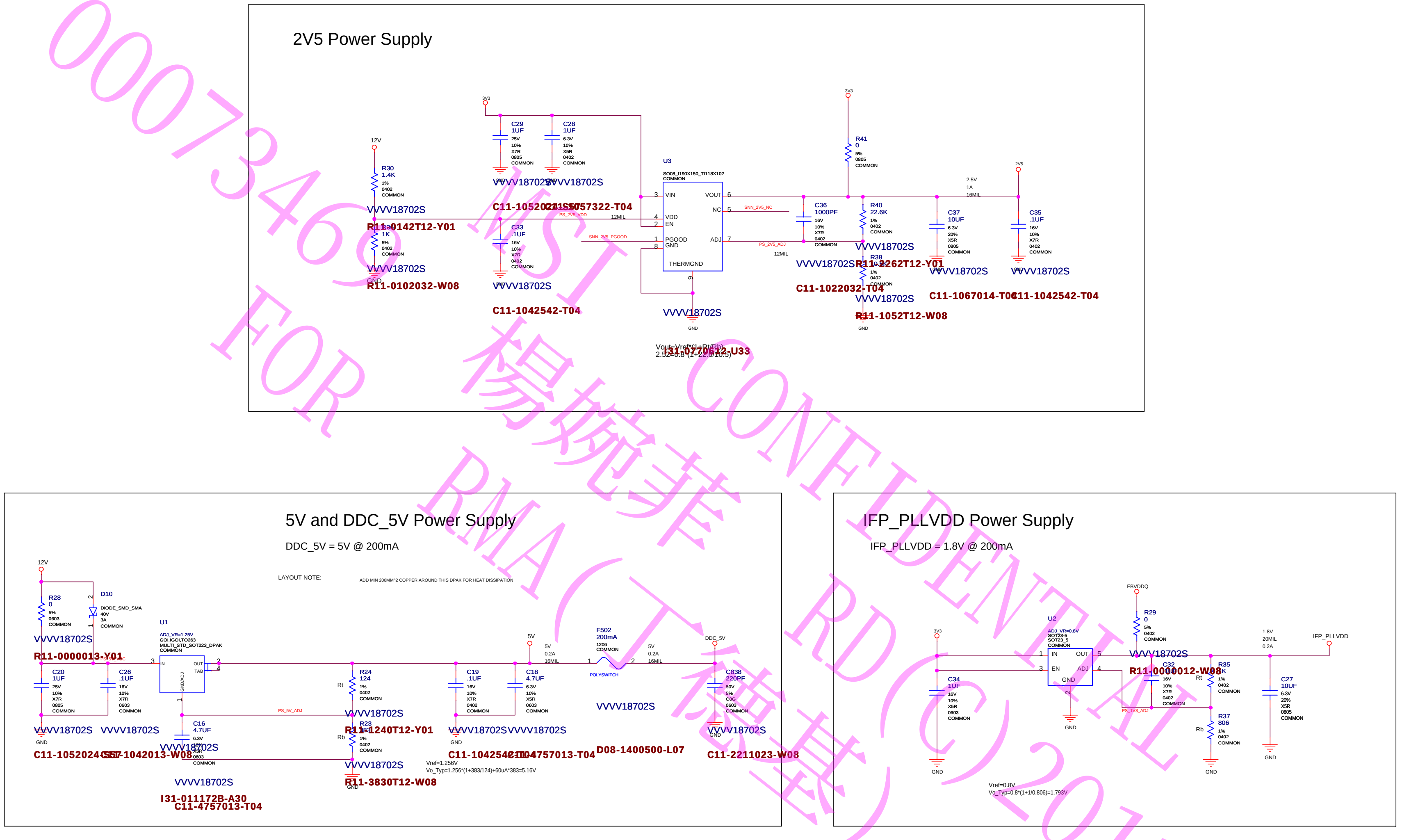
NV_PN	600-10562-base-000 A		
ID		PAGE	
NAME		DATE	30-DEC-2008

Page27: PS II: IFP_PLLVDD, 2V5, 5V, and DDC_5V Power Supply



ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PS II: IFP_PLLVDD, 2V5, 5V, and DDC_5V Power Supply

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NV_PN 600-10562-base-000 A
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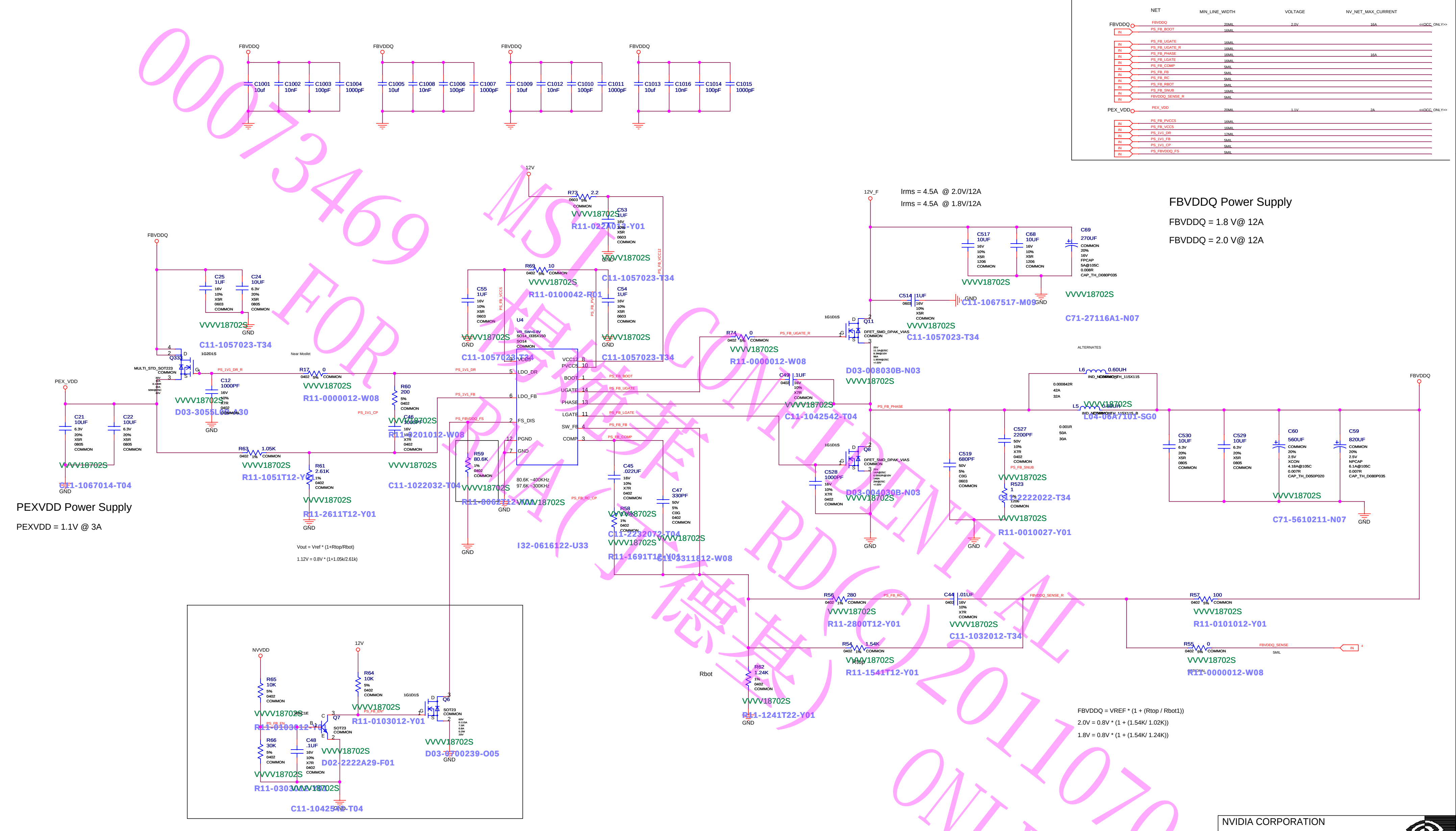
Page28: PS III: FBVDDQ Power Supply



ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PS III: FBVDDQ Power Supply

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NV_PN 600-10562-base-000 A
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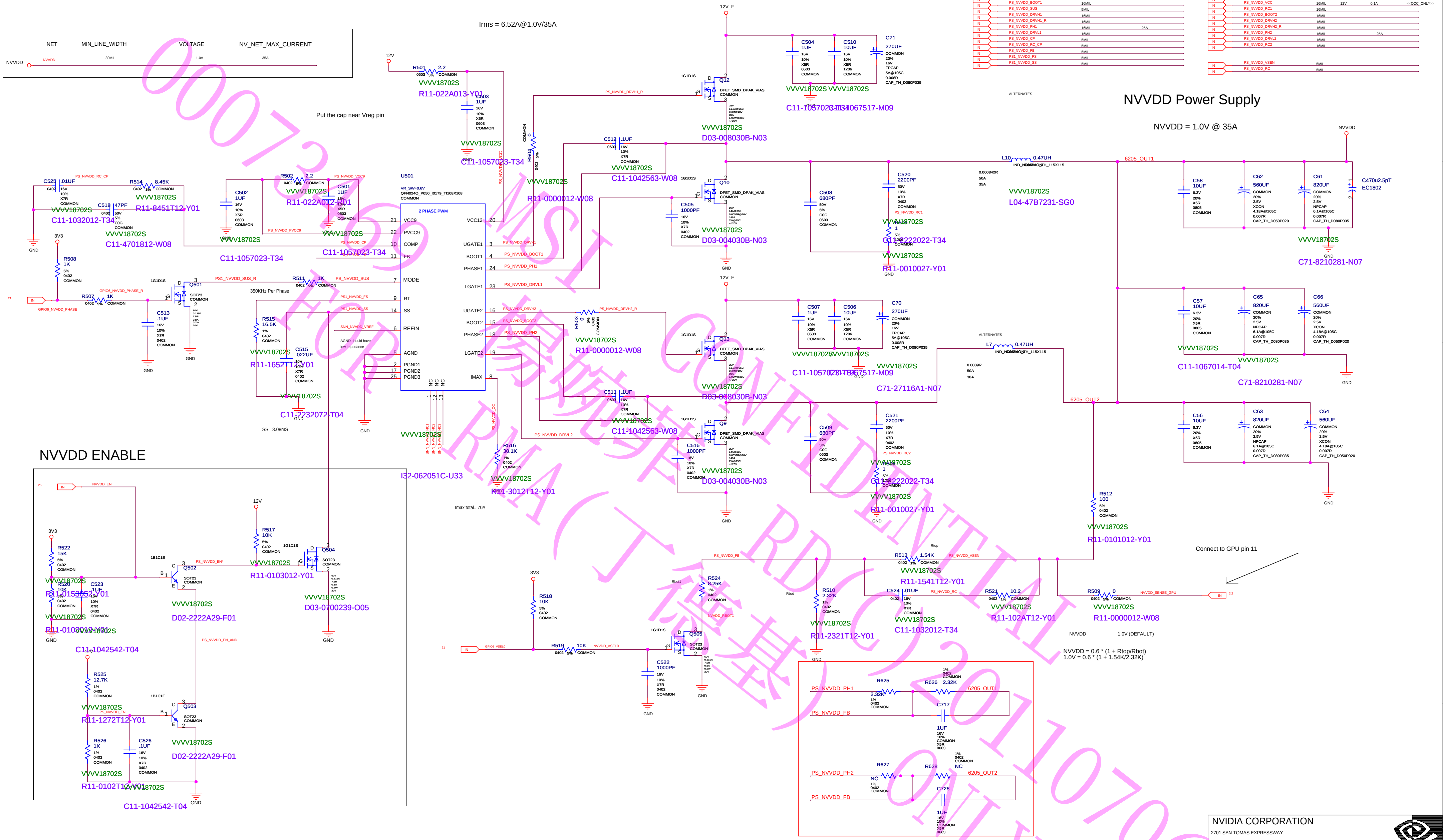
NAME | DATE 30-DEC-2008

Page29: PS IV: NVVDD



ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PS IV: NVVDV

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NV_PN 600-10562-base-000 A
ID PAGE
NAME DATE 30-DEC-2008

BLANK

ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	BLANK

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NV_PN	600-10562-base-000 A		
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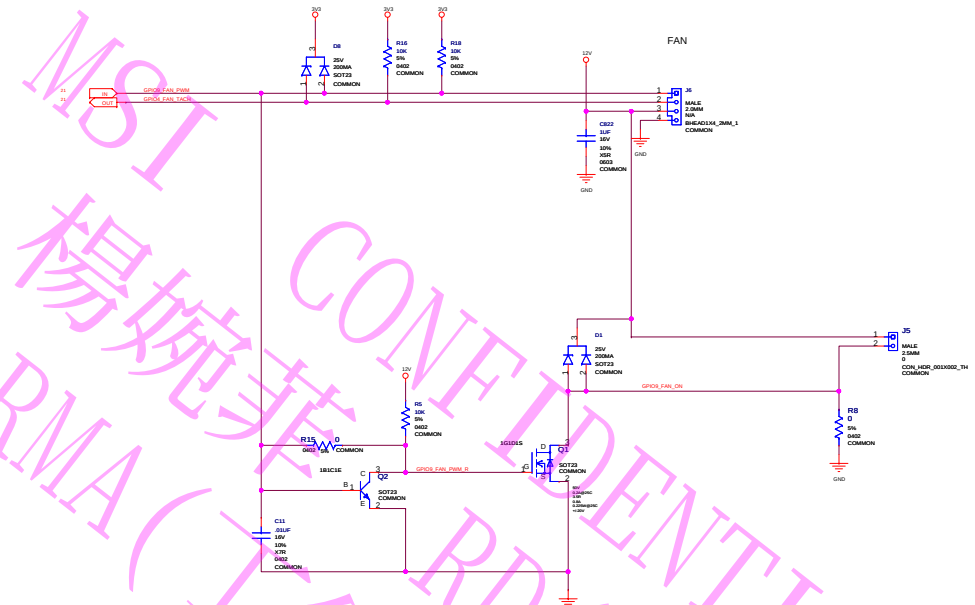
Page31: Thermal Diode and Fan Control



ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Fan Connector

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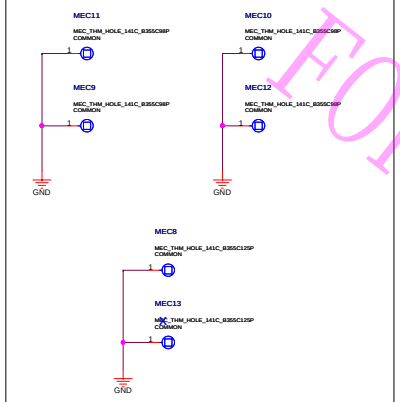
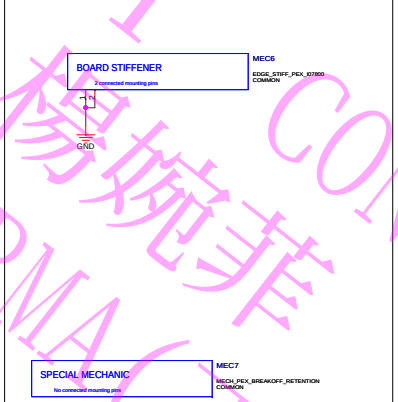
Page32: Thermal, Mechanical, and Bracket

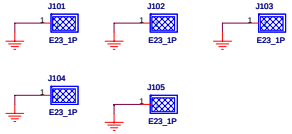
Thermal

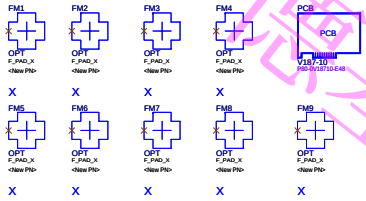


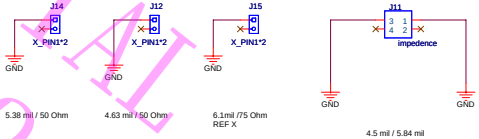
Mechanical



Bracket







ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Thermal, Mechanical, and Bracket

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NV_PN	600-10562-base-000 A		
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Title: Basenet Report
Design: p562_a00
Date: Dec 24 11:33:24 2008

Base nets and synonyms for
p562_a00_lib.P562_A00(@p562_a00_lib.p562
_a00(sch_1))
Base Signal Location([Zone][dir])

3V3_TMDS_IOVDD_EN 25.4B
12V 26.1G
DACA_BLUE 14.3C 14.5A<> 14.5D
DACA_BLUE_C 14.3F> 14.5A<> 16.3G<
DACA_GREEN 14.3C 14.4D 14.5A<>
DACA_GREEN_C 14.3F> 14.5A<> 16.3G<
DACA_HSYNC 14.3C 14.5A<>
DACA_HS_BUF 14.3D 14.5A<>
DACA_HS_BUF_R 14.3E 14.5A<>
DACA_HS_C 14.2G> 14.2G> 14.5A<>
16.3G<
DACA_RED 14.3C 14.3D 14.5A<>
DACA_RED_C 14.3F> 14.5A<> 16.3G<
DACA_RSET 14.3B 14.5A<
DACA_VDD 14.2B> 14.4A< 15.2A<
DACA_VREF 14.3B 14.5A<
DACA_VSYNC 14.3C 14.5A<>
DACA_VS_BUF 14.2D 14.5A<>
DACA_VS_BUF_R 14.2E 14.5A<>
DACA_VS_C 14.2G> 14.2G> 14.5A<>
16.3G<
DACC_BLUE 15.3C 15.5A<> 15.5D
DACC_BLUE_C 15.3F> 15.5A<> 17.3G<
DACC_GREEN 15.3C 15.4D 15.5A<>
DACC_GREEN_C 15.3F> 15.5A<> 17.3G<
DACC_HSYNC 15.3C 15.5A<>
DACC_HS_BUF 15.2D 15.5A<>
DACC_HS_BUF_R 15.2E 15.5A<>
DACC_HS_C 15.2G> 15.2G> 15.5A<>
17.3G<
DACC_RED 15.3C 15.3D 15.5A<>
DACC_RED_C 15.3F> 15.5A<> 17.3G<
DACC_RSET 15.3B 15.5A<
DACC_VREF 15.2B 15.4A<
DACC_VSYNC 15.3C 15.5A<>
DACC_VS_BUF 15.2D 15.5A<>
DACC_VS_BUF_R 15.2E 15.5A<>
DACC_VS_C 15.2G> 15.2G> 15.5A<>
17.3G<
FBA_CLK0 3.4D> 5.2A< 13.1B>
FBA_CLK0* 3.4D> 5.2A< 13.1B>
FBA_CLK0_TERM 5.1A
FBA_CLK1 3.4D> 5.2D< 13.2B>
FBA_CLK1* 3.4D> 5.2D< 13.2B>
FBA_CLK1_TERM 5.1D
FBA_CMD<0> 3.2C 5.1B 5.1G
FBA_CMD<27..0> 3.2D> 5.1B< 13.2B>
FBA_CMD<1> 3.2C 5.1B 5.1E
FBA_CMD<2> 3.2C 5.1B 5.1G
FBA_CMD<3> 3.2C 5.2B 5.2E
FBA_CMD<4> 3.2C 5.1E 5.1G
FBA_CMD<5> 3.2C 5.1E 5.1G
FBA_CMD<6> 3.3C 5.1E 5.1G
FBA_CMD<8> 3.3C 5.1B 5.1E
FBA_CMD<9> 3.3C 5.1B 5.1E
FBA_CMD<10> 3.3C 5.1B 5.1E
FBA_CMD<11> 3.3C 5.1B 5.1E
FBA_CMD<12> 3.3C 5.2B 5.2E
FBA_CMD<13> 3.3C 5.1E 5.1G
FBA_CMD<14> 3.3C 5.2B 5.2E
FBA_CMD<15> 3.3C 5.2B 5.2E
FBA_CMD<16> 3.3C 5.1B 5.1E
FBA_CMD<17> 3.3C 5.1B 5.1E
FBA_CMD<18> 3.3C 5.2B 5.2E
FBA_CMD<19> 3.3C 5.1B 5.1E
FBA_CMD<20> 3.3C 5.1B 5.1E
FBA_CMD<21> 3.3C 5.1B 5.1E
FBA_CMD<22> 3.3C 5.1B 5.1G
FBA_CMD<23> 3.3C 5.1B 5.1E
FBA_CMD<24> 3.3C 5.1B 5.1G
FBA_CMD<25> 3.3C 5.1B 5.1E
FBA_CMD<27> 3.3C 5.2B 5.2E
FBA_CMD_SENA0 5.2B
FBA_CMD_SENA1 5.2E
FBA_D<0> 3.1B 5.4C
FBA_D<63..0> 3.1A<> 5.4A<> 13.2B<>
FBA_D<1> 3.1B 5.4C
FBA_D<2> 3.1B 5.4C
FBA_D<3> 3.1B 5.4C
FBA_D<4> 3.1B 5.4C
FBA_D<5> 3.1B 5.4C
FBA_D<6> 3.1B 5.4C
FBA_D<7> 3.1B 5.4C
FBA_D<8> 3.1B 5.4C
FBA_D<9> 3.2B 5.4C
FBA_D<10> 3.2B 5.4C
FBA_D<11> 3.2B 5.4C

FBA_D<12> 3.2B 5.4C
FBA_D<13> 3.2B 5.4C
FBA_D<14> 3.2B 5.4C
FBA_D<15> 3.2B 5.4C
FBA_D<16> 3.2B 5.4D
FBA_D<17> 3.2B 5.4D
FBA_D<18> 3.2B 5.4D
FBA_D<19> 3.2B 5.4D
FBA_D<20> 3.2B 5.4D
FBA_D<21> 3.2B 5.4D
FBA_D<22> 3.2B 5.4D
FBA_D<23> 3.2B 5.4D
FBA_D<24> 3.2B 5.4E
FBA_D<25> 3.2B 5.4E
FBA_D<26> 3.2B 5.4E
FBA_D<27> 3.2B 5.4E
FBA_D<28> 3.2B 5.4E
FBA_D<29> 3.2B 5.4E
FBA_D<30> 3.2B 5.4E
FBA_D<31> 3.2B 5.4E
FBA_D<32> 3.2B 5.5C
FBA_D<33> 3.2B 5.5C
FBA_D<34> 3.2B 5.5C
FBA_D<35> 3.2B 5.5C
FBA_D<36> 3.2B 5.5C
FBA_D<37> 3.2B 5.5C
FBA_D<38> 3.2B 5.5C
FBA_D<39> 3.3B 5.5C
FBA_D<40> 3.3B 5.5C
FBA_D<41> 3.3B 5.5C
FBA_D<42> 3.3B 5.5C
FBA_D<43> 3.3B 5.5C
FBA_D<44> 3.3B 5.5C
FBA_D<45> 3.3B 5.5C
FBA_D<46> 3.3B 5.5C
FBA_D<47> 3.3B 5.5C
FBA_D<48> 3.3B 5.5D
FBA_D<49> 3.3B 5.5D
FBA_D<50> 3.3B 5.5D
FBA_D<51> 3.3B 5.5D
FBA_D<52> 3.3B 5.5D
FBA_D<53> 3.3B 5.5D
FBA_D<54> 3.3B 5.5D
FBA_D<55> 3.3B 5.5D
FBA_D<56> 3.3B 5.5E
FBA_D<57> 3.3B 5.5E
FBA_D<58> 3.3B 5.5E
FBA_D<59> 3.3B 5.5E
FBA_D<60> 3.3B 5.5E
FBA_D<61> 3.3B 5.5E
FBA_D<62> 3.3B 5.5E
FBA_D<63> 3.3B 5.5E
FBA_DEBUG 3.4C
FBA_DQM<0> 3.3B 5.4B 5.4C
FBA_DQM<7..0> 3.3A> 5.4A<> 13.2B>
FBA_DQM<1> 3.3B 5.4B 5.4C
FBA_DQM<2> 3.3B 5.4B 5.4D
FBA_DQM<3> 3.4B 5.4B 5.4E
FBA_DQM<4> 3.4B 5.4B 5.5C
FBA_DQM<5> 3.4B 5.4B 5.5C
FBA_DQM<6> 3.4B 5.4B 5.5D
FBA_DQM<7> 3.4B 5.4B 5.5E
FBA_DQS_RN<0> 3.4B 5.4B 5.4C
FBA_DQS_RN<7..0> 3.4A< 5.4A<> 13.2B<
FBA_DQS_RN<1> 3.4B 5.4B 5.4C
FBA_DQS_RN<2> 3.4B 5.4B 5.4D
FBA_DQS_RN<3> 3.4B 5.4B 5.4E
FBA_DQS_RN<4> 3.4B 5.4B 5.5C
FBA_DQS_RN<5> 3.4B 5.5B 5.5C
FBA_DQS_RN<6> 3.4B 5.5B 5.5D
FBA_DQS_RN<7> 3.4B 5.5B 5.5E
FBA_DQS_WP<0> 3.4B 5.4C 5.5B
FBA_DQS_WP<7..0> 3.4A> 5.5A<> 13.2B>
FBA_DQS_WP<1> 3.4B 5.4C 5.5B
FBA_DQS_WP<2> 3.4B 5.4D 5.5B
FBA_DQS_WP<3> 3.4B 5.4E 5.5B
FBA_DQS_WP<4> 3.4B 5.5B 5.5C
FBA_DQS_WP<5> 3.4B 5.5B 5.5C
FBA_DQS_WP<6> 3.4B 5.5B 5.5D
FBA_DQS_WP<7> 3.4B 5.5B 5.5E
FBA_VREF0 5.3D> 13.3B<>
FBA_VREF1 5.3G> 13.4B<>
FBA_VREF2 5.3E> 13.4B<>
FBA_VREF3 5.3H> 13.4B<>
FBA_ZQ0 5.2B< 13.4B<>
FBA_ZQ1 5.2E< 13.4B<>
FBB_CLK0 3.4H> 7.2A< 13.2B>
FBB_CLK0* 3.4H> 7.2A< 13.2B>
FBB_CLK0_TERM 7.1A
FBB_CLK1 3.4H> 7.2D< 13.2B>
FBB_CLK1* 3.4H> 7.2D< 13.2B>
FBB_CLK1_TERM 7.1D
FBB_CMD<0> 3.2G 7.1B 7.2G
FBB_CMD<27..0> 3.2H> 7.1B< 13.3B>
FBB_CMD<1> 3.2G 7.1B 7.1E
FBB_CMD<2> 3.2G 7.1B 7.2G

FBB_CMD<3> 3.2G 7.2B 7.2E
FBB_CMD<4> 3.2G 7.1E 7.1G
FBB_CMD<5> 3.2G 7.1E 7.1G
FBB_CMD<6> 3.3G 7.1E 7.1G
FBB_CMD<8> 3.3G 7.1B 7.1E
FBB_CMD<9> 3.3G 7.1B 7.1E
FBB_CMD<10> 3.3G 7.1B 7.1E
FBB_CMD<11> 3.3G 7.1B 7.1E
FBB_CMD<12> 3.3G 7.2B 7.2E
FBB_CMD<13> 3.3G 7.1E 7.1G
FBB_CMD<14> 3.3G 7.2B 7.2E
FBB_CMD<15> 3.3G 7.2B 7.2E
FBB_CMD<16> 3.3G 7.1B 7.1E
FBB_CMD<17> 3.3G 7.1B 7.1E
FBB_CMD<18> 3.3G 7.2B 7.2E
FBB_CMD<19> 3.3G 7.1B 7.1E
FBB_CMD<20> 3.3G 7.1B 7.1E
FBB_CMD<21> 3.3G 7.1B 7.1E
FBB_CMD<22> 3.3G 7.1B 7.2G
FBB_CMD<23> 3.3G 7.1B 7.1E
FBB_CMD<24> 3.3G 7.1B 7.2G
FBB_CMD<25> 3.3G 7.1B 7.1E
FBB_CMD<27> 3.3G 7.2B 7.2E
FBB_CMD_SENB0 7.2B
FBB_CMD_SENB1 7.2E
FBB_D<0> 3.1F 7.4C
FBB_D<63..0> 3.1E<> 7.4A<> 13.3B<>
FBB_D<1> 3.1F 7.4C
FBB_D<2> 3.1F 7.4C
FBB_D<3> 3.1F 7.4C
FBB_D<4> 3.1F 7.4C
FBB_D<5> 3.1F 7.4C
FBB_D<6> 3.1F 7.4C
FBB_D<7> 3.1F 7.4C
FBB_D<8> 3.1F 7.4C
FBB_D<9> 3.2F 7.4C
FBB_D<10> 3.2F 7.4C
FBB_D<11> 3.2F 7.4C
FBB_D<12> 3.2F 7.4C
FBB_D<13> 3.2F 7.4C
FBB_D<14> 3.2F 7.4C
FBB_D<15> 3.2F 7.4C
FBB_D<16> 3.2F 7.4D
FBB_D<17> 3.2F 7.4D
FBB_D<18> 3.2F 7.4D
FBB_D<19> 3.2F 7.4D
FBB_D<20> 3.2F 7.4D
FBB_D<21> 3.2F 7.4D
FBB_D<22> 3.2F 7.4D
FBB_D<23> 3.2F 7.4D
FBB_D<24> 3.2F 7.4E
FBB_D<25> 3.2F 7.4E
FBB_D<26> 3.2F 7.4E
FBB_D<27> 3.2F 7.4E
FBB_D<28> 3.2F 7.4E
FBB_D<29> 3.2F 7.4E
FBB_D<30> 3.2F 7.4E
FBB_D<31> 3.2F 7.4E
FBB_D<32> 3.2F 7.5C
FBB_D<33> 3.2F 7.5C
FBB_D<34> 3.2F 7.5C
FBB_D<35> 3.2F 7.5C
FBB_D<36> 3.2F 7.5C
FBB_D<37> 3.2F 7.5C
FBB_D<38> 3.2F 7.5C
FBB_D<39> 3.3F 7.5C
FBB_D<40> 3.3F 7.5C
FBB_D<41> 3.3F 7.5C
FBB_D<42> 3.3F 7.5C
FBB_D<43> 3.3F 7.5C
FBB_D<44> 3.3F 7.5C
FBB_D<45> 3.3F 7.5C
FBB_D<46> 3.3F 7.5C
FBB_D<47> 3.3F 7.5C
FBB_D<48> 3.3F 7.5D
FBB_D<49> 3.3F 7.5D
FBB_D<50> 3.3F 7.5D
FBB_D<51> 3.3F 7.5D
FBB_D<52> 3.3F 7.5D
FBB_D<53> 3.3F 7.5D
FBB_D<54> 3.3F 7.5D
FBB_D<55> 3.3F 7.5D
FBB_D<56> 3.3F 7.5E
FBB_D<57> 3.3F 7.5E
FBB_D<58> 3.3F 7.5E
FBB_D<59> 3.3F 7.5E
FBB_D<60> 3.3F 7.5E
FBB_D<61> 3.3F 7.5E
FBB_D<62> 3.3F 7.5E
FBB_D<63> 3.3F 7.5E
FBB_DEBUG 3.4G
FBB_DQM<0> 3.3F 7.4B 7.4C
FBB_DQM<7..0> 3.3E> 7.4A<> 13.3B>
FBB_DQM<1> 3.3F 7.4B 7.4C
FBB_DQM<2> 3.3F 7.4B 7.4D
FBB_DQM<3> 3.4F 7.4B 7.4E

FBB_DQM<4> 3.4F 7.4B 7.5C
FBB_DQM<5> 3.4F 7.4B 7.5C
FBB_DQM<6> 3.4F 7.4B 7.5D
FBB_DQM<7> 3.4F 7.4B 7.5E
FBB_DQS_RN<0> 3.4F 7.4B 7.4C
FBB_DQS_RN<7..0> 3.4E< 7.4A<> 13.3B<
FBB_DQS_RN<1> 3.4F 7.4B 7.4C
FBB_DQS_RN<2> 3.4F 7.4B 7.4D
FBB_DQS_RN<3> 3.4F 7.4B 7.4E
FBB_DQS_RN<4> 3.4F 7.5B 7.5C
FBB_DQS_RN<5> 3.4F 7.5B 7.5C
FBB_DQS_RN<6> 3.4F 7.5B 7.5D
FBB_DQS_RN<7> 3.4F 7.5B 7.5E
FBB_DQS_WP<0> 3.4F 7.4C 7.5B
FBB_DQS_WP<7..0> 3.4E> 7.5A<> 13.3B>
FBB_DQS_WP<1> 3.4F 7.4C 7.5B
FBB_DQS_WP<2> 3.4F 7.4D 7.5B
FBB_DQS_WP<3> 3.4F 7.4E 7.5B
FBB_DQS_WP<4> 3.4F 7.5B 7.5C
FBB_DQS_WP<5> 3.4F 7.5B 7.5C
FBB_DQS_WP<6> 3.4F 7.5B 7.5D
FBB_DQS_WP<7> 3.4F 7.5B 7.5E
FBB_VREF0 7.3D> 13.4B<>
FBB_VREF1 7.3G> 13.4B<>
FBB_VREF2 7.3E> 13.4B<>
FBB_VREF3 7.3H> 13.4B<>
FBB_ZQ0 7.2B< 13.4B<>
FBB_ZQ1 7.2E< 13.4B<>
FBC_CLK0 4.4D> 9.2A< 13.1E>
FBC_CLK0* 4.4D> 9.2A< 13.1E>
FBC_CLK0_TERM 9.1A
FBC_CLK1 4.4D> 9.2D< 13.2E>
FBC_CLK1* 4.4D> 9.2D< 13.2E>
FBC_CLK1_TERM 9.1D
FBC_CMD<0> 4.2C 9.1B 9.2G
FBC_CMD<27..0> 4.2D> 9.1B< 13.2E>
FBC_CMD<1> 4.2C 9.1B 9.1E
FBC_CMD<2> 4.2C 9.1B 9.2G
FBC_CMD<3> 4.2C 9.2B 9.2E
FBC_CMD<4> 4.2C 9.1E 9.1G
FBC_CMD<5> 4.2C 9.1E 9.1G
FBC_CMD<6> 4.3C 9.1E 9.1G
FBC_CMD<8> 4.3C 9.1B 9.1E
FBC_CMD<9> 4.3C 9.1B 9.1E
FBC_CMD<10> 4.3C 9.1B 9.1E
FBC_CMD<11> 4.3C 9.1B 9.1E
FBC_CMD<12> 4.3C 9.2B 9.2E
FBC_CMD<13> 4.3C 9.1E 9.1G
FBC_CMD<14> 4.3C 9.2B 9.2E
FBC_CMD<15> 4.3C 9.2B 9.2E
FBC_CMD<16> 4.3C 9.1B 9.1E
FBC_CMD<17> 4.3C 9.1B 9.1E
FBC_CMD<18> 4.3C 9.2B 9.2E
FBC_CMD<19> 4.3C 9.1B 9.1E
FBC_CMD<20> 4.3C 9.1B 9.1E
FBC_CMD<21> 4.3C 9.1B 9.1E
FBC_CMD<22> 4.3C 9.1B 9.2G
FBC_CMD<23> 4.3C 9.1B 9.1E
FBC_CMD<24> 4.3C 9.1B 9.2G
FBC_CMD<25> 4.3C 9.1B 9.1E
FBC_CMD<27> 4.3C 9.2B 9.2E
FBC_CMD_SENC0 9.2B
FBC_CMD_SENC1 9.2E
FBC_D<0> 4.1B 9.4C
FBC_D<63..0> 4.1A<> 9.4A<> 13.2E<>
FBC_D<1> 4.1B 9.4C
FBC_D<2> 4.1B 9.4C
FBC_D<3> 4.1B 9.4C
FBC_D<4> 4.1B 9.4C
FBC_D<5> 4.1B 9.4C
FBC_D<6> 4.1B 9.4C
FBC_D<7> 4.1B 9.4C
FBC_D<8> 4.1B 9.4C
FBC_D<9> 4.2B 9.4C
FBC_D<10> 4.2B 9.4C
FBC_D<11> 4.2B 9.4C
FBC_D<12> 4.2B 9.4C
FBC_D<13> 4.2B 9.4C
FBC_D<14> 4.2B 9.4C
FBC_D<15> 4.2B 9.4C
FBC_D<16> 4.2B 9.4D
FBC_D<17> 4.2B 9.4D
FBC_D<18> 4.2B 9.4D
FBC_D<19> 4.2B 9.4D
FBC_D<20> 4.2B 9.4D
FBC_D<21> 4.2B 9.4D
FBC_D<22> 4.2B 9.4D
FBC_D<23> 4.2B 9.4D
FBC_D<24> 4.2B 9.4E
FBC_D<25> 4.2B 9.4E
FBC_D<26> 4.2B 9.4E
FBC_D<27> 4.2B 9.4E
FBC_D<28> 4.2B 9.4E
FBC_D<29> 4.2B 9.4E
FBC_D<30> 4.2B 9.4E
FBC_D<31> 4.2B 9.4E

FBC_D<32> 4.2B 9.5C
FBC_D<33> 4.2B 9.5C
FBC_D<34> 4.2B 9.5C
FBC_D<35> 4.2B 9.5C
FBC_D<36> 4.2B 9.5C
FBC_D<37> 4.2B 9.5C
FBC_D<38> 4.2B 9.5C
FBC_D<39> 4.3B 9.5C
FBC_D<40> 4.3B 9.5C
FBC_D<41> 4.3B 9.5C
FBC_D<42> 4.3B 9.5C
FBC_D<43> 4.3B 9.5C
FBC_D<44> 4.3B 9.5C
FBC_D<45> 4.3B 9.5C
FBC_D<46> 4.3B 9.5C
FBC_D<47> 4.3B 9.5C
FBC_D<48> 4.3B 9.5D
FBC_D<49> 4.3B 9.5D
FBC_D<50> 4.3B 9.5D
FBC_D<51> 4.3B 9.5D
FBC_D<52> 4.3B 9.5D
FBC_D<53> 4.3B 9.5D
FBC_D<54> 4.3B 9.5D
FBC_D<55> 4.3B 9.5D
FBC_D<56> 4.3B 9.5E
FBC_D<57> 4.3B 9.5E
FBC_D<58> 4.3B 9.5E
FBC_D<59> 4.3B 9.5E
FBC_D<60> 4.3B 9.5E
FBC_D<61> 4.3B 9.5E
FBC_D<62> 4.3B 9.5E
FBC_D<63> 4.3B 9.5E
FBC_DEBUG 4.4C
FBC_DQM<0> 4.3B 9.4B 9.4C
FBC_DQM<7..0> 4.3A> 9.4A<> 13.2E>
FBC_DQM<1> 4.3B 9.4B 9.4C
FBC_DQM<2> 4.3B 9.4B 9.4D
FBC_DQM<3> 4.4B 9.4B 9.4E
FBC_DQM<4> 4.4B 9.4B 9.5C
FBC_DQM<5> 4.4B 9.4B 9.5C
FBC_DQM<6> 4.4B 9.4B 9.5D
FBC_DQM<7> 4.4B 9.4B 9.5E
FBC_DQS_RN<0> 4.4B 9.4B 9.4C
FBC_DQS_RN<7..0> 4.4A< 9.4A<> 13.2E<
FBC_DQS_RN<1> 4.4B 9.4B 9.4C
FBC_DQS_RN<2> 4.4B 9.4B 9.4D
FBC_DQS_RN<3> 4.4B 9.4B 9.4E
FBC_DQS_RN<4> 4.4B 9.5B 9.5C
FBC_DQS_RN<5> 4.4B 9.5B 9.5C
FBC_DQS_RN<6> 4.4B 9.5B 9.5D
FBC_DQS_RN<7> 4.4B 9.5B 9.5E
FBC_DQS_WP<0> 4.4B 9.4C 9.5B
FBC_DQS_WP<7..0> 4.4A> 9.5A<> 13.2E>
FBC_DQS_WP<1> 4.4B 9.4C 9.5B
FBC_DQS_WP<2> 4.4B 9.4D 9.5B
FBC_DQS_WP<3> 4.4B 9.4E 9.5B
FBC_DQS_WP<4> 4.4B 9.5B 9.5C
FBC_DQS_WP<5> 4.4B 9.5B 9.5C
FBC_DQS_WP<6> 4.4B 9.5B 9.5D
FBC_DQS_WP<7> 4.4B 9.5B 9.5E
FBC_VREF0 9.3D> 13.3E<>
FBC_VREF1 9.3G> 13.4E<>
FBC_VREF2 9.3E> 13.4E<>
FBC_VREF3 9.3H> 13.4E<>
FBC_ZQ0 9.2B< 13.4E<>
FBC_ZQ1 9.2E< 13.4E<>
FBD_CLK0 4.4H> 11.2A< 13.2E>
FBD_CLK0* 4.4H> 11.2A< 13.2E>
FBD_CLK0_TERM 11.1A
FBD_CLK1 4.4H> 11.2D< 13.2E>
FBD_CLK1* 4.4H> 11.2D< 13.2E>
FBD_CLK1_TERM 11.1D
FBD_CMD<0> 4.2G 11.1B 11.2G
FBD_CMD<27..0> 4.2H> 11.1B< 13.3E>
FBD_CMD<1> 4.2G 11.1C 11.1F
FBD_CMD<2> 4.2G 11.1B 11.2G
FBD_CMD<3> 4.2G 11.2C 11.2F
FBD_CMD<4> 4.2G 11.1E 11.1G
FBD_CMD<5> 4.2G 11.1E 11.1G
FBD_CMD<6> 4.3G 11.1E 11.1G
FBD_CMD<8> 4.3G 11.1C 11.1F
FBD_CMD<9> 4.3G 11.1C 11.1F
FBD_CMD<10> 4.3G 11.1C 11.1F
FBD_CMD<11> 4.3G 11.1C 11.1F
FBD_CMD<12> 4.3G 11.2C 11.2F
FBD_CMD<13> 4.3G 11.1E 11.2G
FBD_CMD<14> 4.3G 11.2C 11.2F
FBD_CMD<15> 4.3G 11.2C 11.2F
FBD_CMD<16> 4.3G 11.1C 11.1F
FBD_CMD<17> 4.3G 11.1C 11.1F
FBD_CMD<18> 4.3G 11.2C 11.2F
FBD_CMD<19> 4.3G 11.1C 11.1F
FBD_CMD<20> 4.3G 11.1C 11.1F
FBD_CMD<21> 4.3G 11.1C 11.1F
FBD_CMD<22> 4.3G 11.1B 11.2G
FBD_CMD<23> 4.3G 11.1C 11.1F

FBD_CMD<24> 4.3G 11.1B 11.2G
FBD_CMD<25> 4.3G 11.1C 11.1F
FBD_CMD<27> 4.3G 11.2C 11.2F
FBD_CMD_SEND0 11.2C
FBD_CMD_SEND1 11.2F
FBD_D<0> 4.1F 11.4C
FBD_D<63..0> 4.1E<> 11.4A<>
13.3E<>
FBD_D<1> 4.1F 11.4C
FBD_D<2> 4.1F 11.4C
FBD_D<3> 4.1F 11.4C
FBD_D<4> 4.1F 11.4C
FBD_D<5> 4.1F 11.4C
FBD_D<6> 4.1F 11.4C
FBD_D<7> 4.1F 11.4C
FBD_D<8> 4.1F 11.4D
FBD_D<9> 4.2F 11.4D
FBD_D<10> 4.2F 11.4D
FBD_D<11> 4.2F 11.4D
FBD_D<12> 4.2F 11.4D
FBD_D<13> 4.2F 11.4D
FBD_D<14> 4.2F 11.4D
FBD_D<15> 4.2F 11.4D
FBD_D<16> 4.2F 11.4D
FBD_D<17> 4.2F 11.4D
FBD_D<18> 4.2F 11.4D
FBD_D<19> 4.2F 11.4D
FBD_D<20> 4.2F 11.4D
FBD_D<21> 4.2F 11.4D
FBD_D<22> 4.2F 11.4D
FBD_D<23> 4.2F 11.4D
FBD_D<24> 4.2F 11.4E
FBD_D<25> 4.2F 11.4E
FBD_D<26> 4.2F 11.4E
FBD_D<27> 4.2F 11.4E
FBD_D<28> 4.2F 11.4E
FBD_D<29> 4.2F 11.4E
FBD_D<30> 4.2F 11.4E
FBD_D<31> 4.2F 11.4E
FBD_D<32> 4.2F 11.5C
FBD_D<33> 4.2F 11.5C
FBD_D<34> 4.2F 11.5C
FBD_D<35> 4.2F 11.5C
FBD_D<36> 4.2F 11.5C
FBD_D<37> 4.2F 11.5C
FBD_D<38> 4.2F 11.5C
FBD_D<39> 4.3F 11.5C
FBD_D<40> 4.3F 11.5D
FBD_D<41> 4.3F 11.5D
FBD_D<42> 4.3F 11.5D
FBD_D<43> 4.3F 11.5D
FBD_D<44> 4.3F 11.5D
FBD_D<45> 4.3F 11.5D
FBD_D<46> 4.3F 11.5D
FBD_D<47> 4.3F 11.5D
FBD_D<48> 4.3F 11.5D
FBD_D<49> 4.3F 11.5D
FBD_D<50> 4.3F 11.5D
FBD_D<51> 4.3F 11.5D
FBD_D<52> 4.3F 11.5D
FBD_D<53> 4.3F 11.5D
FBD_D<54> 4.3F 11.5D
FBD_D<55> 4.3F 11.5D
FBD_D<56> 4.3F 11.5E
FBD_D<57> 4.3F 11.5E
FBD_D<58> 4.3F 11.5E
FBD_D<59> 4.3F 11.5E
FBD_D<60> 4.3F 11.5E
FBD_D<61> 4.3F 11.5E
FBD_D<62> 4.3F 11.5E
FBD_D<63> 4.3F 11.5E
FBD_DEBUG 4.4G
FBD_DQM<0> 4.3F 11.4B 11.4C
FBD_DQM<7..0> 4.3E> 11.4A<> 13.3E>
FBD_DQM<1> 4.3F 11.4B 11.4D
FBD_DQM<2> 4.3F 11.4B 11.4D
FBD_DQM<3> 4.4F 11.4B 11.4E
FBD_DQM<4> 4.4F 11.4B 11.5C
FBD_DQM<5> 4.4F 11.4B 11.5D
FBD_DQM<6> 4.4F 11.4B 11.5D
FBD_DQM<7> 4.4F 11.4B 11.5E
FBD_DQS_RN<0> 4.4F 11.4B 11.4C
FBD_DQS_RN<7..0> 4.4E< 11.4A<> 13.3E<
FBD_DQS_RN<1> 4.4F 11.4B 11.4D
FBD_DQS_RN<2> 4.4F 11.4B 11.4D
FBD_DQS_RN<3> 4.4F 11.4B 11.4E
FBD_DQS_RN<4> 4.4F 11.5B 11.5C
FBD_DQS_RN<5> 4.4F 11.5B 11.5D
FBD_DQS_RN<6> 4.4F 11.5B 11.5D
FBD_DQS_RN<7> 4.4F 11.5B 11.5E
FBD_DQS_WP<0> 4.4F 11.4C 11.5B
FBD_DQS_WP<7..0> 4.4E> 11.5A<> 13.3E>
FBD_DQS_WP<1> 4.4F 11.4D 11.5B
FBD_DQS_WP<2> 4.4F 11.4D 11.5B
FBD_DQS_WP<3> 4.4F 11.4E 11.5B
FBD_DQS_WP<4> 4.4F 11.5B 11.5C

FBD_DQS_WP<5> 4.4F 11.5B 11.5D
FBD_DQS_WP<6> 4.4F 11.5B 11.5D
FBD_DQS_WP<7> 4.4F 11.5B 11.5E
FBD_VREF0 11.3D> 13.4E<>
FBD_VREF1 11.3G> 13.4E<>
FBD_VREF2 11.3E> 13.4E<>
FBD_VREF3 11.3H> 13.4E<>
FBD_ZQ0 11.2B< 13.4E<>
FBD_ZQ1 11.2E< 13.4E<>
FBVDDQ 28.1G
FBVDDQ_SENSE 4.5H> 28.4H<
FBVDDQ_SENSE_R 28.1G< 28.4F
FB_CAL_PD_VDDQ 3.4G
FB_CAL_PU_GND 3.5G
FB_CAL_TERM_GND 3.5G
FB_PLLAVDD0 3.5D> 13.3B<>
FB_PLLAVDD1 4.5D> 13.3E<>
FB_VREF 3.5A> 13.3B<>
GPIO0_DVI_A_HPD 16.4C> 21.3D<
GPIO0_DVI_A_HPD_C 16.3G
GPIO0_DVI_A_HPD_R 16.4E
GPIO1_DVI_C_HPD 17.4D> 21.3D<
GPIO1_DVI_C_HPD_C 17.3G
GPIO1_DVI_C_HPD_R 17.4E
GPIO4_FAN_TACH 21.3D> 31.2C>
GPIO5_VSEL0 21.3D> 29.4C<
GPIO6_NVVDD_PHASE 21.3D> 29.2A<
GPIO6_NVVDD_PHASE_ 29.2A
R
GPIO8_GPU_SLOW* 21.3D> 25.2C<
GPIO9_FAN_ON 31.3E
GPIO9_FAN_PWM 21.3D> 31.2C<
GPIO9_FAN_PWM_R 31.3E
GPIO11_SLI_SYNC1 20.2E<> 21.3D>
GPIO22_SWAPRDY_A 20.2E<> 21.4E>
GPU_PLLVDD 21.1G< 21.4F
GPU_TESTMODE 2.5D 2.5G>
HDA_RST* 21.1A
HDMI_PD 17.1F
HDMI_PD_EN 17.2F
I2CA_SCL 14.3C
I2CA_SCL_C 14.1G> 14.1G> 16.3G<
I2CA_SCL_T 14.1D
I2CA_SDA 14.3C
I2CA_SDA_C 14.1G> 14.1G> 16.3G<
I2CA_SDA_T 14.1D
I2CB_SCL 15.2C
I2CB_SCL_C 15.1G> 15.1G> 17.3G<
I2CB_SCL_T 15.1D
I2CB_SDA 15.2C
I2CB_SDA_C 15.1G> 15.1G> 17.3G<
I2CB_SDA_T 15.1D
I2CC_SCL 21.2C
I2CC_SCL_R 21.3E>
I2CC_SDA 21.3C
I2CC_SDA_R 21.3E>
I2CH_SCL 21.2C 21.2F
I2CH_SDA 21.2C 21.2F
I2CH_SDA_R 21.2F
I2CS_SCL 21.2C
I2CS_SDA 21.2C
IFPABCD_PLLVDD 16.3B< 17.2B>
IFPAB_IOVDD 16.1G<> 16.3B
IFPAB_RSET 16.1G<> 16.2B
IFPAB_TXC 16.2D
IFPAB_TXC* 16.2D
IFPAB_TXD0 16.2D
IFPAB_TXD0* 16.2D
IFPAB_TXD1 16.3D
IFPAB_TXD1* 16.2D
IFPAB_TXD2 16.3D
IFPAB_TXD2* 16.3D
IFPAB_TXD4 16.3D
IFPAB_TXD4* 16.3D
IFPAB_TXD5 16.3D
IFPAB_TXD5* 16.3D
IFPAB_TXD6 16.3D
IFPAB_TXD6* 16.3D
IFPCD_IOVDD 17.1G<> 17.3B
IFPCD_PLLVDD 17.1G<>
IFPCD_RSET 17.1G<> 17.2B
IFPCD_TXC 17.1E 17.2E
IFPCD_TXC* 17.1E 17.2E
IFPCD_TXD0 17.1E 17.2E
IFPCD_TXD0* 17.1E 17.2E
IFPCD_TXD1 17.1E 17.2E
IFPCD_TXD1* 17.1E 17.2E
IFPCD_TXD2 17.1E 17.2E
IFPCD_TXD2* 17.1E 17.2E
IFPCD_TXD4 17.1E 17.2G 17.3E
IFPCD_TXD4* 17.1E 17.2G 17.3E
IFPCD_TXD5 17.1E 17.3E 17.3G
IFPCD_TXD5* 17.1E 17.3E 17.3G
IFPCD_TXD6 17.1E 17.3E 17.3G
IFPCD_TXD6* 17.1E 17.3E 17.3G
IFPC_TXC 17.2C

ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	<edit here to insert page detail>

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NV_PN	600-10562-base-000 A		
ID		PAGE	
NAME	<ENGINEER>	DATE	30-DEC-2008

IFPC_TXC* 17.2C
IFPC_TXD0 17.2C
IFPC_TXD0* 17.2C
IFPC_TXD1 17.2C
IFPC_TXD1* 17.2C
IFPC_TXD2 17.2C
IFPC_TXD2* 17.2C
IFPD_TXD4 17.3C
IFPD_TXD4* 17.3C
IFPD_TXD5 17.3C
IFPD_TXD5* 17.3C
IFPD_TXD6 17.3C
IFPD_TXD6* 17.3C
JTAG_TCLK 2.1F> 2.1F> 21.3A<
JTAG_TDI 2.1F> 2.1F> 21.3A<
JTAG_TDO 2.1F< 2.1F< 21.3A>
JTAG_TMS 2.1F> 2.1F> 21.3A<
JTAG_TRST* 2.1F> 2.1F> 21.3A<
MIOA_CAL_PD_VDDQ 20.2C 20.5A<
MIOA_CAL_PU_GND 20.2C 20.5A<
MIOA_CLKIN 20.2E 20.5A<
MIOA_CLKOUT 20.2E 20.5A<
MIOA_D0 20.1E 20.1F
20.1F< 20.5A<
MIOA_D1 20.1E 20.1F
20.1F< 20.5A<
MIOA_D2 20.1E 20.1F
20.1F< 20.5A<
MIOA_D3 20.1E 20.1F
20.1F< 20.5A<
MIOA_D4 20.1E 20.1F
20.1F< 20.5A<
MIOA_D5 20.1E 20.1F
20.1F< 20.5A<
MIOA_D6 20.2E 20.2F
20.1F< 20.5A<
MIOA_D7 20.2E 20.2F
20.1F< 20.5A<
MIOA_D8 20.2E 20.2F
20.1F< 20.5A<
MIOA_D9 20.2E 20.2F
20.1F< 20.5A<
MIOA_D10 20.2E 20.2F
20.1F< 20.5A<
MIOA_D11 20.2E 20.2F
20.1F< 20.5A<
MIOA_D12 20.2E 20.2F
20.1F< 20.5A<
MIOA_D13 20.2E 20.2F
20.1F< 20.5A<
MIOA_D14 20.2E 20.2F
20.1F< 20.5A<
MIOA_DE 20.2E 20.5A<
MIOA_VREF 20.2C 20.5A<
MIOB_CLKIN 20.4E
MIOB_D15_STRAP0 20.4E> 22.3A<
MIOB_D16_STRAP1 20.4E> 22.4A<
MIOB_D17_STRAP2 20.4E> 22.5A<
NVVDD 29.1A
NVVDD_EN 25.3D> 29.3A<
NVVDD_RBOT1 29.4D
NVVDD_SENSE_GPU 2.4E> 2.5G> 29.4G<
NVVDD_VSEL0 29.4D
PEX_CAL_PD_VDDQ 2.5D
PEX_CAL_PU_GND 2.5D
PEX_PLLVDD 2.4D 2.5G<
PEX_PLL_CLK_OUT 2.4D 2.4G>
PEX_PLL_CLK_OUT* 2.4D 2.4G>
PEX_PRSNT* 2.1A 2.4A 2.4G>
PEX_REFCLK 2.1G> 2.2B
PEX_REFCLK* 2.1G> 2.2B
PEX_RST* 2.2C> 2.4G> 25.3A<
PEX_RST_R* 25.3A
PEX_RX0 2.2B 2.2G>
PEX_RX0* 2.2B 2.2G>
PEX_RX1 2.2B 2.2G>
PEX_RX1* 2.2B 2.2G>
PEX_RX2 2.2B 2.2G>
PEX_RX2* 2.2B 2.2G>
PEX_RX3 2.2G> 2.3B
PEX_RX3* 2.2G> 2.3B
PEX_RX4 2.2G> 2.3B
PEX_RX4* 2.2G> 2.3B
PEX_RX5 2.2G> 2.3B
PEX_RX5* 2.2G> 2.3B
PEX_RX6 2.2G> 2.3B
PEX_RX6* 2.2G> 2.3B
PEX_RX7 2.2G> 2.3B
PEX_RX7* 2.3B 2.3G>
PEX_RX8 2.3G> 2.4B
PEX_RX8* 2.3G> 2.4B
PEX_RX9 2.3G> 2.4B
PEX_RX9* 2.3G> 2.4B
PEX_RX10 2.3G> 2.4B
PEX_RX10* 2.3G> 2.4B
PEX_RX11 2.3G> 2.4B

PEX_RX11* 2.3G> 2.4B
PEX_RX12 2.3G> 2.4B
PEX_RX12* 2.3G> 2.4B
PEX_RX13 2.3G> 2.5B
PEX_RX13* 2.3G> 2.5B
PEX_RX14 2.3G> 2.5B
PEX_RX14* 2.3G> 2.5B
PEX_RX15 2.3G> 2.5B
PEX_RX15* 2.3G> 2.5B
PEX_SMCLK 2.1C> 21.2E<
PEX_SMDAT 2.1C> 21.2E<
PEX_TCLK 2.1B
PEX_TDI 2.1B
PEX_TDO 2.1B
PEX_TERMP 2.5D
PEX_TMS 2.1B
PEX_TRST* 2.1B
PEX_TX0 2.2C 2.3G>
PEX_TX0* 2.2C 2.3G>
PEX_TX1 2.2C 2.3G>
PEX_TX1* 2.2C 2.3G>
PEX_TX2 2.2C 2.3G>
PEX_TX2* 2.2C 2.3G>
PEX_TX3 2.2C 2.3G>
PEX_TX3* 2.2C 2.3G>
PEX_TX4 2.3C 2.3G>
PEX_TX4* 2.3C 2.3G>
PEX_TX5 2.3C 2.3G>
PEX_TX5* 2.3C 2.3G>
PEX_TX6 2.3C 2.4G>
PEX_TX6* 2.3C 2.4G>
PEX_TX7 2.3C 2.4G>
PEX_TX7* 2.3C 2.4G>
PEX_TX8 2.3C 2.4G>
PEX_TX8* 2.3C 2.4G>
PEX_TX9 2.4C 2.4G>
PEX_TX9* 2.4C 2.4G>
PEX_TX10 2.4C 2.4G>
PEX_TX10* 2.4C 2.4G>
PEX_TX11 2.4C 2.4G>
PEX_TX11* 2.4C 2.4G>
PEX_TX12 2.4C 2.4G>
PEX_TX12* 2.4C 2.4G>
PEX_TX13 2.4C 2.4G>
PEX_TX13* 2.4C 2.4G>
PEX_TX14 2.4G> 2.5C
PEX_TX14* 2.4G> 2.5C
PEX_TX15 2.4G> 2.5C
PEX_TX15* 2.4G> 2.5C
PEX_TXX0 2.1G> 2.2B
PEX_TXX0* 2.1G> 2.2B
PEX_TXX1 2.1G> 2.2B
PEX_TXX1* 2.1G> 2.2B
PEX_TXX2 2.1G> 2.2B
PEX_TXX2* 2.1G> 2.2B
PEX_TXX3 2.1G> 2.2B
PEX_TXX3* 2.1G> 2.2B
PEX_TXX4 2.1G> 2.3B
PEX_TXX4* 2.1G> 2.3B
PEX_TXX5 2.1G> 2.3B
PEX_TXX5* 2.1G> 2.3B
PEX_TXX6 2.1G> 2.3B
PEX_TXX6* 2.1G> 2.3B
PEX_TXX7 2.1G> 2.3B
PEX_TXX7* 2.1G> 2.3B
PEX_TXX8 2.1G> 2.3B
PEX_TXX8* 2.1G> 2.3B
PEX_TXX9 2.2G> 2.4B
PEX_TXX9* 2.2G> 2.4B
PEX_TXX10 2.2G> 2.4B
PEX_TXX10* 2.2G> 2.4B
PEX_TXX11 2.2G> 2.4B
PEX_TXX11* 2.2G> 2.4B
PEX_TXX12 2.2G> 2.4B
PEX_TXX12* 2.2G> 2.4B
PEX_TXX13 2.2G> 2.4B
PEX_TXX13* 2.2G> 2.4B
PEX_TXX14 2.2G> 2.5B
PEX_TXX14* 2.2G> 2.5B
PEX_TXX15 2.2G> 2.5B
PEX_TXX15* 2.2G> 2.5B
PEX_VDD 28.1G
PS1_NVVDD_FS 29.1F< 29.2B
PS1_NVVDD_SS 29.1F< 29.2B
PS1_NVVDD_SUS_R 29.2B
PS_1V1_CP 28.1G< 28.3B
PS_1V1_DR 28.1G< 28.3C
PS_1V1_FB 28.1G< 28.3C
PS_1V8_ADJ 27.4F
PS_2V5_ADJ 27.2D
PS_2V5_VDD 27.2D
PS_5V_ADJ 27.4B
PS_FBVDDQ_FS 28.2G< 28.3C
PS_FB_BOOT 28.1G< 28.3D
PS_FB_COMP 28.1G< 28.3D
PS_FB_EN 28.5B

PS_FB_EN* 28.5C
PS_FB_FB 28.1G< 28.3D
PS_FB_LGATE 28.1G< 28.3D
PS_FB_PHASE 28.1G< 28.3E
PS_FB_PVCC5 28.1G< 28.2D
PS_FB_RBOT 28.1G<
PS_FB_RC 28.1G< 28.4E
PS_FB_RC_CP 28.3D
PS_FB_SNUB 28.1G< 28.3F
PS_FB_UGATE 28.1G< 28.3D
PS_FB_UGATE_R 28.1G< 28.2E
PS_FB_VCC5 28.1G< 28.2C
PS_FB_VCC12 28.2D
PS_NVVDD_BOOT1 29.1F< 29.2C
PS_NVVDD_BOOT2 29.1G< 29.2C
PS_NVVDD_CP 29.1F< 29.2B
PS_NVVDD_DRVH1 29.1F< 29.2C
PS_NVVDD_DRVH1_R 29.1D 29.1F<
PS_NVVDD_DRVH2 29.1G< 29.2C
PS_NVVDD_DRVH2_R 29.1G< 29.2D
PS_NVVDD_DRVL1 29.1F< 29.2C
PS_NVVDD_DRVL2 29.1G< 29.3C
PS_NVVDD_EN 29.5A
PS_NVVDD_EN* 29.4B
PS_NVVDD_EN_AND 29.4B
PS_NVVDD_FB 29.1F< 29.2B 29.4D
PS_NVVDD_OC 29.3C
PS_NVVDD_PH1 29.1F< 29.2C
PS_NVVDD_PH2 29.1G< 29.2C
PS_NVVDD_PVCC9 29.1G< 29.2B
PS_NVVDD_RC 29.1G< 29.4F
PS_NVVDD_RC1 29.1G< 29.2E
PS_NVVDD_RC2 29.1G< 29.3E
PS_NVVDD_RC_CP 29.1F< 29.2A
PS_NVVDD_SUS 29.1F< 29.2B
PS_NVVDD_VCC 29.1G< 29.2C
PS_NVVDD_VCC9 29.1F< 29.2B
PS_NVVDD_VSEN 29.1G< 29.4F
ROM_CS* 21.1C
ROM_SCLK 21.1D> 22.3A<
ROM_SI 21.1D> 22.1A<
ROM_SO 21.1D> 22.2A<
SNN_2V5_NC 27.2D
SNN_2V5_PGOOD 27.2D
SNN_A_MON_ID0 14.3H
SNN_A_MON_ID2 14.4F
SNN_BBIASN 21.1A
SNN_BBIASP 21.1A
SNN_BUFRST* 21.2C
SNN_C_MON_ID0 15.3H
SNN_C_MON_ID2 15.4F
SNN_DACB_COUT 19.3C
SNN_DACB_CSYNC 19.3C
SNN_DACB_PBOUT 19.3C
SNN_DACB_RSET 19.3B
SNN_DACB_VREF 19.3B
SNN_DACB_YOUT 19.3C
SNN_FBA0_NC1 5.2B
SNN_FBA1_NC1 5.2E
SNN_FBA_CMD<7> 3.3C
SNN_FBA_CMD<26> 3.3C
SNN_FBA_CMD<28> 3.3C
SNN_FBA_CMD<29> 3.3C
SNN_FBA_CMD<30> 3.3C
SNN_FBA_DBI<0> 3.4B
SNN_FBA_DBI<1> 3.4B
SNN_FBA_DBI<2> 3.5B
SNN_FBA_DBI<3> 3.5B
SNN_FBA_DBI<4> 3.5B
SNN_FBA_DBI<5> 3.5B
SNN_FBA_DBI<6> 3.5B
SNN_FBA_DBI<7> 3.5B
SNN_FBA_WDS0 3.4C
SNN_FBA_WDS0* 3.4C
SNN_FBA_WDS1 3.4C
SNN_FBA_WDS1* 3.4C
SNN_FBA_WDS2 3.4C
SNN_FBA_WDS2* 3.4C
SNN_FBA_WDS3 3.4C
SNN_FBA_WDS3* 3.4C
SNN_FBB0_NC1 7.2B
SNN_FBB1_NC1 7.2E
SNN_FBB_CMD<7> 3.3G
SNN_FBB_CMD<26> 3.3G
SNN_FBB_CMD<28> 3.3G
SNN_FBB_CMD<29> 3.3G
SNN_FBB_CMD<30> 3.3G
SNN_FBB_DBI<0> 3.4F
SNN_FBB_DBI<1> 3.4F
SNN_FBB_DBI<2> 3.5F
SNN_FBB_DBI<3> 3.5F
SNN_FBB_DBI<4> 3.5F
SNN_FBB_DBI<5> 3.5F
SNN_FBB_DBI<6> 3.5F
SNN_FBB_DBI<7> 3.5F
SNN_FBB_WDS0 3.4G

SNN_FBB_WDS0* 3.4G
SNN_FBB_WDS1 3.4G
SNN_FBB_WDS1* 3.4G
SNN_FBB_WDS2 3.4G
SNN_FBB_WDS2* 3.4G
SNN_FBB_WDS3 3.4G
SNN_FBB_WDS3* 3.4G
SNN_FBC0_NC1 9.2B
SNN_FBC1_NC1 9.2E
SNN_FBC_CMD<7> 4.3C
SNN_FBC_CMD<26> 4.3C
SNN_FBC_CMD<28> 4.3C
SNN_FBC_CMD<29> 4.3C
SNN_FBC_CMD<30> 4.3C
SNN_FBC_DBI<0> 4.4B
SNN_FBC_DBI<1> 4.4B
SNN_FBC_DBI<2> 4.5B
SNN_FBC_DBI<3> 4.5B
SNN_FBC_DBI<4> 4.5B
SNN_FBC_DBI<5> 4.5B
SNN_FBC_DBI<6> 4.5B
SNN_FBC_DBI<7> 4.5B
SNN_FBC_WDS0 4.4C
SNN_FBC_WDS0* 4.4C
SNN_FBC_WDS1 4.4C
SNN_FBC_WDS1* 4.4C
SNN_FBC_WDS2 4.4C
SNN_FBC_WDS2* 4.4C
SNN_FBC_WDS3 4.4C
SNN_FBC_WDS3* 4.4C
SNN_FBD0_NC1 11.2C
SNN_FBD1_NC1 11.2F
SNN_FBD_CMD<7> 4.3G
SNN_FBD_CMD<26> 4.3G
SNN_FBD_CMD<28> 4.3G
SNN_FBD_CMD<29> 4.3G
SNN_FBD_CMD<30> 4.3G
SNN_FBD_DBI<0> 4.4F
SNN_FBD_DBI<1> 4.4F
SNN_FBD_DBI<2> 4.5F
SNN_FBD_DBI<3> 4.5F
SNN_FBD_DBI<4> 4.5F
SNN_FBD_DBI<5> 4.5F
SNN_FBD_DBI<6> 4.5F
SNN_FBD_DBI<7> 4.5F
SNN_FBD_WDS0 4.4G
SNN_FBD_WDS0* 4.4G
SNN_FBD_WDS1 4.4G
SNN_FBD_WDS1* 4.4G
SNN_FBD_WDS2 4.4G
SNN_FBD_WDS2* 4.4G
SNN_FBD_WDS3 4.4G
SNN_FBD_WDS3* 4.4G
SNN_GPIO2 21.3C
SNN_GPIO3 21.3C
SNN_GPIO7 21.3C
SNN_GPIO10 21.3C
SNN_GPIO12 21.3C
SNN_GPIO13 21.3C
SNN_GPIO14 21.3C
SNN_GPIO15 21.3C
SNN_GPIO16 21.3C
SNN_GPIO17 21.3C
SNN_GPIO18 21.3C
SNN_GPIO19 21.3C
SNN_GPIO20 21.3C
SNN_GPIO21 21.3C
SNN_GPIO23_STEREO 21.4C
SNN_HDA_SDI 21.1A
SNN_HDA_SDO 21.2A
SNN_HDA_SYNC 21.2A
SNN_HDSA_BCLK 21.1A
SNN_I2CD_SCL 21.3C
SNN_I2CD_SDA 21.3C
SNN_I2CE_SCL 21.3C
SNN_I2CE_SDA 21.3C
SNN_IFPAB_ATXD3 16.3D
SNN_IFPAB_ATXD3* 16.3D
SNN_IFPAB_BTXC 16.3D
SNN_IFPAB_BTXC* 16.3D
SNN_IFPAB_TXD7 16.3D
SNN_IFPAB_TXD7* 16.3D
SNN_IFPC_AUX 17.2C
SNN_IFPC_AUX* 17.2C
SNN_IFPD_AUX 17.3C
SNN_IFPD_AUX* 17.3C
SNN_IFPD_L3 17.3C
SNN_IFPD_L3* 17.3C
SNN_IFPEF_RSET 18.3B
SNN_IFPE_AUX 18.3D
SNN_IFPE_AUX* 18.3D
SNN_IFPE_L0 18.3D
SNN_IFPE_L0* 18.3D
SNN_IFPE_L1 18.3D
SNN_IFPE_L1* 18.3D
SNN_IFPE_L2 18.3D

SNN_IFPE_L2* 18.3D
SNN_IFPE_TXC 18.3D
SNN_IFPE_TXC* 18.3D
SNN_IFPF_AUX 18.4D
SNN_IFPF_AUX* 18.4D
SNN_IFPF_L0 18.4D
SNN_IFPF_L0* 18.4D
SNN_IFPF_L1 18.4D
SNN_IFPF_L1* 18.4D
SNN_IFPF_L2 18.4D
SNN_IFPF_L2* 18.4D
SNN_IFPF_L3 18.4D
SNN_IFPF_L3* 18.4D
SNN_MIOA_CLKOUT* 20.2E
SNN_MIOA_D12 20.2E
SNN_MIOA_D13 20.2E
SNN_MIOA_D14 20.2E
SNN_MIOB_CAL_PD_VD 20.4C
DQ
SNN_MIOB_CAL_PU_GN 20.4C
D
SNN_MIOB_CLKOUT 20.4E
SNN_MIOB_CLKOUT* 20.4E
SNN_MIOB_CTL3 20.4E
SNN_MIOB_D0 20.3E
SNN_MIOB_D1 20.3E
SNN_MIOB_D2 20.3E
SNN_MIOB_D3 20.3E
SNN_MIOB_D4 20.3E
SNN_MIOB_D5 20.3E
SNN_MIOB_D6 20.3E
SNN_MIOB_D7 20.3E
SNN_MIOB_D8 20.3E
SNN_MIOB_D9 20.3E
SNN_MIOB_D10 20.3E
SNN_MIOB_D11 20.4E
SNN_MIOB_D12 20.4E
SNN_MIOB_D13 20.4E
SNN_MIOB_D14 20.4E
SNN_MIOB_DE 20.4E
SNN_MIOB_HSYNC 20.4E
SNN_MIOB_VREF 20.4C
SNN_MIOB_VSYNC 20.4E
SNN_NC<1> 22.1G
SNN_NC<2> 22.1G
SNN_NC<3> 22.1G
SNN_NC<4> 22.1G
SNN_NC<5> 22.1G
SNN_NC<6> 22.1G
SNN_NC<7> 22.1G
SNN_NC<8> 22.1G
SNN_NC<9> 22.1G
SNN_NC<10> 22.1G
SNN_NC<11> 22.1G
SNN_NC<12> 22.1G
SNN_NC<13> 22.1G
SNN_NC<14> 22.1G
SNN_NC<15> 22.1G
SNN_NC<16> 22.1G
SNN_NC<17> 22.1G
SNN_NC<18> 22.1G
SNN_NC<19> 22.1G
SNN_NC<20> 22.1G
SNN_NC<21> 22.2G
SNN_NC<22> 22.2G
SNN_NC<23> 22.2G
SNN_NC<24> 22.2G
SNN_NC<25> 22.2G
SNN_NC<26> 22.2G
SNN_NC<27> 22.2G
SNN_NC<28> 22.2G
SNN_NC<29> 22.2G
SNN_NC<30> 22.2G
SNN_NC<31> 22.2G
SNN_NC<32> 22.2G
SNN_NC<33> 22.2G
SNN_NC<34> 22.2G
SNN_NC<35> 22.2G
SNN_NC<36> 22.2G
SNN_NC<37> 22.2G
SNN_NC<38> 22.2G
SNN_NC<39> 22.2G
SNN_NC<40> 22.2G
SNN_NC<41> 22.2G
SNN_NC<42> 22.2G
SNN_NC<43> 22.2G
SNN_NC<44> 22.2G
SNN_NC<45> 22.2G
SNN_NC<46> 22.2G
SNN_NC<47> 22.2G
SNN_NC<48> 22.2G
SNN_NC<49> 22.2G
SNN_NC<50> 22.2G
SNN_NC<51> 22.3G
SNN_NC<52> 22.3G
SNN_NC<53> 22.3G

SNN_NC<54> 22.3G
SNN_NC<55> 22.3G
SNN_NC<56> 22.3G
SNN_NC<57> 22.3G
SNN_NC<58> 22.3G
SNN_NC<59> 22.3G
SNN_NC<60> 22.3G
SNN_NC<61> 22.3G
SNN_NC<62> 22.3G
SNN_NC<63> 22.3G
SNN_NC<64> 22.3G
SNN_NC<65> 22.3G
SNN_NC<66> 22.3G
SNN_NC<67> 22.3G
SNN_NC<68> 22.3G
SNN_NC<69> 22.3G
SNN_NC<70> 22.3G
SNN_NC<71> 22.3G
SNN_NC<72> 22.3G
SNN_NC<73> 22.3G
SNN_NC<74> 22.3G
SNN_NC<75> 22.3G
SNN_NVVDD_GND_SENS 2.4D
E_GPU
SNN_NVVDD_NC1 29.3C
SNN_NVVDD_NC2 29.3C
SNN_NVVDD_NC3 29.3C
SNN_NVVDD_VREF 29.2B
SNN_PEX_CLKREQ* 2.2C
SNN_PEX_WAKE* 2.2B
SNN_PE_PRSNT2_A 2.1A
SNN_PE_PRSNT2_B 2.2A
SNN_PE_PRSNT2_C 2.3A
SNN_PE_RSVD1 2.1A
SNN_PE_RSVD2 2.2A
SNN_PE_RSVD3 2.2A
SNN_PE_RSVD4 2.2A
SNN_PE_RSVD5 2.3A
SNN_PE_RSVD6 2.4A
SNN_PE_RSVD7 2.4A
SNN_PGOOD_OUT* 21.2C
SNN_THERMDN 21.3A
SNN_THERMDP 21.3A
SPDIF 21.2D< 25.1G< 25.2H>
SPDIF_GND 25.2E
SPDIF_IN 25.1G< 25.2E
SPDIF_IN_C 25.1G< 25.3F
SPDIF_IN_COMP2_D 25.1G< 25.3G
SPDIF_IN_COMP2_Q 25.1G< 25.3H
SPDIF_IN_R 25.1G< 25.3F
STRAP_CALPD_MIOB 21.2A
STRAP_CALPD_MISC 21.2A
THERM_N_EN1 25.2A
THERM_N_EN1_R 25.2B
THERM_N_EN2 25.2C
THERM_N_EN3 25.2B
THERM_N_EN3_R1 25.2C
THERM_N_EN3_R2 25.3C
XTALIN 21.1G< 21.5F
XTALOUT 21.1G< 21.5G
XTALOUTBUFF 21.1G< 21.4H
XTALSSIN 21.1G< 21.4F

ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	<edit here to insert page detail>

NVIDIA CORPORATION
2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA
NV_PN 600-10562-base-000 A
ID
NAME <ENGINEER>
PAGE
DATE 30-DEC-2008

Title: Netsbypage Report
Design: p562_a00
Date: Dec 24 11:33:24 2008

Page wise report of nets and their base nets

Net Name	Location([Zone][dir])
GPU_TESTMODE	2.5D 2.5G>
GPU_TESTMODE	2.5D 2.5G>
JTAG_TCLK	2.1F> 2.1F> 21.3A<
JTAG_TCLK	2.1F> 2.1F> 21.3A<
JTAG_TDI	2.1F> 2.1F> 21.3A<
JTAG_TDI	2.1F> 2.1F> 21.3A<
JTAG_TDO	2.1F< 2.1F< 21.3A>
JTAG_TDO	2.1F< 2.1F< 21.3A>
JTAG_TMS	2.1F> 2.1F> 21.3A<
JTAG_TMS	2.1F> 2.1F> 21.3A<
JTAG_TRST*	2.1F> 2.1F> 21.3A<
JTAG_TRST*	2.1F> 2.1F> 21.3A<
NVVDD_SENSE_GPU	2.4E> 2.5G> 29.4G<
NVVDD_SENSE_GPU	2.4E> 2.5G> 29.4G<
PEX_CAL_PD_VDDQ	2.5D
PEX_CAL_PU_GND	2.5D
PEX_PLLVDD	2.4D 2.5G<
PEX_PLLVDD	2.4D 2.5G<
PEX_PLL_CLK_OUT	2.4D 2.4G>
PEX_PLL_CLK_OUT	2.4D 2.4G>
PEX_PLL_CLK_OUT*	2.4D 2.4G>
PEX_PLL_CLK_OUT*	2.4D 2.4G>
PEX_PRSNT*	2.1A 2.4A 2.4G>
PEX_PRSNT*	2.1A 2.4A 2.4G>
PEX_PRSNT*	2.1A 2.4A 2.4G>
PEX_REFCLK	2.1G> 2.2B
PEX_REFCLK	2.1G> 2.2B
PEX_REFCLK*	2.1G> 2.2B
PEX_REFCLK*	2.1G> 2.2B
PEX_RST*	2.2C> 2.4G> 25.3A<
PEX_RST*	2.2C> 2.4G> 25.3A<
PEX_RX0	2.2B 2.2G>
PEX_RX0	2.2B 2.2G>
PEX_RX0*	2.2B 2.2G>
PEX_RX0*	2.2B 2.2G>
PEX_RX1	2.2B 2.2G>
PEX_RX1	2.2B 2.2G>
PEX_RX1*	2.2B 2.2G>
PEX_RX1*	2.2B 2.2G>
PEX_RX2	2.2B 2.2G>
PEX_RX2	2.2B 2.2G>
PEX_RX2*	2.2B 2.2G>
PEX_RX2*	2.2B 2.2G>
PEX_RX3	2.2G> 2.3B
PEX_RX3	2.2G> 2.3B
PEX_RX3*	2.2G> 2.3B
PEX_RX3*	2.2G> 2.3B
PEX_RX4	2.2G> 2.3B
PEX_RX4	2.2G> 2.3B
PEX_RX4*	2.2G> 2.3B
PEX_RX4*	2.2G> 2.3B
PEX_RX5	2.2G> 2.3B
PEX_RX5	2.2G> 2.3B
PEX_RX5*	2.2G> 2.3B
PEX_RX5*	2.2G> 2.3B
PEX_RX6	2.2G> 2.3B
PEX_RX6	2.2G> 2.3B
PEX_RX6*	2.2G> 2.3B
PEX_RX6*	2.2G> 2.3B
PEX_RX7	2.2G> 2.3B
PEX_RX7	2.2G> 2.3B
PEX_RX7*	2.3B 2.3G>
PEX_RX7*	2.3B 2.3G>
PEX_RX8	2.3G> 2.4B
PEX_RX8	2.3G> 2.4B
PEX_RX8*	2.3G> 2.4B
PEX_RX8*	2.3G> 2.4B
PEX_RX9	2.3G> 2.4B
PEX_RX9	2.3G> 2.4B
PEX_RX9*	2.3G> 2.4B
PEX_RX9*	2.3G> 2.4B
PEX_RX10	2.3G> 2.4B
PEX_RX10	2.3G> 2.4B
PEX_RX10*	2.3G> 2.4B
PEX_RX10*	2.3G> 2.4B
PEX_RX11	2.3G> 2.4B
PEX_RX11	2.3G> 2.4B
PEX_RX11*	2.3G> 2.4B
PEX_RX11*	2.3G> 2.4B
PEX_RX12	2.3G> 2.4B
PEX_RX12	2.3G> 2.4B
PEX_RX12*	2.3G> 2.4B
PEX_RX12*	2.3G> 2.4B
PEX_RX13	2.3G> 2.5B
PEX_RX13	2.3G> 2.5B
PEX_RX13*	2.3G> 2.5B
PEX_RX13*	2.3G> 2.5B
PEX_RX14	2.3G> 2.5B
PEX_RX14	2.3G> 2.5B
PEX_RX14*	2.3G> 2.5B
PEX_RX14*	2.3G> 2.5B
PEX_RX15	2.3G> 2.5B
PEX_RX15	2.3G> 2.5B
PEX_RX15*	2.3G> 2.5B
PEX_RX15*	2.3G> 2.5B
PEX_SMCLK	2.1C> 21.2E<
PEX_SMDAT	2.1C> 21.2E<
PEX_TCLK	2.1B
PEX_TDI	2.1B
PEX_TDO	2.1B
PEX_TERMP	2.5D
PEX_TMS	2.1B
PEX_TRST*	2.1B
PEX_TX0	2.2C 2.3G>
PEX_TX0	2.2C 2.3G>
PEX_TX0*	2.2C 2.3G>
PEX_TX0*	2.2C 2.3G>
PEX_TX1	2.2C 2.3G>
PEX_TX1	2.2C 2.3G>
PEX_TX1*	2.2C 2.3G>
PEX_TX1*	2.2C 2.3G>
PEX_TX2	2.2C 2.3G>
PEX_TX2	2.2C 2.3G>
PEX_TX2*	2.2C 2.3G>
PEX_TX2*	2.2C 2.3G>
PEX_TX3	2.2C 2.3G>
PEX_TX3	2.2C 2.3G>
PEX_TX3*	2.2C 2.3G>
PEX_TX3*	2.2C 2.3G>
PEX_TX4	2.3C 2.3G>
PEX_TX4	2.3C 2.3G>
PEX_TX4*	2.3C 2.3G>
PEX_TX4*	2.3C 2.3G>
PEX_TX5	2.3C 2.3G>
PEX_TX5	2.3C 2.3G>
PEX_TX5*	2.3C 2.3G>
PEX_TX5*	2.3C 2.3G>
PEX_TX6	2.3C 2.4G>
PEX_TX6	2.3C 2.4G>
PEX_TX6*	2.3C 2.4G>
PEX_TX6*	2.3C 2.4G>
PEX_TX7	2.3C 2.4G>
PEX_TX7	2.3C 2.4G>
PEX_TX7*	2.3C 2.4G>
PEX_TX7*	2.3C 2.4G>
PEX_TX8	2.3C 2.4G>
PEX_TX8	2.3C 2.4G>
PEX_TX8*	2.3C 2.4G>
PEX_TX8*	2.3C 2.4G>
PEX_TX9	2.4C 2.4G>
PEX_TX9	2.4C 2.4G>
PEX_TX9*	2.4C 2.4G>
PEX_TX9*	2.4C 2.4G>
PEX_TX10	2.4C 2.4G>
PEX_TX10	2.4C 2.4G>
PEX_TX10*	2.4C 2.4G>
PEX_TX10*	2.4C 2.4G>
PEX_TX11	2.4C 2.4G>
PEX_TX11	2.4C 2.4G>
PEX_TX11*	2.4C 2.4G>
PEX_TX11*	2.4C 2.4G>
PEX_TX12	2.4C 2.4G>
PEX_TX12	2.4C 2.4G>
PEX_TX12*	2.4C 2.4G>
PEX_TX12*	2.4C 2.4G>
PEX_TX13	2.4C 2.4G>
PEX_TX13	2.4C 2.4G>
PEX_TX13*	2.4C 2.4G>
PEX_TX13*	2.4C 2.4G>
PEX_TX14	2.4G> 2.5C
PEX_TX14	2.4G> 2.5C
PEX_TX14*	2.4G> 2.5C
PEX_TX14*	2.4G> 2.5C
PEX_TX15	2.4G> 2.5C
PEX_TX15	2.4G> 2.5C
PEX_TX15*	2.4G> 2.5C
PEX_TX15*	2.4G> 2.5C
PEX_TXX0	2.1G> 2.2B
PEX_TXX0	2.1G> 2.2B
PEX_TXX0*	2.1G> 2.2B
PEX_TXX0*	2.1G> 2.2B
PEX_TXX1	2.1G> 2.2B
PEX_TXX1	2.1G> 2.2B
PEX_TXX1*	2.1G> 2.2B
PEX_TXX1*	2.1G> 2.2B
PEX_TXX2	2.1G> 2.2B
PEX_TXX2	2.1G> 2.2B
PEX_TXX2*	2.1G> 2.2B
PEX_TXX2*	2.1G> 2.2B
PEX_TXX3	2.1G> 2.2B
PEX_TXX3	2.1G> 2.2B
PEX_TXX3*	2.1G> 2.2B
PEX_TXX3*	2.1G> 2.2B
PEX_TXX4	2.1G> 2.3B
PEX_TXX4	2.1G> 2.3B
PEX_TXX4*	2.1G> 2.3B
PEX_TXX4*	2.1G> 2.3B
PEX_TXX5	2.1G> 2.3B
PEX_TXX5	2.1G> 2.3B
PEX_TXX5*	2.1G> 2.3B
PEX_TXX5*	2.1G> 2.3B
PEX_TXX6	2.1G> 2.3B
PEX_TXX6	2.1G> 2.3B
PEX_TXX6*	2.1G> 2.3B
PEX_TXX6*	2.1G> 2.3B
PEX_TXX7	2.1G> 2.3B
PEX_TXX7	2.1G> 2.3B
PEX_TXX7*	2.1G> 2.3B
PEX_TXX7*	2.1G> 2.3B
PEX_TXX8	2.1G> 2.3B
PEX_TXX8	2.1G> 2.3B
PEX_TXX8*	2.1G> 2.3B
PEX_TXX8*	2.1G> 2.3B
PEX_TXX9	2.2G> 2.4B
PEX_TXX9	2.2G> 2.4B
PEX_TXX9*	2.2G> 2.4B
PEX_TXX9*	2.2G> 2.4B
PEX_TXX10	2.2G> 2.4B
PEX_TXX10	2.2G> 2.4B
PEX_TXX10*	2.2G> 2.4B
PEX_TXX10*	2.2G> 2.4B
PEX_TXX11	2.2G> 2.4B
PEX_TXX11	2.2G> 2.4B
PEX_TXX11*	2.2G> 2.4B
PEX_TXX11*	2.2G> 2.4B
PEX_TXX12	2.2G> 2.4B
PEX_TXX12	2.2G> 2.4B
PEX_TXX12*	2.2G> 2.4B
PEX_TXX12*	2.2G> 2.4B
PEX_TXX13	2.2G> 2.4B
PEX_TXX13	2.2G> 2.4B
PEX_TXX13*	2.2G> 2.4B
PEX_TXX13*	2.2G> 2.4B
PEX_TXX14	2.2G> 2.5B
PEX_TXX14	2.2G> 2.5B
PEX_TXX14*	2.2G> 2.5B
PEX_TXX14*	2.2G> 2.5B
PEX_TXX15	2.2G> 2.5B
PEX_TXX15	2.2G> 2.5B
PEX_TXX15*	2.2G> 2.5B
PEX_TXX15*	2.2G> 2.5B
SNN_NVVDD_GND_SENSE_GP U	2.4D
SNN_PEX_CLKREQ*	2.2C
SNN_PEX_WAKE*	2.2B
SNN_PE_PRSNT2_A	2.1A
SNN_PE_PRSNT2_B	2.2A
SNN_PE_PRSNT2_C	2.3A
SNN_PE_RSVD1	2.1A
SNN_PE_RSVD2	2.2A
SNN_PE_RSVD3	2.2A
SNN_PE_RSVD4	2.2A
SNN_PE_RSVD5	2.3A
SNN_PE_RSVD6	2.4A
SNN_PE_RSVD7	2.4A
FBA_CLK0	3.4D> 5.2A< 13.1B>
FBA_CLK0*	3.4D> 5.2A< 13.1B>
FBA_CLK1	3.4D> 5.2D< 13.2B>
FBA_CLK1*	3.4D> 5.2D< 13.2B>
FBA_CMD<0>	3.2C 5.1B 5.1G
FBA_CMD<27..0>	3.3C 5.2B 5.2E
FBA_CMD<1>	3.2C 5.1B 5.1E
FBA_CMD<2>	3.2C 5.1B 5.1G
FBA_CMD<3>	3.2C 5.2B 5.2E
FBA_CMD<4>	3.2C 5.1E 5.1G
FBA_CMD<5>	3.2C 5.1E 5.1G
FBA_CMD<6>	3.3C 5.1E 5.1G
FBA_CMD<8>	3.3C 5.1B 5.1E
FBA_CMD<9>	3.3C 5.1B 5.1E
FBA_CMD<10>	3.3C 5.1B 5.1E
FBA_CMD<11>	3.3C 5.1B 5.1E
FBA_CMD<12>	3.3C 5.2B 5.2E
FBA_CMD<13>	3.3C 5.1E 5.1G
FBA_CMD<14>	3.3C 5.2B 5.2E
FBA_CMD<15>	3.3C 5.2B 5.2E
FBA_CMD<16>	3.3C 5.1B 5.1E
FBA_CMD<17>	3.3C 5.1B 5.1E
FBA_CMD<18>	3.3C 5.2B 5.2E
FBA_CMD<19>	3.3C 5.1B 5.1E
FBA_CMD<20>	3.3C 5.1B 5.1E
FBA_CMD<21>	3.3C 5.1B 5.1E
FBA_CMD<22>	3.3C 5.1B 5.1G
FBA_CMD<23>	3.3C 5.1B 5.1E
FBA_CMD<24>	3.3C 5.1B 5.1G
FBA_CMD<25>	3.3C 5.1B 5.1E
FBA_CMD<27>	3.3C 5.2B 5.2E
FBA_D<0>	3.1B 5.4C
FBA_D<63..0>	3.3B 5.5E
FBA_D<1>	3.1B 5.4C
FBA_D<2>	3.1B 5.4C
FBA_D<3>	3.1B 5.4C
FBA_D<4>	3.1B 5.4C
FBA_D<5>	3.1B 5.4C
FBA_D<6>	3.1B 5.4C
FBA_D<7>	3.1B 5.4C
FBA_D<8>	3.1B 5.4C
FBA_D<9>	3.2B 5.4C
FBA_D<10>	3.2B 5.4C
FBA_D<11>	3.2B 5.4C
FBA_D<12>	3.2B 5.4C
FBA_D<13>	3.2B 5.4C
FBA_D<14>	3.2B 5.4C
FBA_D<15>	3.2B 5.4C
FBA_D<16>	3.2B 5.4D
FBA_D<17>	3.2B 5.4D
FBA_D<18>	3.2B 5.4D
FBA_D<19>	3.2B 5.4D
FBA_D<20>	3.2B 5.4D
FBA_D<21>	3.2B 5.4D
FBA_D<22>	3.2B 5.4D
FBA_D<23>	3.2B 5.4D
FBA_D<24>	3.2B 5.4E
FBA_D<25>	3.2B 5.4E
FBA_D<26>	3.2B 5.4E
FBA_D<27>	3.2B 5.4E
FBA_D<28>	3.2B 5.4E
FBA_D<29>	3.2B 5.4E
FBA_D<30>	3.2B 5.4E
FBA_D<31>	3.2B 5.4E
FBA_D<32>	3.2B 5.5C
FBA_D<33>	3.2B 5.5C
FBA_D<34>	3.2B 5.5C
FBA_D<35>	3.2B 5.5C
FBA_D<36>	3.2B 5.5C
FBA_D<37>	3.2B 5.5C
FBA_D<38>	3.2B 5.5C
FBA_D<39>	3.3B 5.5C
FBA_D<40>	3.3B 5.5C
FBA_D<41>	3.3B 5.5C
FBA_D<42>	3.3B 5.5C
FBA_D<43>	3.3B 5.5C
FBA_D<44>	3.3B 5.5C
FBA_D<45>	3.3B 5.5C
FBA_D<46>	3.3B 5.5C
FBA_D<47>	3.3B 5.5C
FBA_D<48>	3.3B 5.5D
FBA_D<49>	3.3B 5.5D
FBA_D<50>	3.3B 5.5D
FBA_D<51>	3.3B 5.5D
FBA_D<52>	3.3B 5.5D
FBA_D<53>	3.3B 5.5D
FBA_D<54>	3.3B 5.5D
FBA_D<55>	3.3B 5.5D
FBA_D<56>	3.3B 5.5E
FBA_D<57>	3.3B 5.5E
FBA_D<58>	3.3B 5.5E
FBA_D<59>	3.3B 5.5E
FBA_D<60>	3.3B 5.5E
FBA_D<61>	3.3B 5.5E
FBA_D<62>	3.3B 5.5E
FBA_D<63>	3.3B 5.5E
FBA_DEBUG	3.4C
FBA_DQM<0>	3.3B 5.4B 5.4C
FBA_DQM<7..0>	3.4B 5.4B 5.5E
FBA_DQM<1>	3.3B 5.4B 5.4C
FBA_DQM<2>	3.3B 5.4B 5.4D
FBA_DQM<3>	3.4B 5.4B 5.4E
FBA_DQM<4>	3.4B 5.4B 5.5C
FBA_DQM<5>	3.4B 5.4B 5.5C
FBA_DQM<6>	3.4B 5.4B 5.5D
FBA_DQM<7>	3.4B 5.4B 5.5E
FBA_DQS_RN<0>	3.4B 5.4B 5.4C
FBA_DQS_RN<7..0>	3.4B 5.5B 5.5E
FBA_DQS_RN<1>	3.4B 5.4B 5.4C
FBA_DQS_RN<2>	3.4B 5.4B 5.4D
FBA_DQS_RN<3>	3.4B 5.4B 5.4E
FBA_DQS_RN<4>	3.4B 5.4B 5.5C
FBA_DQS_RN<5>	3.4B 5.5B 5.5C
FBA_DQS_RN<6>	3.4B 5.5B 5.5D
FBA_DQS_RN<7>	3.4B 5.5B 5.5E
FBA_DQS_WP<0>	3.4B 5.4C 5.5B
FBA_DQS_WP<7..0>	3.4B 5.5B 5.5E
FBA_DQS_WP<1>	3.4B 5.4C 5.5B
FBA_DQS_WP<2>	3.4B 5.4D 5.5B
FBA_DQS_WP<3>	3.4B 5.4E 5.5B
FBA_DQS_WP<4>	3.4B 5.5B 5.5C
FBA_DQS_WP<5>	3.4B 5.5B 5.5C
FBA_DQS_WP<6>	3.4B 5.5B 5.5D
FBA_DQS_WP<7>	3.4B 5.5B 5.5E
FBB_CLK0	3.4H> 7.2A< 13.2B>
FBB_CLK0*	3.4H> 7.2A< 13.2B>
FBB_CLK1	3.4H> 7.2D< 13.2B>
FBB_CLK1*	3.4H> 7.2D< 13.2B>
FBB_CMD<0>	3.2G 7.1B 7.2G
FBB_CMD<27..0>	3.3G 7.2B 7.2E
FBB_CMD<1>	3.2G 7.1B 7.1E
FBB_CMD<2>	3.2G 7.1B 7.2G
FBB_CMD<3>	3.2G 7.2B 7.2E
FBB_CMD<4>	3.2G 7.1E 7.1G
FBB_CMD<5>	3.2G 7.1E 7.1G
FBB_CMD<6>	3.3G 7.1E 7.1G
FBB_CMD<8>	3.3G 7.1B 7.1E
FBB_CMD<9>	3.3G 7.1B 7.1E
FBB_CMD<10>	3.3G 7.1B 7.1E
FBB_CMD<11>	3.3G 7.1B 7.1E
FBB_CMD<12>	3.3G 7.2B 7.2E
FBB_CMD<13>	3.3G 7.1E 7.1G
FBB_CMD<14>	3.3G 7.2B 7.2E
FBB_CMD<15>	3.3G 7.2B 7.2E
FBB_CMD<16>	3.3G 7.1B 7.1E
FBB_CMD<17>	3.3G 7.1B 7.1E
FBB_CMD<18>	3.3G 7.2B 7.2E
FBB_CMD<19>	3.3G 7.1B 7.1E
FBB_CMD<20>	3.3G 7.1B 7.1E
FBB_CMD<21>	3.3G 7.1B 7.1E
FBB_CMD<22>	3.3G 7.1B 7.2G
FBB_CMD<23>	3.3G 7.1B 7.1E
FBB_CMD<24>	3.3G 7.1B 7.2G
FBB_CMD<25>	3.3G 7.1B 7.1E
FBB_CMD<27>	3.3G 7.2B 7.2E
FBB_D<0>	3.1F 7.4C
FBB_D<63..0>	3.3F 7.5E
FBB_D<1>	3.1F 7.4C
FBB_D<2>	3.1F 7.4C
FBB_D<3>	3.1F 7.4C
FBB_D<4>	3.1F 7.4C
FBB_D<5>	3.1F 7.4C
FBB_D<6>	3.1F 7.4C
FBB_D<7>	3.1F 7.4C
FBB_D<8>	3.1F 7.4C
FBB_D<9>	3.2F 7.4C
FBB_D<10>	3.2F 7.4C
FBB_D<11>	3.2F 7.4C
FBB_D<12>	3.2F 7.4C
FBB_D<13>	3.2F 7.4C
FBB_D<14>	3.2F 7.4C
FBB_D<15>	3.2F 7.4C
FBB_D<16>	3.2F 7.4D
FBB_D<17>	3.2F 7.4D
FBB_D<18>	3.2F 7.4D
FBB_D<19>	3.2F 7.4D
FBB_D<20>	3.2F 7.4D
FBB_D<21>	3.2F 7.4D
FBB_D<22>	3.2F 7.4D
FBB_D<23>	3.2F 7.4D
FBB_D<24>	3.2F 7.4E
FBB_D<25>	3.2F 7.4E
FBB_D<26>	3.2F 7.4E
FBB_D<27>	3.2F 7.4E
FBB_D<28>	3.2F 7.4E
FBB_D<29>	3.2F 7.4E
FBB_D<30>	3.2F 7.4E
FBB_D<31>	3.2F 7.4E
FBB_D<32>	3.2F 7.5C
FBB_D<33>	3.2F 7.5C
FBB_D<34>	3.2F 7.5C
FBB_D<35>	3.2F 7.5C
FBB_D<36>	3.2F 7.5C
FBB_D<37>	3.2F 7.5C
FBB_D<38>	3.2F 7.5C
FBB_D<39>	3.3F 7.5C
FBB_D<40>	3.3F 7.5C
FBB_D<41>	3.3F 7.5C
FBB_D<42>	3.3F 7.5C
FBB_D<43>	3.3F 7.5C
FBB_D<44>	3.3F 7.5C
FBB_D<45>	3.3F 7.5C
FBB_D<46>	3.3F 7.5C
FBB_D<47>	3.3F 7.5C
FBB_D<48>	3.3F 7.5D
FBB_D<49>	3.3F 7.5D
FBB_D<50>	3.3F 7.5D
FBB_D<51>	3.3F 7.5D
FBB_D<52>	3.3F 7.5D
FBB_D<53>	3.3F 7.5D
FBB_D<54>	3.3F 7.5D
FBB_D<55>	3.3F 7.5D
FBB_D<56>	3.3F 7.5E
FBB_D<57>	3.3F 7.5E
FBB_D<58>	3.3F 7.5E
FBB_D<59>	3.3F 7.5E
FBB_D<60>	3.3F 7.5E
FBB_D<61>	3.3F 7.5E
FBB_D<62>	3.3F 7.5E
FBB_D<63>	3.3F 7.5E
FBB_DEBUG	3.4G
FBB_DQM<0>	3.3F 7.4B 7.4C
FBB_DQM<7..0>	3.4F 7.4B 7.5E
FBB_DQM<1>	3.3F 7.4B 7.4C
FBB_DQM<2>	3.3F 7.4B 7.4D
FBB_DQM<3>	3.4F 7.4B 7.4E
FBB_DQM<4>	3.4F 7.4B 7.5C
FBB_DQM<5>	3.4F 7.4B 7.5C
FBB_DQM<6>	3.4F 7.4B 7.5D
FBB_DQM<7>	3.4F 7.4B 7.5E
FBB_DQS_RN<0>	3.4F 7.4B 7.4C
FBB_DQS_RN<7..0>	3.4F 7.5B 7.5E
FBB_DQS_RN<1>	3.4F 7.4B 7.4C
FBB_DQS_RN<2>	3.4F 7.4B 7.4D
FBB_DQS_RN<3>	3.4F 7.4B 7.4E
FBB_DQS_RN<4>	3.4F 7.5B 7.5C
FBB_DQS_RN<5>	3.4F 7.5B 7.5C
FBB_DQS_RN<6>	3.4F 7.5B 7.5D
FBB_DQS_RN<7>	3.4F 7.5B 7.5E
FBB_DQS_WP<0>	3.4F 7.4C 7.5B
FBB_DQS_WP<7..0>	3.4F 7.5B 7.5E
FBB_DQS_WP<1>	3.4F 7.4C 7.5B
FBB_DQS_WP<2>	3.4F 7.4D 7.5B
FBB_DQS_WP<3>	3.4F 7.4E 7.5B
FBB_DQS_WP<4>	3.4F 7.5B 7.5C
FBB_DQS_WP<5>	3.4F 7.5B 7.5C
FBB_DQS_WP<6>	3.4F 7.5B 7.5D
FBB_DQS_WP<7>	3.4F 7.5B 7.5E
FB_CAL_PD_VDDQ	3.4G
FB_CAL_PU_GND	3.5G
FB_CAL_TERM_GND	3.5G
FB_PLLAVDD0	3.5D> 13.3B<>
FB_VREF	3.5A> 13.3B<>
SNN_FBA_CMD<7>	3.3C
SNN_FBA_CMD<26>	3.3C
SNN_FBA_CMD<28>	3.3C
SNN_FBA_CMD<29>	3.3C
SNN_FBA_CMD<30>	3.3C
SNN_FBA_DBI<0>	3.4B
SNN_FBA_DBI<1>	3.4B
SNN_FBA_DBI<2>	3.5B
SNN_FBA_DBI<3>	3.5B
SNN_FBA_DBI<4>	3.5B
SNN_FBA_DBI<5>	3.5B
SNN_FBA_DBI<6>	3.5B
SNN_FBA_DBI<7>	3.5B
SNN_FBA_WDS0	3.4C
SNN_FBA_WDS0*	3.4C
SNN_FBA_WDS1	3.4C
SNN_FBA_WDS1*	3.4C
SNN_FBA_WDS2	3.4C
SNN_FBA_WDS2*	3.4C
SNN_FBA_WDS3	3.4C
SNN_FBA_WDS3*	3.4C
SNN_FBB_CMD<7>	3.3G
SNN_FBB_CMD<26>	3.3G
SNN_FBB_CMD<28>	3.3G
SNN_FBB_CMD<29>	3.3G
SNN_FBB_CMD<30>	3.3G
SNN_FBB_DBI<0>	3.4F
SNN_FBB_DBI<1>	3.4F
SNN_FBB_DBI<2>	3.5F
SNN_FBB_DBI<3>	3.5F
SNN_FBB_DBI<4>	3.5F
SNN_FBB_DBI<5>	3.5F
SNN_FBB_DBI<6>	3.5F
SNN_FBB_DBI<7>	3.5F
SNN_FBB_WDS0	3.4G
SNN_FBB_WDS0*	3.4G
SNN_FBB_WDS1	3.4G
SNN_FBB_WDS1*	3.4G
SNN_FBB_WDS2	3.4G
SNN_FBB_WDS2*	3.4G
SNN_FBB_WDS3	3.4G
SNN_FBB_WDS3*	3.4G
FBC_CLK0	4.4D> 9.2A< 13.1E>
FBC_CLK0*	4.4D> 9.2A< 13.1E>
FBC_CLK1	4.4D> 9.2D< 13.2E>
FBC_CLK1*	4.4D> 9.2D< 13.2E>
FBC_CMD<0>	4.2C 9.1B 9.2G
FBC_CMD<27..0>	4.3C 9.2B 9.2E
FBC_CMD<1>	4.2C 9.1B 9.1E
FBC_CMD<2>	4.2C 9.1B 9.2G
FBC_CMD<3>	4.2C 9.2B 9.2E
FBC_CMD<4>	4.2C 9.1E 9.1G
FBC_CMD<5>	4.2C 9.1E 9.1G
FBC_CMD<6>	4.3C 9.1E 9.1G
FBC_CMD<8>	4.3C 9.1B 9.1E
FBC_CMD<9>	4.3C 9.1B 9.1E
FBC_CMD<10>	4.3C 9.1B 9.1E
FBC_CMD<11>	4.3C 9.1B 9.1E
FBC_CMD<12>	4.3C 9.2B 9.2E
FBC_CMD<13>	4.3C 9.1E 9.1G
FBC_CMD<14>	4.3C 9.2B 9.2E
FBC_CMD<15>	4.3C 9.2B 9.2E
FBC_CMD<16>	4.3C 9.1B 9.1E
FBC_CMD<17>	4.3C 9.1B 9.1E
FBC_CMD<18>	4.3C 9.2B 9.2E
FBC_CMD<19>	4.3C 9.1B 9.1E
FBC_CMD<20>	4.3C 9.1B 9.1E
FBC_CMD<21>	4.3C 9.1B 9.1E
FBC_CMD<22>	4.3C 9.1B 9.2G
FBC_CMD<23>	4.3C 9.1B 9.1E
FBC_CMD<24>	4.3C 9.1B 9.2G

ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	<edit here to insert page detail>

NVIDIA CORPORATION
2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA

NV_PN	600-10562-base-000 A		
ID		PAGE	
NAME	<ENGINEER>	DATE	30-DEC-2008

FBC_CMD<25>	4.3C 9.1B 9.1E
FBC_CMD<27>	4.3C 9.2B 9.2E
FBC_D<0>	4.1B 9.4C
FBC_D<63..0>	4.3B 9.5E
FBC_D<1>	4.1B 9.4C
FBC_D<2>	4.1B 9.4C
FBC_D<3>	4.1B 9.4C
FBC_D<4>	4.1B 9.4C
FBC_D<5>	4.1B 9.4C
FBC_D<6>	4.1B 9.4C
FBC_D<7>	4.1B 9.4C
FBC_D<8>	4.1B 9.4C
FBC_D<9>	4.2B 9.4C
FBC_D<10>	4.2B 9.4C
FBC_D<11>	4.2B 9.4C
FBC_D<12>	4.2B 9.4C
FBC_D<13>	4.2B 9.4C
FBC_D<14>	4.2B 9.4C
FBC_D<15>	4.2B 9.4C
FBC_D<16>	4.2B 9.4D
FBC_D<17>	4.2B 9.4D
FBC_D<18>	4.2B 9.4D
FBC_D<19>	4.2B 9.4D
FBC_D<20>	4.2B 9.4D
FBC_D<21>	4.2B 9.4D
FBC_D<22>	4.2B 9.4D
FBC_D<23>	4.2B 9.4D
FBC_D<24>	4.2B 9.4E
FBC_D<25>	4.2B 9.4E
FBC_D<26>	4.2B 9.4E
FBC_D<27>	4.2B 9.4E
FBC_D<28>	4.2B 9.4E
FBC_D<29>	4.2B 9.4E
FBC_D<30>	4.2B 9.4E
FBC_D<31>	4.2B 9.4E
FBC_D<32>	4.2B 9.5C
FBC_D<33>	4.2B 9.5C
FBC_D<34>	4.2B 9.5C
FBC_D<35>	4.2B 9.5C
FBC_D<36>	4.2B 9.5C
FBC_D<37>	4.2B 9.5C
FBC_D<38>	4.2B 9.5C
FBC_D<39>	4.3B 9.5C
FBC_D<40>	4.3B 9.5C
FBC_D<41>	4.3B 9.5C
FBC_D<42>	4.3B 9.5C
FBC_D<43>	4.3B 9.5C
FBC_D<44>	4.3B 9.5C
FBC_D<45>	4.3B 9.5C
FBC_D<46>	4.3B 9.5C
FBC_D<47>	4.3B 9.5C
FBC_D<48>	4.3B 9.5D
FBC_D<49>	4.3B 9.5D
FBC_D<50>	4.3B 9.5D
FBC_D<51>	4.3B 9.5D
FBC_D<52>	4.3B 9.5D
FBC_D<53>	4.3B 9.5D
FBC_D<54>	4.3B 9.5D
FBC_D<55>	4.3B 9.5D
FBC_D<56>	4.3B 9.5E
FBC_D<57>	4.3B 9.5E
FBC_D<58>	4.3B 9.5E
FBC_D<59>	4.3B 9.5E
FBC_D<60>	4.3B 9.5E
FBC_D<61>	4.3B 9.5E
FBC_D<62>	4.3B 9.5E
FBC_D<63>	4.3B 9.5E
FBC_DEBUG	4.4C
FBC_DQM<0>	4.3B 9.4B 9.4C
FBC_DQM<7..0>	4.4B 9.4B 9.5E
FBC_DQM<1>	4.3B 9.4B 9.4C
FBC_DQM<2>	4.3B 9.4B 9.4D
FBC_DQM<3>	4.4B 9.4B 9.4E
FBC_DQM<4>	4.4B 9.4B 9.5C
FBC_DQM<5>	4.4B 9.4B 9.5C
FBC_DQM<6>	4.4B 9.4B 9.5D
FBC_DQM<7>	4.4B 9.4B 9.5E
FBC_DQS_RN<0>	4.4B 9.4B 9.4C
FBC_DQS_RN<7..0>	4.4B 9.5B 9.5E
FBC_DQS_RN<1>	4.4B 9.4B 9.4C
FBC_DQS_RN<2>	4.4B 9.4B 9.4D
FBC_DQS_RN<3>	4.4B 9.4B 9.4E
FBC_DQS_RN<4>	4.4B 9.5B 9.5C
FBC_DQS_RN<5>	4.4B 9.5B 9.5C
FBC_DQS_RN<6>	4.4B 9.5B 9.5D
FBC_DQS_RN<7>	4.4B 9.5B 9.5E
FBC_DQS_WP<0>	4.4B 9.4C 9.5B
FBC_DQS_WP<7..0>	4.4B 9.5B 9.5E
FBC_DQS_WP<1>	4.4B 9.4C 9.5B
FBC_DQS_WP<2>	4.4B 9.4D 9.5B
FBC_DQS_WP<3>	4.4B 9.4E 9.5B
FBC_DQS_WP<4>	4.4B 9.5B 9.5C
FBC_DQS_WP<5>	4.4B 9.5B 9.5C
FBC_DQS_WP<6>	4.4B 9.5B 9.5D
FBC_DQS_WP<7>	4.4B 9.5B 9.5E
FBD_CLK0	4.4H> 11.2A< 13.2E>
FBD_CLK0*	4.4H> 11.2A< 13.2E>
FBD_CLK1	4.4H> 11.2D< 13.2E>
FBD_CLK1*	4.4H> 11.2D< 13.2E>
FBD_CMD<0>	4.2G 11.1B 11.2G
FBD_CMD<27..0>	4.3G 11.2C 11.2F
FBD_CMD<1>	4.2G 11.1C 11.1F
FBD_CMD<2>	4.2G 11.1B 11.2G
FBD_CMD<3>	4.2G 11.2C 11.2F
FBD_CMD<4>	4.2G 11.1E 11.1G
FBD_CMD<5>	4.2G 11.1E 11.1G
FBD_CMD<6>	4.3G 11.1E 11.1G
FBD_CMD<8>	4.3G 11.1C 11.1F
FBD_CMD<9>	4.3G 11.1C 11.1F
FBD_CMD<10>	4.3G 11.1C 11.1F
FBD_CMD<11>	4.3G 11.1C 11.1F
FBD_CMD<12>	4.3G 11.2C 11.2F
FBD_CMD<13>	4.3G 11.1E 11.2G
FBD_CMD<14>	4.3G 11.2C 11.2F
FBD_CMD<15>	4.3G 11.2C 11.2F
FBD_CMD<16>	4.3G 11.1C 11.1F
FBD_CMD<17>	4.3G 11.1C 11.1F
FBD_CMD<18>	4.3G 11.2C 11.2F
FBD_CMD<19>	4.3G 11.1C 11.1F
FBD_CMD<20>	4.3G 11.1C 11.1F
FBD_CMD<21>	4.3G 11.1C 11.1F
FBD_CMD<22>	4.3G 11.1B 11.2G
FBD_CMD<23>	4.3G 11.1C 11.1F
FBD_CMD<24>	4.3G 11.1B 11.2G
FBD_CMD<25>	4.3G 11.1C 11.1F
FBD_CMD<27>	4.3G 11.2C 11.2F
FBD_D<0>	4.1F 11.4C
FBD_D<63..0>	4.3F 11.5E
FBD_D<1>	4.1F 11.4C
FBD_D<2>	4.1F 11.4C
FBD_D<3>	4.1F 11.4C
FBD_D<4>	4.1F 11.4C
FBD_D<5>	4.1F 11.4C
FBD_D<6>	4.1F 11.4C
FBD_D<7>	4.1F 11.4C
FBD_D<8>	4.1F 11.4D
FBD_D<9>	4.2F 11.4D
FBD_D<10>	4.2F 11.4D
FBD_D<11>	4.2F 11.4D
FBD_D<12>	4.2F 11.4D
FBD_D<13>	4.2F 11.4D
FBD_D<14>	4.2F 11.4D
FBD_D<15>	4.2F 11.4D
FBD_D<16>	4.2F 11.4D
FBD_D<17>	4.2F 11.4D
FBD_D<18>	4.2F 11.4D
FBD_D<19>	4.2F 11.4D
FBD_D<20>	4.2F 11.4D
FBD_D<21>	4.2F 11.4D
FBD_D<22>	4.2F 11.4D
FBD_D<23>	4.2F 11.4D
FBD_D<24>	4.2F 11.4E
FBD_D<25>	4.2F 11.4E
FBD_D<26>	4.2F 11.4E
FBD_D<27>	4.2F 11.4E
FBD_D<28>	4.2F 11.4E
FBD_D<29>	4.2F 11.4E
FBD_D<30>	4.2F 11.4E
FBD_D<31>	4.2F 11.4E
FBD_D<32>	4.2F 11.5C
FBD_D<33>	4.2F 11.5C
FBD_D<34>	4.2F 11.5C
FBD_D<35>	4.2F 11.5C
FBD_D<36>	4.2F 11.5C
FBD_D<37>	4.2F 11.5C
FBD_D<38>	4.2F 11.5C
FBD_D<39>	4.3F 11.5C
FBD_D<40>	4.3F 11.5D
FBD_D<41>	4.3F 11.5D
FBD_D<42>	4.3F 11.5D
FBD_D<43>	4.3F 11.5D
FBD_D<44>	4.3F 11.5D
FBD_D<45>	4.3F 11.5D
FBD_D<46>	4.3F 11.5D
FBD_D<47>	4.3F 11.5D
FBD_D<48>	4.3F 11.5D
FBD_D<49>	4.3F 11.5D
FBD_D<50>	4.3F 11.5D
FBD_D<51>	4.3F 11.5D
FBD_D<52>	4.3F 11.5D
FBD_D<53>	4.3F 11.5D
FBD_D<54>	4.3F 11.5D
FBD_D<55>	4.3F 11.5D
FBD_D<56>	4.3F 11.5E
FBD_D<57>	4.3F 11.5E
FBD_D<58>	4.3F 11.5E
FBD_D<59>	4.3F 11.5E
FBD_D<60>	4.3F 11.5E
FBD_D<61>	4.3F 11.5E
FBD_D<62>	4.3F 11.5E
FBD_D<63>	4.3F 11.5E
FBD_DEBUG	4.4G
FBD_DQM<0>	4.3F 11.4B 11.4C
FBD_DQM<7..0>	4.4F 11.4B 11.5E
FBD_DQM<1>	4.3F 11.4B 11.4D
FBD_DQM<2>	4.3F 11.4B 11.4D
FBD_DQM<3>	4.4F 11.4B 11.4E
FBD_DQM<4>	4.4F 11.4B 11.5C
FBD_DQM<5>	4.4F 11.4B 11.5D
FBD_DQM<6>	4.4F 11.4B 11.5D
FBD_DQM<7>	4.4F 11.4B 11.5E
FBD_DQS_RN<0>	4.4F 11.4B 11.4C
FBD_DQS_RN<7..0>	4.4F 11.5B 11.5E
FBD_DQS_RN<1>	4.4F 11.4B 11.4D
FBD_DQS_RN<2>	4.4F 11.4B 11.4D
FBD_DQS_RN<3>	4.4F 11.4B 11.4E
FBD_DQS_RN<4>	4.4F 11.5B 11.5C
FBD_DQS_RN<5>	4.4F 11.5B 11.5D
FBD_DQS_RN<6>	4.4F 11.5B 11.5D
FBD_DQS_RN<7>	4.4F 11.5B 11.5E
FBD_DQS_WP<0>	4.4F 11.4C 11.5B
FBD_DQS_WP<7..0>	4.4F 11.5B 11.5E
FBD_DQS_WP<1>	4.4F 11.4D 11.5B
FBD_DQS_WP<2>	4.4F 11.4D 11.5B
FBD_DQS_WP<3>	4.4F 11.4E 11.5B
FBD_DQS_WP<4>	4.4F 11.5B 11.5C
FBD_DQS_WP<5>	4.4F 11.5B 11.5D
FBD_DQS_WP<6>	4.4F 11.5B 11.5D
FBD_DQS_WP<7>	4.4F 11.5B 11.5E
FBVDDQ_SENSE	4.5H> 28.4H<
FB_PLLAVDD1	4.5D> 13.3E<>
SNN_FBC_CMD<7>	4.3C
SNN_FBC_CMD<26>	4.3C
SNN_FBC_CMD<28>	4.3C
SNN_FBC_CMD<29>	4.3C
SNN_FBC_CMD<30>	4.3C
SNN_FBC_DBI<0>	4.4B
SNN_FBC_DBI<1>	4.4B
SNN_FBC_DBI<2>	4.5B
SNN_FBC_DBI<3>	4.5B
SNN_FBC_DBI<4>	4.5B
SNN_FBC_DBI<5>	4.5B
SNN_FBC_DBI<6>	4.5B
SNN_FBC_DBI<7>	4.5B
SNN_FBC_WDS0	4.4C
SNN_FBC_WDS0*	4.4C
SNN_FBC_WDS1	4.4C
SNN_FBC_WDS1*	4.4C
SNN_FBC_WDS2	4.4C
SNN_FBC_WDS2*	4.4C
SNN_FBC_WDS3	4.4C
SNN_FBC_WDS3*	4.4C
SNN_FBD_CMD<7>	4.3G
SNN_FBD_CMD<26>	4.3G
SNN_FBD_CMD<28>	4.3G
SNN_FBD_CMD<29>	4.3G
SNN_FBD_CMD<30>	4.3G
SNN_FBD_DBI<0>	4.4F
SNN_FBD_DBI<1>	4.4F
SNN_FBD_DBI<2>	4.5F
SNN_FBD_DBI<3>	4.5F
SNN_FBD_DBI<4>	4.5F
SNN_FBD_DBI<5>	4.5F
SNN_FBD_DBI<6>	4.5F
SNN_FBD_DBI<7>	4.5F
SNN_FBD_WDS0	4.4G
SNN_FBD_WDS0*	4.4G
SNN_FBD_WDS1	4.4G
SNN_FBD_WDS1*	4.4G
SNN_FBD_WDS2	4.4G
SNN_FBD_WDS2*	4.4G
SNN_FBD_WDS3	4.4G
SNN_FBD_WDS3*	4.4G
FBA_CLK0	3.4D> 5.2A< 13.1B>
FBA_CLK0*	3.4D> 5.2A< 13.1B>
FBA_CLK0_TERM	5.1A
FBA_CLK1	3.4D> 5.2D< 13.2B>
FBA_CLK1*	3.4D> 5.2D< 13.2B>
FBA_CLK1_TERM	5.1D
FBA_CMD<0>	3.2C 5.1B 5.1G
FBA_CMD<0>	3.2C 5.1B 5.1G
FBA_CMD<27..0>	3.3C 5.2B 5.2E
FBA_CMD<1>	3.2C 5.1B 5.1E
FBA_CMD<1>	3.2C 5.1B 5.1E
FBA_CMD<2>	3.2C 5.1B 5.1G
FBA_CMD<2>	3.2C 5.1B 5.1G
FBA_CMD<3>	3.2C 5.2B 5.2E
FBA_CMD<3>	3.2C 5.2B 5.2E
FBA_CMD<4>	3.2C 5.1E 5.1G
FBA_CMD<4>	3.2C 5.1E 5.1G
FBA_CMD<5>	3.2C 5.1E 5.1G
FBA_CMD<5>	3.2C 5.1E 5.1G
FBA_CMD<6>	3.3C 5.1E 5.1G
FBA_CMD<6>	3.3C 5.1E 5.1G
FBA_CMD<8>	3.3C 5.1B 5.1E
FBA_CMD<8>	3.3C 5.1B 5.1E
FBA_CMD<9>	3.3C 5.1B 5.1E
FBA_CMD<9>	3.3C 5.1B 5.1E
FBA_CMD<10>	3.3C 5.1B 5.1E
FBA_CMD<10>	3.3C 5.1B 5.1E
FBA_CMD<11>	3.3C 5.1B 5.1E
FBA_CMD<11>	3.3C 5.1B 5.1E
FBA_CMD<12>	3.3C 5.2B 5.2E
FBA_CMD<12>	3.3C 5.2B 5.2E
FBA_CMD<13>	3.3C 5.1E 5.1G
FBA_CMD<13>	3.3C 5.1E 5.1G
FBA_CMD<14>	3.3C 5.2B 5.2E
FBA_CMD<14>	3.3C 5.2B 5.2E
FBA_CMD<15>	3.3C 5.2B 5.2E
FBA_CMD<15>	3.3C 5.2B 5.2E
FBA_CMD<16>	3.3C 5.1B 5.1E
FBA_CMD<16>	3.3C 5.1B 5.1E
FBA_CMD<17>	3.3C 5.1B 5.1E
FBA_CMD<17>	3.3C 5.1B 5.1E
FBA_CMD<18>	3.3C 5.2B 5.2E
FBA_CMD<18>	3.3C 5.2B 5.2E
FBA_CMD<19>	3.3C 5.1B 5.1E
FBA_CMD<19>	3.3C 5.1B 5.1E
FBA_CMD<20>	3.3C 5.1B 5.1E
FBA_CMD<20>	3.3C 5.1B 5.1E
FBA_CMD<21>	3.3C 5.1B 5.1E
FBA_CMD<21>	3.3C 5.1B 5.1E
FBA_CMD<22>	3.3C 5.1B 5.1G
FBA_CMD<22>	3.3C 5.1B 5.1G
FBA_CMD<23>	3.3C 5.1B 5.1E
FBA_CMD<23>	3.3C 5.1B 5.1E
FBA_CMD<24>	3.3C 5.1B 5.1G
FBA_CMD<24>	3.3C 5.1B 5.1G
FBA_CMD<25>	3.3C 5.1B 5.1E
FBA_CMD<25>	3.3C 5.1B 5.1E
FBA_CMD<27>	3.3C 5.2B 5.2E
FBA_CMD<27>	3.3C 5.2B 5.2E
FBA_CMD_SENA0	5.2B
FBA_CMD_SENA1	5.2E
FBA_D<0>	3.1B 5.4C
FBA_D<63..0>	3.3B 5.5E
FBA_D<1>	3.1B 5.4C
FBA_D<2>	3.1B 5.4C
FBA_D<3>	3.1B 5.4C
FBA_D<4>	3.1B 5.4C
FBA_D<5>	3.1B 5.4C
FBA_D<6>	3.1B 5.4C
FBA_D<7>	3.1B 5.4C
FBA_D<8>	3.1B 5.4C
FBA_D<9>	3.2B 5.4C
FBA_D<10>	3.2B 5.4C
FBA_D<11>	3.2B 5.4C
FBA_D<12>	3.2B 5.4C
FBA_D<13>	3.2B 5.4C
FBA_D<14>	3.2B 5.4C
FBA_D<15>	3.2B 5.4C
FBA_D<16>	3.2B 5.4D
FBA_D<17>	3.2B 5.4D
FBA_D<18>	3.2B 5.4D
FBA_D<19>	3.2B 5.4D
FBA_D<20>	3.2B 5.4D
FBA_D<21>	3.2B 5.4D
FBA_D<22>	3.2B 5.4D
FBA_D<23>	3.2B 5.4D
FBA_D<24>	3.2B 5.4E
FBA_D<25>	3.2B 5.4E
FBA_D<26>	3.2B 5.4E
FBA_D<27>	3.2B 5.4E
FBA_D<28>	3.2B 5.4E
FBA_D<29>	3.2B 5.4E
FBA_D<30>	3.2B 5.4E
FBA_D<31>	3.2B 5.4E
FBA_D<32>	3.2B 5.5C
FBA_D<33>	3.2B 5.5C
FBA_D<34>	3.2B 5.5C
FBA_D<35>	3.2B 5.5C
FBA_D<36>	3.2B 5.5C
FBA_D<37>	3.2B 5.5C
FBA_D<38>	3.2B 5.5C
FBA_D<39>	3.3B 5.5C
FBA_D<40>	3.3B 5.5C
FBA_D<41>	3.3B 5.5C
FBA_D<42>	3.3B 5.5C
FBA_D<43>	3.3B 5.5C
FBA_D<44>	3.3B 5.5C
FBA_D<45>	3.3B 5.5C
FBA_D<46>	3.3B 5.5C
FBA_D<47>	3.3B 5.5C
FBA_D<48>	3.3B 5.5D
FBA_D<49>	3.3B 5.5D
FBA_D<50>	3.3B 5.5D
FBA_D<51>	3.3B 5.5D
FBA_D<52>	3.3B 5.5D
FBA_D<53>	3.3B 5.5D
FBA_D<54>	3.3B 5.5D
FBA_D<55>	3.3B 5.5D
FBA_D<56>	3.3B 5.5E
FBA_D<57>	3.3B 5.5E
FBA_D<58>	3.3B 5.5E
FBA_D<59>	3.3B 5.5E
FBA_D<60>	3.3B 5.5E
FBA_D<61>	3.3B 5.5E
FBA_D<62>	3.3B 5.5E
FBA_D<63>	3.3B 5.5E
FBA_DQM<0>	3.3B 5.4B 5.4C
FBA_DQM<0>	3.3B 5.4B 5.4C
FBA_DQM<7..0>	3.4B 5.4B 5.5E
FBA_DQM<1>	3.3B 5.4B 5.4C
FBA_DQM<1>	3.3B 5.4B 5.4C
FBA_DQM<2>	3.3B 5.4B 5.4D
FBA_DQM<2>	3.3B 5.4B 5.4D
FBA_DQM<3>	3.4B 5.4B 5.4E
FBA_DQM<3>	3.4B 5.4B 5.4E
FBA_DQM<4>	3.4B 5.4B 5.5C
FBA_DQM<4>	3.4B 5.4B 5.5C
FBA_DQM<5>	3.4B 5.4B 5.5C
FBA_DQM<5>	3.4B 5.4B 5.5C
FBA_DQM<6>	3.4B 5.4B 5.5D
FBA_DQM<6>	3.4B 5.4B 5.5D
FBA_DQM<7>	3.4B 5.4B 5.5E
FBA_DQM<7>	3.4B 5.4B 5.5E
FBA_DQS_RN<0>	3.4B 5.4B 5.4C
FBA_DQS_RN<0>	3.4B 5.4B 5.4C
FBA_DQS_RN<7..0>	3.4B 5.5B 5.5E
FBA_DQS_RN<1>	3.4B 5.4B 5.4C
FBA_DQS_RN<1>	3.4B 5.4B 5.4C
FBA_DQS_RN<2>	3.4B 5.4B 5.4D
FBA_DQS_RN<2>	3.4B 5.4B 5.4D
FBA_DQS_RN<3>	3.4B 5.4B 5.4E
FBA_DQS_RN<3>	3.4B 5.4B 5.4E
FBA_DQS_RN<4>	3.4B 5.4B 5.5C
FBA_DQS_RN<4>	3.4B 5.4B 5.5C
FBA_DQS_RN<5>	3.4B 5.5B 5.5C
FBA_DQS_RN<5>	3.4B 5.5B 5.5C
FBA_DQS_RN<6>	3.4B 5.5B 5.5D
FBA_DQS_RN<6>	3.4B 5.5B 5.5D
FBA_DQS_RN<7>	3.4B 5.5B 5.5E
FBA_DQS_RN<7>	3.4B 5.5B 5.5E
FBA_DQS_WP<0>	3.4B 5.4C 5.5B
FBA_DQS_WP<0>	3.4B 5.4C 5.5B
FBA_DQS_WP<7..0>	3.4B 5.5B 5.5E
FBA_DQS_WP<1>	3.4B 5.4C 5.5B
FBA_DQS_WP<1>	3.4B 5.4C 5.5B
FBA_DQS_WP<2>	3.4B 5.4D 5.5B
FBA_DQS_WP<2>	3.4B 5.4D 5.5B
FBA_DQS_WP<3>	3.4B 5.4E 5.5B
FBA_DQS_WP<3>	3.4B 5.4E 5.5B
FBA_DQS_WP<4>	3.4B 5.5B 5.5C
FBA_DQS_WP<4>	3.4B 5.5B 5.5C
FBA_DQS_WP<5>	3.4B 5.5B 5.5C
FBA_DQS_WP<5>	3.4B 5.5B 5.5C
FBA_DQS_WP<6>	3.4B 5.5B 5.5D
FBA_DQS_WP<6>	3.4B 5.5B 5.5D
FBA_DQS_WP<7>	3.4B 5.5B 5.5E
FBA_DQS_WP<7>	3.4B 5.5B 5.5E
FBA_VREF0	5.3D> 13.3B<>
FBA_VREF1	5.3G> 13.4B<>
FBA_VREF2	5.3E> 13.4B<>
FBA_VREF3	5.3H> 13.4B<>
FBA_ZQ0	5.2B< 13.4B<>
FBA_ZQ1	5.2E< 13.4B<>
SNN_FBA0_NC1	5.2B
SNN_FBA1_NC1	5.2E
FBB_CLK0	3.4H> 7.2A< 13.2B>
FBB_CLK0*	3.4H> 7.2A< 13.2B>
FBB_CLK0_TERM	7.1A
FBB_CLK1	3.4H> 7.2D< 13.2B>
FBB_CLK1*	3.4H> 7.2D< 13.2B>
FBB_CLK1_TERM	7.1D
FBB_CMD<0>	3.2G 7.1B 7.2G
FBB_CMD<0>	3.2G 7.1B 7.2G
FBB_CMD<27..0>	3.3G 7.2B 7.2E
FBB_CMD<1>	3.2G 7.1B 7.1E
FBB_CMD<1>	3.2G 7.1B 7.1E
FBB_CMD<2>	3.2G 7.1B 7.2G
FBB_CMD<2>	3.2G 7.1B 7.2G
FBB_CMD<3>	3.2G 7.2B 7.2E
FBB_CMD<3>	3.2G 7.2B 7.2E
FBB_CMD<4>	3.2G 7.1E 7.1G
FBB_CMD<4>	3.2G 7.1E 7.1G
FBB_CMD<5>	3.2G 7.1E 7.1G
FBB_CMD<5>	3.2G 7.1E 7.1G
FBB_CMD<6>	3.3G 7.1E 7.1G
FBB_CMD<6>	3.3G 7.1E 7.1G
FBB_CMD<8>	3.3G 7.1B 7.1E
FBB_CMD<8>	3.3G 7.1B 7.1E
FBB_CMD<9>	3.3G 7.1B 7.1E
FBB_CMD<9>	3.3G 7.1B 7.1E
FBB_CMD<10>	3.3G 7.1B 7.1E
FBB_CMD<10>	3.3G 7.1B 7.1E
FBB_CMD<11>	3.3G 7.1B 7.1E
FBB_CMD<11>	3.3G 7.1B 7.1E
FBB_CMD<12>	3.3G 7.2B 7.2E
FBB_CMD<12>	3.3G 7.2B 7.2E
FBB_CMD<13>	3.3G 7.1E 7.1G
FBB_CMD<13>	3.3G 7.1E 7.1G
FBB_CMD<14>	3.3G 7.2B 7.2E
FBB_CMD<14>	3.3G 7.2B 7.2E
FBB_CMD<15>	3.3G 7.2B 7.2E
FBB_CMD<15>	3.3G 7.2B 7.2E
FBB_CMD<16>	3.3G 7.1B 7.1E
FBB_CMD<16>	3.3G 7.1B 7.1E
FBB_CMD<17>	3.3G 7.1B 7.1E
FBB_CMD<17>	3.3G 7.1B 7.1E
FBB_CMD<18>	3.3G 7.2B 7.2E
FBB_CMD<18>	3.3G 7.2B 7.2E
FBB_CMD<19>	3.3G 7.1B 7.1E
FBB_CMD<19>	3.3G 7.1B 7.1E
FBB_CMD<20>	3.3G 7.1B 7.1E
FBB_CMD<20>	3.3G 7.1B 7.1E
FBB_CMD<21>	3.3G 7.1B 7.1E
FBB_CMD<21>	3.3G 7.1B 7.1E
FBB_CMD<22>	3.3G 7.1B 7.2G
FBB_CMD<22>	3.3G 7.1B 7.2G
FBB_CMD<23>	3.3G 7.1B 7.1E
FBB_CMD<23>	3.3G 7.1B 7.1E
FBB_CMD<24>	3.3G 7.1B 7.2G
FBB_CMD<24>	3.3G 7.1B 7.2G
FBB_CMD<25>	3.3G 7.1B 7.1E
FBB_CMD<25>	3.3G 7.1B 7.1E
FBB_CMD<27>	3.3G 7.2B 7.2E
FBB_CMD<27>	3.3G 7.2B 7.2E
FBB_CMD_SENB0	7.2B
FBB_CMD_SENB1	7.2E
FBB_D<0>	3.1F 7.4C
FBB_D<63..0>	3.3F 7.5E
FBB_D<1>	3.1F 7.4C
FBB_D<2>	3.1F 7.4C
FBB_D<3>	3.1F 7.4C
FBB_D<4>	3.1F 7.4C
FBB_D<5>	3.1F 7.4C
FBB_D<6>	3.1F 7.4C
FBB_D<7>	3.1F 7.4C
FBB_D<8>	3.1F 7.4C
FBB_D<9>	3.2F 7.4C
FBB_D<10>	3.2F 7.4C
FBB_D<11>	3.2F 7.4C
FBB_D<12>	3.2F 7.4C
FBB_D<13>	3.2F 7.4C
FBB_D<14>	3.2F 7.4C
FBB_D<15>	3.2F 7.4C
FBB_D<16>	3.2F 7.4D
FBB_D<17>	3.2F 7.4D
FBB_D<18>	3.2F 7.4D
FBB_D<19>	3.2F 7.4D
FBB_D<20>	3.2F 7.4D
FBB_D<21>	3.2F 7.4D
FBB_D<22>	3.2F 7.4D
FBB_D<23>	3.2F 7.4D
FBB_D<24>	3.2F 7.4E
FBB_D<25>	3.2F 7.4E
FBB_D<26>	3.2F 7.4E
FBB_D<27>	3.2F 7.4E
FBB_D<28>	3.2F 7.4E
FBB_D<29>	3.2F 7.4E
FBB_D<30>	3.2F 7.4E
FBB_D<31>	3.2F 7.4E
FBB_D<32>	3.2F 7.5C
FBB_D<33>	3.2F 7.5C
FBB_D<34>	3.2F 7.5C
FBB_D<35>	3.2F 7.5C
FBB_D<36>	3.2F 7.5C
FBB_D<37>	3.2F 7.5C
FBB_D<38>	3.2F 7.5C
FBB_D<39>	3.3F 7.5C
FBB_D<40>	3.3F 7.5C
FBB_D<41>	3.3F 7.5C
FBB_D<42>	3.3F 7.5C
FBB_D<43>	3.3F 7.5C
FBB_D<44>	3.3F 7.5C
FBB_D<45>	3.3F 7.5C
FBB_D<46>	3.3F 7.5C
FBB_D<47>	3.3F 7.5C
FBB_D<48>	3.3F 7.5D
FBB_D<49>	3.3F 7.5D
FBB_D<50>	3.3F 7.5D
FBB_D<51>	3.3F 7.5D
FBB_D<52>	3.3F 7.5D
FBB_D<53>	3.3F 7.5D
FBB_D<54>	3.3F 7.5D
FBB_D<55>	3.3F 7.5D
FBB_D<56>	3.3F 7.5E
FBB_D<57>	3.3F 7.5E
FBB_D<58>	3.3F 7.5E
FBB_D<59>	3.3F 7.5E
FBB_D<60>	3.3F 7.5E
FBB_D<61>	3.3F 7.5E
FBB_D<62>	3.3F 7.5E
FBB_D<63>	3.3F 7.5E
FBB_DQM<0>	3.3F 7.4B 7.4C
FBB_DQM<0>	3.3F 7.4B 7.4C

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NV_PN	600-10562-base-000 A		
ID		PAGE	
NAME	<ENGINEER>	DATE	30-DEC-2008

FBB_DQM<7..0>	3.4F 7.4B 7.5E
FBB_DQM<1>	3.3F 7.4B 7.4C
FBB_DQM<1>	3.3F 7.4B 7.4C
FBB_DQM<2>	3.3F 7.4B 7.4D
FBB_DQM<2>	3.3F 7.4B 7.4D
FBB_DQM<3>	3.4F 7.4B 7.4E
FBB_DQM<3>	3.4F 7.4B 7.4E
FBB_DQM<4>	3.4F 7.4B 7.5C
FBB_DQM<4>	3.4F 7.4B 7.5C
FBB_DQM<5>	3.4F 7.4B 7.5C
FBB_DQM<5>	3.4F 7.4B 7.5C
FBB_DQM<6>	3.4F 7.4B 7.5D
FBB_DQM<6>	3.4F 7.4B 7.5D
FBB_DQM<7>	3.4F 7.4B 7.5E
FBB_DQM<7>	3.4F 7.4B 7.5E
FBB_DQS_RN<0>	3.4F 7.4B 7.4C
FBB_DQS_RN<0>	3.4F 7.4B 7.4C
FBB_DQS_RN<7..0>	3.4F 7.5B 7.5E
FBB_DQS_RN<1>	3.4F 7.4B 7.4C
FBB_DQS_RN<1>	3.4F 7.4B 7.4C
FBB_DQS_RN<2>	3.4F 7.4B 7.4D
FBB_DQS_RN<2>	3.4F 7.4B 7.4D
FBB_DQS_RN<3>	3.4F 7.4B 7.4E
FBB_DQS_RN<3>	3.4F 7.4B 7.4E
FBB_DQS_RN<4>	3.4F 7.5B 7.5C
FBB_DQS_RN<4>	3.4F 7.5B 7.5C
FBB_DQS_RN<5>	3.4F 7.5B 7.5C
FBB_DQS_RN<5>	3.4F 7.5B 7.5C
FBB_DQS_RN<6>	3.4F 7.5B 7.5D
FBB_DQS_RN<6>	3.4F 7.5B 7.5D
FBB_DQS_RN<7>	3.4F 7.5B 7.5E
FBB_DQS_RN<7>	3.4F 7.5B 7.5E
FBB_DQS_WP<0>	3.4F 7.4C 7.5B
FBB_DQS_WP<0>	3.4F 7.4C 7.5B
FBB_DQS_WP<7..0>	3.4F 7.5B 7.5E
FBB_DQS_WP<1>	3.4F 7.4C 7.5B
FBB_DQS_WP<1>	3.4F 7.4C 7.5B
FBB_DQS_WP<2>	3.4F 7.4D 7.5B
FBB_DQS_WP<2>	3.4F 7.4D 7.5B
FBB_DQS_WP<3>	3.4F 7.4E 7.5B
FBB_DQS_WP<3>	3.4F 7.4E 7.5B
FBB_DQS_WP<4>	3.4F 7.5B 7.5C
FBB_DQS_WP<4>	3.4F 7.5B 7.5C
FBB_DQS_WP<5>	3.4F 7.5B 7.5C
FBB_DQS_WP<5>	3.4F 7.5B 7.5C
FBB_DQS_WP<6>	3.4F 7.5B 7.5D
FBB_DQS_WP<6>	3.4F 7.5B 7.5D
FBB_DQS_WP<7>	3.4F 7.5B 7.5E
FBB_DQS_WP<7>	3.4F 7.5B 7.5E
FBB_VREF0	7.3D> 13.4B<>
FBB_VREF1	7.3G> 13.4B<>
FBB_VREF2	7.3E> 13.4B<>
FBB_VREF3	7.3H> 13.4B<>
FBB_ZQ0	7.2B< 13.4B<>
FBB_ZQ1	7.2E< 13.4B<>
SNN_FBB0_NC1	7.2B
SNN_FBB1_NC1	7.2E
FBC_CLK0	4.4D> 9.2A< 13.1E>
FBC_CLK0*	4.4D> 9.2A< 13.1E>
FBC_CLK0_TERM	9.1A
FBC_CLK1	4.4D> 9.2D< 13.2E>
FBC_CLK1*	4.4D> 9.2D< 13.2E>
FBC_CLK1_TERM	9.1D
FBC_CMD<0>	4.2C 9.1B 9.2G
FBC_CMD<0>	4.2C 9.1B 9.2G
FBC_CMD<27..0>	4.3C 9.2B 9.2E
FBC_CMD<1>	4.2C 9.1B 9.1E
FBC_CMD<1>	4.2C 9.1B 9.1E
FBC_CMD<2>	4.2C 9.1B 9.2G
FBC_CMD<2>	4.2C 9.1B 9.2G
FBC_CMD<3>	4.2C 9.2B 9.2E
FBC_CMD<3>	4.2C 9.2B 9.2E
FBC_CMD<4>	4.2C 9.1E 9.1G
FBC_CMD<4>	4.2C 9.1E 9.1G
FBC_CMD<5>	4.2C 9.1E 9.1G
FBC_CMD<5>	4.2C 9.1E 9.1G
FBC_CMD<6>	4.3C 9.1E 9.1G
FBC_CMD<6>	4.3C 9.1E 9.1G
FBC_CMD<8>	4.3C 9.1B 9.1E
FBC_CMD<8>	4.3C 9.1B 9.1E
FBC_CMD<9>	4.3C 9.1B 9.1E
FBC_CMD<9>	4.3C 9.1B 9.1E
FBC_CMD<10>	4.3C 9.1B 9.1E
FBC_CMD<10>	4.3C 9.1B 9.1E
FBC_CMD<11>	4.3C 9.1B 9.1E
FBC_CMD<11>	4.3C 9.1B 9.1E
FBC_CMD<12>	4.3C 9.2B 9.2E
FBC_CMD<12>	4.3C 9.2B 9.2E
FBC_CMD<13>	4.3C 9.1E 9.1G
FBC_CMD<13>	4.3C 9.1E 9.1G
FBC_CMD<14>	4.3C 9.2B 9.2E
FBC_CMD<14>	4.3C 9.2B 9.2E
FBC_CMD<15>	4.3C 9.2B 9.2E
FBC_CMD<15>	4.3C 9.2B 9.2E
FBC_CMD<16>	4.3C 9.1B 9.1E
FBC_CMD<16>	4.3C 9.1B 9.1E

FBC_CMD<17>	4.3C 9.1B 9.1E
FBC_CMD<17>	4.3C 9.1B 9.1E
FBC_CMD<18>	4.3C 9.2B 9.2E
FBC_CMD<18>	4.3C 9.2B 9.2E
FBC_CMD<19>	4.3C 9.1B 9.1E
FBC_CMD<19>	4.3C 9.1B 9.1E
FBC_CMD<20>	4.3C 9.1B 9.1E
FBC_CMD<20>	4.3C 9.1B 9.1E
FBC_CMD<21>	4.3C 9.1B 9.1E
FBC_CMD<21>	4.3C 9.1B 9.1E
FBC_CMD<22>	4.3C 9.1B 9.2G
FBC_CMD<22>	4.3C 9.1B 9.2G
FBC_CMD<23>	4.3C 9.1B 9.1E
FBC_CMD<23>	4.3C 9.1B 9.1E
FBC_CMD<24>	4.3C 9.1B 9.2G
FBC_CMD<24>	4.3C 9.1B 9.2G
FBC_CMD<25>	4.3C 9.1B 9.1E
FBC_CMD<25>	4.3C 9.1B 9.1E
FBC_CMD<27>	4.3C 9.2B 9.2E
FBC_CMD<27>	4.3C 9.2B 9.2E
FBC_CMD_SENC0	9.2B
FBC_CMD_SENC1	9.2E
FBC_D<0>	4.1B 9.4C
FBC_D<63..0>	4.3B 9.5E
FBC_D<1>	4.1B 9.4C
FBC_D<2>	4.1B 9.4C
FBC_D<3>	4.1B 9.4C
FBC_D<4>	4.1B 9.4C
FBC_D<5>	4.1B 9.4C
FBC_D<6>	4.1B 9.4C
FBC_D<7>	4.1B 9.4C
FBC_D<8>	4.1B 9.4C
FBC_D<9>	4.2B 9.4C
FBC_D<10>	4.2B 9.4C
FBC_D<11>	4.2B 9.4C
FBC_D<12>	4.2B 9.4C
FBC_D<13>	4.2B 9.4C
FBC_D<14>	4.2B 9.4C
FBC_D<15>	4.2B 9.4C
FBC_D<16>	4.2B 9.4D
FBC_D<17>	4.2B 9.4D
FBC_D<18>	4.2B 9.4D
FBC_D<19>	4.2B 9.4D
FBC_D<20>	4.2B 9.4D
FBC_D<21>	4.2B 9.4D
FBC_D<22>	4.2B 9.4D
FBC_D<23>	4.2B 9.4D
FBC_D<24>	4.2B 9.4E
FBC_D<25>	4.2B 9.4E
FBC_D<26>	4.2B 9.4E
FBC_D<27>	4.2B 9.4E
FBC_D<28>	4.2B 9.4E
FBC_D<29>	4.2B 9.4E
FBC_D<30>	4.2B 9.4E
FBC_D<31>	4.2B 9.4E
FBC_D<32>	4.2B 9.5C
FBC_D<33>	4.2B 9.5C
FBC_D<34>	4.2B 9.5C
FBC_D<35>	4.2B 9.5C
FBC_D<36>	4.2B 9.5C
FBC_D<37>	4.2B 9.5C
FBC_D<38>	4.2B 9.5C
FBC_D<39>	4.3B 9.5C
FBC_D<40>	4.3B 9.5C
FBC_D<41>	4.3B 9.5C
FBC_D<42>	4.3B 9.5C
FBC_D<43>	4.3B 9.5C
FBC_D<44>	4.3B 9.5C
FBC_D<45>	4.3B 9.5C
FBC_D<46>	4.3B 9.5C
FBC_D<47>	4.3B 9.5C
FBC_D<48>	4.3B 9.5D
FBC_D<49>	4.3B 9.5D
FBC_D<50>	4.3B 9.5D
FBC_D<51>	4.3B 9.5D
FBC_D<52>	4.3B 9.5D
FBC_D<53>	4.3B 9.5D
FBC_D<54>	4.3B 9.5D
FBC_D<55>	4.3B 9.5D
FBC_D<56>	4.3B 9.5E
FBC_D<57>	4.3B 9.5E
FBC_D<58>	4.3B 9.5E
FBC_D<59>	4.3B 9.5E
FBC_D<60>	4.3B 9.5E
FBC_D<61>	4.3B 9.5E
FBC_D<62>	4.3B 9.5E
FBC_D<63>	4.3B 9.5E
FBC_DQM<0>	4.3B 9.4B 9.4C
FBC_DQM<0>	4.3B 9.4B 9.4C
FBC_DQM<7..0>	4.4B 9.4B 9.5E
FBC_DQM<1>	4.3B 9.4B 9.4C
FBC_DQM<1>	4.3B 9.4B 9.4C
FBC_DQM<2>	4.3B 9.4B 9.4D
FBC_DQM<2>	4.3B 9.4B 9.4D
FBC_DQM<3>	4.4B 9.4B 9.4E
FBC_DQM<3>	4.4B 9.4B 9.4E

FBC_DQM<4>	4.4B 9.4B 9.5C
FBC_DQM<4>	4.4B 9.4B 9.5C
FBC_DQM<5>	4.4B 9.4B 9.5C
FBC_DQM<5>	4.4B 9.4B 9.5C
FBC_DQM<6>	4.4B 9.4B 9.5D
FBC_DQM<6>	4.4B 9.4B 9.5D
FBC_DQM<7>	4.4B 9.4B 9.5E
FBC_DQM<7>	4.4B 9.4B 9.5E
FBC_DQS_RN<0>	4.4B 9.4B 9.4C
FBC_DQS_RN<0>	4.4B 9.4B 9.4C
FBC_DQS_RN<7..0>	4.4B 9.5B 9.5E
FBC_DQS_RN<1>	4.4B 9.4B 9.4C
FBC_DQS_RN<1>	4.4B 9.4B 9.4C
FBC_DQS_RN<2>	4.4B 9.4B 9.4D
FBC_DQS_RN<2>	4.4B 9.4B 9.4D
FBC_DQS_RN<3>	4.4B 9.4B 9.4E
FBC_DQS_RN<3>	4.4B 9.4B 9.4E
FBC_DQS_RN<4>	4.4B 9.5B 9.5C
FBC_DQS_RN<4>	4.4B 9.5B 9.5C
FBC_DQS_RN<5>	4.4B 9.5B 9.5C
FBC_DQS_RN<5>	4.4B 9.5B 9.5C
FBC_DQS_RN<6>	4.4B 9.5B 9.5D
FBC_DQS_RN<6>	4.4B 9.5B 9.5D
FBC_DQS_RN<7>	4.4B 9.5B 9.5E
FBC_DQS_RN<7>	4.4B 9.5B 9.5E
FBC_DQS_WP<0>	4.4B 9.4C 9.5B
FBC_DQS_WP<0>	4.4B 9.4C 9.5B
FBC_DQS_WP<7..0>	4.4B 9.5B 9.5E
FBC_DQS_WP<1>	4.4B 9.4C 9.5B
FBC_DQS_WP<1>	4.4B 9.4C 9.5B
FBC_DQS_WP<2>	4.4B 9.4D 9.5B
FBC_DQS_WP<2>	4.4B 9.4D 9.5B
FBC_DQS_WP<3>	4.4B 9.4E 9.5B
FBC_DQS_WP<3>	4.4B 9.4E 9.5B
FBC_DQS_WP<4>	4.4B 9.5B 9.5C
FBC_DQS_WP<4>	4.4B 9.5B 9.5C
FBC_DQS_WP<5>	4.4B 9.5B 9.5C
FBC_DQS_WP<5>	4.4B 9.5B 9.5C
FBC_DQS_WP<6>	4.4B 9.5B 9.5D
FBC_DQS_WP<6>	4.4B 9.5B 9.5D
FBC_DQS_WP<7>	4.4B 9.5B 9.5E
FBC_DQS_WP<7>	4.4B 9.5B 9.5E
FBC_VREF0	9.3D> 13.3E<>
FBC_VREF1	9.3G> 13.4E<>
FBC_VREF2	9.3E> 13.4E<>
FBC_VREF3	9.3H> 13.4E<>
FBC_ZQ0	9.2B< 13.4E<>
FBC_ZQ1	9.2E< 13.4E<>
SNN_FBC0_NC1	9.2B
SNN_FBC1_NC1	9.2E
FBD_CLK0	4.4H> 11.2A< 13.2E>
FBD_CLK0*	4.4H> 11.2A< 13.2E>
FBD_CLK0_TERM	11.1A
FBD_CLK1	4.4H> 11.2D< 13.2E>
FBD_CLK1*	4.4H> 11.2D< 13.2E>
FBD_CLK1_TERM	11.1D
FBD_CMD<0>	4.2G 11.1B 11.2G
FBD_CMD<0>	4.2G 11.1B 11.2G
FBD_CMD<27..0>	4.3G 11.2C 11.2F
FBD_CMD<1>	4.2G 11.1C 11.1F
FBD_CMD<1>	4.2G 11.1C 11.1F
FBD_CMD<2>	4.2G 11.1B 11.2G
FBD_CMD<2>	4.2G 11.1B 11.2G
FBD_CMD<3>	4.2G 11.2C 11.2F
FBD_CMD<3>	4.2G 11.2C 11.2F
FBD_CMD<4>	4.2G 11.1E 11.1G
FBD_CMD<4>	4.2G 11.1E 11.1G
FBD_CMD<5>	4.2G 11.1E 11.1G
FBD_CMD<5>	4.2G 11.1E 11.1G
FBD_CMD<6>	4.3G 11.1E 11.1G
FBD_CMD<6>	4.3G 11.1E 11.1G
FBD_CMD<8>	4.3G 11.1C 11.1F
FBD_CMD<8>	4.3G 11.1C 11.1F
FBD_CMD<9>	4.3G 11.1C 11.1F
FBD_CMD<9>	4.3G 11.1C 11.1F
FBD_CMD<10>	4.3G 11.1C 11.1F
FBD_CMD<10>	4.3G 11.1C 11.1F
FBD_CMD<11>	4.3G 11.1C 11.1F
FBD_CMD<11>	4.3G 11.1C 11.1F
FBD_CMD<12>	4.3G 11.2C 11.2F
FBD_CMD<12>	4.3G 11.2C 11.2F
FBD_CMD<13>	4.3G 11.1E 11.2G
FBD_CMD<13>	4.3G 11.1E 11.2G
FBD_CMD<14>	4.3G 11.2C 11.2F
FBD_CMD<14>	4.3G 11.2C 11.2F
FBD_CMD<15>	4.3G 11.2C 11.2F
FBD_CMD<15>	4.3G 11.2C 11.2F
FBD_CMD<16>	4.3G 11.1C 11.1F
FBD_CMD<16>	4.3G 11.1C 11.1F
FBD_CMD<17>	4.3G 11.1C 11.1F
FBD_CMD<17>	4.3G 11.1C 11.1F
FBD_CMD<18>	4.3G 11.2C 11.2F
FBD_CMD<18>	4.3G 11.2C 11.2F
FBD_CMD<19>	4.3G 11.1C 11.1F
FBD_CMD<19>	4.3G 11.1C 11.1F
FBD_CMD<20>	4.3G 11.1C 11.1F

FBD_CMD<20>	4.3G 11.1C 11.1F
FBD_CMD<21>	4.3G 11.1C 11.1F
FBD_CMD<21>	4.3G 11.1C 11.1F
FBD_CMD<22>	4.3G 11.1B 11.2G
FBD_CMD<22>	4.3G 11.1B 11.2G
FBD_CMD<23>	4.3G 11.1C 11.1F
FBD_CMD<23>	4.3G 11.1C 11.1F
FBD_CMD<24>	4.3G 11.1B 11.2G
FBD_CMD<24>	4.3G 11.1B 11.2G
FBD_CMD<25>	4.3G 11.1C 11.1F
FBD_CMD<25>	4.3G 11.1C 11.1F
FBD_CMD<27>	4.3G 11.2C 11.2F
FBD_CMD<27>	4.3G 11.2C 11.2F
FBD_CMD_SEND0	11.2C
FBD_CMD_SEND1	11.2F
FBD_D<0>	4.1F 11.4C
FBD_D<63..0>	4.3F 11.5E
FBD_D<1>	4.1F 11.4C
FBD_D<2>	4.1F 11.4C
FBD_D<3>	4.1F 11.4C
FBD_D<4>	4.1F 11.4C
FBD_D<5>	4.1F 11.4C
FBD_D<6>	4.1F 11.4C
FBD_D<7>	4.1F 11.4C
FBD_D<8>	4.1F 11.4D
FBD_D<9>	4.2F 11.4D
FBD_D<10>	4.2F 11.4D
FBD_D<11>	4.2F 11.4D
FBD_D<12>	4.2F 11.4D
FBD_D<13>	4.2F 11.4D
FBD_D<14>	4.2F 11.4D
FBD_D<15>	4.2F 11.4D
FBD_D<16>	4.2F 11.4D
FBD_D<17>	4.2F 11.4D
FBD_D<18>	4.2F 11.4D
FBD_D<19>	4.2F 11.4D
FBD_D<20>	4.2F 11.4D
FBD_D<21>	4.2F 11.4D
FBD_D<22>	4.2F 11.4D
FBD_D<23>	4.2F 11.4D
FBD_D<24>	4.2F 11.4E
FBD_D<25>	4.2F 11.4E
FBD_D<26>	4.2F 11.4E
FBD_D<27>	4.2F 11.4E
FBD_D<28>	4.2F 11.4E
FBD_D<29>	4.2F 11.4E
FBD_D<30>	4.2F 11.4E
FBD_D<31>	4.2F 11.4E
FBD_D<32>	4.2F 11.5C
FBD_D<33>	4.2F 11.5C
FBD_D<34>	4.2F 11.5C
FBD_D<35>	4.2F 11.5C
FBD_D<36>	4.2F 11.5C
FBD_D<37>	4.2F 11.5C
FBD_D<38>	4.2F 11.5C
FBD_D<39>	4.3F 11.5C
FBD_D<40>	4.3F 11.5D
FBD_D<41>	4.3F 11.5D
FBD_D<42>	4.3F 11.5D
FBD_D<43>	4.3F 11.5D
FBD_D<44>	4.3F 11.5D
FBD_D<45>	4.3F 11.5D
FBD_D<46>	4.3F 11.5D
FBD_D<47>	4.3F 11.5D
FBD_D<48>	4.3F 11.5D
FBD_D<49>	4.3F 11.5D
FBD_D<50>	4.3F 11.5D
FBD_D<51>	4.3F 11.5D
FBD_D<52>	4.3F 11.5D
FBD_D<53>	4.3F 11.5D
FBD_D<54>	4.3F 11.5D
FBD_D<55>	4.3F 11.5D
FBD_D<56>	4.3F 11.5E
FBD_D<57>	4.3F 11.5E
FBD_D<58>	4.3F 11.5E
FBD_D<59>	4.3F 11.5E
FBD_D<60>	4.3F 11.5E
FBD_D<61>	4.3F 11.5E
FBD_D<62>	4.3F 11.5E
FBD_D<63>	4.3F 11.5E
FBD_DQM<0>	4.3F 11.4B 11.4C
FBD_DQM<0>	4.3F 11.4B 11.4C
FBD_DQM<7..0>	4.4F 11.4B 11.5E
FBD_DQM<1>	4.3F 11.4B 11.4D
FBD_DQM<1>	4.3F 11.4B 11.4D
FBD_DQM<2>	4.3F 11.4B 11.4D
FBD_DQM<2>	4.3F 11.4B 11.4D
FBD_DQM<3>	4.4F 11.4B 11.4E
FBD_DQM<3>	4.4F 11.4B 11.4E
FBD_DQM<4>	4.4F 11.4B 11.5C
FBD_DQM<4>	4.4F 11.4B 11.5C
FBD_DQM<5>	4.4F 11.4B 11.5D
FBD_DQM<5>	4.4F 11.4B 11.5D
FBD_DQM<6>	4.4F 11.4B 11.5D
FBD_DQM<6>	4.4F 11.4B 11.5D
FBD_DQM<7>	4.4F 11.4B 11.5E

FBD_DQM<7>	4.4F 11.4B 11.5E
FBD_DQS_RN<0>	4.4F 11.4B 11.4C
FBD_DQS_RN<0>	4.4F 11.4B 11.4C
FBD_DQS_RN<7..0>	4.4F 11.5B 11.5E
FBD_DQS_RN<1>	4.4F 11.4B 11.4D
FBD_DQS_RN<1>	4.4F 11.4B 11.4D
FBD_DQS_RN<2>	4.4F 11.4B 11.4D
FBD_DQS_RN<2>	4.4F 11.4B 11.4D
FBD_DQS_RN<3>	4.4F 11.4B 11.4E
FBD_DQS_RN<3>	4.4F 11.4B 11.4E
FBD_DQS_RN<4>	4.4F 11.5B 11.5C
FBD_DQS_RN<4>	4.4F 11.5B 11.5C
FBD_DQS_RN<5>	4.4F 11.5B 11.5D
FBD_DQS_RN<5>	4.4F 11.5B 11.5D
FBD_DQS_RN<6>	4.4F 11.5B 11.5D
FBD_DQS_RN<6>	4.4F 11.5B 11.5D
FBD_DQS_RN<7>	4.4F 11.5B 11.5E
FBD_DQS_RN<7>	4.4F 11.5B 11.5E
FBD_DQS_WP<0>	4.4F 11.4C 11.5B
FBD_DQS_WP<0>	4.4F 11.4C 11.5B
FBD_DQS_WP<7..0>	4.4F 11.5B 11.5E
FBD_DQS_WP<1>	4.4F 11.4D 11.5B
FBD_DQS_WP<1>	4.4F 11.4D 11.5B
FBD_DQS_WP<2>	4.4F 11.4D 11.5B
FBD_DQS_WP<2>	4.4F 11.4D 11.5B
FBD_DQS_WP<3>	4.4F 11.4E 11.5B
FBD_DQS_WP<3>	4.4F 11.4E 11.5B
FBD_DQS_WP<4>	4.4F 11.5B 11.5C
FBD_DQS_WP<4>	4.4F 11.5B 11.5C
FBD_DQS_WP<5>	4.4F 11.5B 11.5D
FBD_DQS_WP<5>	4.4F 11.5B 11.5D
FBD_DQS_WP<6>	4.4F 11.5B 11.5D
FBD_DQS_WP<6>	4.4F 11.5B 11.5D
FBD_DQS_WP<7>	4.4F 11.5B 11.5E
FBD_DQS_WP<7>	4.4F 11.5B 11.5E
FBD_VREF0	11.3D> 13.4E<>
FBD_VREF1	11.3G> 13.4E<>
FBD_VREF2	11.3E> 13.4E<>
FBD_VREF3	11.3H> 13.4E<>
FBD_ZQ0	11.2B< 13.4E<>
FBD_ZQ1	11.2E< 13.4E<>
SNN_FBD0_NC1	11.2C
SNN_FBD1_NC1	11.2F
FBA_CLK0	3.4D> 5.2A< 13.1B>
FBA_CLK0*	3.4D> 5.2A< 13.1B>
FBA_CLK1	3.4D> 5.2D< 13.2B>
FBA_CLK1*	3.4D> 5.2D< 13.2B>
FBA_CMD<27..0>	3.3C 5.2B 5.2E
FBA_D<63..0>	3.3B 5.5E
FBA_DQM<7..0>	3.4B 5.4B 5.5E
FBA_DQS_RN<7..0>	3.4B 5.5B 5.5E
FBA_DQS_WP<7..0>	3.4B 5.5B 5.5E
FBA_VREF0	5.3D> 13.3B<>
FBA_VREF1	5.3G> 13.4B<>
FBA_VREF2	5.3E> 13.4B<>
FBA_VREF3	5.3H> 13.4B<>
FBA_ZQ0	5.2B< 13.4B<>
FBA_ZQ1	5.2E< 13.4B<>
FBB_CLK0	3.4H> 7.2A< 13.2B>
FBB_CLK0*	3.4H> 7.2A< 13.2B>
FBB_CLK1	3.4H> 7.2D< 13.2B>
FBB_CLK1*	3.4H> 7.2D< 13.2B>
FBB_CMD<27..0>	3.3G 7.2B 7.2E
FBB_D<63..0>	3.3F 7.5E
FBB_DQM<7..0>	3.4F 7.4B 7.5E
FBB_DQS_RN<7..0>	3.4F 7.5B 7.5E
FBB_DQS_WP<7..0>	3.4F 7.5B 7.5E
FBB_VREF0	7.3D> 13.4B<>
FBB_VREF1	7.3G> 13.4B<>
FBB_VREF2	7.3E> 13.4B<>
FBB_VREF3	7.3H> 13.4B<>
FBB_ZQ0	7.2B< 13.4B<>
FBB_ZQ1	7.2E< 13.4B<>
FBC_CLK0	4.4D> 9.2A< 13.1E>
FBC_CLK0*	4.4D> 9.2A< 13.1E>
FBC_CLK1	4.4D> 9.2D< 13.2E>
FBC_CLK1*	4.4D> 9.2D< 13.2E>
FBC_CMD<27..0>	4.3C 9.2B 9.2E
FBC_D<63..0>	4.3B 9.5E
FBC_DQM<7..0>	4.4B 9.4B 9.5E
FBC_DQS_RN<7..0>	4.4B 9.5B 9.5E
FBC_DQS_WP<7..0>	4.4B 9.5B 9.5E
FBC_VREF0	9.3D> 13.3E<>
FBC_VREF1	9.3G> 13.4E<>
FBC_VREF2	9.3E> 13.4E<>
FBC_VREF3	9.3H> 13.4E<>
FBC_ZQ0	9.2B< 13.4E<>
FBC_ZQ1	9.2E< 13.4E<>
FBD_CLK0	4.4H> 11.2A< 13.2E>
FBD_CLK0*	4.4H> 11.2A< 13.2E>
FBD_CLK1	4.4H> 11.2D< 13.2E>
FBD_CLK1*	4.4H> 11.2D< 13.2E>
FBD_CMD<27..0>	4.3G 11.2C 11.2F
FBD_D<63..0>	4.3F 11.5E
FBD_DQM<7..0>	4.4F 11.4B 11.5E
FBD_DQS_RN<7..0>	4.4F 11.5B 11.5E

FBD_DQS_WP<7..0>	4.4F 11.5B 11.5E
FBD_VREF0	11.3D> 13.4E<>
FBD_VREF1	11.3G> 13.4E<>
FBD_VREF2	11.3E> 13.4E<>
FBD_VREF3	11.3H> 13.4E<>
FBD_ZQ0	11.2B< 13.4E<>
FBD_ZQ1	11.2E< 13.4E<>
FB_PLLAVDD0	3.5D> 13.3B<>
FB_PLLAVDD1	4.5D> 13.3E<>
FB_VREF	3.5A> 13.3B<>
DACA_BLUE	14.3C 14.5A<> 14.5D
DACA_BLUE	14.3C 14.5A<> 14.5D
DACA_BLUE	14.3C 14.5A<> 14.5D
DACA_BLUE_C	14.3F> 14.5A<> 16.3G<
DACA_BLUE_C	14.3F> 14.5A<> 16.3G<
DACA_GREEN	14.3C 14.4D 14.5A<>
DACA_GREEN	14.3C 14.4D 14.5A<>
DACA_GREEN	14.3C 14.4D 14.5A<>
DACA_GREEN_C	14.3F> 14.5A<> 16.3G<
DACA_GREEN_C	14.3F> 14.5A<> 16.3G<
DACA_HSYNC	14.3C 14.5A<>
DACA_HSYNC	14.3C 14.5A<>
DACA_HS_BUF	14.3D 14.5A<>
DACA_HS_BUF	14.3D 14.5A<>
DACA_HS_BUF_R	14.3E 14.5A<>
DACA_HS_BUF_R	14.3E 14.5A<>
DACA_HS_C	14.2G> 14.2G> 14.5A<> 16.3G<
DACA_HS_C	14.2G> 14.2G> 14.5A<> 16.3G<
DACA_HS_C	14.2G> 14.2G> 14.5A<> 16.3G<
DACA_RED	14.3C 14.3D 14.5A<>
DACA_RED	14.3C 14.3D 14.5A<>
DACA_RED	14.3C 14.3D 14.5A<>
DACA_RED_C	14.3F> 14.5A<> 16.3G<
DACA_RED_C	14.3F> 14.5A<> 16.3G<
DACA_RSET	14.3B 14.5A<
DACA_RSET	14.3B 14.5A<
DACA_VDD	14.2B> 14.4A< 15.2A<
DACA_VDD	14.2B> 14.4A< 15.2A<
DACA_VREF	14.3B 14.5A<
DACA_VREF	14.3B 14.5A<
DACA_VSYNC	14.3C 14.5A<>
DACA_VSYNC	14.3C 14.5A<>
DACA_VS_BUF	14.2D 14.5A<>
DACA_VS_BUF	14.2D 14.5A<>
DACA_VS_BUF_R	14.2E 14.5A<>
DACA_VS_BUF_R	14.2E 14.5A<>
DACA_VS_C	14.2G> 14.2G> 14.5A<> 16.3G<
DACA_VS_C	14.2G> 14.2G> 14.5A<> 16.3G<
DACA_VS_C	14.2G> 14.2G> 14.5A<> 16.3G<
I2CA_SCL	14.3C
I2CA_SCL_C	14.1G> 14.1G> 16.3G<
I2CA_SCL_C	14.1G> 14.1G> 16.3G<
I2CA_SCL_T	14.1D
I2CA_SDA	14.3C
I2CA_SDA_C	14.1G> 14.1G> 16.3G<
I2CA_SDA_C	14.1G> 14.1G> 16.3G<
I2CA_SDA_T	14.1D
SNN_A_MON_ID0	14.3H
SNN_A_MON_ID2	14.4F
DACA_VDD	14.2B> 14.4A< 15.2A<
DACC_BLUE	15.3C 15.5A<> 15.5D
DACC_BLUE	15.3C 15.5A<> 15.5D
DACC_BLUE	15.3C 15.5A<> 15.5D
DACC_BLUE_C	15.3F> 15.5A<> 17.3G<
DACC_BLUE_C	15.3F> 15.5A<> 17.3G<
DACC_GREEN	15.3C 15.4D 15.5A<>
DACC_GREEN	15.3C 15.4D 15.5A<>
DACC_GREEN	15.3C 15.4D 15.5A<>
DACC_GREEN_C	15.3F> 15.5A<> 17.3G<
DACC_GREEN_C	15.3F> 15.5A<> 17.3G<
DACC_HSYNC	15.3C 15.5A<>
DACC_HSYNC	15.3C 15.5A<>
DACC_HS_BUF	15.2D 15.5A<>
DACC_HS_BUF	15.2D 15.5A<>
DACC_HS_BUF_R	15.2E 15.5A<>
DACC_HS_BUF_R	15.2E 15.5A<>
DACC_HS_C	15.2G> 15.2G> 15.5A<> 17.3G<
DACC_HS_C	15.2G> 15.2G> 15.5A<> 17.3G<
DACC_HS_C	15.2G> 15.2G> 15.5A<> 17.3G<
DACC_RED	15.3C 15.3D 15.5A<>
DACC_RED	15.3C 15.3D 15.5A<>
DACC_RED	15.3C 15.3D 15.5A<>
DACC_RED_C	15.3F> 15.5A<> 17.3G<
DACC_RED_C	15.3F> 15.5A<> 17.3G<
DACC_RSET	15.3B 15.5A<
DACC_RSET	15.3B 15.5A<
DACC_VREF	15.2B 15.4A<

ASSEMBLY	P547 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
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SANTA CLARA, CA 95050, USA

NV_PN	600-10562-base-000 A		
ID		PAGE	
NAME	<ENGINEER>	DATE	30-DEC-2008

DACC_VREF	15.2B 15.4A<
DACC_VSYNC	15.3C 15.5A<>
DACC_VSYNC	15.3C 15.5A<>
DACC_VS_BUF	15.2D 15.5A<>
DACC_VS_BUF	15.2D 15.5A<>
DACC_VS_BUF_R	15.2E 15.5A<>
DACC_VS_BUF_R	15.2E 15.5A<>
DACC_VS_C	15.2G> 15.2G> 15.5A<> 17.3G<
DACC_VS_C	15.2G> 15.2G> 15.5A<> 17.3G<
DACC_VS_C	15.2G> 15.2G> 15.5A<> 17.3G<
I2CB_SCL	15.2C
I2CB_SCL_C	15.1G> 15.1G> 17.3G<
I2CB_SCL_C	15.1G> 15.1G> 17.3G<
I2CB_SCL_T	15.1D
I2CB_SDA	15.2C
I2CB_SDA_C	15.1G> 15.1G> 17.3G<
I2CB_SDA_C	15.1G> 15.1G> 17.3G<
I2CB_SDA_T	15.1D
SNN_C_MON_ID0	15.3H
SNN_C_MON_ID2	15.4F
DACA_BLUE_C	14.3F> 14.5A<> 16.3G<
DACA_GREEN_C	14.3F> 14.5A<> 16.3G<
DACA_HS_C	14.2G> 14.2G> 14.5A<> 16.3G<
DACA_RED_C	14.3F> 14.5A<> 16.3G<
DACA_VS_C	14.2G> 14.2G> 14.5A<> 16.3G<
GPIO0_DVI_A_HPD	16.4C> 21.3D<
GPIO0_DVI_A_HPD_C	16.3G
GPIO0_DVI_A_HPD_R	16.4E
I2CA_SCL_C	14.1G> 14.1G> 16.3G<
I2CA_SDA_C	14.1G> 14.1G> 16.3G<
IFPABCD_PLLVDD	16.3B< 17.2B>
IFPAB_IOVDD	16.1G<> 16.3B
IFPAB_IOVDD	16.1G<> 16.3B
IFPAB_RSET	16.1G<> 16.2B
IFPAB_RSET	16.1G<> 16.2B
IFPAB_TXC	16.2D
IFPAB_TXC*	16.2D
IFPAB_TXD0	16.2D
IFPAB_TXD0*	16.2D
IFPAB_TXD1	16.3D
IFPAB_TXD1*	16.2D
IFPAB_TXD2	16.3D
IFPAB_TXD2*	16.3D
IFPAB_TXD4	16.3D
IFPAB_TXD4*	16.3D
IFPAB_TXD5	16.3D
IFPAB_TXD5*	16.3D
IFPAB_TXD6	16.3D
IFPAB_TXD6*	16.3D
SNN_IFPAB_ATXD3	16.3D
SNN_IFPAB_ATXD3*	16.3D
SNN_IFPAB_BTXC	16.3D
SNN_IFPAB_BTXC*	16.3D
SNN_IFPAB_TXD7	16.3D
SNN_IFPAB_TXD7*	16.3D
DACC_BLUE_C	15.3F> 15.5A<> 17.3G<
DACC_GREEN_C	15.3F> 15.5A<> 17.3G<
DACC_HS_C	15.2G> 15.2G> 15.5A<> 17.3G<
DACC_RED_C	15.3F> 15.5A<> 17.3G<
DACC_VS_C	15.2G> 15.2G> 15.5A<> 17.3G<
GPIO1_DVI_C_HPD	17.4D> 21.3D<
GPIO1_DVI_C_HPD_C	17.3G
GPIO1_DVI_C_HPD_R	17.4E
HDMI_PD	17.1F
HDMI_PD_EN	17.2F
I2CB_SCL_C	15.1G> 15.1G> 17.3G<
I2CB_SDA_C	15.1G> 15.1G> 17.3G<
IFPABCD_PLLVDD	16.3B< 17.2B>
IFPCD_IOVDD	17.1G<> 17.3B
IFPCD_IOVDD	17.1G<> 17.3B
IFPCD_PLLVDD	17.1G<>
IFPCD_RSET	17.1G<> 17.2B
IFPCD_RSET	17.1G<> 17.2B
IFPCD_TXC	17.1E 17.2E
IFPCD_TXC	17.1E 17.2E
IFPCD_TXC*	17.1E 17.2E
IFPCD_TXC*	17.1E 17.2E
IFPCD_TXD0	17.1E 17.2E
IFPCD_TXD0	17.1E 17.2E
IFPCD_TXD0*	17.1E 17.2E
IFPCD_TXD0*	17.1E 17.2E
IFPCD_TXD1	17.1E 17.2E
IFPCD_TXD1	17.1E 17.2E
IFPCD_TXD1*	17.1E 17.2E
IFPCD_TXD1*	17.1E 17.2E
IFPCD_TXD2	17.1E 17.2E
IFPCD_TXD2	17.1E 17.2E
IFPCD_TXD2*	17.1E 17.2E
IFPCD_TXD2*	17.1E 17.2E

IFPCD_TXD4	17.1E 17.2G 17.3E
IFPCD_TXD4	17.1E 17.2G 17.3E
IFPCD_TXD4	17.1E 17.2G 17.3E
IFPCD_TXD4*	17.1E 17.2G 17.3E
IFPCD_TXD4*	17.1E 17.2G 17.3E
IFPCD_TXD4*	17.1E 17.2G 17.3E
IFPCD_TXD5	17.1E 17.3E 17.3G
IFPCD_TXD5	17.1E 17.3E 17.3G
IFPCD_TXD5	17.1E 17.3E 17.3G
IFPCD_TXD5*	17.1E 17.3E 17.3G
IFPCD_TXD5*	17.1E 17.3E 17.3G
IFPCD_TXD5*	17.1E 17.3E 17.3G
IFPCD_TXD6	17.1E 17.3E 17.3G
IFPCD_TXD6	17.1E 17.3E 17.3G
IFPCD_TXD6	17.1E 17.3E 17.3G
IFPCD_TXD6*	17.1E 17.3E 17.3G
IFPCD_TXD6*	17.1E 17.3E 17.3G
IFPCD_TXD6*	17.1E 17.3E 17.3G
IFPC_TXC	17.2C
IFPC_TXC*	17.2C
IFPC_TXD0	17.2C
IFPC_TXD0*	17.2C
IFPC_TXD1	17.2C
IFPC_TXD1*	17.2C
IFPC_TXD2	17.2C
IFPC_TXD2*	17.2C
IFPD_TXD4	17.3C
IFPD_TXD4*	17.3C
IFPD_TXD5	17.3C
IFPD_TXD5*	17.3C
IFPD_TXD6	17.3C
IFPD_TXD6*	17.3C
SNN_IFPC_AUX	17.2C
SNN_IFPC_AUX*	17.2C
SNN_IFPD_AUX	17.3C
SNN_IFPD_AUX*	17.3C
SNN_IFPD_L3	17.3C
SNN_IFPD_L3*	17.3C
SNN_IFPEF_RSET	18.3B
SNN_IFPE_AUX	18.3D
SNN_IFPE_AUX*	18.3D
SNN_IFPE_L0	18.3D
SNN_IFPE_L0*	18.3D
SNN_IFPE_L1	18.3D
SNN_IFPE_L1*	18.3D
SNN_IFPE_L2	18.3D
SNN_IFPE_L2*	18.3D
SNN_IFPE_TXC	18.3D
SNN_IFPE_TXC*	18.3D
SNN_IFPF_AUX	18.4D
SNN_IFPF_AUX*	18.4D
SNN_IFPF_L0	18.4D
SNN_IFPF_L0*	18.4D
SNN_IFPF_L1	18.4D
SNN_IFPF_L1*	18.4D
SNN_IFPF_L2	18.4D
SNN_IFPF_L2*	18.4D
SNN_IFPF_L3	18.4D
SNN_IFPF_L3*	18.4D
SNN_DACB_COUT	19.3C
SNN_DACB_CSYNC	19.3C
SNN_DACB_PBOUT	19.3C
SNN_DACB_RSET	19.3B
SNN_DACB_VREF	19.3B
SNN_DACB_YOUT	19.3C
GPIO11_SLI_SYNC1	20.2E<> 21.3D>
GPIO22_SWAPRDY_A	20.2E<> 21.4E>
MIOA_CAL_PD_VDDQ	20.2C 20.5A<
MIOA_CAL_PD_VDDQ	20.2C 20.5A<
MIOA_CAL_PU_GND	20.2C 20.5A<
MIOA_CAL_PU_GND	20.2C 20.5A<
MIOA_CLKIN	20.2E 20.5A<
MIOA_CLKIN	20.2E 20.5A<
MIOA_CLKOUT	20.2E 20.5A<
MIOA_CLKOUT	20.2E 20.5A<
MIOA_D0	20.1E 20.1F
MIOA_D0	20.1E 20.1F
MIOA_D1	20.1E 20.1F
MIOA_D1	20.1E 20.1F
MIOA_D2	20.1E 20.1F
MIOA_D2	20.1E 20.1F
MIOA_D3	20.1E 20.1F
MIOA_D3	20.1E 20.1F
MIOA_D4	20.1E 20.1F
MIOA_D4	20.1E 20.1F
MIOA_D5	20.1E 20.1F
MIOA_D5	20.1E 20.1F
MIOA_D6	20.2E 20.2F
MIOA_D6	20.2E 20.2F
MIOA_D7	20.2E 20.2F
MIOA_D7	20.2E 20.2F
MIOA_D8	20.2E 20.2F
MIOA_D8	20.2E 20.2F
MIOA_D9	20.2E 20.2F
MIOA_D9	20.2E 20.2F
MIOA_D10	20.2E 20.2F

MIOA_D10	20.2E 20.2F
MIOA_D11	20.2E 20.2F
MIOA_D11	20.2E 20.2F
MIOA_D12	20.2E 20.2F
MIOA_D12	20.2E 20.2F
MIOA_D13	20.2E 20.2F
MIOA_D13	20.2E 20.2F
MIOA_D14	20.2E 20.2F
MIOA_D14	20.2E 20.2F
MIOA_DE	20.2E 20.5A<
MIOA_DE	20.2E 20.5A<
MIOA_VREF	20.2C 20.5A<
MIOA_VREF	20.2C 20.5A<
MIOB_CLKIN	20.4E
MIOB_D15_STRAP0	20.4E> 22.3A<
MIOB_D16_STRAP1	20.4E> 22.4A<
MIOB_D17_STRAP2	20.4E> 22.5A<
SNN_MIOA_CLKOUT*	20.2E
SNN_MIOA_D12	20.2E
SNN_MIOA_D13	20.2E
SNN_MIOA_D14	20.2E
SNN_MIOB_CAL_PD_VDDQ	20.4C
SNN_MIOB_CAL_PU_GND	20.4C
SNN_MIOB_CLKOUT	20.4E
SNN_MIOB_CLKOUT*	20.4E
SNN_MIOB_CTL3	20.4E
SNN_MIOB_D0	20.3E
SNN_MIOB_D1	20.3E
SNN_MIOB_D2	20.3E
SNN_MIOB_D3	20.3E
SNN_MIOB_D4	20.3E
SNN_MIOB_D5	20.3E
SNN_MIOB_D6	20.3E
SNN_MIOB_D7	20.3E
SNN_MIOB_D8	20.3E
SNN_MIOB_D9	20.3E
SNN_MIOB_D10	20.3E
SNN_MIOB_D11	20.4E
SNN_MIOB_D12	20.4E
SNN_MIOB_D13	20.4E
SNN_MIOB_D14	20.4E
SNN_MIOB_DE	20.4E
SNN_MIOB_HSYNC	20.4E
SNN_MIOB_VREF	20.4C
SNN_MIOB_VSYNC	20.4E
GPIO0_DVI_A_HPD	16.4C> 21.3D<
GPIO1_DVI_C_HPD	17.4D> 21.3D<
GPIO4_FAN_TACH	21.3D> 31.2C>
GPIO5_VSEL0	21.3D> 29.4C<
GPIO6_NVVDD_PHASE	21.3D> 29.2A<
GPIO8_GPU_SLOW*	21.3D> 25.2C<
GPIO9_FAN_PWM	21.3D> 31.2C<
GPIO11_SLI_SYNC1	20.2E<> 21.3D>
GPIO22_SWAPRDY_A	20.2E<> 21.4E>
GPU_PLLVDD	21.1G< 21.4F
GPU_PLLVDD	21.1G< 21.4F
HDA_RST*	21.1A
I2CC_SCL	21.2C
I2CC_SCL_R	21.3E>
I2CC_SDA	21.3C
I2CC_SDA_R	21.3E>
I2CH_SCL	21.2C 21.2F
I2CH_SCL	21.2C 21.2F
I2CH_SDA	21.2C 21.2F
I2CH_SDA	21.2C 21.2F
I2CH_SDA_R	21.2F
I2CS_SCL	21.2C
I2CS_SDA	21.2C
JTAG_TCLK	2.1F> 2.1F> 21.3A<
JTAG_TDI	2.1F> 2.1F> 21.3A<
JTAG_TDO	2.1F< 2.1F< 21.3A>
JTAG_TMS	2.1F> 2.1F> 21.3A<
JTAG_TRST*	2.1F> 2.1F> 21.3A<
PEX_SMCLK	2.1C> 21.2E<
PEX_SMDAT	2.1C> 21.2E<
ROM_CS*	21.1C
ROM_SCLK	21.1D> 22.3A<
ROM_SI	21.1D> 22.1A<
ROM_SO	21.1D> 22.2A<
SNN_BBIASN	21.1A
SNN_BBIASP	21.1A
SNN_BUFRST*	21.2C
SNN_GPIO2	21.3C
SNN_GPIO3	21.3C
SNN_GPIO7	21.3C
SNN_GPIO10	21.3C
SNN_GPIO12	21.3C
SNN_GPIO13	21.3C
SNN_GPIO14	21.3C
SNN_GPIO15	21.3C
SNN_GPIO16	21.3C
SNN_GPIO17	21.3C
SNN_GPIO18	21.3C
SNN_GPIO19	21.3C
SNN_GPIO20	21.3C
SNN_GPIO21	21.3C

SNN_GPIO23_STEREO	21.4C
SNN_HDA_SDI	21.1A
SNN_HDA_SDO	21.2A
SNN_HDA_SYNC	21.2A
SNN_HDSA_BCLK	21.1A
SNN_I2CD_SCL	21.3C
SNN_I2CD_SDA	21.3C
SNN_I2CE_SCL	21.3C
SNN_I2CE_SDA	21.3C
SNN_PGOOD_OUT*	21.2C
SNN_THERMDN	21.3A
SNN_THERMDP	21.3A
SPDIF	21.2D< 25.1G< 25.2H>
STRAP_CALPD_MIOB	21.2A
STRAP_CALPD_MISC	21.2A
XTALIN	21.1G< 21.5F
XTALIN	21.1G< 21.5F
XTALOUT	21.1G< 21.5G
XTALOUT	21.1G< 21.5G
XTALOUTBUFF	21.1G< 21.4H
XTALOUTBUFF	21.1G< 21.4H
XTALSSIN	21.1G< 21.4F
XTALSSIN	21.1G< 21.4F
MIOB_D15_STRAP0	20.4E> 22.3A<
MIOB_D16_STRAP1	20.4E> 22.4A<
MIOB_D17_STRAP2	20.4E> 22.5A<
ROM_SCLK	21.1D> 22.3A<
ROM_SI	21.1D> 22.1A<
ROM_SO	21.1D> 22.2A<
SNN_NC<1>	22.1G
SNN_NC<2>	22.1G
SNN_NC<3>	22.1G
SNN_NC<4>	22.1G
SNN_NC<5>	22.1G
SNN_NC<6>	22.1G
SNN_NC<7>	22.1G
SNN_NC<8>	22.1G
SNN_NC<9>	22.1G
SNN_NC<10>	22.1G
SNN_NC<11>	22.1G
SNN_NC<12>	22.1G
SNN_NC<13>	22.1G
SNN_NC<14>	22.1G
SNN_NC<15>	22.1G
SNN_NC<16>	22.1G
SNN_NC<17>	22.1G
SNN_NC<18>	22.1G
SNN_NC<19>	22.1G
SNN_NC<20>	22.1G
SNN_NC<21>	22.2G
SNN_NC<22>	22.2G
SNN_NC<23>	22.2G
SNN_NC<24>	22.2G
SNN_NC<25>	22.2G
SNN_NC<26>	22.2G
SNN_NC<27>	22.2G
SNN_NC<28>	22.2G
SNN_NC<29>	22.2G
SNN_NC<30>	22.2G
SNN_NC<31>	22.2G
SNN_NC<32>	22.2G
SNN_NC<33>	22.2G
SNN_NC<34>	22.2G
SNN_NC<35>	22.2G
SNN_NC<36>	22.2G
SNN_NC<37>	22.2G
SNN_NC<38>	22.2G
SNN_NC<39>	22.2G
SNN_NC<40>	22.2G
SNN_NC<41>	22.2G
SNN_NC<42>	22.2G
SNN_NC<43>	22.2G
SNN_NC<44>	22.2G
SNN_NC<45>	22.2G
SNN_NC<46>	22.2G
SNN_NC<47>	22.2G
SNN_NC<48>	22.2G
SNN_NC<49>	22.2G
SNN_NC<50>	22.2G
SNN_NC<51>	22.3G
SNN_NC<52>	22.3G
SNN_NC<53>	22.3G
SNN_NC<54>	22.3G
SNN_NC<55>	22.3G
SNN_NC<56>	22.3G
SNN_NC<57>	22.3G
SNN_NC<58>	22.3G
SNN_NC<59>	22.3G
SNN_NC<60>	22.3G
SNN_NC<61>	22.3G
SNN_NC<62>	22.3G
SNN_NC<63>	22.3G
SNN_NC<64>	22.3G
SNN_NC<65>	22.3G
SNN_NC<66>	22.3G
SNN_NC<67>	22.3G

SNN_NC<68>	22.3G
SNN_NC<69>	22.3G
SNN_NC<70>	22.3G
SNN_NC<71>	22.3G
SNN_NC<72>	22.3G
SNN_NC<73>	22.3G
SNN_NC<74>	22.3G
SNN_NC<75>	22.3G
3V3_TMDS_IOVDD_EN	25.4B
GPIO8_GPU_SLOW*	21.3D> 25.2C<
NVVDD_EN	25.3D> 29.3A<
PEX_RST*	2.2C> 2.4G> 25.3A<
PEX_RST_R*	25.3A
SPDIF	21.2D< 25.1G< 25.2H>
SPDIF	21.2D< 25.1G< 25.2H>
SPDIF_GND	25.2E
SPDIF_IN	25.1G< 25.2E
SPDIF_IN	25.1G< 25.2E
SPDIF_IN_C	25.1G< 25.3F
SPDIF_IN_C	25.1G< 25.3F
SPDIF_IN_COMP2_D	25.1G< 25.3G
SPDIF_IN_COMP2_D	25.1G< 25.3G
SPDIF_IN_COMP2_Q	25.1G< 25.3H
SPDIF_IN_COMP2_Q	25.1G< 25.3H
SPDIF_IN_R	25.1G< 25.3F
SPDIF_IN_R	25.1G< 25.3F
THERM_N_EN1	25.2A
THERM_N_EN1_R	25.2B
THERM_N_EN2	25.2C
THERM_N_EN3	25.2B
THERM_N_EN3_R1	25.2C
THERM_N_EN3_R2	25.3C
12V	26.1G
PS_1V8_ADJ	27.4F
PS_2V5_ADJ	27.2D
PS_2V5_VDD	27.2D
PS_5V_ADJ	27.4B
SNN_2V5_NC	27.2D
SNN_2V5_PGOOD	27.2D
FBVDDQ	28.1G
FBVDDQ_SENSE	4.5H> 28.4H<
FBVDDQ_SENSE_R	28.1G< 28.4F
FBVDDQ_SENSE_R	28.1G< 28.4F
PEX_VDD	28.1G
PS_1V1_CP	28.1G< 28.3B
PS_1V1_CP	28.1G< 28.3B
PS_1V1_DR	28.1G< 28.3C
PS_1V1_DR	28.1G< 28.3C
PS_1V1_FB	28.1G< 28.3C
PS_1V1_FB	28.1G< 28.3C
PS_FBVDDQ_FS	28.2G< 28.3C
PS_FBVDDQ_FS	28.2G< 28.3C
PS_FB_BOOT	28.1G< 28.3D
PS_FB_BOOT	28.1G< 28.3D
PS_FB_COMP	28.1G< 28.3D
PS_FB_COMP	28.1G< 28.3D
PS_FB_EN	28.5B
PS_FB_EN*	28.5C
PS_FB_FB	28.1G< 28.3D
PS_FB_FB	28.1G< 28.3D
PS_FB_LGATE	28.1G< 28.3D
PS_FB_LGATE	28.1G< 28.3D
PS_FB_PHASE	28.1G< 28.3E
PS_FB_PHASE	28.1G< 28.3E
PS_FB_PVCC5	28.1G< 28.2D
PS_FB_PVCC5	28.1G< 28.2D
PS_FB_RBOT	28.1G<
PS_FB_RC	28.1G< 28.4E
PS_FB_RC	28.1G< 28.4E
PS_FB_RC_CP	28.3D
PS_FB_SNUB	28.1G< 28.3F
PS_FB_SNUB	28.1G< 28.3F
PS_FB_UGATE	28.1G< 28.3D
PS_FB_UGATE	28.1G< 28.3D
PS_FB_UGATE_R	28.1G< 28.2E
PS_FB_UGATE_R	28.1G< 28.2E
PS_FB_VCC5	28.1G< 28.2C
PS_FB_VCC5	28.1G< 28.2C
PS_FB_VCC12	28.2D
GPIO5_VSEL0	21.3D> 29.4C<
GPIO6_NVVDD_PHASE	21.3D> 29.2A<
GPIO6_NVVDD_PHASE_R	29.2A
NVVDD	29.1A
NVVDD_EN	25.3D> 29.3A<
NVVDD_RBOT1	29.4D
NVVDD_SENSE_GPU	2.4E> 2.5G> 29.4G<
NVVDD_VSEL0	29.4D
PS1_NVVDD_FS	29.1F< 29.2B
PS1_NVVDD_FS	29.1F< 29.2B
PS1_NVVDD_SS	29.1F< 29.2B
PS1_NVVDD_SS	29.1F< 29.2B
PS1_NVVDD_SUS_R	29.2B
PS_NVVDD_BOOT1	29.1F< 29.2C
PS_NVVDD_BOOT1	29.1F< 29.2C
PS_NVVDD_BOOT2	29.1G< 29.2C
PS_NVVDD_BOOT2	29.1G< 29.2C

PS_NVVDD_CP	29.1F< 29.2B
PS_NVVDD_CP	29.1F< 29.2B
PS_NVVDD_DRVH1	29.1F< 29.2C
PS_NVVDD_DRVH1	29.1F< 29.2C
PS_NVVDD_DRVH1_R	29.1D 29.1F<
PS_NVVDD_DRVH1_R	29.1D 29.1F<
PS_NVVDD_DRVH2	29.1G< 29.2C
PS_NVVDD_DRVH2	29.1G< 29.2C
PS_NVVDD_DRVH2_R	29.1G< 29.2D
PS_NVVDD_DRVH2_R	29.1G< 29.2D
PS_NVVDD_DRVL1	29.1F< 29.2C
PS_NVVDD_DRVL1	29.1F< 29.2C
PS_NVVDD_DRVL2	29.1G< 29.3C
PS_NVVDD_DRVL2	29.1G< 29.3C
PS_NVVDD_EN	29.5A
PS_NVVDD_EN*	29.4B
PS_NVVDD_EN_AND	29.4B
PS_NVVDD_FB	29.1F< 29.2B 29.4D
PS_NVVDD_FB	29.1F< 29.2B 29.4D
PS_NVVDD_FB	29.1F< 29.2B 29.4D
PS_NVVDD_OC	29.3C
PS_NVVDD_PH1	29.1F< 29.2C
PS_NVVDD_PH1	29.1F< 29.2C
PS_NVVDD_PH2	29.1G< 29.2C
PS_NVVDD_PH2	29.1G< 29.2C
PS_NVVDD_PVCC9	29.1G< 29.2B
PS_NVVDD_PVCC9	29.1G< 29.2B
PS_NVVDD_RC	29.1G< 29.4F
PS_NVVDD_RC	29.1G< 29.4F
PS_NVVDD_RC1	29.1G< 29.2E
PS_NVVDD_RC1	29.1G< 29.2E
PS_NVVDD_RC2	29.1G< 29.3E
PS_NVVDD_RC2	29.1G< 29.3E
PS_NVVDD_RC_CP	29.1F< 29.2A
PS_NVVDD_RC_CP	29.1F< 29.2A
PS_NVVDD_SUS	29.1F< 29.2B
PS_NVVDD_SUS	29.1F< 29.2B
PS_NVVDD_VCC	29.1G< 29.2C
PS_NVVDD_VCC	29.1G< 29.2C
PS_NVVDD_VCC9	29.1F< 29.2B
PS_NVVDD_VCC9	29.1F< 29.2B
PS_NVVDD_VSEN	29.1G< 29.4F
PS_NVVDD_VSEN	29.1G< 29.4F
SNN_NVVDD_NC1	29.3C
SNN_NVVDD_NC2	29.3C
SNN_NVVDD_NC3	29.3C
SNN_NVVDD_VREF	29.2B
GPIO4_FAN_TACH	21.3D> 31.2C>
GPIO9_FAN_ON	31.3E
GPIO9_FAN_PWM	21.3D> 31.2C<
GPIO9_FAN_PWM_R	31.3E

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BKT1	[32.3F]
C1	[15.1F]
C2	[15.1F]
C3	[17.4F]
C4	[14.1F]
C5	[14.1F]
C6	[16.5F]
C7	[15.2F]
C8	[15.3F]
C9	[14.2F]
C10	[14.3F]
C11	[31.4D]
C12	[16.5E]
C13	[17.4E]
C14	[25.2F]
C15	[25.3F]
C16	[27.4B]
C17	[27.4C]
C18	[27.4C]
C19	[25.3G]
C20	[11.3E]
C21	[27.4A]
C22	[27.4A]
C23	[27.4G]
C24	[27.1D]
C25	[27.1C]
C26	[27.4F]
C27	[27.2C]
C28	[27.4E]
C29	[27.2F]
C30	[6.3C]
C31	[27.2E]
C32	[27.2E]
C33	[2.1A]
C34	[11.3G]
C35	[17.3A]
C36	[28.3F]
C37	[21.5H]
C38	[29.2E]
C39	[6.3E]
C40	[8.3C]
C41	[21.5F]
C42	[9.3E]
C43	[9.3G]
C44	[7.3E]
C45	[7.3G]
C46	[5.3E]
C47	[5.3G]
C48	[29.3E]
C49	[28.3H]
C50	[28.3B]
C51	[29.3G]
C53	[28.3A]
C54	[29.2E]
C55	[28.2C]
C56	[29.2H]
C57	[28.3H]
C58	[28.2B]
C59	[28.3F]
C60	[29.5A]
C61	[29.1E]
C62	[29.1E]
C63	[29.1E]
C65	[29.2G]
C66	[28.2F]
C67	[29.3E]
C68	[28.2F]
C69	[29.3G]
C70	[29.2G]
C71	[29.2H]
C72	[29.2E]
C73	[26.2C]
C74	[29.3H]
C75	[29.2E]
C76	[28.2F]
C77	[8.3E]
C78	[10.3C]
C79	[10.3E]
C80	[12.3C]
C81	[12.3E]
C505	[28.2F]
C506	[26.2D]
C507	[29.3D]
C508	[28.3G]
C509	[28.3G]
C511	[29.4A]
C512	[28.3E]
C513	[29.3B]
C514	[29.3D]

C516	[29.2G]
C517	[29.2E]
C519	[29.4F]
C520	[29.1C]
C521	[29.2A]
C522	[29.2B]
C523	[29.2A]
C524	[29.2B]
C525	[29.4D]
C526	[28.2D]
C527	[28.2C]
C528	[28.2D]
C530	[29.2A]
C531	[29.1D]
C532	[28.3E]
C533	[29.2D]
C535	[29.2G]
C536	[28.3D]
C537	[28.3C]
C538	[28.3D]
C539	[28.4F]
C541	[28.5B]
C542	[8.3E]
C543	[8.3E]
C544	[6.3C]
C545	[6.3E]
C546	[6.3C]
C547	[6.3E]
C548	[6.2C]
C549	[5.3C]
C550	[6.3C]
C551	[8.2C]
C552	[8.2E]
C553	[6.3E]
C554	[8.2E]
C555	[6.3C]
C556	[7.3F]
C557	[5.1A]
C558	[6.3E]
C559	[6.3C]
C560	[7.1D]
C561	[6.2E]
C562	[8.3E]
C563	[8.3E]
C564	[5.3F]
C565	[6.2E]
C566	[6.3E]
C567	[6.2E]
C568	[8.2E]
C569	[7.3H]
C570	[5.1D]
C571	[8.3E]
C572	[6.3E]
C573	[6.2C]
C574	[6.3C]
C575	[5.3B]
C576	[6.2E]
C577	[8.2E]
C578	[7.3E]
C579	[6.2C]
C580	[5.3E]
C581	[5.3H]
C582	[6.2C]
C583	[8.3E]
C584	[5.3D]
C585	[8.3C]
C586	[8.3C]
C587	[8.3C]
C588	[8.2C]
C589	[8.3C]
C590	[24.2D]
C591	[8.3C]
C592	[24.2D]
C593	[7.3C]
C594	[7.1A]
C595	[7.3D]
C596	[7.3B]
C597	[8.2C]
C598	[8.2C]
C599	[24.2D]
C600	[8.3C]
C601	[2.5C]
C602	[24.2D]
C603	[2.5C]
C604	[2.5C]
C605	[2.5C]
C606	[2.5C]
C607	[10.2E]
C608	[2.4C]
C609	[10.3E]
C610	[10.2E]
C611	[10.3E]
C612	[10.3E]
C613	[3.5A]
C614	[2.4C]
C617	[2.4C]

C618	[9.1D]
C619	[24.2F]
C620	[9.3H]
C621	[9.3E]
C622	[24.2D]
C623	[24.2F]
C624	[2.4C]
C625	[2.4C]
C626	[9.3F]
C627	[24.3F]
C628	[24.2F]
C629	[24.3E]
C630	[24.2E]
C631	[24.2F]
C632	[24.3E]
C633	[3.5E]
C634	[24.3F]
C635	[3.5D]
C636	[3.5D]
C637	[24.2E]
C638	[2.4C]
C639	[24.2A]
C640	[10.3E]
C641	[24.2A]
C642	[2.4C]
C644	[3.5D]
C645	[24.2A]
C646	[10.2E]
C647	[10.3E]
C648	[24.2D]
C649	[10.2E]
C650	[2.2F]
C651	[2.2F]
C652	[24.3A]
C653	[2.2E]
C654	[2.5F]
C656	[2.2E]
C657	[24.3A]
C658	[4.5D]
C659	[2.2E]
C660	[24.2A]
C661	[2.4C]
C662	[10.3E]
C664	[24.2A]
C665	[2.4C]
C666	[2.2E]
C667	[24.2A]
C668	[2.2E]
C670	[17.3B]
C671	[24.3A]
C674	[24.3A]
C675	[4.5E]
C676	[24.2A]
C677	[24.3A]
C678	[24.2D]
C679	[17.3A]
C680	[21.4D]
C681	[2.2E]
C682	[2.2E]
C683	[24.2A]
C684	[24.2A]
C685	[24.3A]
C686	[2.4C]
C687	[4.5D]
C688	[4.5D]
C689	[2.2E]
C690	[2.5F]
C692	[21.4F]
C693	[2.2F]
C694	[24.2A]
C695	[2.5E]
C696	[2.3C]
C698	[2.5E]
C699	[24.2A]
C700	[24.2A]
C701	[24.2E]
C702	[24.3A]
C703	[24.2A]
C704	[21.4F]
C705	[21.4E]
C706	[2.3C]
C708	[2.2F]
C709	[24.2A]
C710	[24.1F]
C711	[21.4E]
C712	[24.2A]
C714	[2.3C]
C715	[24.2A]
C716	[2.3E]
C718	[2.3F]
C719	[15.3A]
C720	[16.3C]
C721	[14.3A]
C722	[10.3C]
C723	[2.3F]
C724	[10.3C]

C726	[2.3C]
C727	[24.3F]
C728	[24.3F]
C729	[24.1F]
C734	[2.3C]
C735	[20.2C]
C736	[17.3B]
C737	[24.2F]
C738	[20.1B]
C739	[17.2B]
C740	[10.3C]
C741	[2.3C]
C742	[15.3A]
C743	[20.1B]
C744	[10.3C]
C745	[2.3C]
C746	[16.3B]
C747	[9.3B]
C748	[9.3C]
C749	[10.2C]
C750	[24.2F]
C751	[24.1E]
C756	[2.3C]
C757	[24.1F]
C759	[10.2C]
C760	[9.3D]
C761	[2.3C]
C762	[24.2D]
C763	[14.3A]
C764	[9.1A]
C765	[14.3B]
C766	[17.2A]
C767	[14.3A]
C768	[2.3E]
C769	[14.3A]
C770	[10.3C]
C771	[10.2C]
C772	[10.2C]
C773	[2.3C]
C774	[17.2A]
C776	[16.3B]
C777	[17.2B]
C778	[2.2C]
C779	[10.3C]
C781	[16.3B]
C782	[2.2C]
C783	[16.3A]
C784	[2.2C]
C786	[17.2E]
C787	[17.2E]
C788	[17.2E]
C789	[17.2E]
C790	[17.2E]
C791	[17.2E]
C792	[17.2E]
C793	[17.2E]
C794	[2.2C]
C795	[2.2C]
C796	[2.2C]
C797	[12.3E]
C798	[11.3H]
C800	[2.2C]
C801	[12.2E]
C802	[11.1D]
C803	[11.3F]
C804	[17.3E]
C805	[17.3E]
C806	[17.3E]
C807	[17.3E]
C808	[17.3E]
C809	[17.3E]
C810	[12.3E]
C811	[12.2E]
C812	[24.2D]
C813	[12.2C]
C814	[21.1E]
C815	[12.3E]
C816	[25.3D]
C817	[25.3C]
C818	[12.3E]
C819	[12.3E]
C820	[17.2G]
C821	[11.3E]
C822	[20.1A]
C823	[2.1A]
C824	[2.1A]
C825	[12.3E]
C826	[12.3E]
C827	[25.3C]
C828	[25.2C]
C829	[21.2G]
C830	[11.3B]
C831	[20.1B]
C832	[24.2D]
C833	[12.2C]
C834	[2.1A]

C836	[2.1A]
C837	[12.2C]
C838	[12.3C]
C839	[11.3D]
C840	[25.4D]
C841	[25.3C]
C842	[12.3C]
C843	[12.3C]
C844	[25.3B]
C845	[12.3C]
C846	[12.2E]
C847	[11.1A]
C848	[11.3C]
C849	[12.3C]
C850	[12.3C]
C851	[12.3C]
C855	[31.2E]
C856	[15.5E]
C857	[15.4E]
C858	[15.3E]
C859	[14.4E]
C860	[14.5E]
C864	[14.4E]
C865	[14.3E]
C869	[15.5F]
C870	[15.4F]
C871	[15.3F]
C872	[16.2G]
C873	[14.4F]
C874	[17.2G]
C875	[14.5F]
C876	[14.4F]
C877	[27.4D]
CN1	[20.2G]
CN2	[2.3B]
D1	[31.3E]
D2	[17.4E]
D3	[15.2E]
D4	[15.3E]
D5	[16.5E]
D6	[14.2E]
D7	[14.3E]
D8	[31.2D]
D9	[25.2G]
D504	[14.5E]
D505	[14.4E]
D506	[14.4E]
D507	[15.5E]
D508	[15.4E]
D509	[15.4E]
F501	[25.2E]
F502	[27.4D]
G1	[2.3D]
G1	[3.3C 3.3G]
G1	[4.3C 4.3G]
G1	[14.3C]
G1	[15.3C]
G1	[16.3C]
G1	[17.3C]
G1	[18.4C]
G1	[19.3C]
G1	[20.4D 20.2D]
G1	[21.2B 21.4G 21.3B]
G1	[22.2H]
G1	[23.3E 23.3A]
J1	[15.3G]
J2	[14.4G]
J4	[17.3H]
J5	[16.3H]
J6	[31.3F]
J7	[31.2F]
J8	[25.2E]
J501	[2.1D]
L1	[15.2F]
L2	[15.2F]
L3	[14.2F]
L4	[14.3F]
L5	[29.2F]
L6	[29.1F]
L7	[29.3F]
L8	[29.3F]
L9	[28.3G]
L10	[28.3G]
L11	[26.2C]
L12	[28.2G]
L13	[29.3F]
L14	[29.2F]
L505	[15.5E]
L506	[15.4E]
L507	[15.3E]
L508	[14.4F]
L509	[14.5F]
L510	[14.3F]
LB1	[15.1F]
LB2	[15.1F]

LB3	[14.1F]
LB4	[14.1F]
LB5	[17.4F]
LB6	[16.4F]
LB7	[26.3C]
LB8	[26.3C]
LB9	[26.3C]
LB10	[26.3C]
LB501	[3.5E]
LB502	[2.4F]
LB503	[17.3A]
LB504	[4.5D]
LB505	[21.4E]
LB508	[14.3A]
LB509	[17.2A]
LB511	[16.3A]
M1	[11.4D 11.4E 11.4F 11.4C 11.2C]
M2	[11.5E 11.5C 11.5F 11.2F 11.5D]
M3	[9.4F 9.4E 9.4D 9.2C 9.4C]
M4	[9.2F 9.5D 9.5E 9.5F 9.5C]
M5	[7.4C 7.4E 7.4D 7.2C 7.4F]
M6	[5.4E 5.4C 5.2C 5.4F 5.4D]
M7	[5.2F 5.5E 5.5F 5.5C 5.5D]
M8	[7.2F 7.5C 7.5D 7.5E 7.5F]
MEC1	[32.3G]
MEC2	[32.3G]
MEC3	[32.3G]
MEC4	[32.3G]
MEC5	[32.3F]
MEC6	[32.3D]
MEC7	[32.2D]
Q1	[31.3E]
Q2	[25.3G]
Q3	[25.3H]
Q4	[28.3B]
Q5	[29.5A]
Q6	[29.2D]
Q7	[29.1D]
Q8	[31.3D]
Q9	[28.3E]
Q11	[29.3D]
Q12	[28.2E]
Q14	[29.3D]
Q503	[29.4B]
Q504	[29.4A]
Q506	[29.4D]
Q507	[29.2B]
Q508	[28.5C]
Q509	[28.5B]
Q511	[17.2F]
Q512	[25.2C]
Q513	[25.3C]
Q514	[25.2B]
Q515	[25.4B]
Q516	[25.4C]
Q517	[25.2B]
Q518	[25.2B]
R1	[15.1E]
R2	[15.1E]
R3	[14.1E]
R4	[14.1E]
R5	[17.4E]
R6	[16.4E]
R7	[31.3F]
R8	[15.1D]
R9	[15.1D]
R10	[14.1D]
R11	[14.1D]
R12	[17.4E]
R13	[16.5D]
R14	[31.2E]
R15	[31.2E]
R16	[25.3F]
R17	[25.3F]
R18	[25.2G]
R19	[25.2G]
R20	[25.3F]
R21	[27.2E]
R22	[25.3H]
R23	[27.4C]
R24	[27.4C]
R25	[25.3G]
R26	[11.3E]
R27	[11.3E]
R28	[27.4F]
R29	[29.4A]

R30	[27.2C]
R31	[27.4G]
R32	[27.2C]
R33	[27.4G]
R34	[21.3D]
R35	[29.5A]
R36	[29.2A]
R37	[27.2E]
R38	[11.3G]
R39	[27.2E]
R40	[11.3G]
R41	[31.3D]
R42	[31.3E]
R45	[22.3B]
R46	[22.4B]
R47	[22.5B]
R48	[22.3B]
R49	[22.4B]
R50	[22.5B]
R51	[9.3D]
R52	[9.3D]
R53	[9.3G]
R54	[9.3G]
R55	[7.3E]
R56	[7.3E]
R57	[7.3G]
R58	[7.3G]
R59	[5.3E]
R60	[5.3E]
R61	[5.3G]
R62	[5.3G]
R63	[28.3B]
R64	[28.3B]
R65	[29.2E]
R66	[28.3F]
R67	[29.3E]
R507	[29.4A]
R508	[29.4A]
R509	[29.2D]
R510	[29.4B]
R512	[29.3G]
R513	[29.4G]
R514	[29.4F]
R515	[29.1C]
R516	[29.4F]
R517	[29.4E]
R519	[29.2B]
R521	[29.4D]
R522	[29.2A]
R524	[29.2B]
R525	[29.3C]
R526	[28.2D]
R527	[29.4D]
R528	[28.3E]
R530	[29.2B]
R532	[29.1C]
R533	[28.2D]
R534	[29.4C]
R535	[29.2A]
R536	[28.4E]
R537	[28.3C]
R538	[28.3C]
R539	[28.4D]
R540	[28.4E]
R541	[28.4G]
R542	[28.4E]
R543	[28.4G]
R547	[28.4C]
R548	[28.4B]
R549	[28.5B]
R550	[7.2F]
R551	[5.2B]
R552	[5.1H]
R553	[5.1H]
R554	[7.1H]
R555	[5.2A]
R556	[5.2F]
R557	[5.1H]
R558	[5.2A]
R559	[5.2E]
R560	[5.2C]
R561	[5.1H]
R562	[7.1H]
R565	[7.2E]
R566	[5.2B]
R567	[7.2D]
R568	[5.2B]
R569	[5.1H]
R570	[7.1H]
R571	[5.1H]
R572	[7.3H]
R573	[7.2B]
R574	[5.2D]
R575	[7.3H]
R576	[7.2E]
R577	[5.1H]

R578	[5.1H]
R579	[5.2D]
R581	[7.1H]
R582	[5.3H]
R583	[5.3D]
R584	[5.3D]
R585	[5.3H]
R586	[7.2B]
R587	[7.2B]
R588	[7.2H]
R589	[7.2H]
R590	[7.2H]
R591	[7.2A]
R592	[7.2A]
R594	[7.2H]
R595	[7.3D]
R596	[7.2C]
R597	[7.3D]
R598	[3.5A]
R599	[9.2F]
R600	[3.5A]
R603	[9.3H]
R604	[9.2D]
R605	[9.2D]
R606	[9.1H]
R607	[3.5H]
R608	[3.5H]
R610	[9.3H]
R611	[9.1H]
R612	[9.1H]
R613	[3.5H]
R614	[9.1H]
R616	[2.5E]
R617	[9.2E]
R618	[2.5E]
R619	[2.5E]
R620	[2.5E]
R621	[9.2B]
R622	[9.2B]
R623	[2.4E]
R625	[14.4D]
R626	[9.2B]
R628	[14.5D]
R629	[14.4D]
R630	[21.2A]
R631	[21.2A]
R633	[9.2H]
R634	[9.2H]
R635	[21.2A]
R636	[15.5D]
R637	[15.3B]
R639	[15.4D]
R640	[9.2H]
R641	[9.3D]
R642	[9.2H]
R643	[16.3B]
R644	[15.3D]
R645	[17.2B]
R646	[9.2A]
R647	[9.2A]
R648	[20.4E]
R651	[9.3D]
R655	[20.2C]
R656	[21.5H]
R657	[21.5F]
R659	[14.3B]
R660	[20.2C]
R662	[9.2C]
R663	[20.2C]
R664	[20.2C]
R668	[22.1B]
R669	[22.3B]
R670	[22.2B]
R671	[17.1E]
R672	[17.1E]
R673	[17.1E]
R674	[17.1E]
R675	[17.1E]
R676	[17.1E]
R677	[17.1E]
R678	[17.1E]
R679	[22.2B]
R680	[22.2B]
R681	[22.1B]
R682	[11.3H]
R684	[11.3H]
R685	[11.2D]
R686	[11.2F]
R687	[2.1E]
R688	[11.2D]
R689	[21.1D]
R690	[2.1E]
R691	[2.1C]
R692	[21.3D]
R693	[21.3D]
R694	[11.1H]

R695	[11.1H]
R696	[17.1E]
R697	[17.1E]
R698	[17.1E]
R699	[17.1E]
R700	[17.1E]
R701	[17.1E]
R702	[2.1E]
R703	[2.1C]
R704	[11.2H]
R705	[11.2H]
R706	[21.2D]
R707	[21.2E]
R708	[21.2G]
R709	[21.2F]
R710	[2.1E]
R711	[2.1C]
R712	[2.1C]
R713	[2.1C]
R714	[2.1C]
R715	[2.1E]
R716	[21.1F]
R717	[11.2E]
R718	[17.1G]
R719	[21.1F]
R720	[21.2F]
R721	[21.2D]
R722	[21.2D]
R723	[11.2B]
R724	[25.3B]
R725	[25.3C]
R726	[25.2C]
R727	[25.2C]
R728	[25.2C]
R729	[21.2C]
R730	[21.2D]
R731	[11.2B]
R732	[11.2B]
R733	[11.2H]
R734	[11.3D]
R735	[25.4B]
R736	[11.3D]
R737	[11.2H]
R738	[25.2B]
R739	[25.2A]
R740	[11.2H]
R741	[25.3A]
R742	[11.2B]
R744	[11.2A]
R745	[11.2H]
R746	[11.2C]
R747	[25.2E]
R749	[15.2E]
R751	[14.2E]
R752	[15.5E]
R753	[15.4E]
R754	[15.3E]
R758	[14.5E]
R759	[14.4E]
R760	[14.4E]
R762	[15.2E]
R764	[14.3E]
TP1	[3.4D]
TP2	[3.4H]
TP3	[4.4D]
TP4	[4.4H]
U1	[27.4B]
U2	[27.4F]
U3	[27.2D]
U501	[29.2C]
U502	[28.3D]
U503	[21.1E]
U504	[21.2G]
U505	[14.3D 14.2D]
U505	[15.2D 15.2D]
Y1	[21.5G]

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