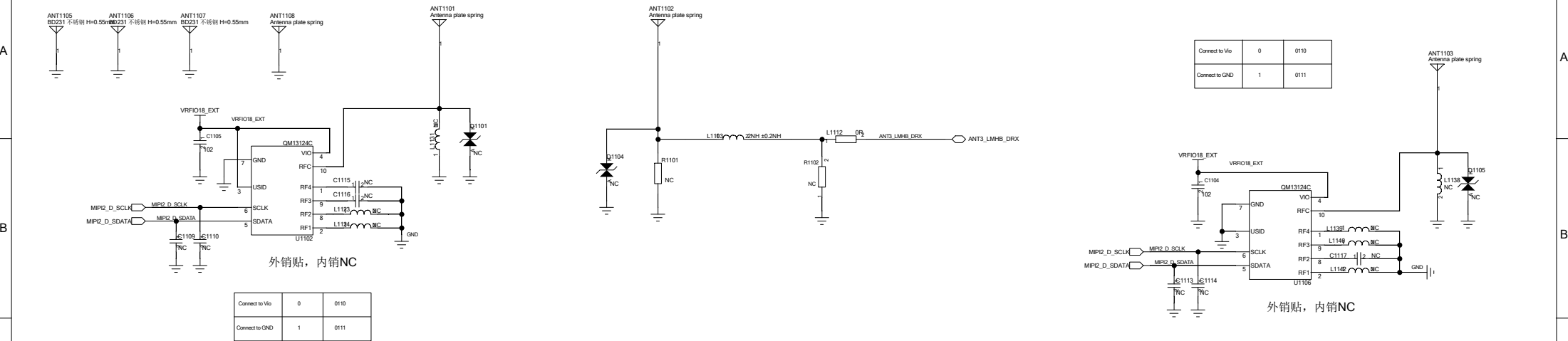
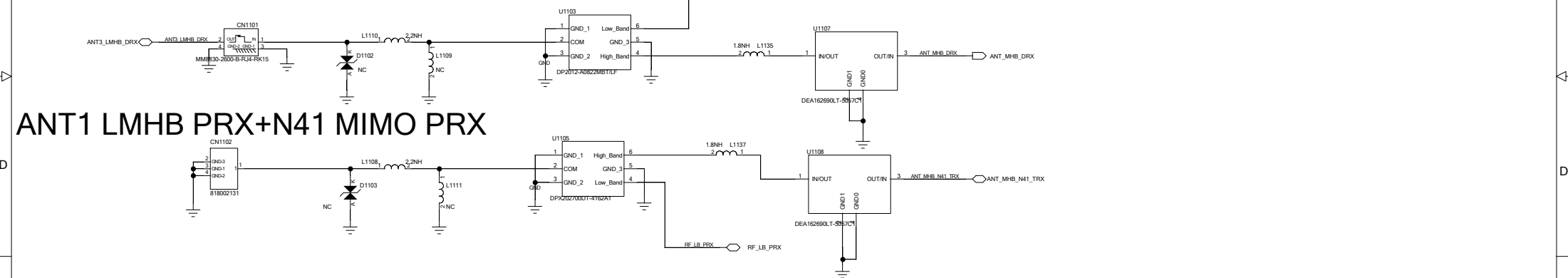


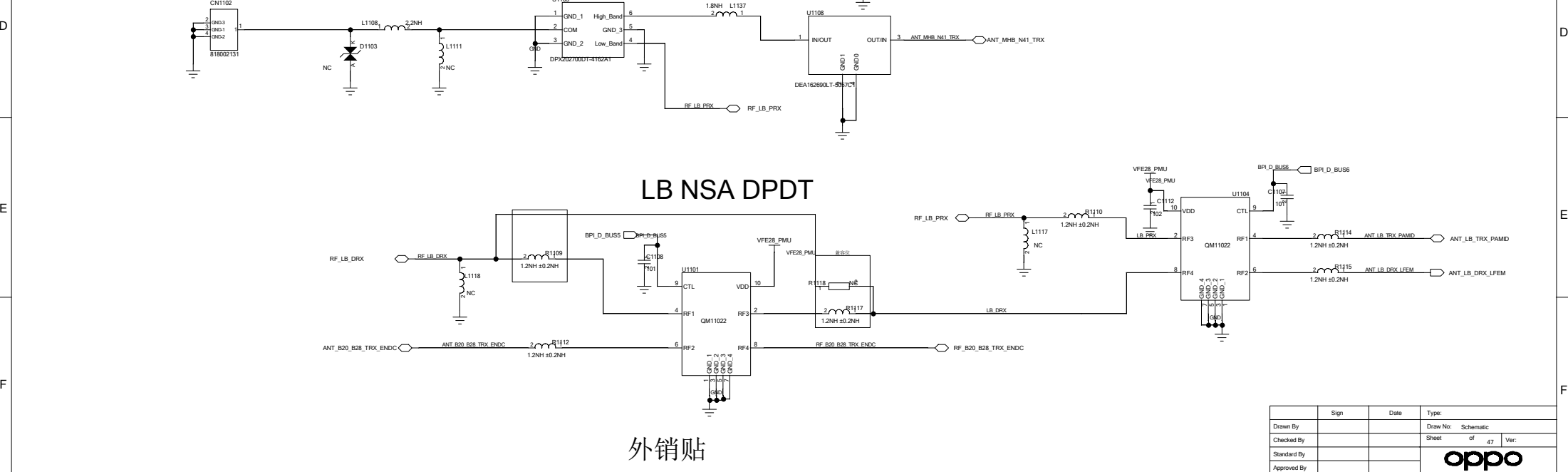
Ant3 LMHB DRX+N41 MIMO DRX 上天线 右侧



Ant3 LMHB DRX+N41 MIMO DRX



ANT1 LMHB PRX+N41 MIMO PRX

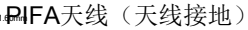


外销贴

Sign	Date	Type:
Drawn By		Draw No: Schematic
Checked By		Sheet of 47 Ver:
Standard By		
Approved By		

oppo

1



不能用电容或NC

天线空口网络RF端

ANT1205
 零件: 1. TOPM1 01M1 60mm



D



不能和U1102相同USID

F

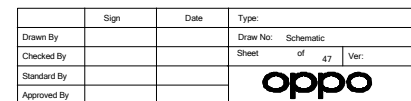
B



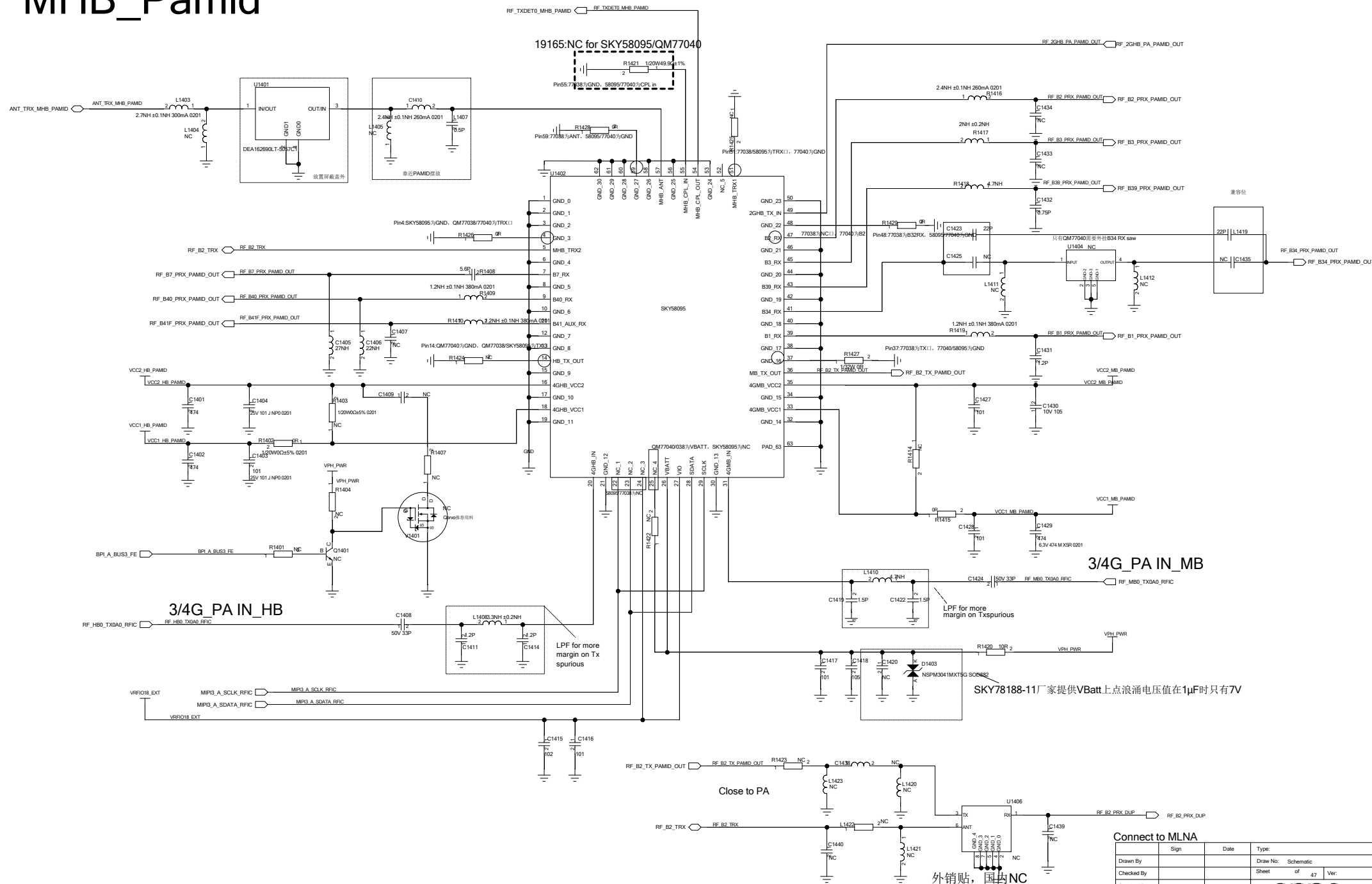
D



E




MHB Pamid

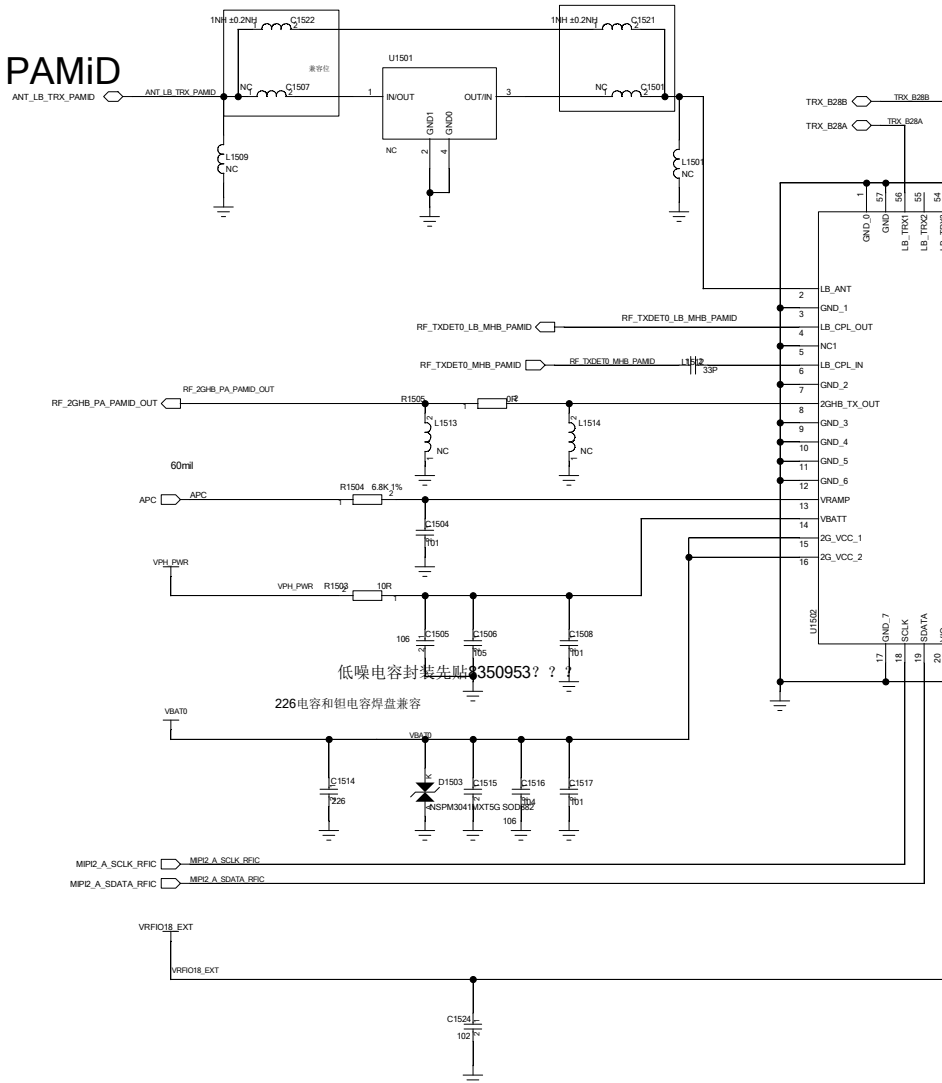


外销贴, 国内NC

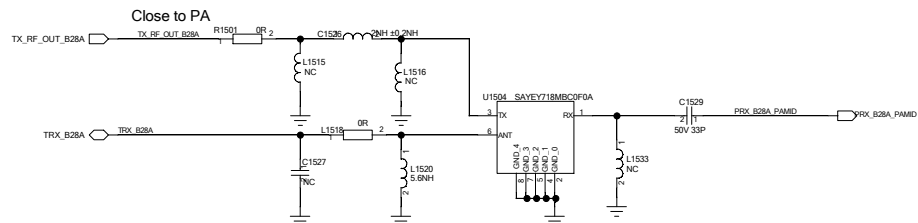
Connect to MLNA

Sign		Date	Type:	
Drawn By			Draw No: Schematic	
Checked By			Sheet of 47	Ver:
Standard By				
Approved By				

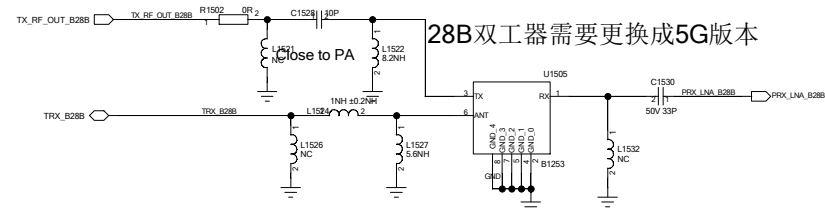
LB PAMiD




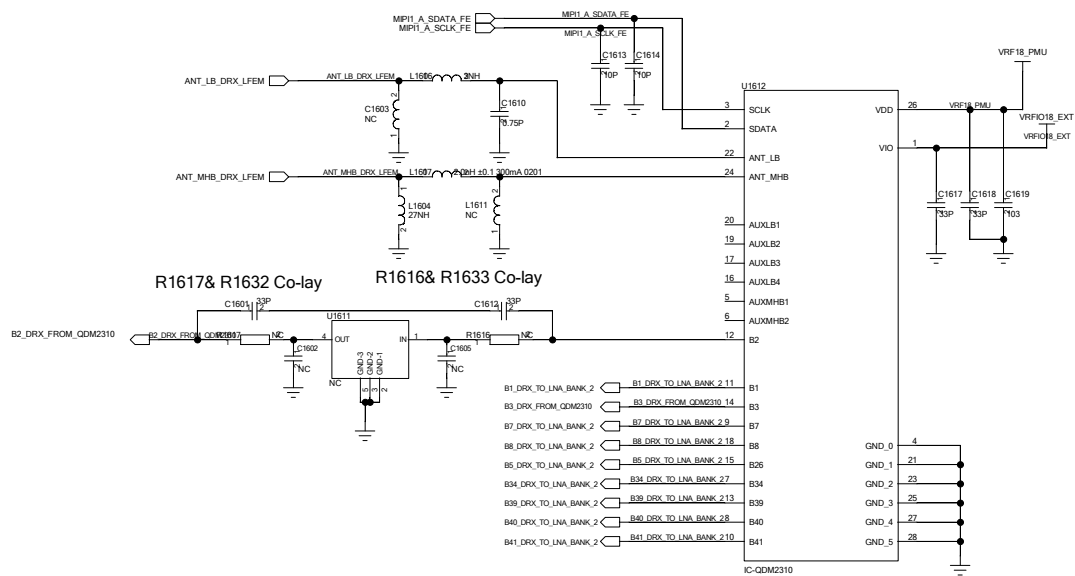
B28A TRX



B28B TRX

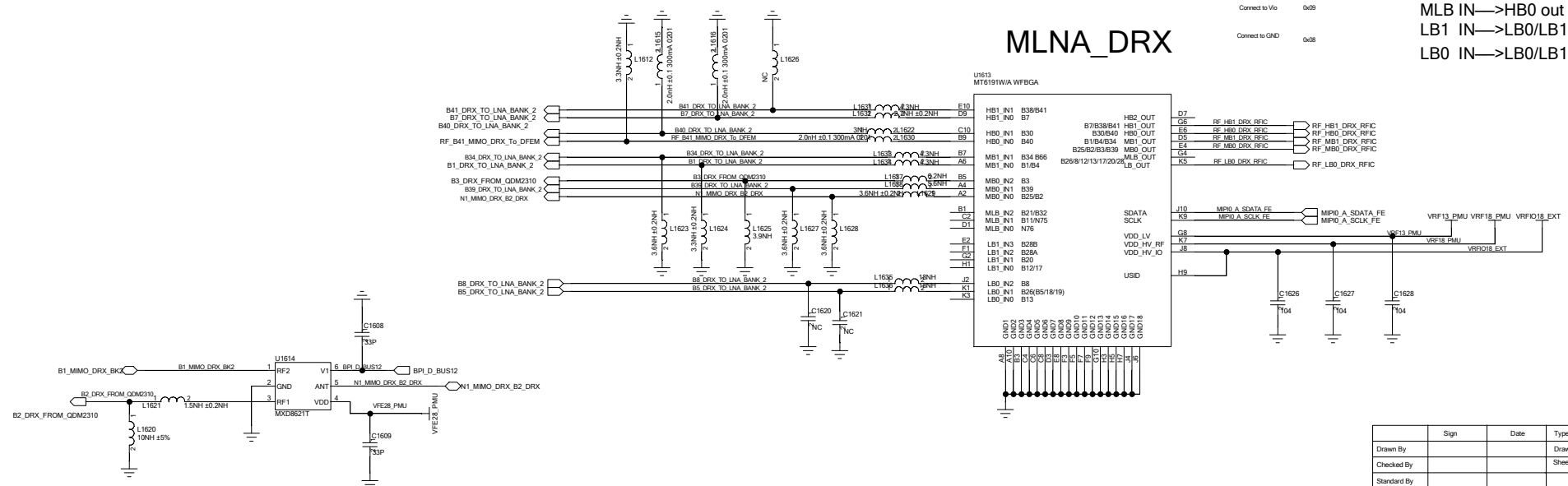



	Sign	Date	Type:		
Drawn By			Draw No: Schematic		
Checked By			Sheet	of 47	Ver:
Standard By					
Approved By					



Close to MLNA

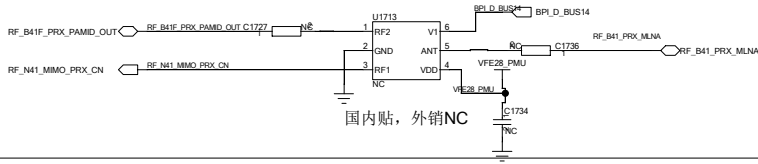
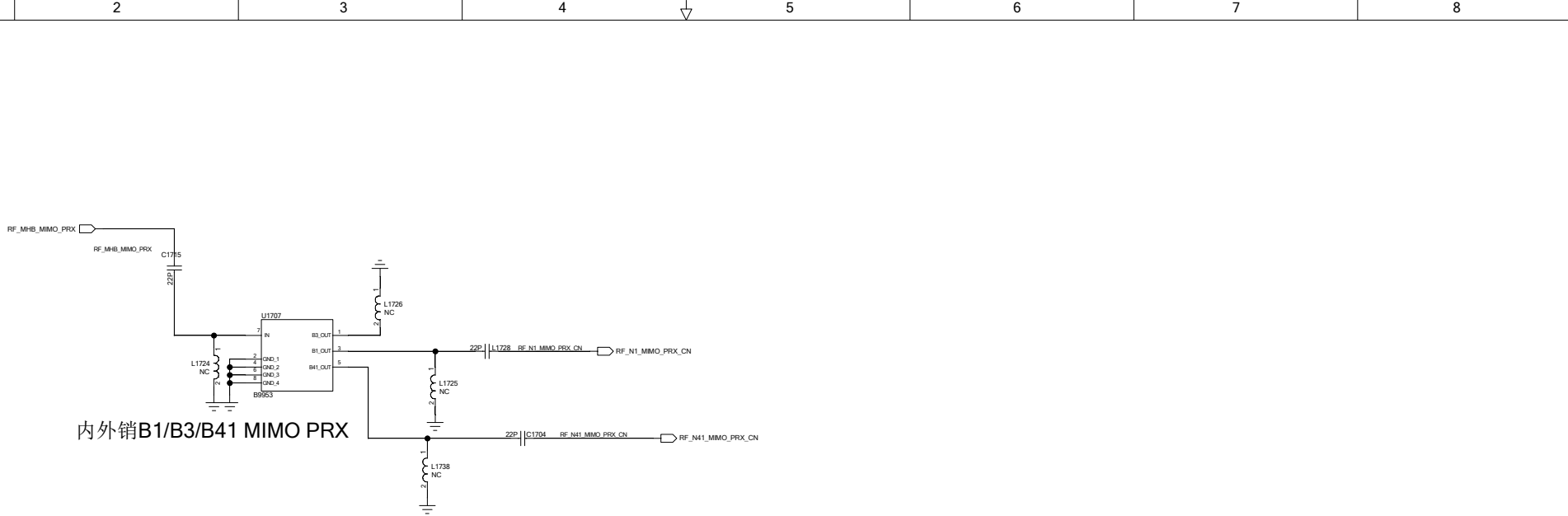
MLNA_DRX



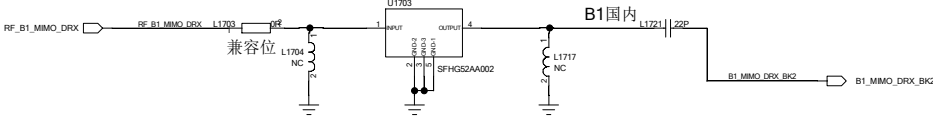
	Sign	Date	Type:	
Drawn By			Draw No: A2SHEET	
Checked By			Sheet of 2	Ver:
Standard By				
Approved By				



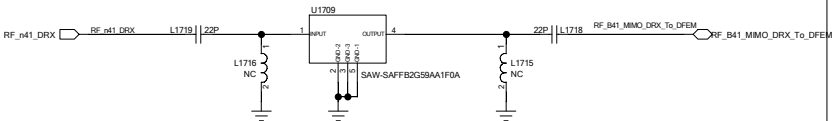
MIMO_PRX



MIMO_DRX

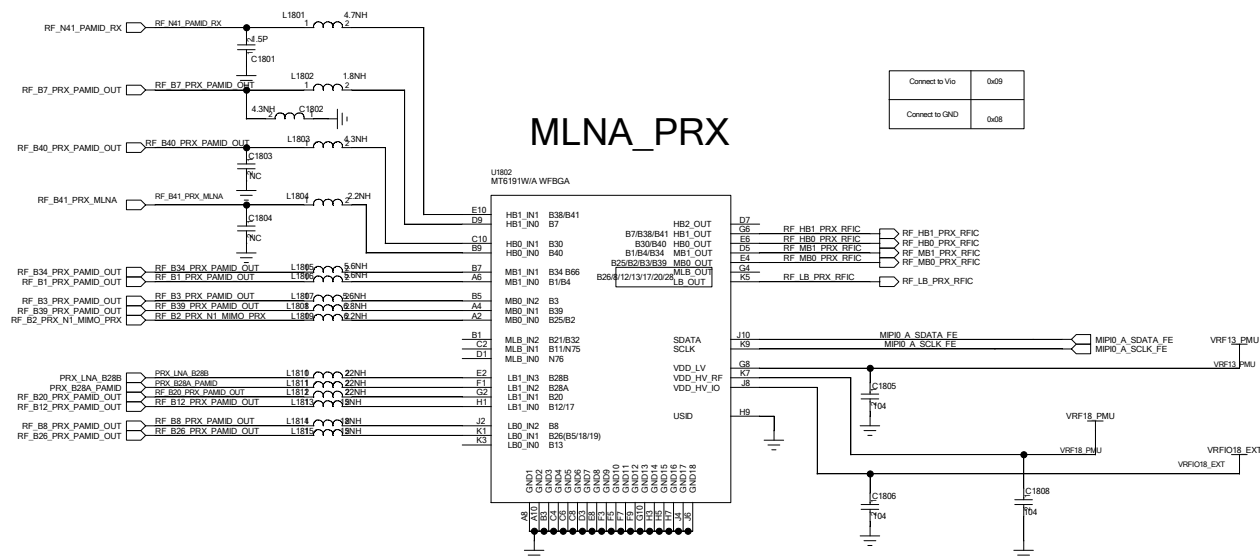


B41 DRX



	Sign	Date	Type:
Drawn By			Draw No: Schematic
Checked By			Sheet of 47 Ver:
Standard By			oppo
Approved By			

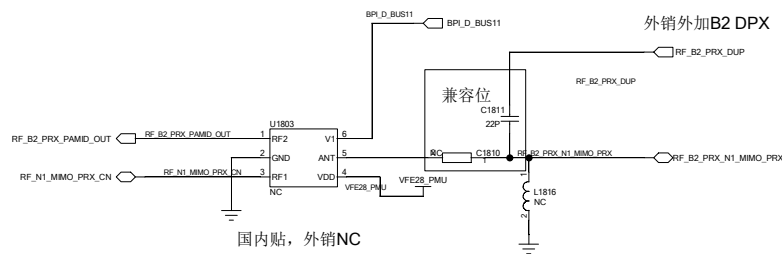
MLNA_PRX



Connect to Vio	0x09
Connect to GND	0x08

HB2 out is reserved
 HB1 IN→HB1 out
 HB0 IN→HB0 out
 MB1 IN→MB1 out
 MB0 IN→MB0 out
 MLB IN→HB0 out
 LB1 IN→LB0/LB1 out
 LB0 IN→LB0/LB1 out

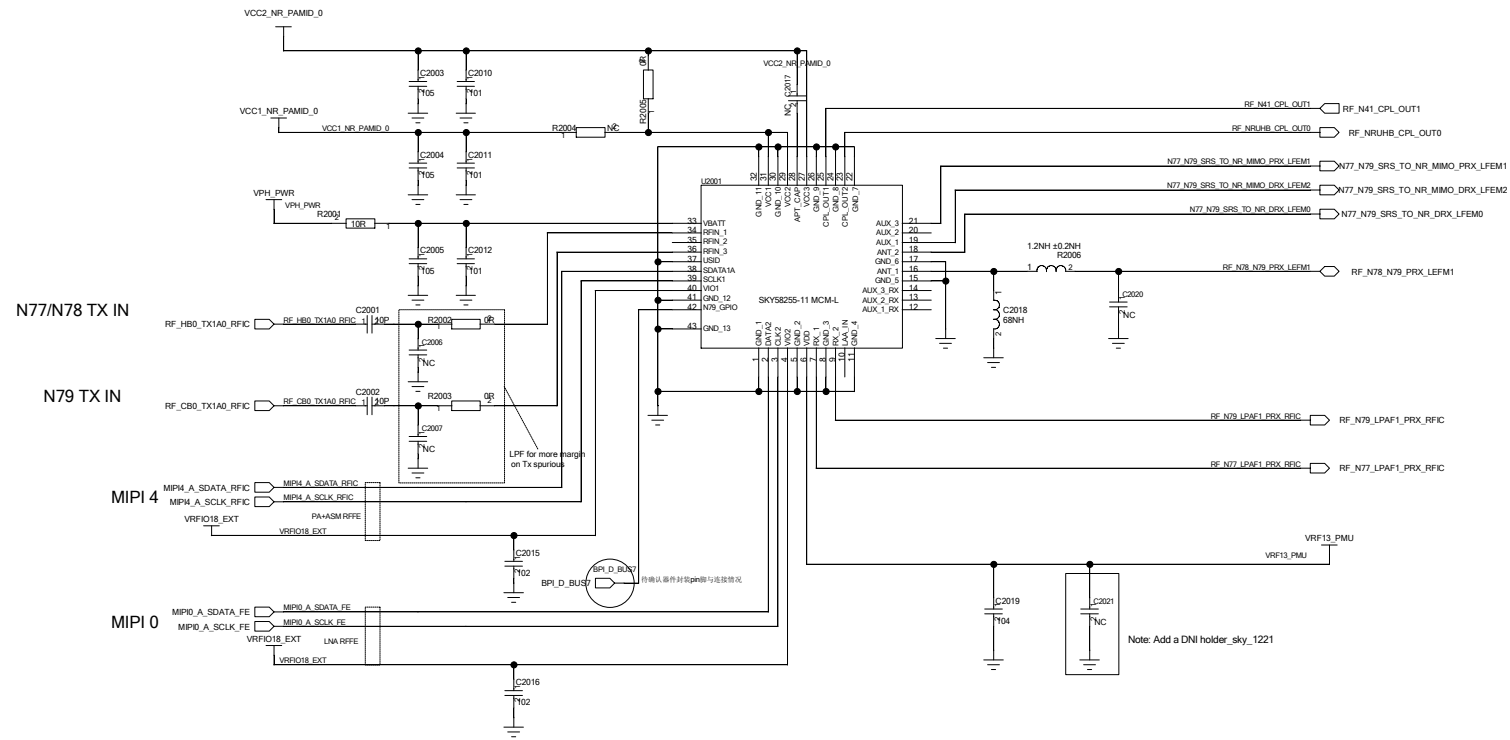
* MTK参考电路中该模块所有电容耐压值均为50V




	Sign	Date	Type:
Drawn By			Draw No: Schematic
Checked By			Sheet of 47 Ver:
Standard By			
Approved By			

oppo

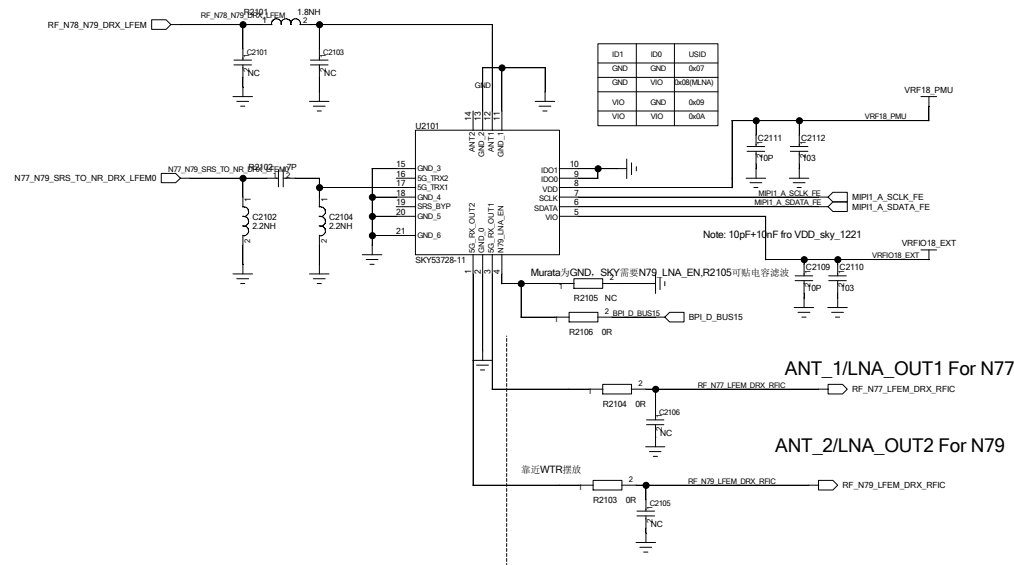
NR PAMID 0(N77/79Main)



Note: Add a DNI holder_sky_1221

	Sign	Date	Type:	
Drawn By			Draw No:	Schematic
Checked By			Sheet	of 47 Ver:
Standard By				
Approved By				

NR_LFEM_DRX




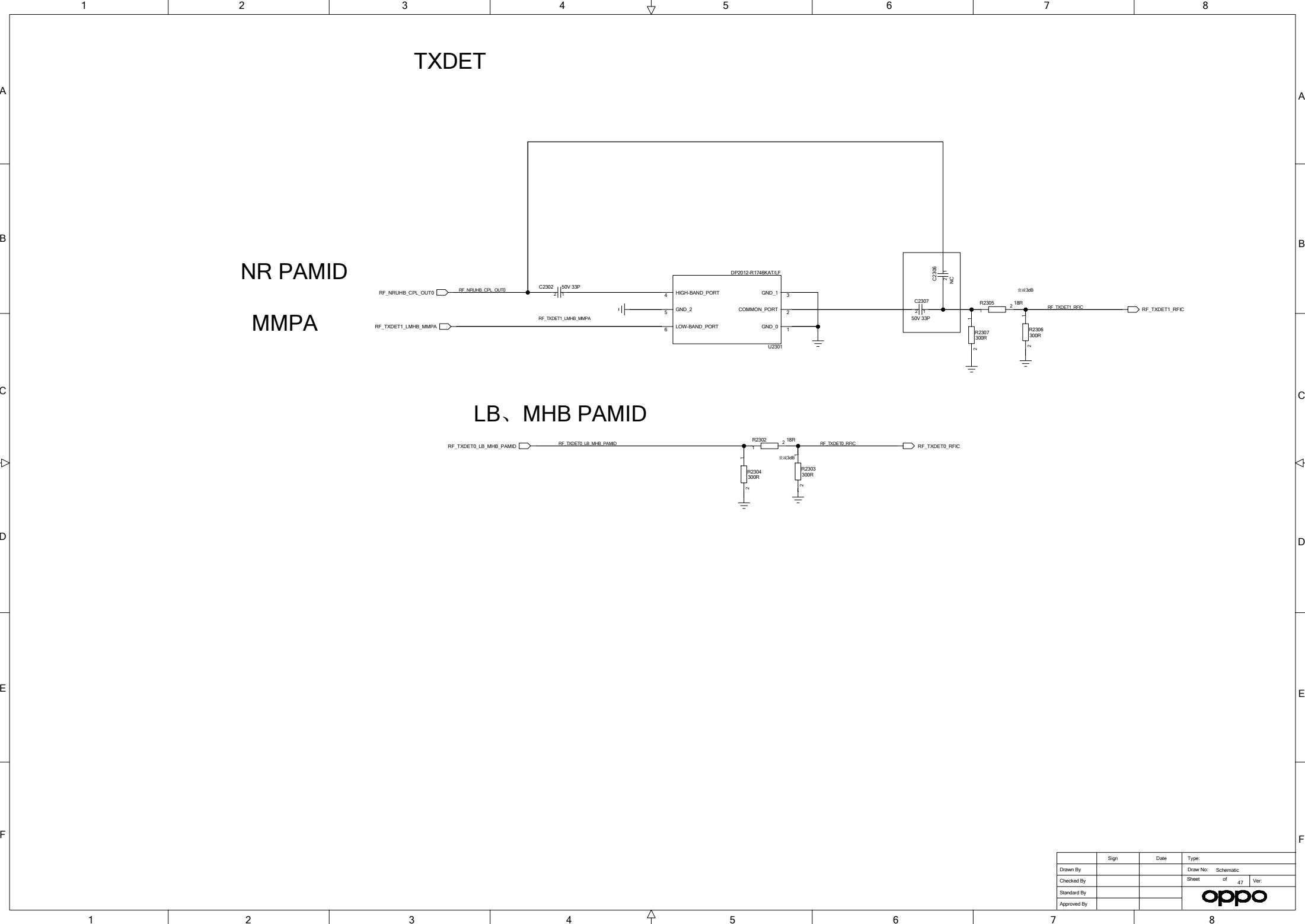
[illegible]

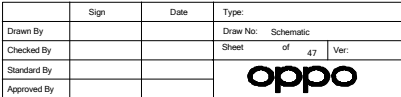
IID1	IID0	USID
GND	GND	0x0A
GND	VIO	0x08
VIO	GND	0x09
VIO	VIO	0x01

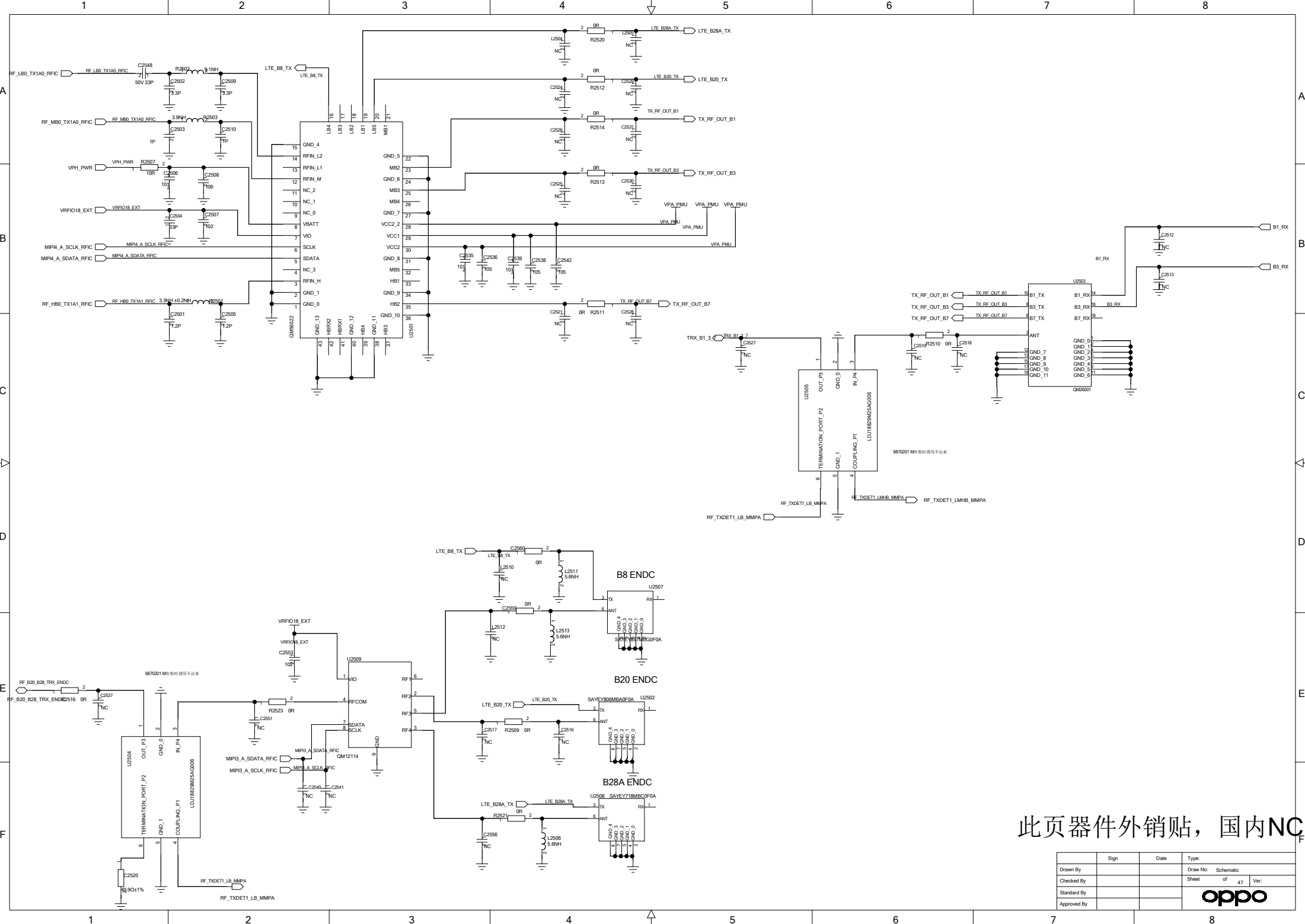
可换SKY53726

ID1	ID0	USID
GND	GND	0x07(NR DR)
GND	VIO	0x08(MLNA)
VIO	GND	0x09
VIO	VIO	0x0A

	Sign	Date	Type:	
Drawn By			Draw No:	Schematic
Checked By			Sheet	of 47 Ver:
Standard By				
Approved By				







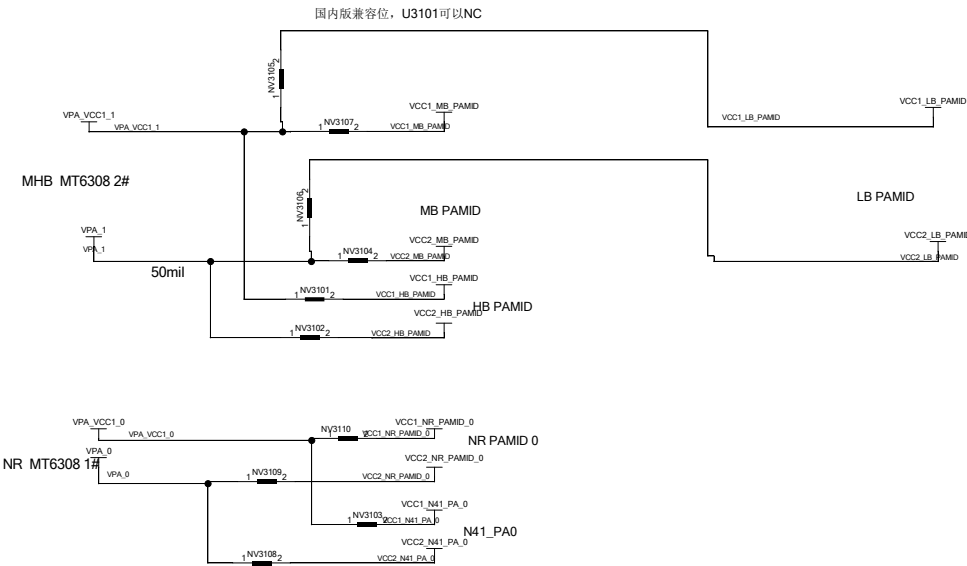
此页器件外销贴，国内NC

Sign	Date	Type
Drawn By		Draw No: Schematic
Checked By		Sheet of 47 Ver:
Standard By		
Approved By		



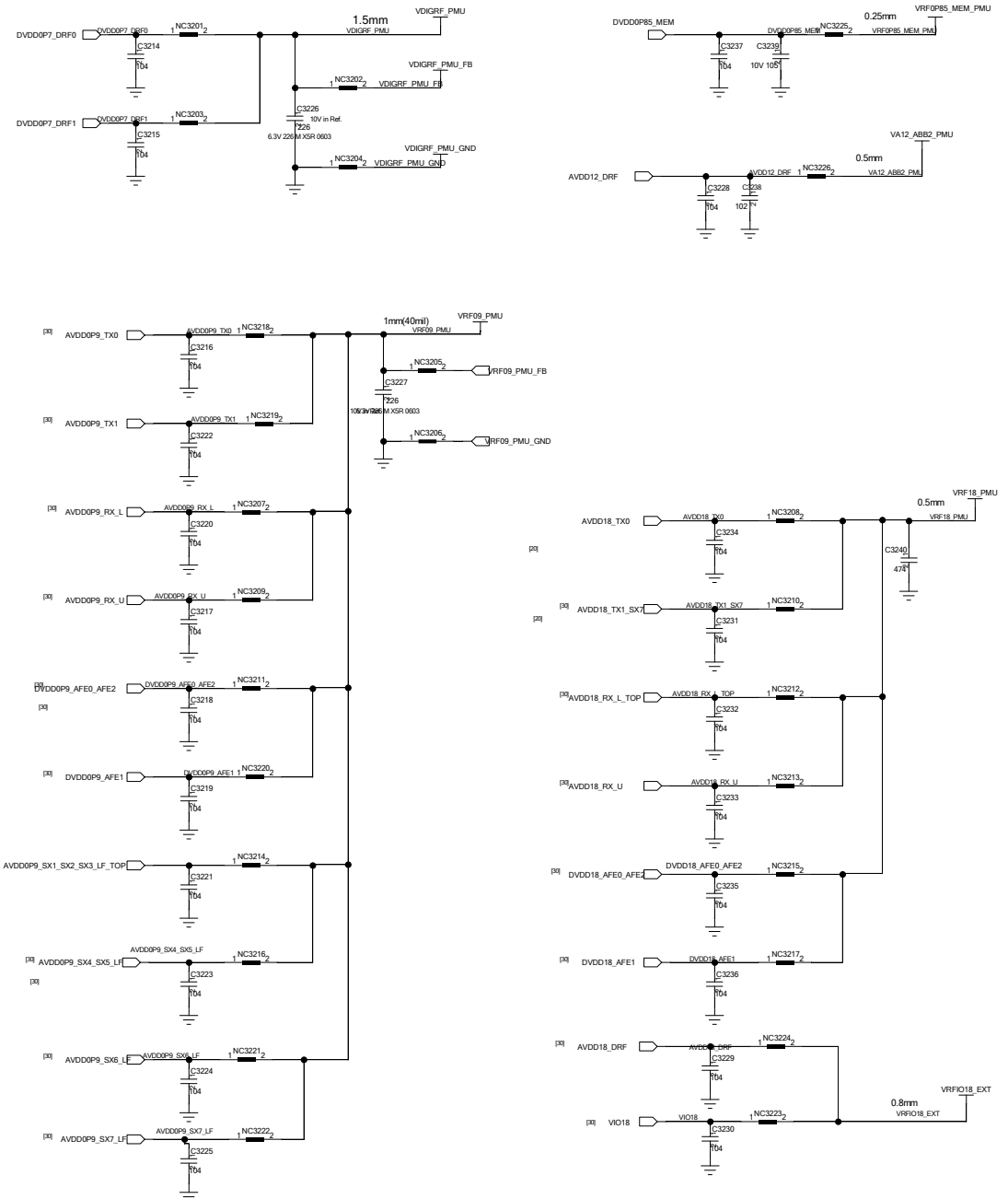
NR_PAMid_n41_SatPA_ET1_NR1/ET Ref design


MT6308 ET供电转接



外销LB PAM只能用APT
APT电路需要MTK检查1102

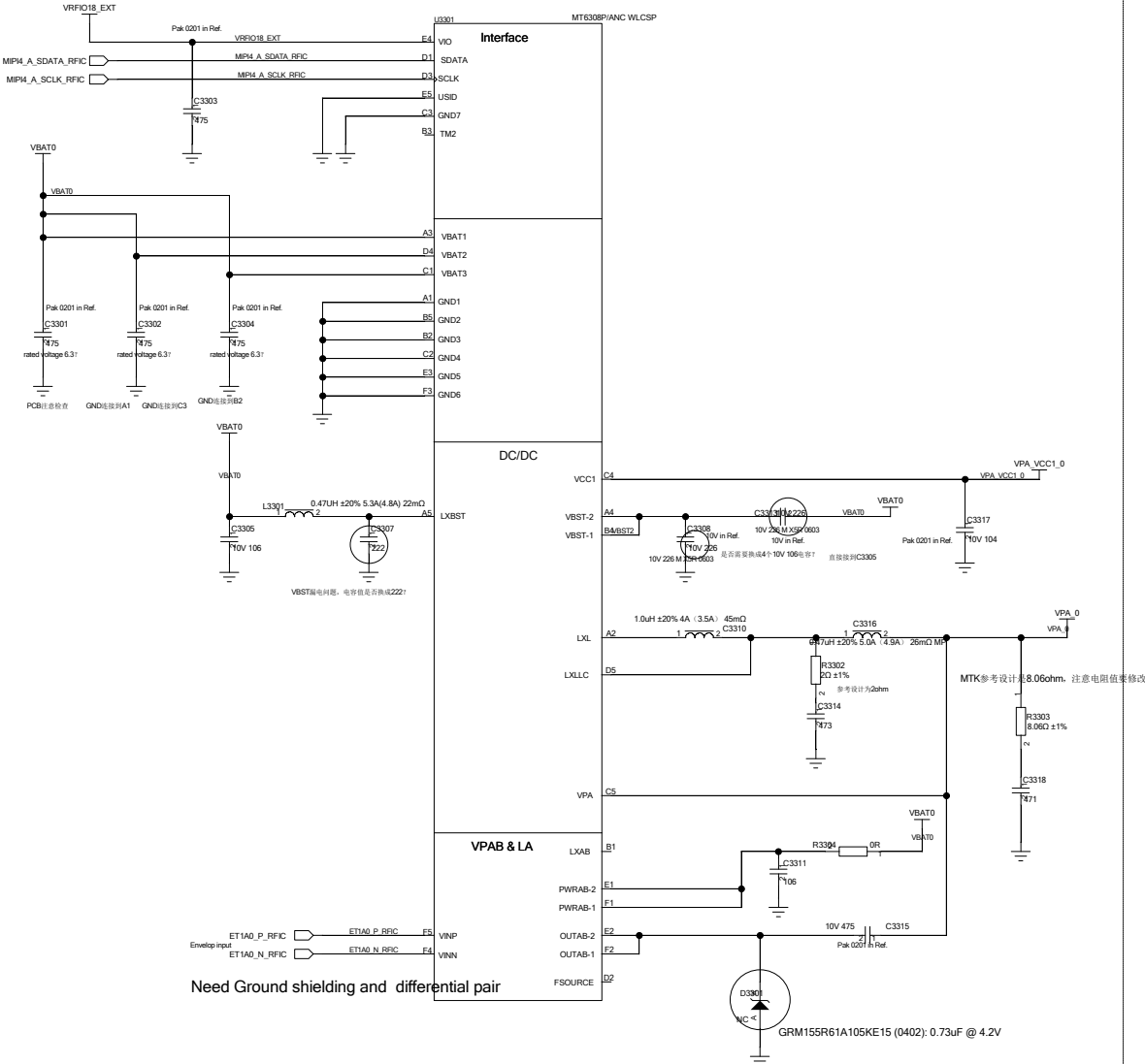
LB/MHB_PAMID_ET MT6308 2#



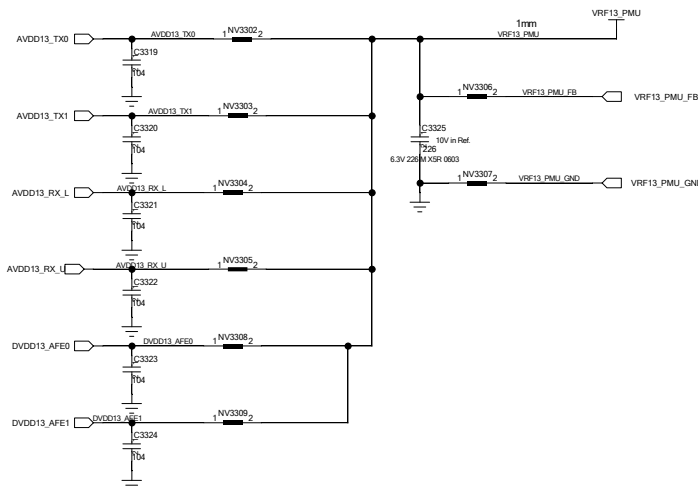
	Sign	Date	Type:	
Drawn By			Draw No:	Schematic
Checked By			Sheet	of 47 Ver:
Standard By				
Approved By				

NR_PAMID + N41 PAD /APT Ref Design

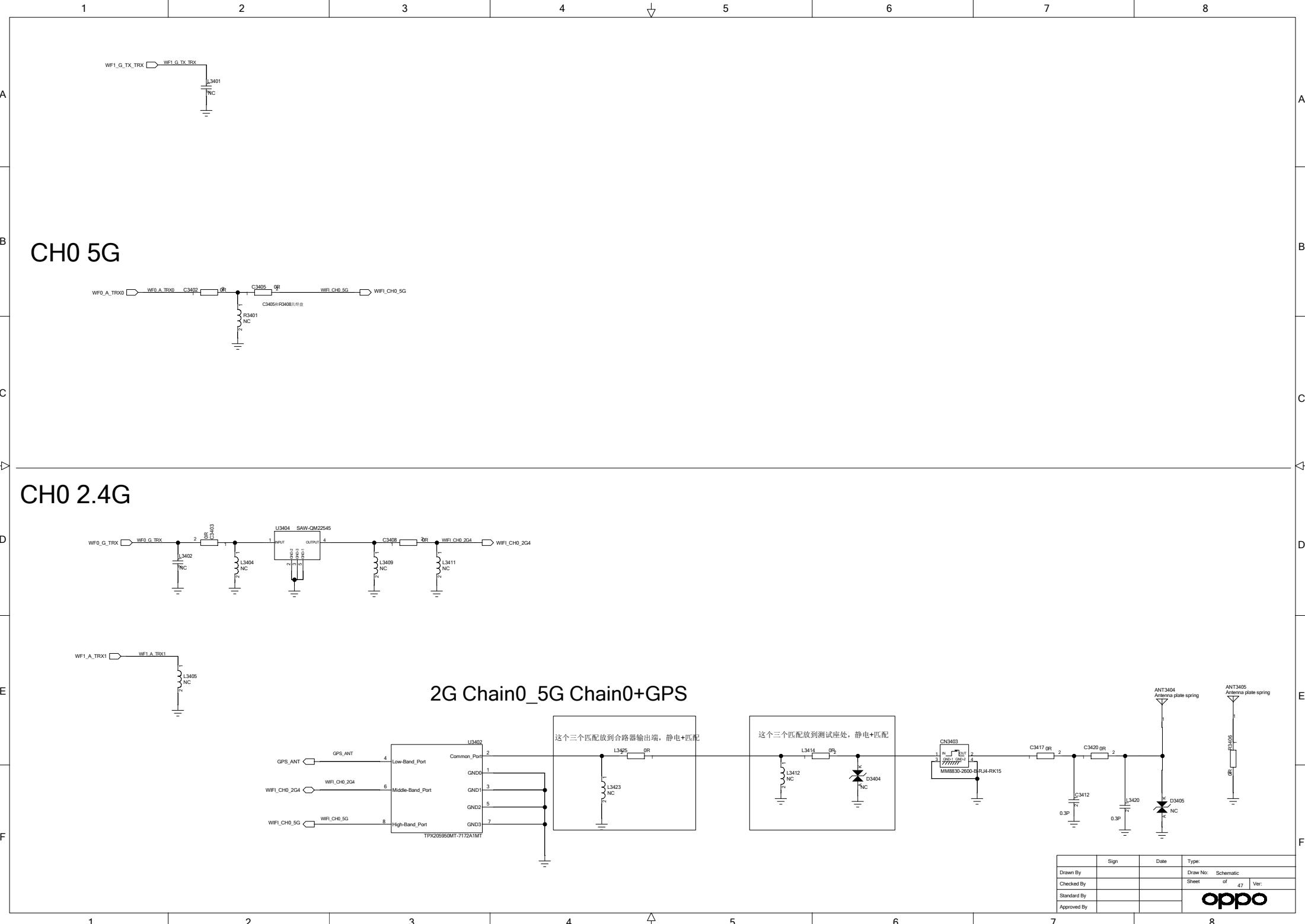
MT6308 1#



MT6190供电TX部分



	Sign	Date	Type:
Drawn By			Draw No: Schematic
Checked By			Sheet of 47 Ver:
Standard By			
Approved By			oppo



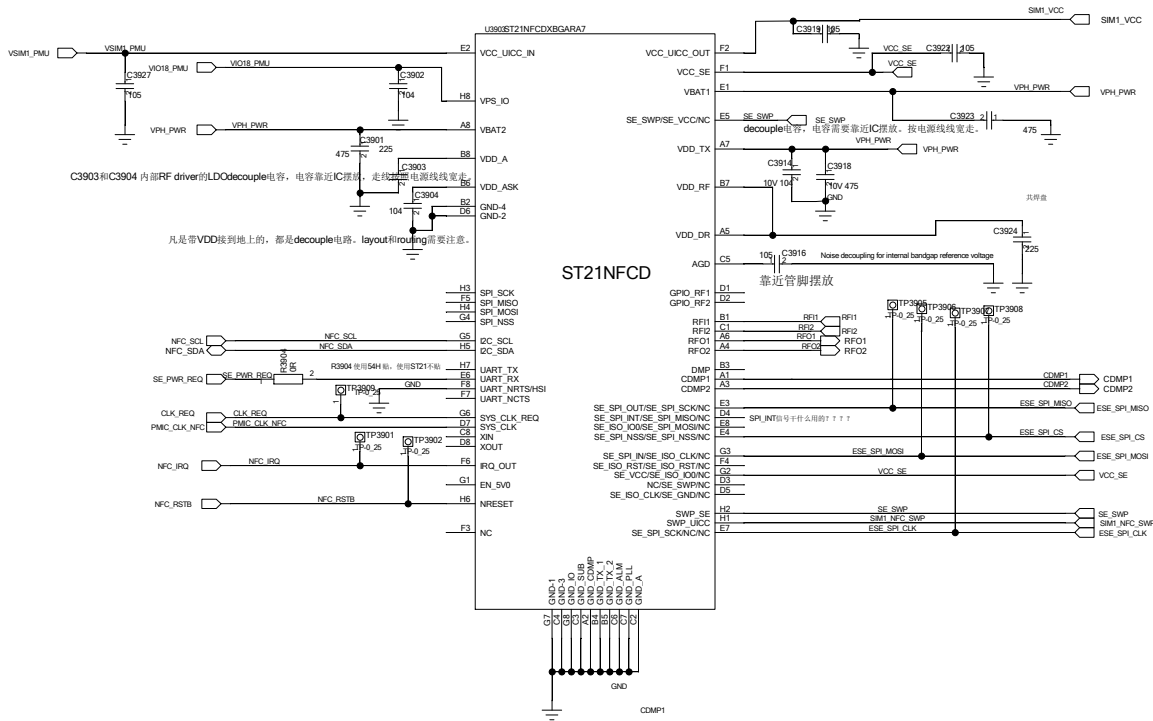
2G Chain0_5G Chain0+GPS

这三个匹配放到合路器输出端，静电+匹配

这三个匹配放到测试座处，静电+匹配

Drawn By	Sign	Date	Type:
Checked By			Draw No: Schematic
Standard By			Sheet of 47 Ver:
Approved By			oppo

NFC

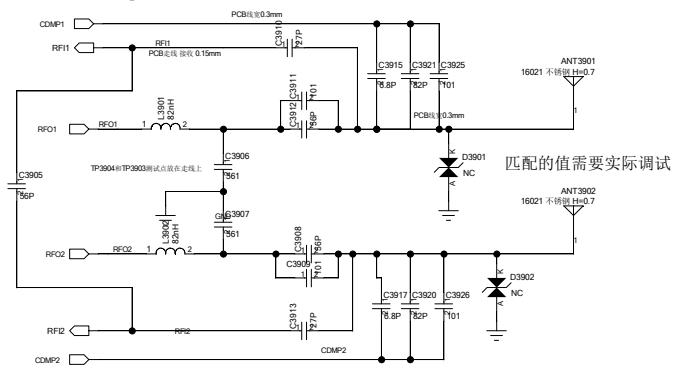


线宽要求:
1. VDO


NFC电源走线线宽要求:

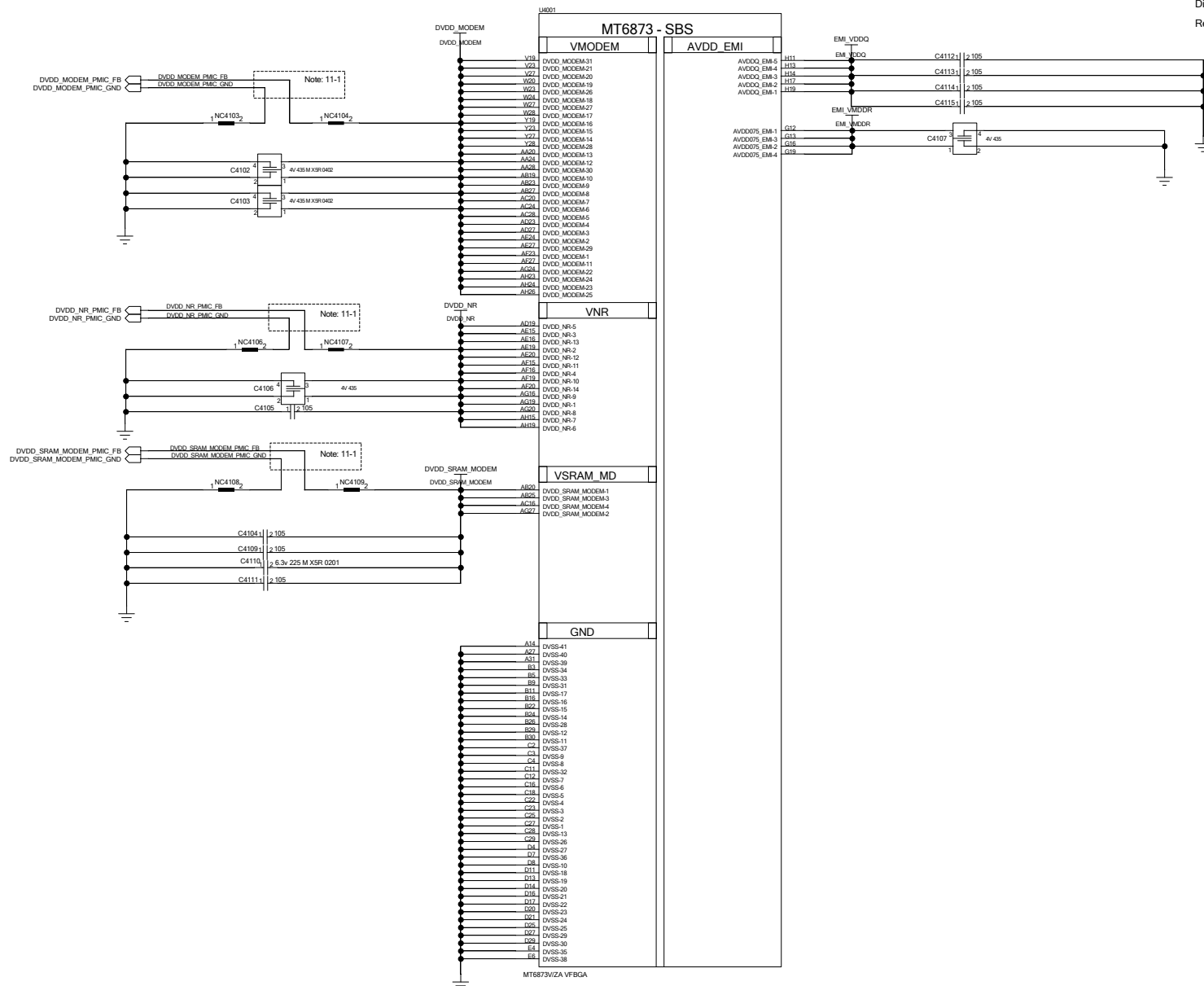
- 1、到VDD_TX的VPH_PWR走线宽度要达到0.3mm以上；
- 2、VPH_PWR到VBAT1&VBAT2 要到到0.15mm，然后分到两个电源管脚分别为0.1mm。

为确保BB IO口和NFC IC IO口上电的一时序的一致，VPS_IO需要接到系统BB的GPIO口电源上。




1. **VBAT1+VBAT2** 消耗最大200mA, **VBAT2**是给芯片内部（除射频发射部分）的模拟电路供电, **Vbat1**给其它片上资源供电（如CPU, Flash, 逻辑电路）。考虑到浪涌的因素**ST54H**的**Vbat**建议接到系统的**VPH_PWR**上面, 不建议接到电池的**VBAT**上面。
2. **eSE**模块消耗最大30mA, 正常工作情况下, 实际消耗的电流根据当时**eSE**的工作状态大概在 100uA 20mA 之间。
3. **VDD_RF**, 是载波的功率源。
4. **VDD_ASK**, 调制的功率源, 是调制信号的驱动。
5. **VDD_A**, 给芯片内部的模拟电路供电, 比如小信号处理, 内部滤波器, 运放等。
6. **R4620**建议先按照**NC**调试, 若是调试结果需要使用电阻, 一定要计算流过的电流, 避免电流过大烧电阻

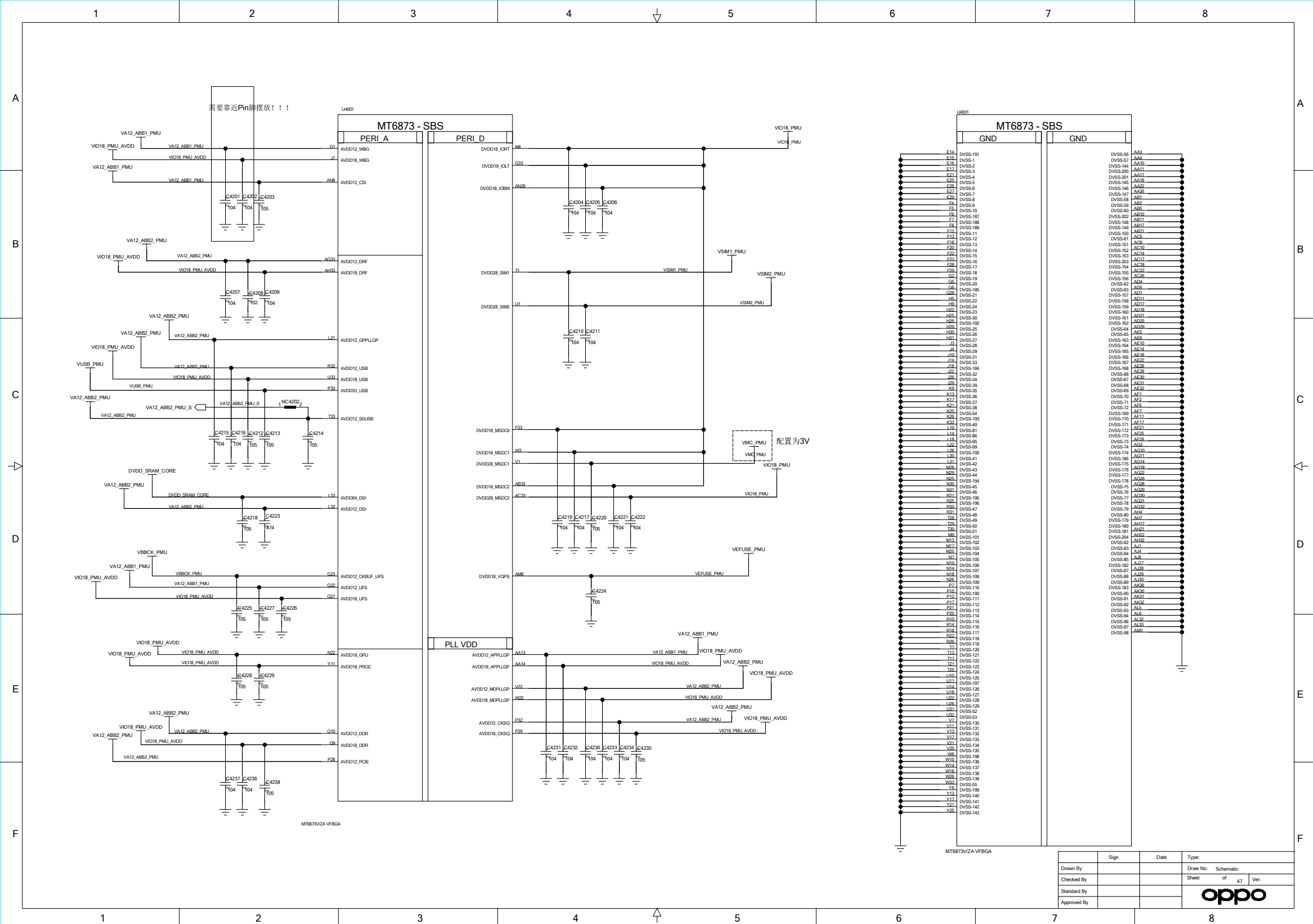
	Sign	Date	Type:	
Drawn By			Draw No: Schematic	
Checked By			Sheet of 47	Ver:
Standard By				
Approved By				



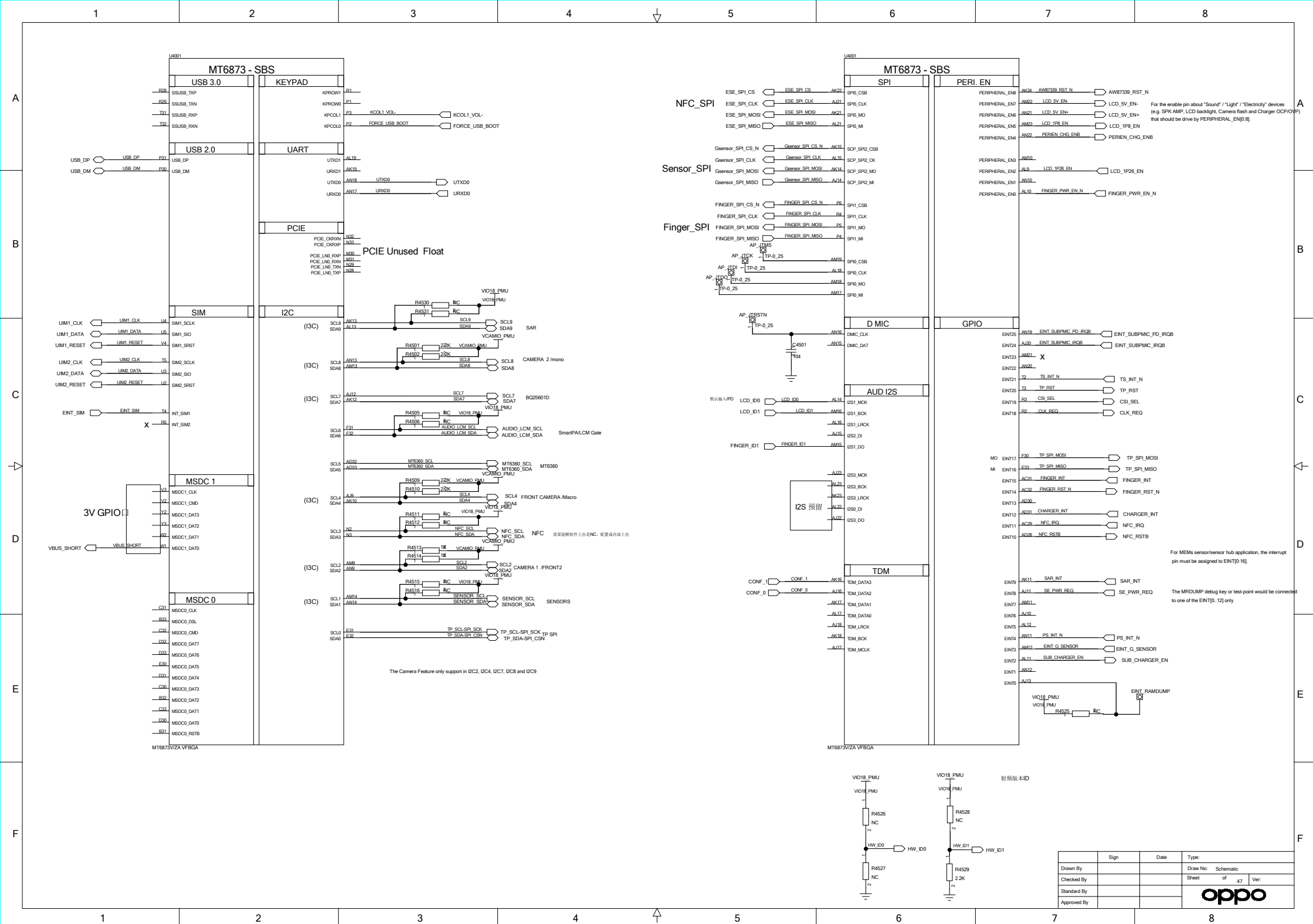
Schematic design notice of "11_BB_POWER_PDN" page.

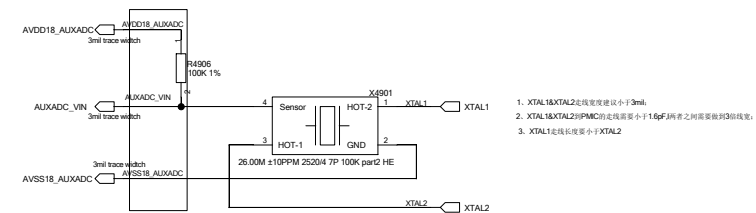
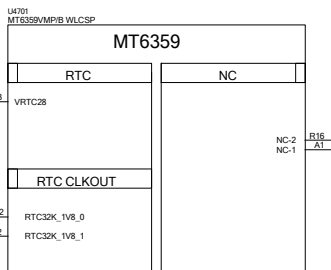
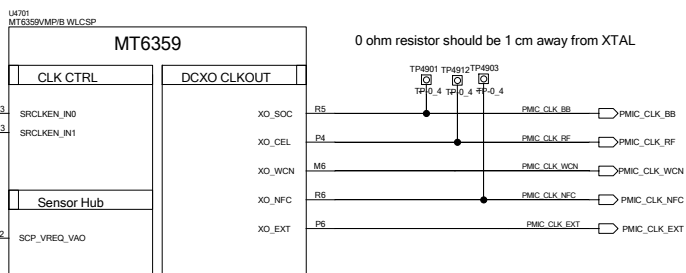
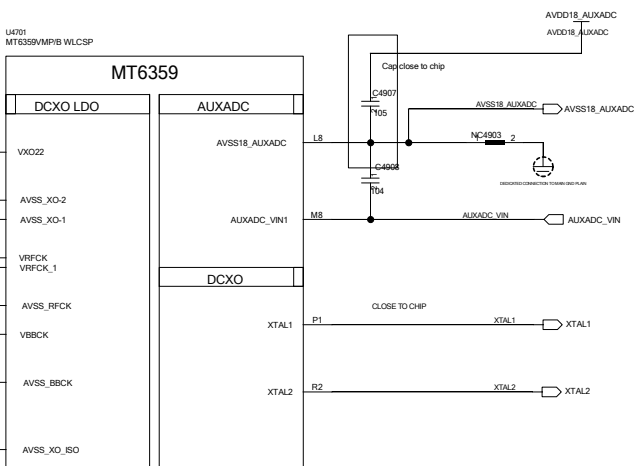
Note 11-1: Differential pair of PMIC remote sense must be close to BB's ball.
Remote sense trace with GND shielding to PMIC (Differential)

	Sign	Date	Type:	
Drawn By			Draw No:	Schematic
Checked By			Sheet	of 47 Ver:
Standard By				
Approved By				

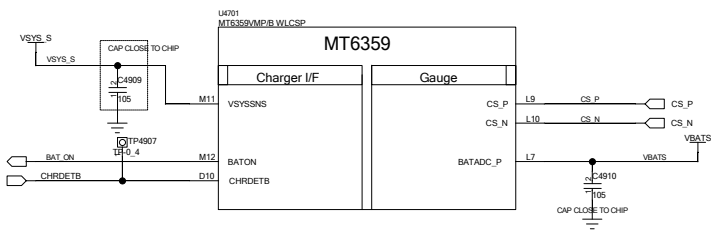
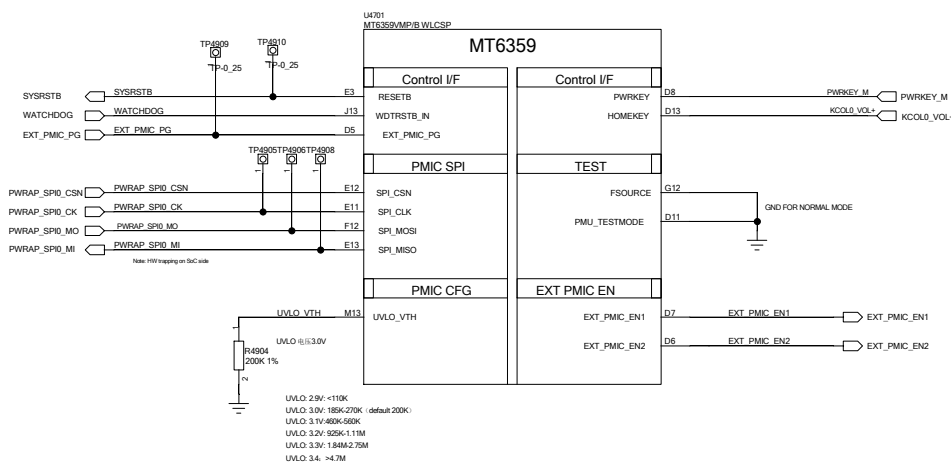







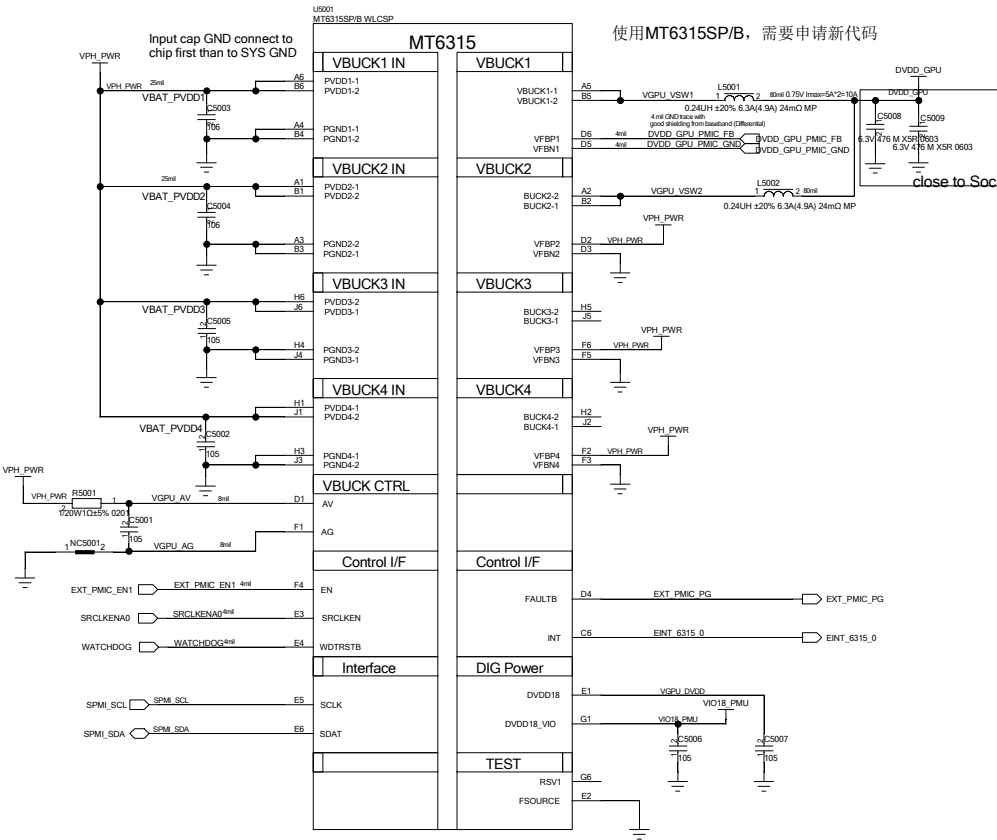


Route AVDD18_AUXADC/AUXADC_VIN (3 mil each) with well GND shielding

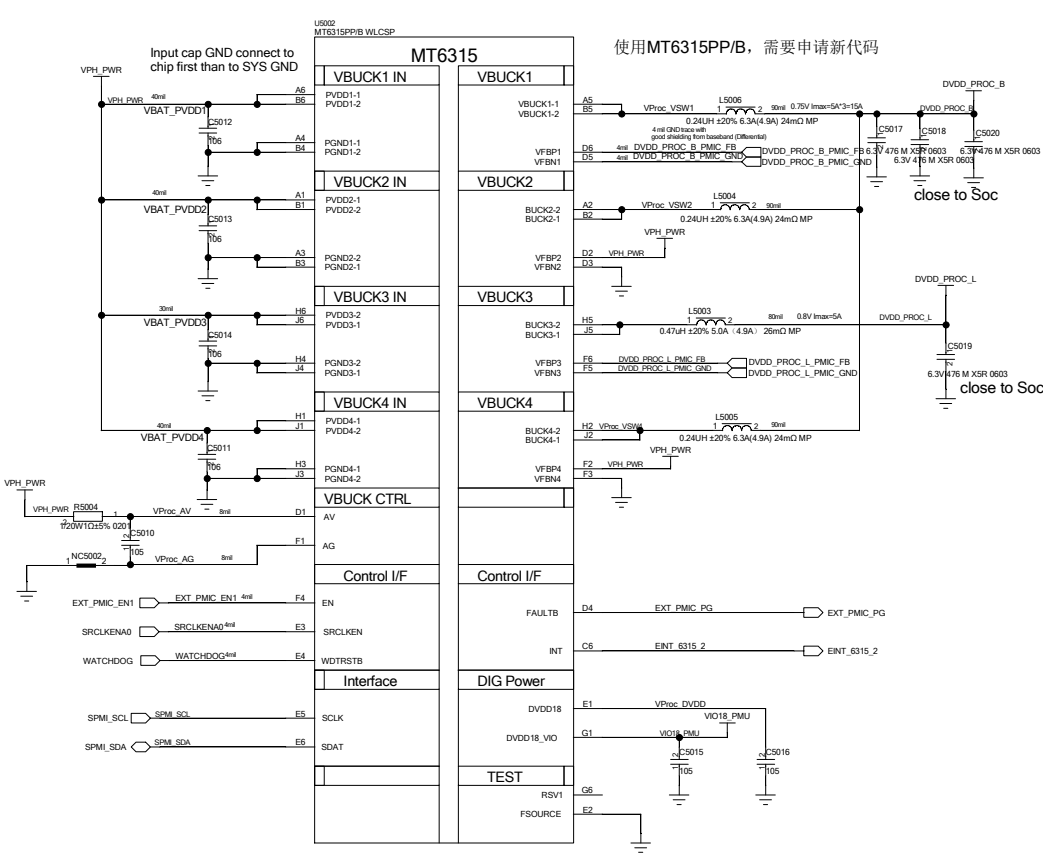


	Sign	Date	Type:	
Drawn By			Draw No: Schematic	
Checked By			Sheet of 47	Ver:
Standard By				
Approved By				

MT6315 4-Phase Buck (GPU)

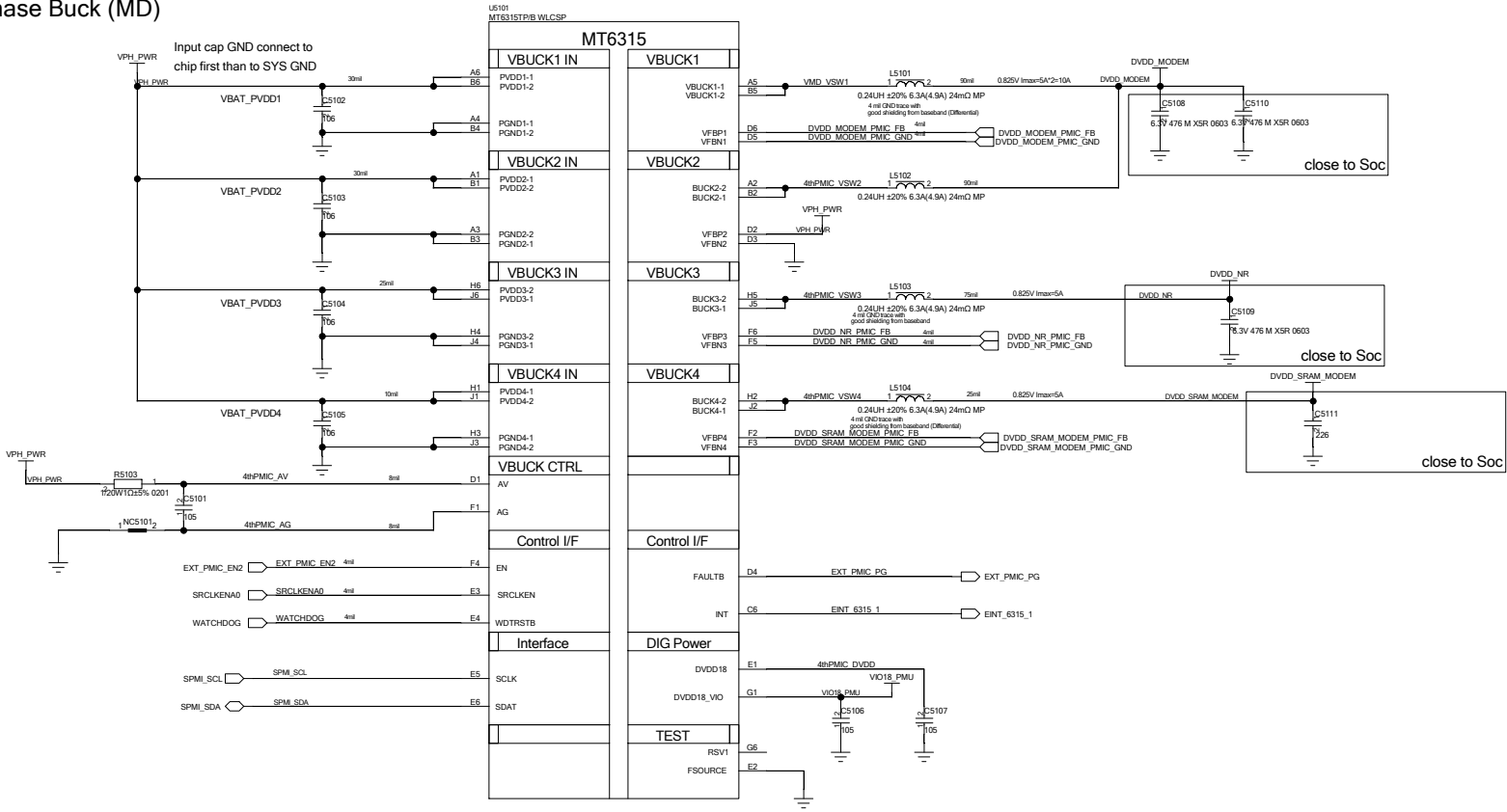


MT6315 4-Phase Buck (CPU)

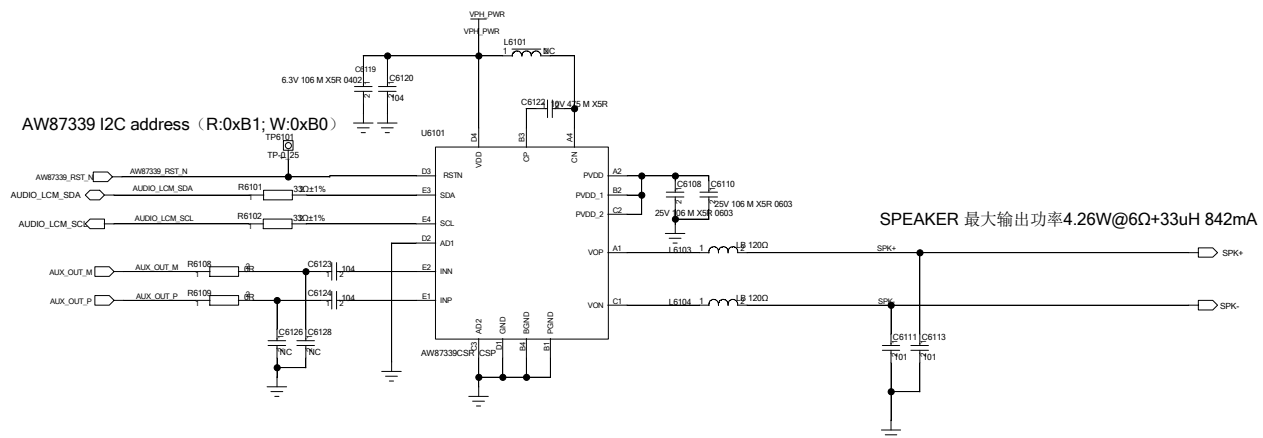


MT6315 4-Phase Buck (MD)

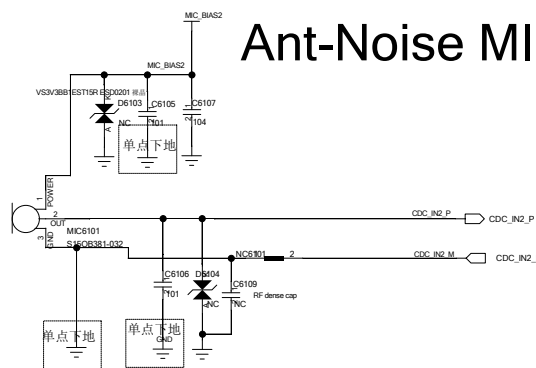
使用MT6315TP/B, 需要申请新代码



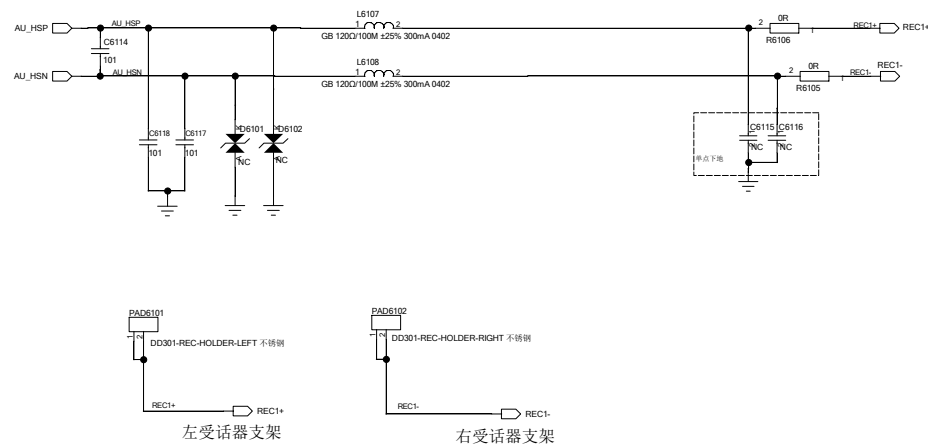
AUDIO PA




Ant-Noise MIC

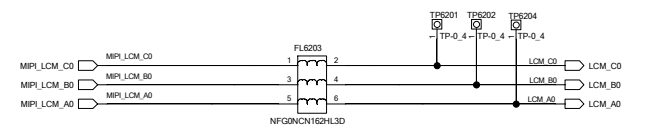
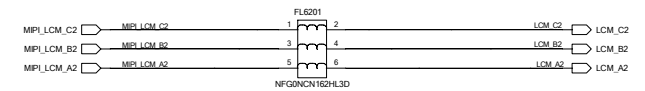
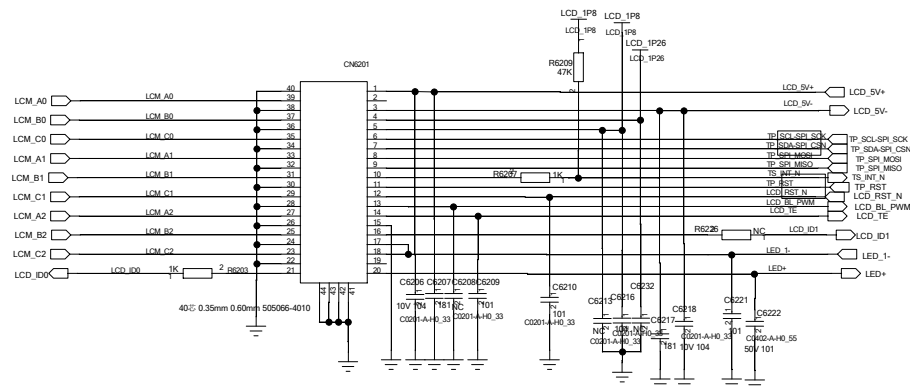


Receiver



	Sign	Date	Type:		
Drawn By			Draw No: Schematic		
Checked By			Sheet	of 47	Ver:
Standard By					
Approved By					

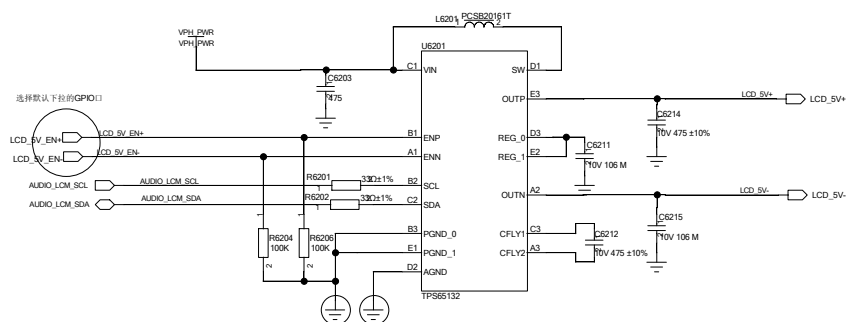
DSI CMF



0欧与共模滤波器，DVT改为不兼容设计

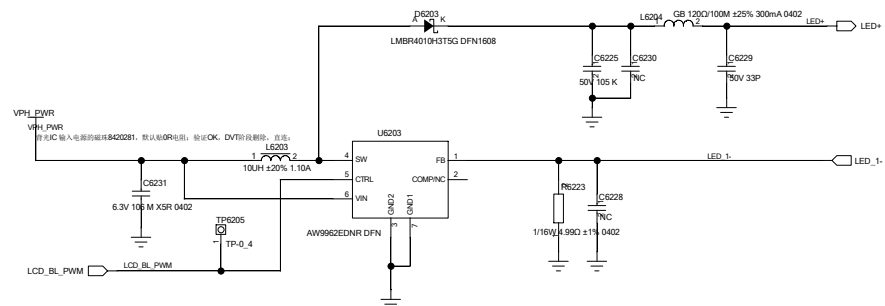
裸晶IC，注意布局位置应力

裸晶IC，注意布局位置应力

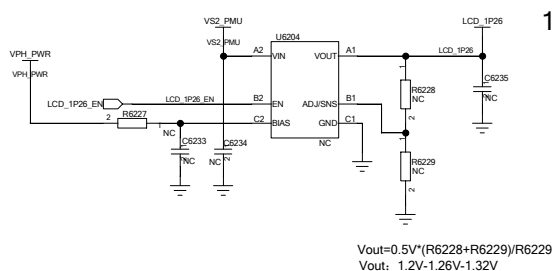
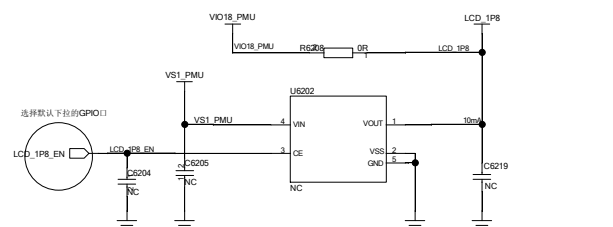


电源地模拟地分开下主地


AW9962 I2C address (R:0x6D; W:0x6C)



1.26V

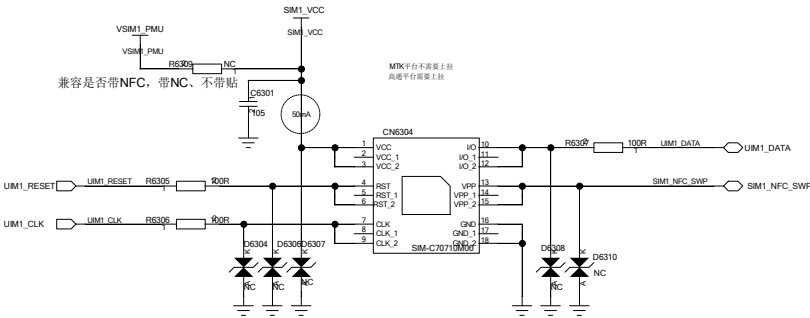

$$V_{out} = 0.5V \cdot (R_{6228} + R_{6229}) / R_{6229}$$

$$V_{out}: 1.2V - 1.26V - 1.32V$$

	Sign	Date	Type:
Drawn By			Draw No: Schematic
Checked By			Sheet of 47 Ver:
Standard By			
Approved By			

Plug Detect

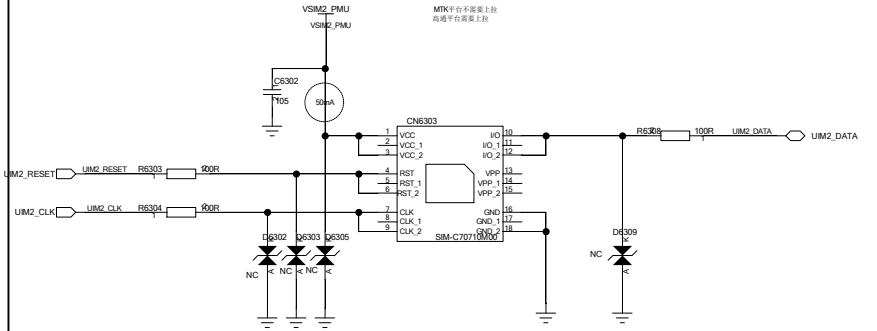
SIM1 Card



Hot plug

SIM2 Card

靠外侧SIM卡

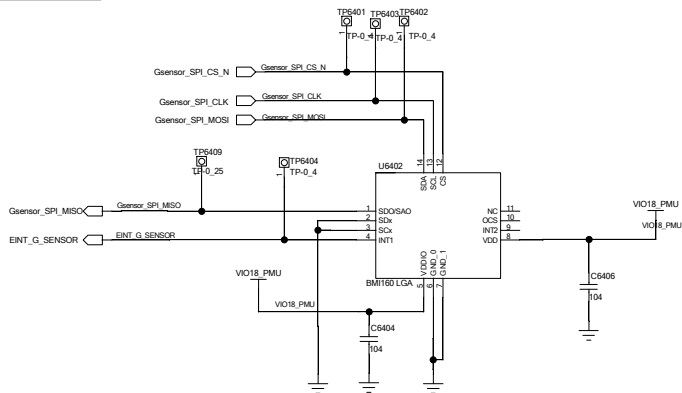


方案只支持单SIM卡 NFC

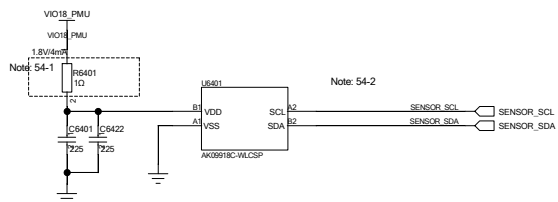
	Sign	Date	Type:
Drawn By			Draw No: Schematic
Checked By			Sheet 15 of 32 Ver:
Standard By			
Approved By			



1	G-Sensor/Gyo
---	--------------



Magnetic Sensor



Note 54-1: 1.8V与VDD之间串接1欧电阻，减小MMC5603做reset时对LDO带来的纹波；

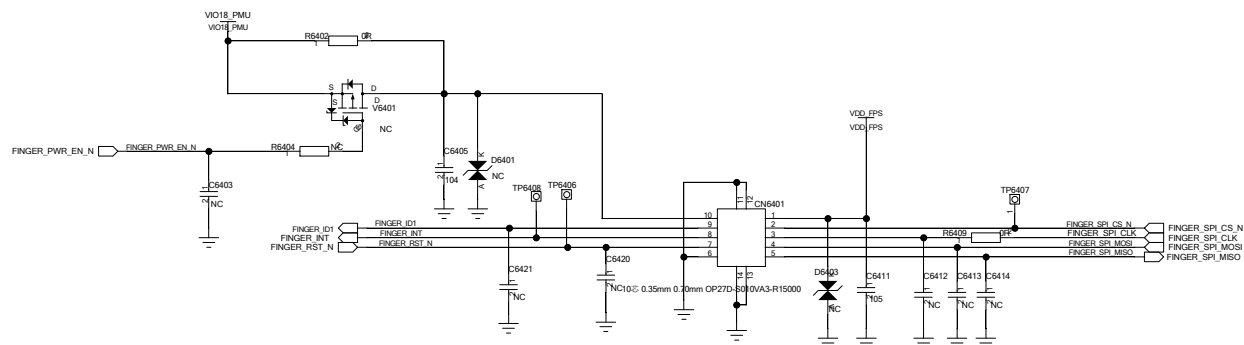
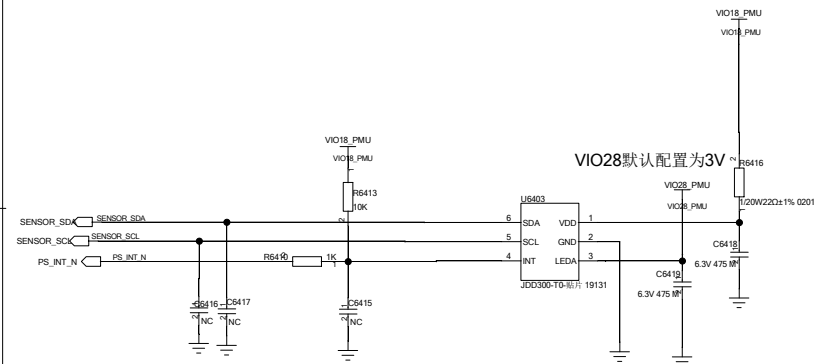
Note 54-2: I2C串联0欧姆电阻作为开发初期防止高速信号干扰预留位，后期无问题可以去掉：


M-sensor	Read	Write
AK09918	0X19	0X18
MMC5603NJ	0X61	0X60

P.S sensor module

A.L.S. sensor1

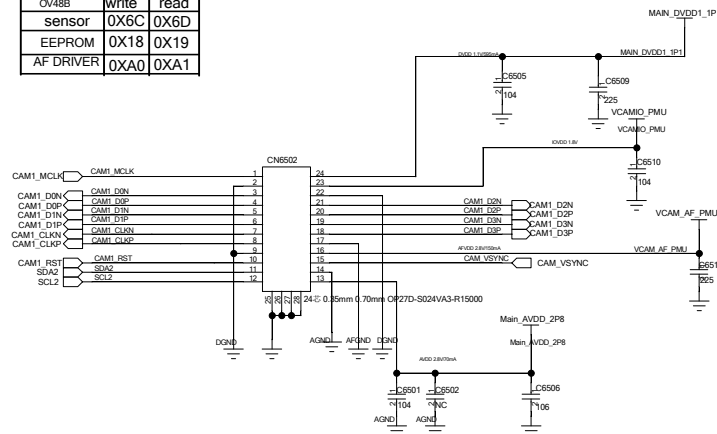
G-sensor	Read	Write
STK33502	0X8C	0X8D



	Sign	Date	Type:	
Drawn By	Elvis	2014/01/15	Draw No: Schematic	
Checked By			Sheet ¹⁴ of ³²	Ver:
Standard By				
Approved By				

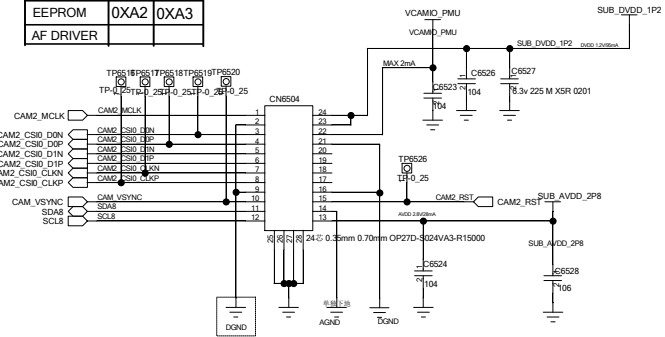
Main CAMERA

OV48B	write	read
sensor	0X6C	0X6D
EEPROM	0X18	0X19
AF DRIVER	0XA0	0XA1



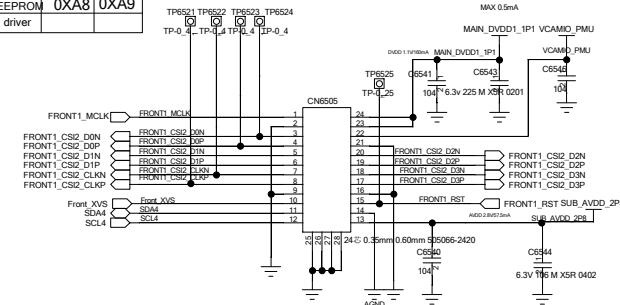
Sub_Rear_Camera

GC8054	write	read
sensor	0X20	0X21
EEPROM	0XA2	0XA3
AF DRIVER		



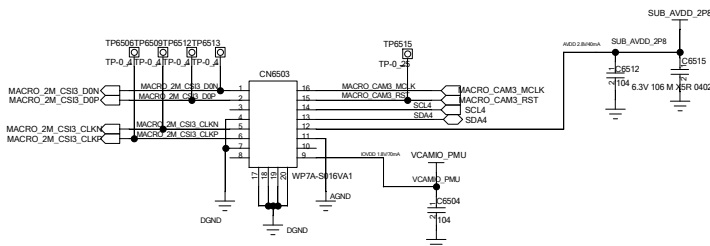
Front1_Camera

SSK3P9SP	write	read
sensor	0X20	0X21
EEPROM	0XA8	0XA9
driver		



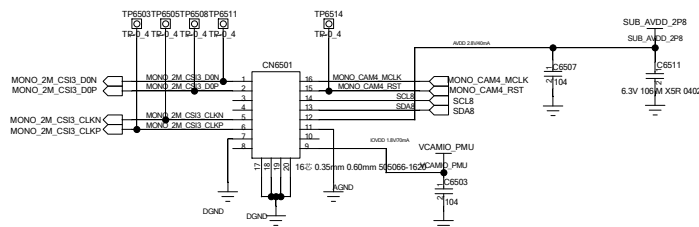
MACRO_Camera

GC02K0B	write	read
sensor	0X6E	0X6F
EEPROM		
driver		

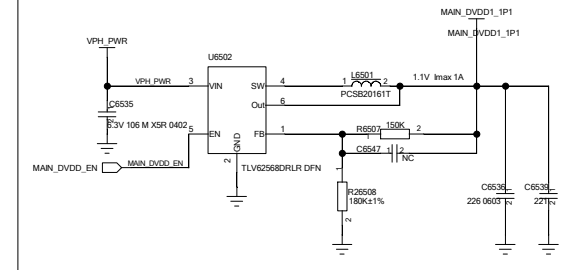


Mono_Camera

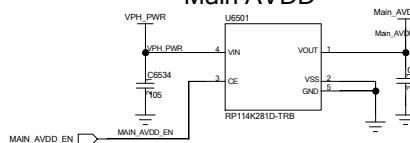
GC02M0B	write	read
sensor	0X6E	0X6F
EEPROM		
driver		



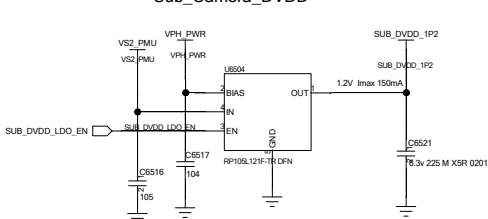
Main_Camera_DVDD(1.1V)



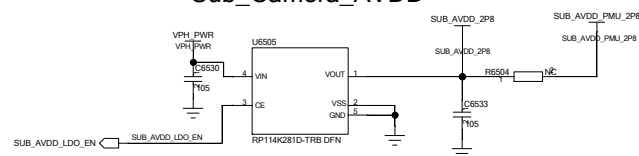
Main AVDD



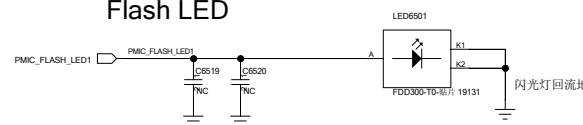
Sub_Camera_DVDD



Sub_Camera_AVDD



Flash LED



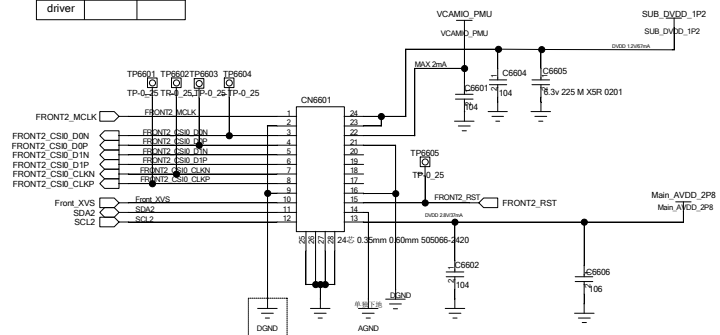
装饰件接地弹片

Sign	Date	Type
Drawn By		Draw No: A2SHEET
Checked By		Sheet of 2 Ver:
Standard By		
Approved By		

oppo

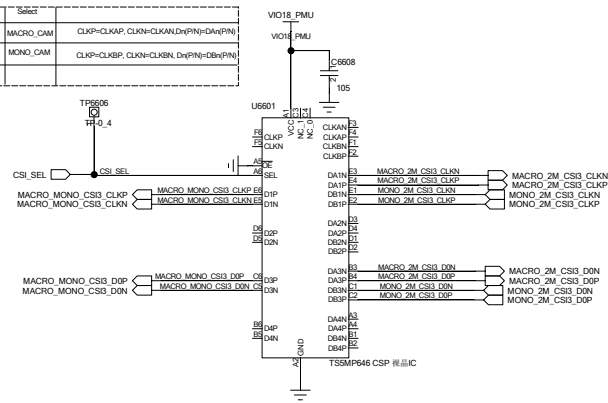
FRONT2_Camera

GC02M0B	write	read
sensor	0X20	0X21
EEPROM	0XA3	0XA2
driver		

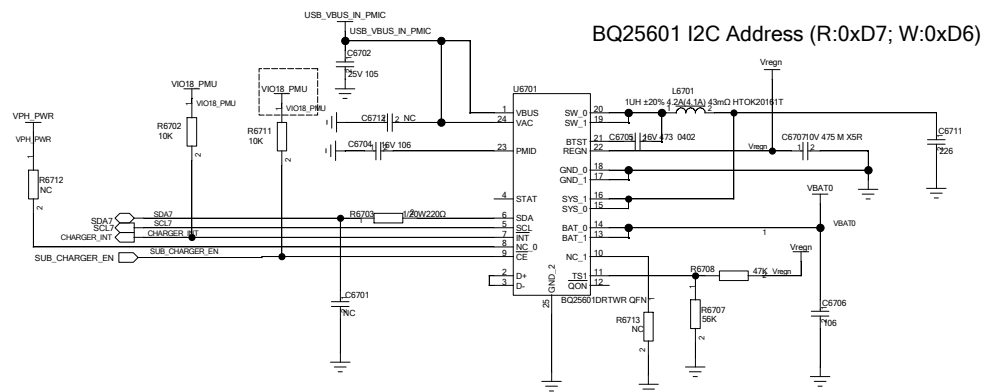


CAM switch1

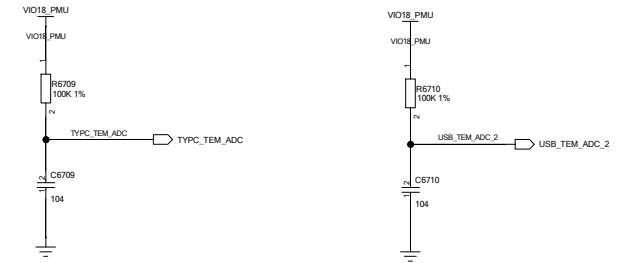
SEL	SW	SW
LOW	MACRO_CAM	CLKP=CLKAP, CLKIN=CLKAND(PIN)=DIN(PIN)
HIGH	MONO_CAM	CLKP=CLKBP, CLKIN=CLKBN, Dn(PIN)=Dn(PIN)




Fast Charge

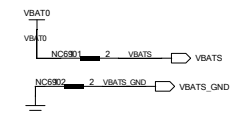
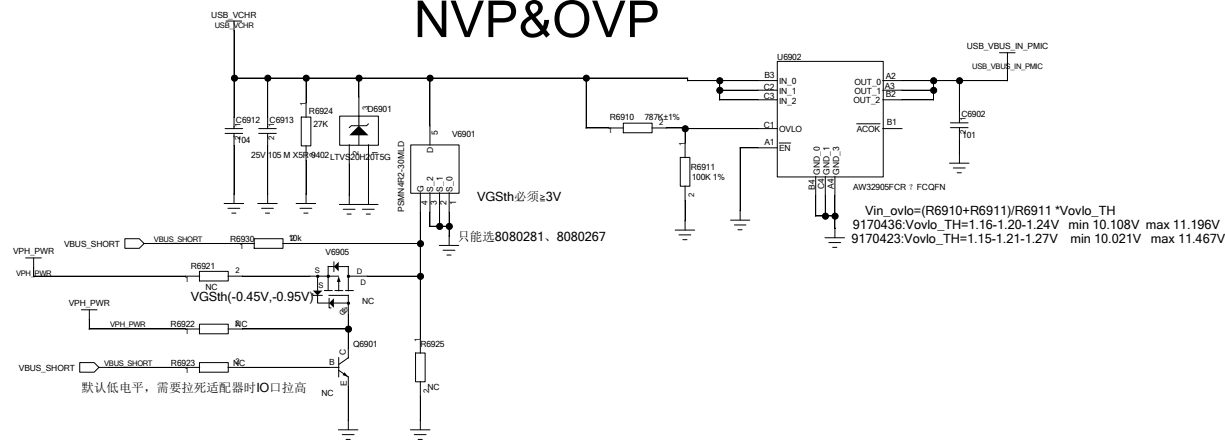


Thermistor

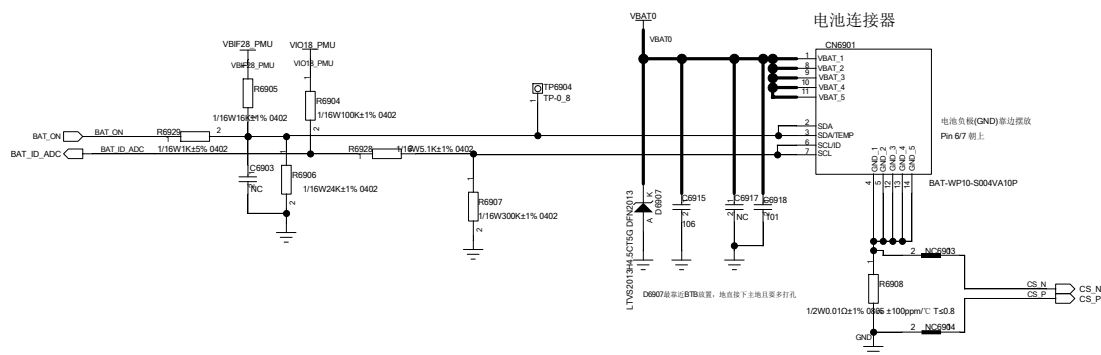


	Sign	Date	Type:	
Drawn By			Draw No: Schematic	
Checked By			Sheet of 47	Ver:
Standard By				
Approved By				

NVP&OVP



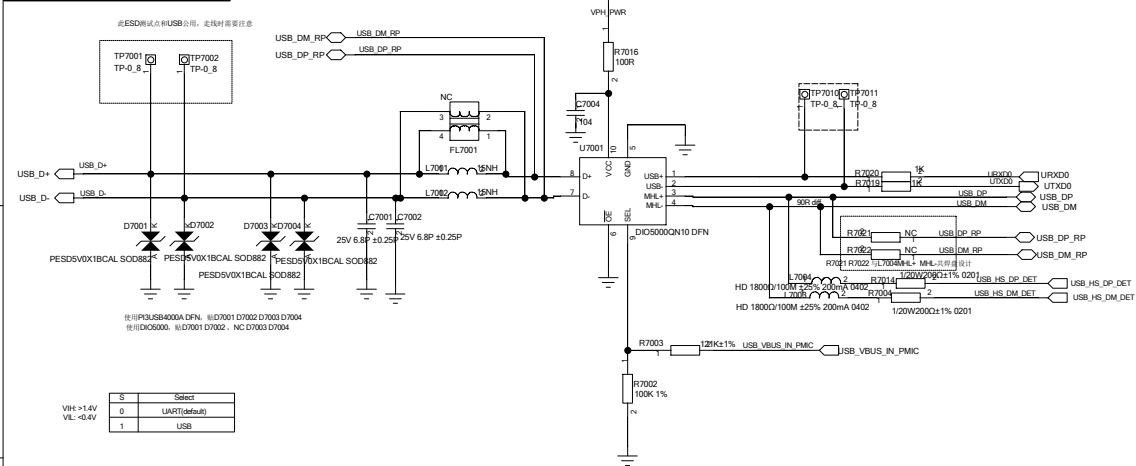
Battery Connector



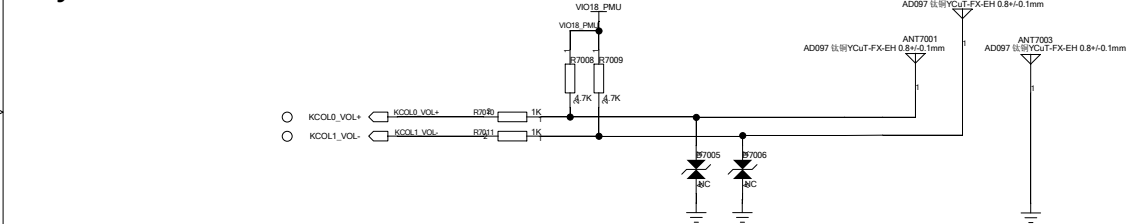
	Sign	Date	Type:
Drawn By			Draw No: Schematic
Checked By			Sheet of 47 Ver:
Standard By			
Approved By			

oppo

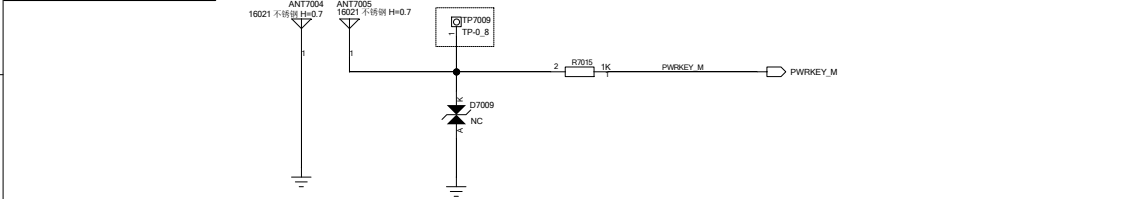
USB Switch



Key of Volume + -

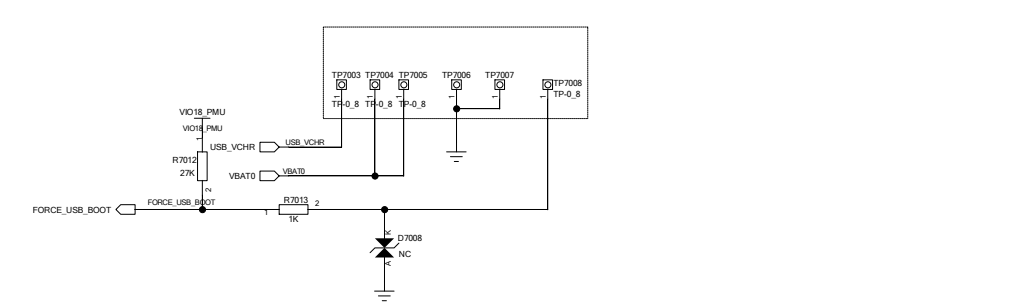


Power Key

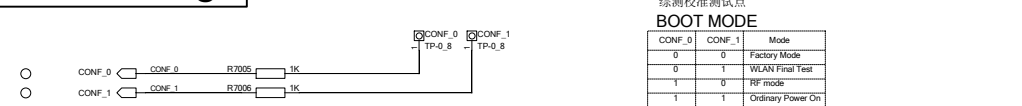


JTAG

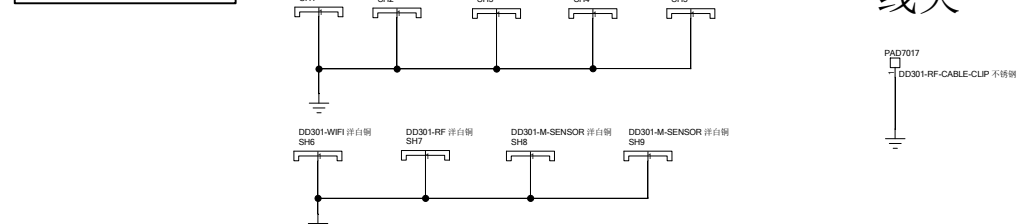
Test Point



Test Confige



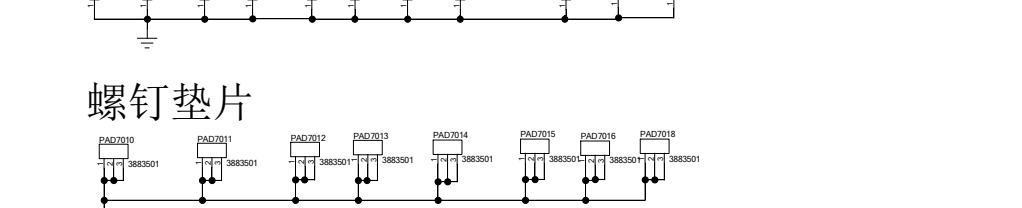
Screw Hole




Mark Point DBII.1

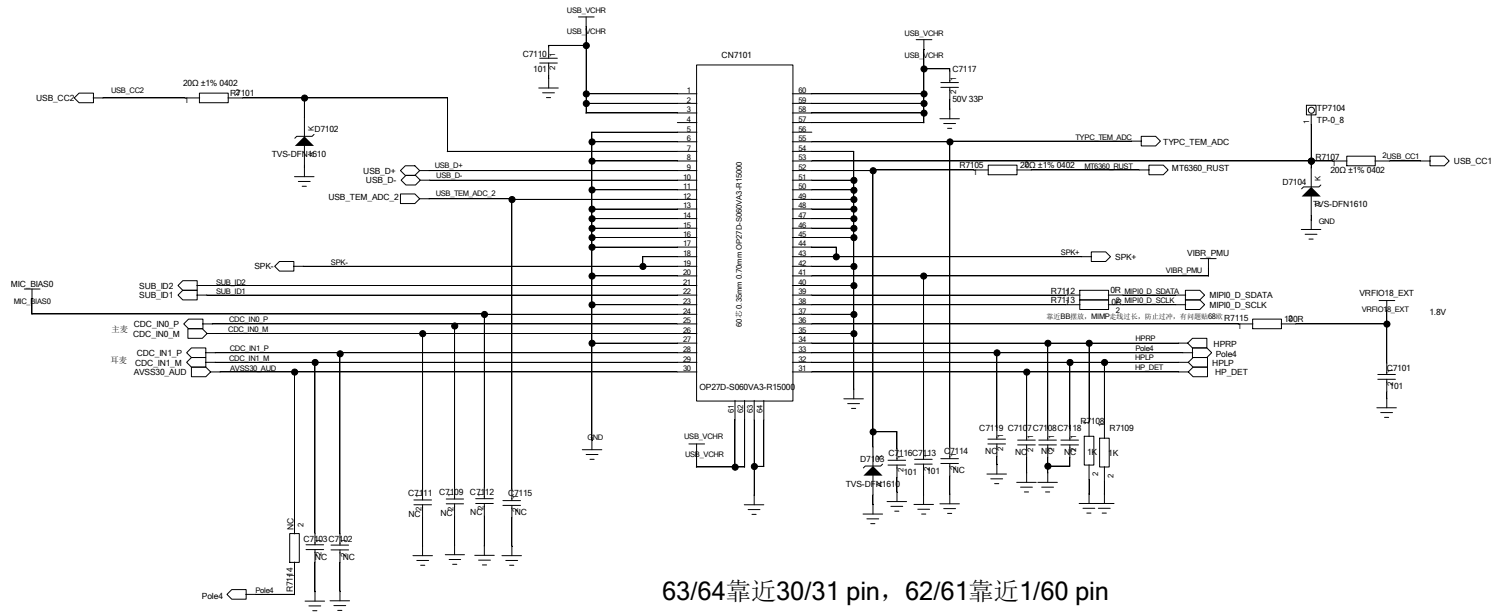


DRILL7001 DRILL7002 DRILL7003 DRILL7004




	Sign	Date	Type:	
Drawn By			Draw No: Schematic	
Checked By			Sheet of 47	Ver:
Standard By				
Approved By				

BTB TO UBoard & ABoard



63/64靠近30/31 pin, 62/61靠近1/60 pin

	Sign	Date	Type:	
Drawn By			Draw No: Schematic	
Checked By			Sheet of 47	Ver:
Standard By				
Approved By				

