


NV11, 4MX16 SDR SDRAM-6ns, RGB, AGP4X, TV DOWN.

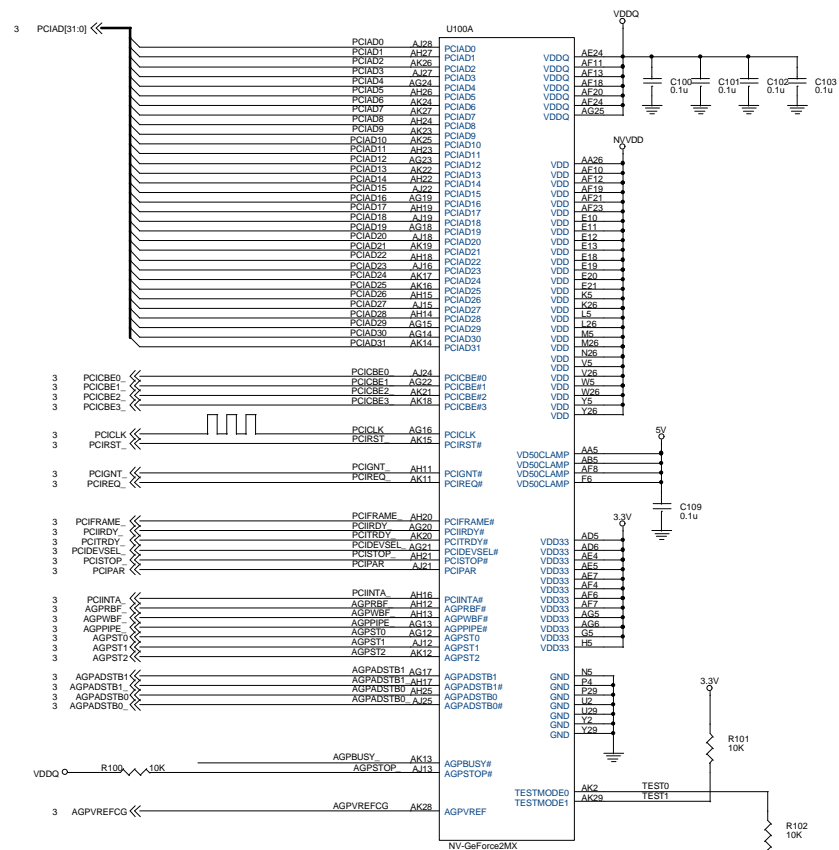
PCI DEVICE ID 0X0=0X110 FOR NV11.

STRAPS: AGP4X, FAST WRITE ENA, ADP BIOS, NORMAL PCIAD, 14.318MHZ  
STRAP\_RAM\_TYPE [5:2]=0001-4MX16 SDR SDRAM

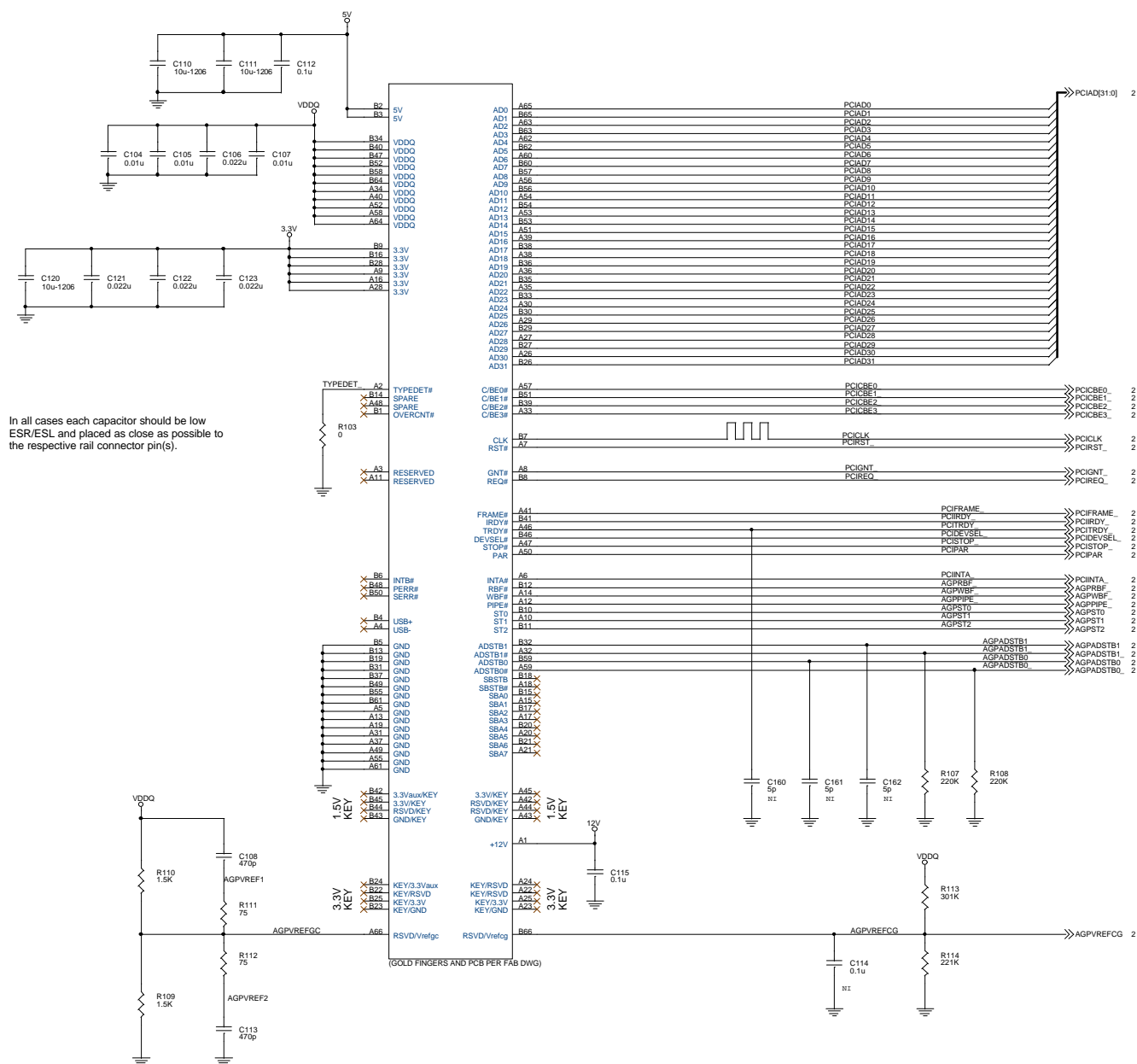
HISTORY REV. A00 BASED ON P39-C00:  
HISTORY REV. A01: NO netlist change;redo delay for  
FB\*CLK\*

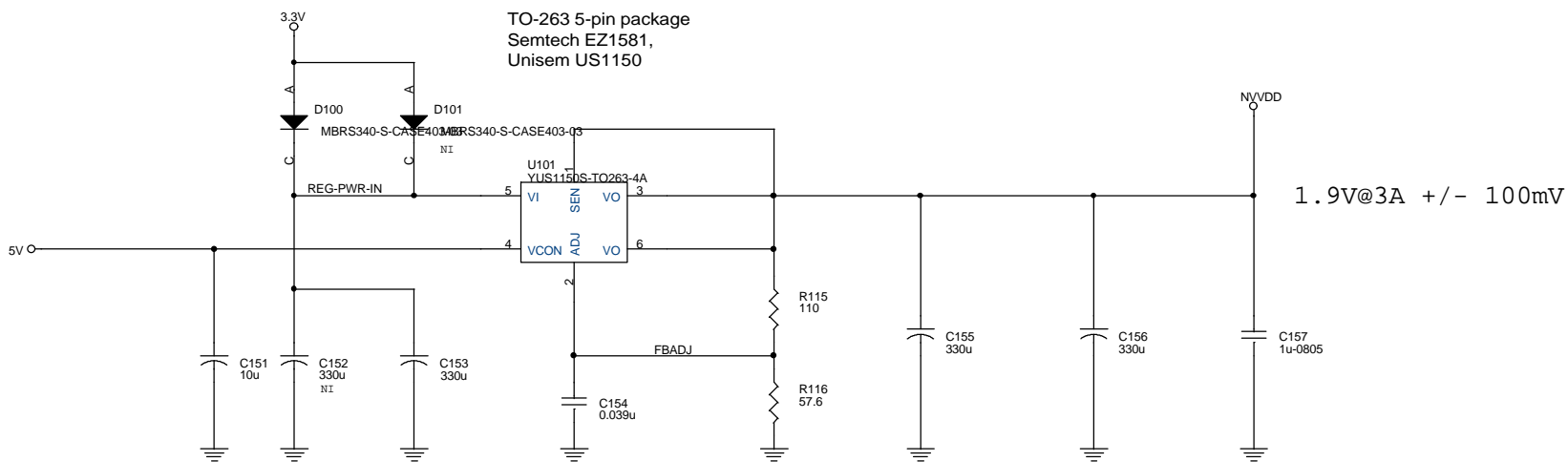
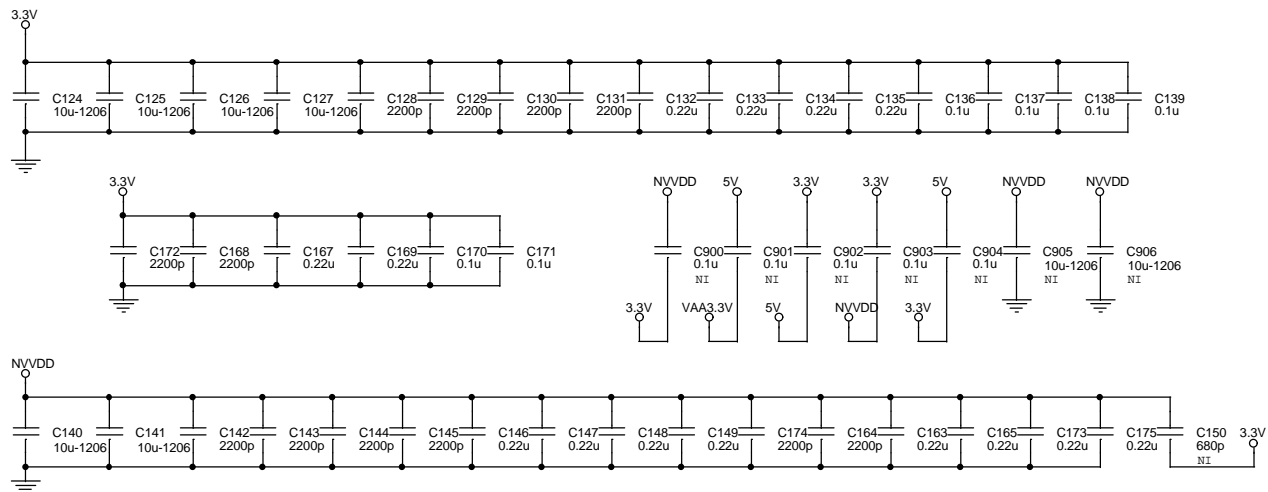


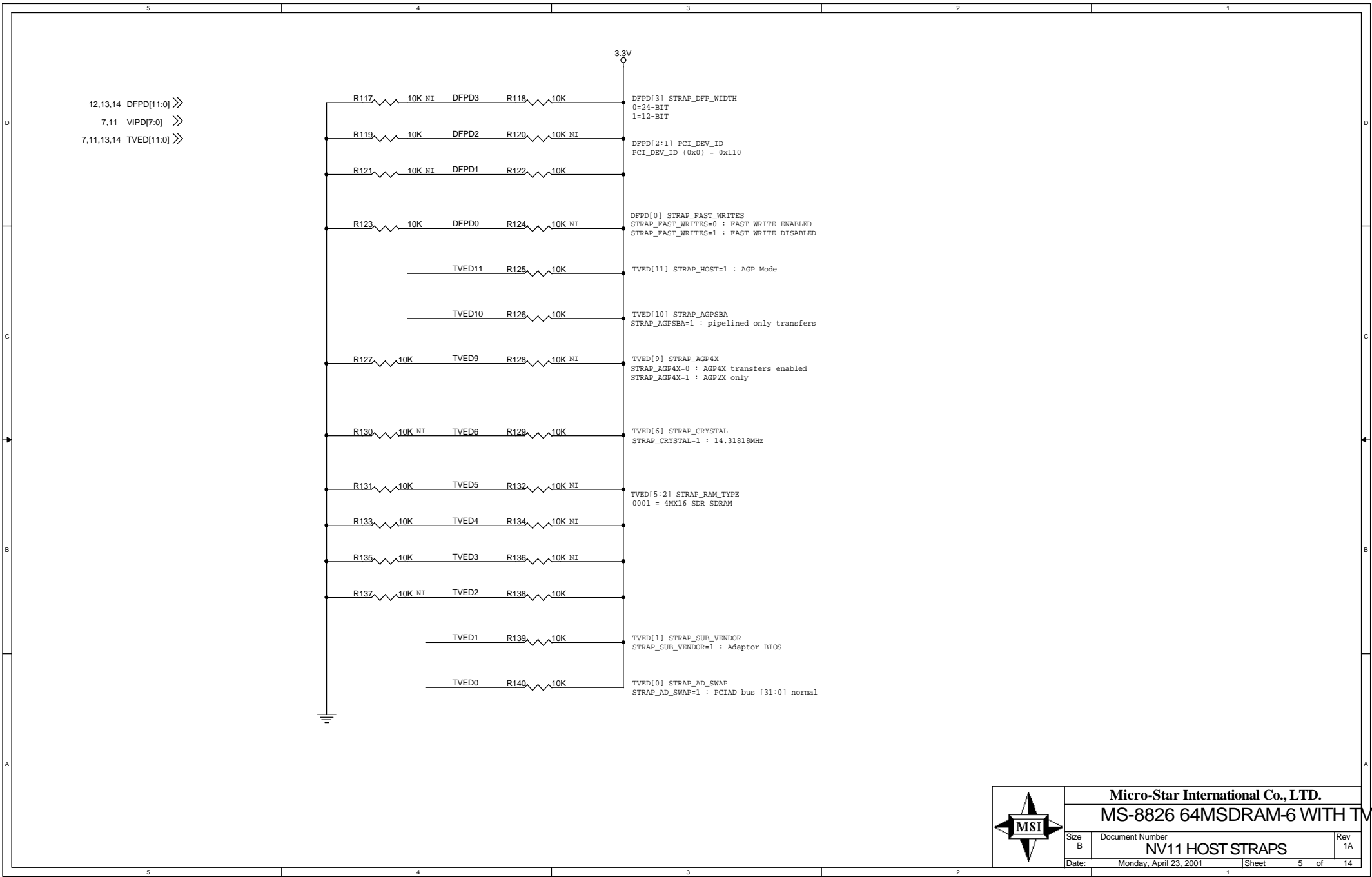
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In all cases each capacitor should be low ESR/ESL and placed as close as possible to the respective rail connector pin(s).



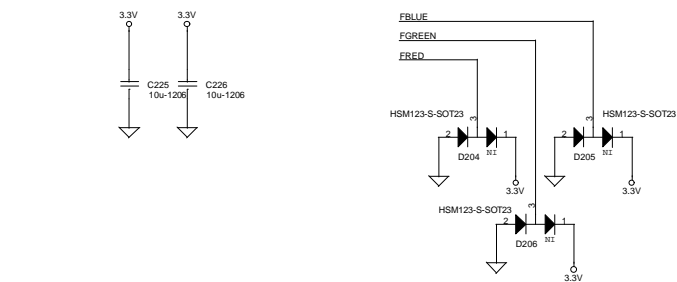


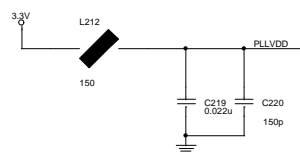


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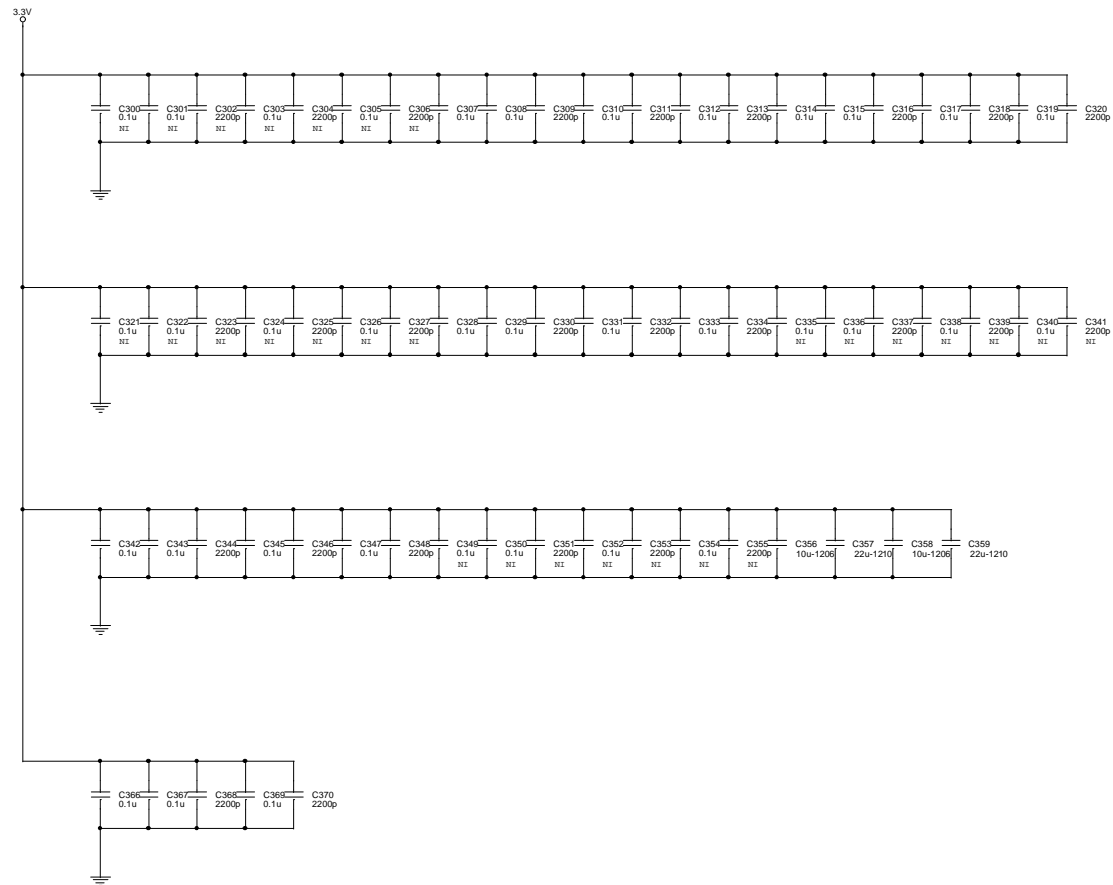
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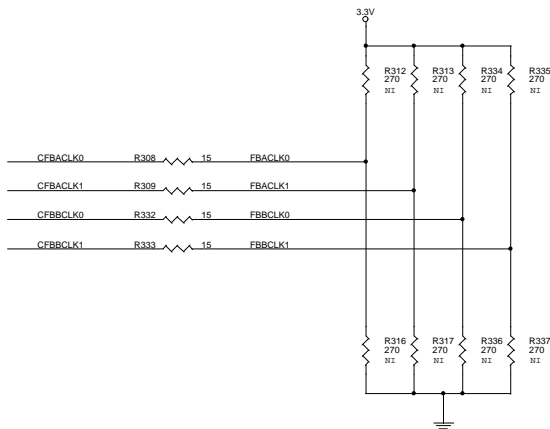
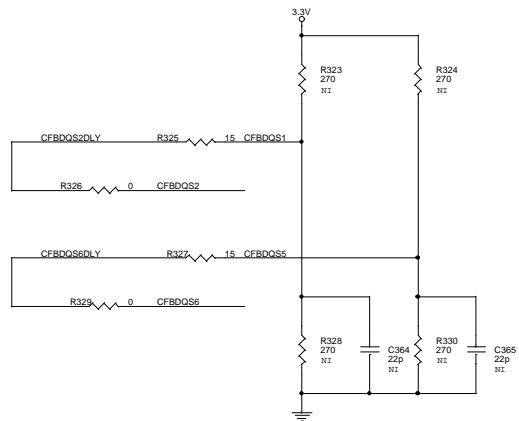
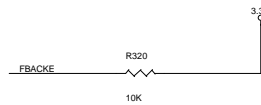
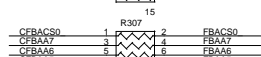
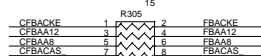
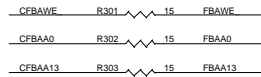




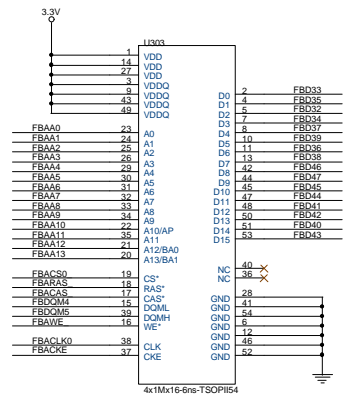
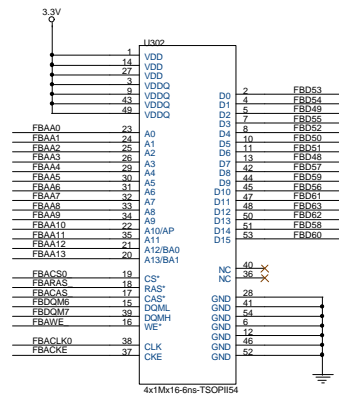
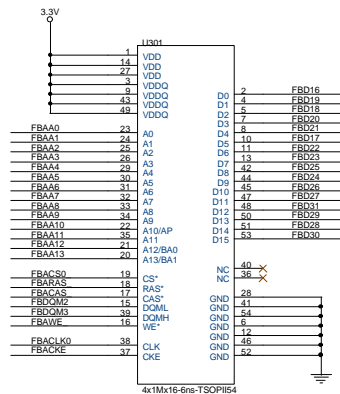
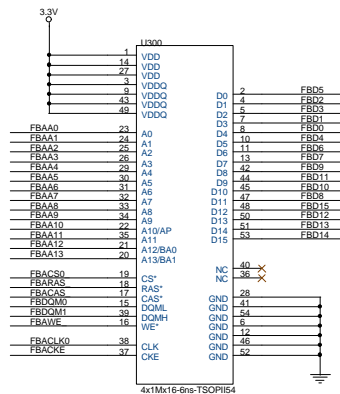


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>>CFBAA[13:0]  
>>CFBARAS\_  
>>CFBACAS\_  
>>CFBAWE\_  
>>CFBACS0\_  
>>CFBDQS[7:0]  
>>CFBACKE  
>>CFBACLK0  
>>CFBACLK1  
>>CFBCLK0  
>>CFBCLK1



>>FBA[13:0]  
>>FBARAS\_  
>>FBACAS\_  
>>FBAWE\_  
>>FBACS0\_  
>>FBACKE  
>>FBACLK0  
>>FBACLK1  
>>FBBCLK0  
>>FBBCLK1



FBD[127:0] <<  
FBA[13:0] <<  
FBARAS <<  
FBACAS <<  
FBAWE <<  
FBAC0 <<  
CFBDQS[7:0] <<  
FBDQM[7:0] <<  
FBDQM[15:8] <<  
FBACKE <<  
FBACLK0 <<  
FBCLK1 <<  
FBBCLK0 <<  
FBBCLK1 <<



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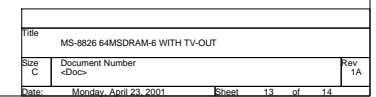
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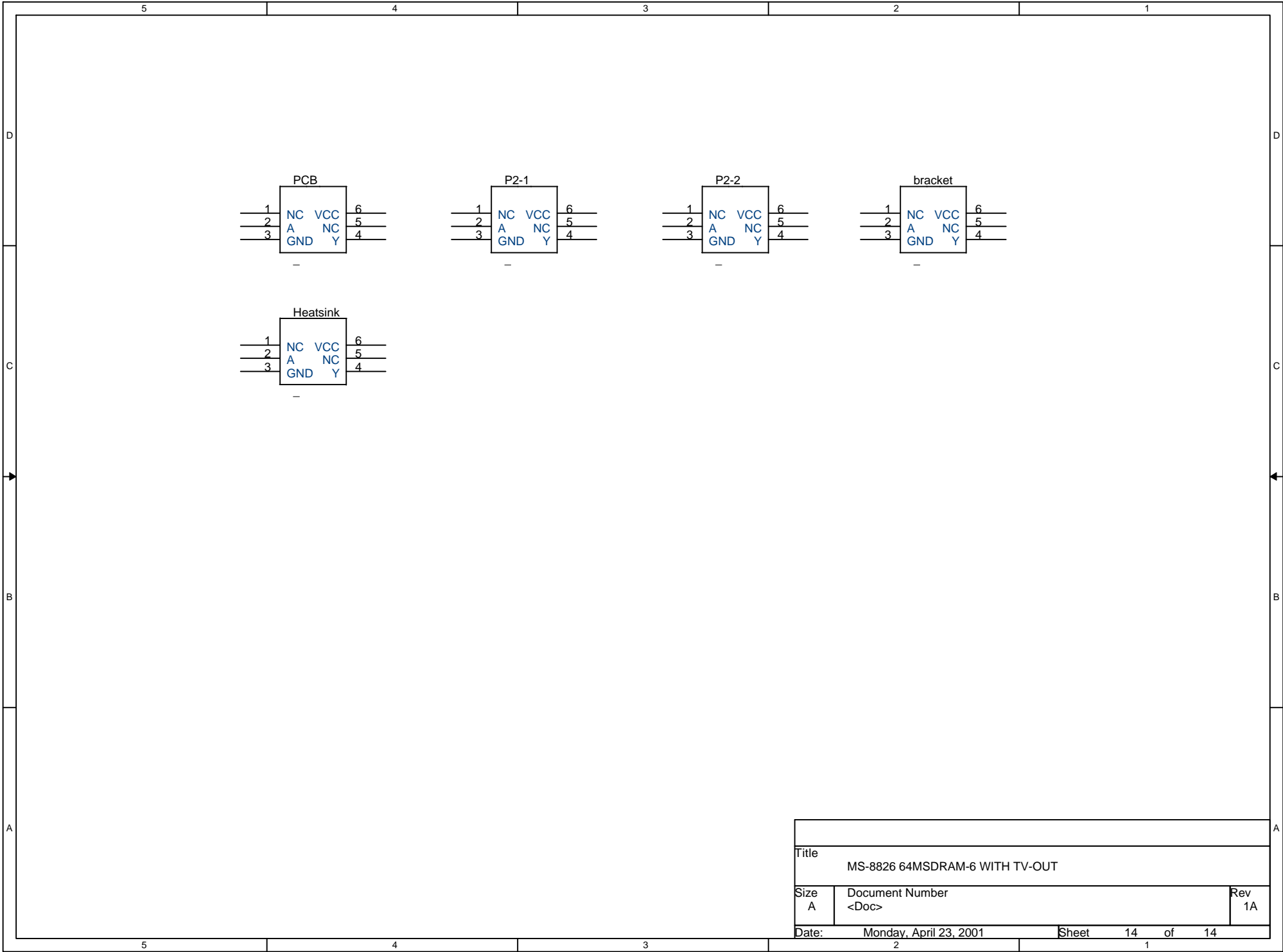
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