

# ENGTX285 TOP Repair Guide

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CSC-GRMA

|                        |  |
|------------------------|--|
| <b>Vendor Kit</b>      | <b>C892P</b>   |
| <b>ASUS model name</b> | <b>ENGTX285 TOP/HTDI/1GD3/A</b>                                |
| <b>PCB rev.</b>        | <b>ATX R1.00</b>   |
| <b>p/n</b>             | <b>80-C1CMN0-L0A01</b>   |
| <b>Chip</b>            | <b>G200-350-B3</b>   |
| <b>GPU p/n</b>         | <b>02G190016311</b>  |
| <b>Memory</b>          | <b>16M*32 -0.77 GDDR3*16pcs<br/>(1GB---512 bit)GPU(512bit)</b> |
| <b>Memory p/n</b>      | <b>03G15134J014/Hynix</b>                                      |
| <b>CoreClock</b>       | <b>669MHZ</b>  |
| <b>ShaderClock</b>     | <b>1550MHz</b>   |
| <b>MemoryClock</b>     | <b>2600MHZ</b>   |

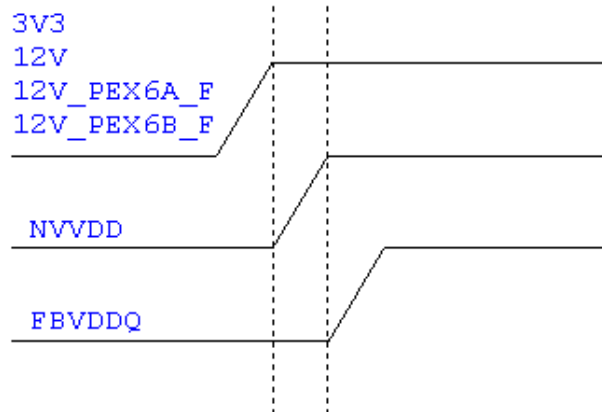
Page1. Basic Information about the Model

Page2. Power Sequence and Power Flow

Page3. Power Point Map (Top Side)

Page4. Power Point Map (Bottom Side)

## Power sequence



Other power doesn't care power on sequence, but when THERM\_SHUTDOWN\* is triggering, all of them must be pulled low at the same time.

Switch ON/OFF  
Control signal

Switching  
Power Rail

Linear

## Power flow

12V\_PEX6A\_F

12V\_PEX6B\_F

12V

3V3

UP6208  
6 Phase  
(RJK0355+RJK0353\*2)

NVVDD  
1.2V/138A

UP6205  
2 Phase  
(RJK0355+RJK0353)

FBVDDQ  
2.1V/30A

UP6103  
(AP4800+AP4800)

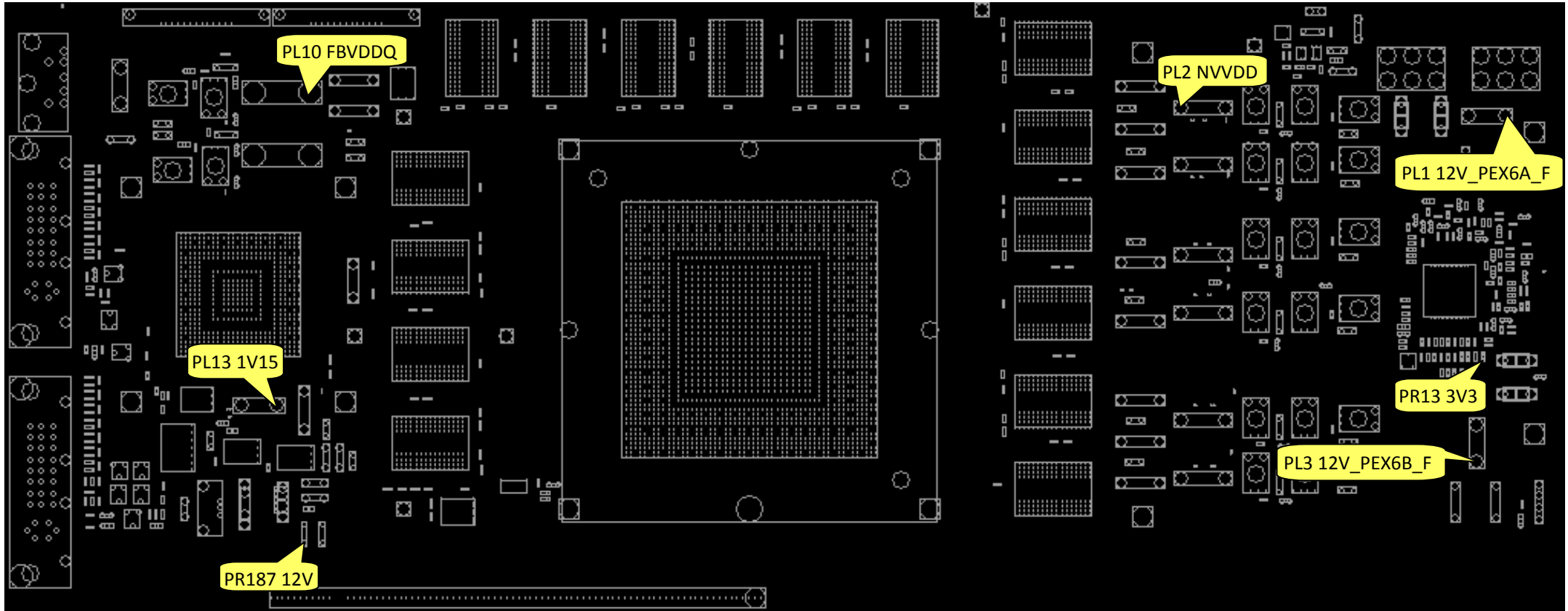
1V15  
6A

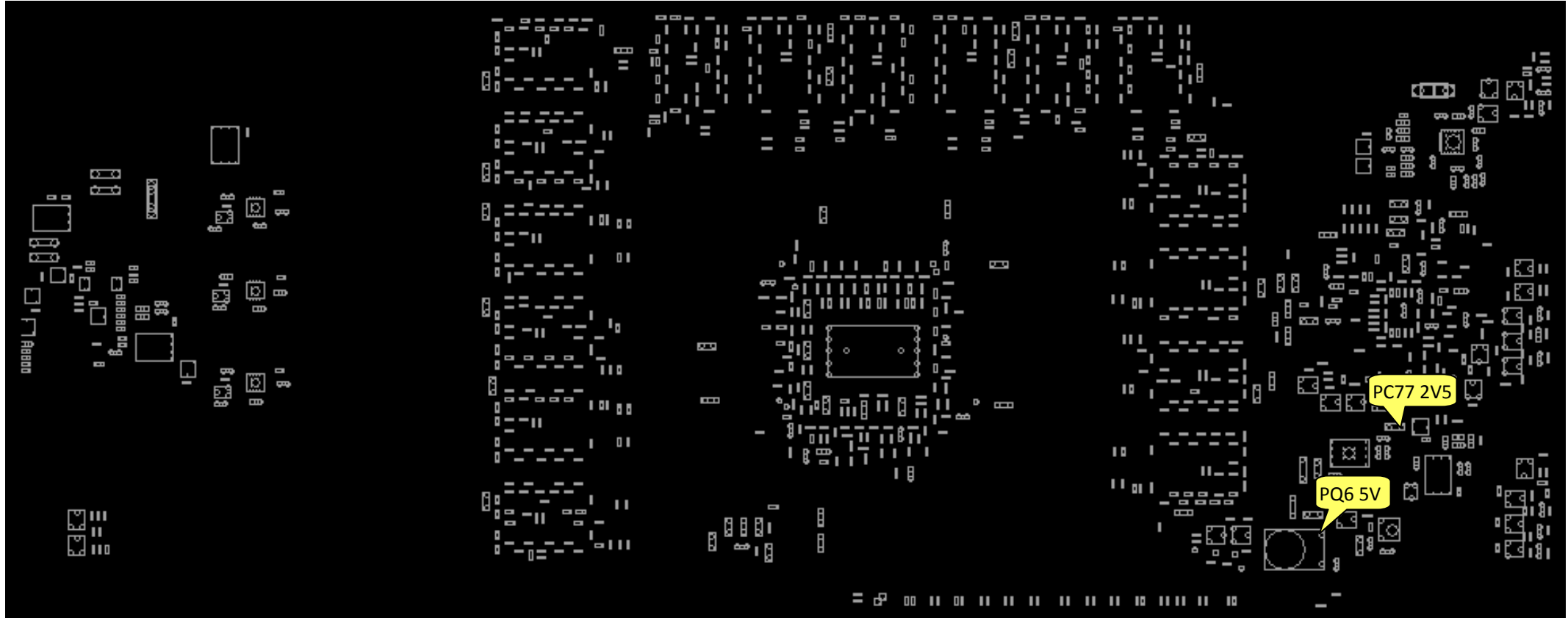
LM7805BJ3

5V  
0.2A

UP7704

2V5  
2A





PC77 2V5

PQ6 5V