

P2010-A02

75W/100W GK107/GF108 128-BIT GDDR5 in x32/x16 MODE
DVI-I-DL + DVI-D-DL/HDMI +mHDMI/DP

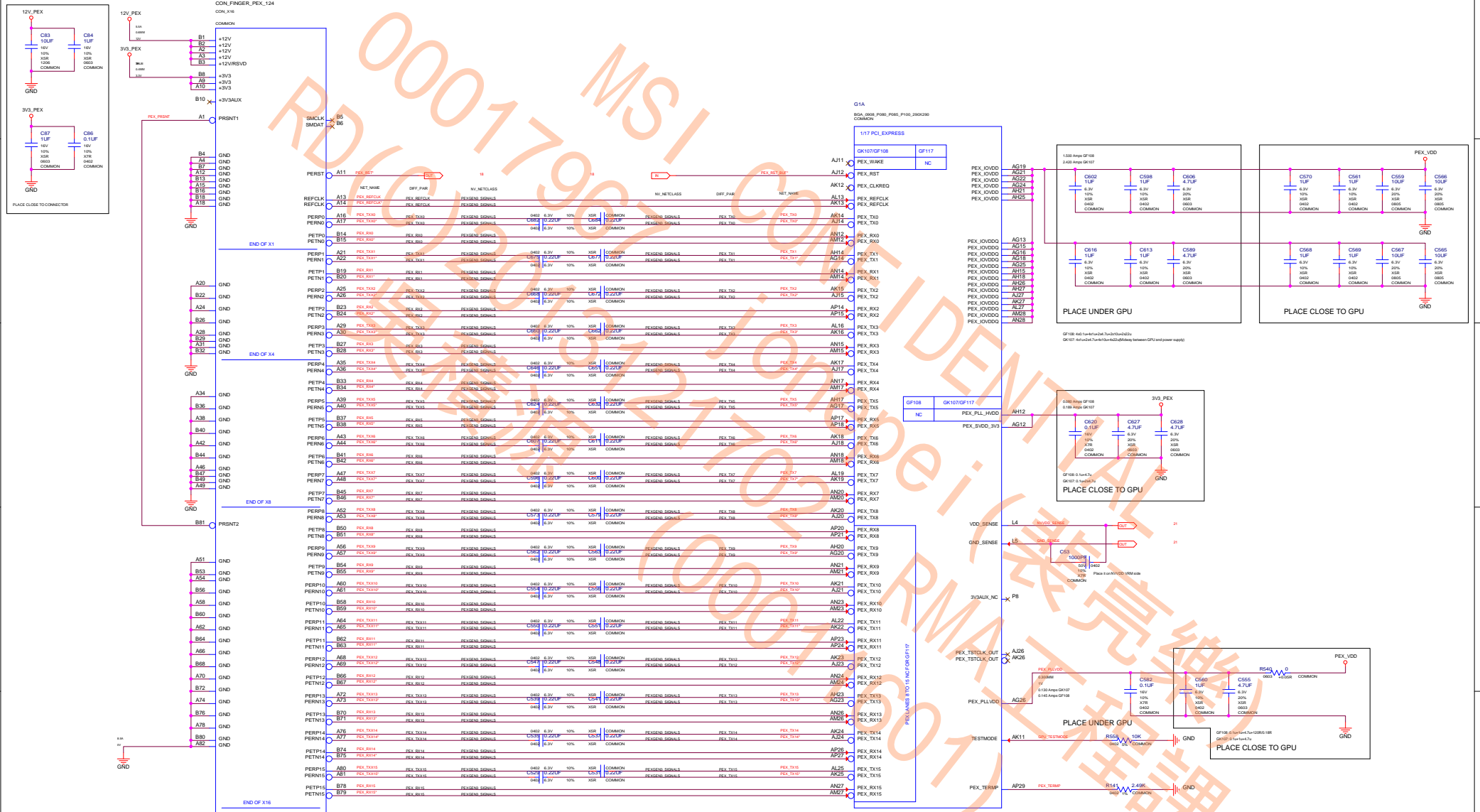
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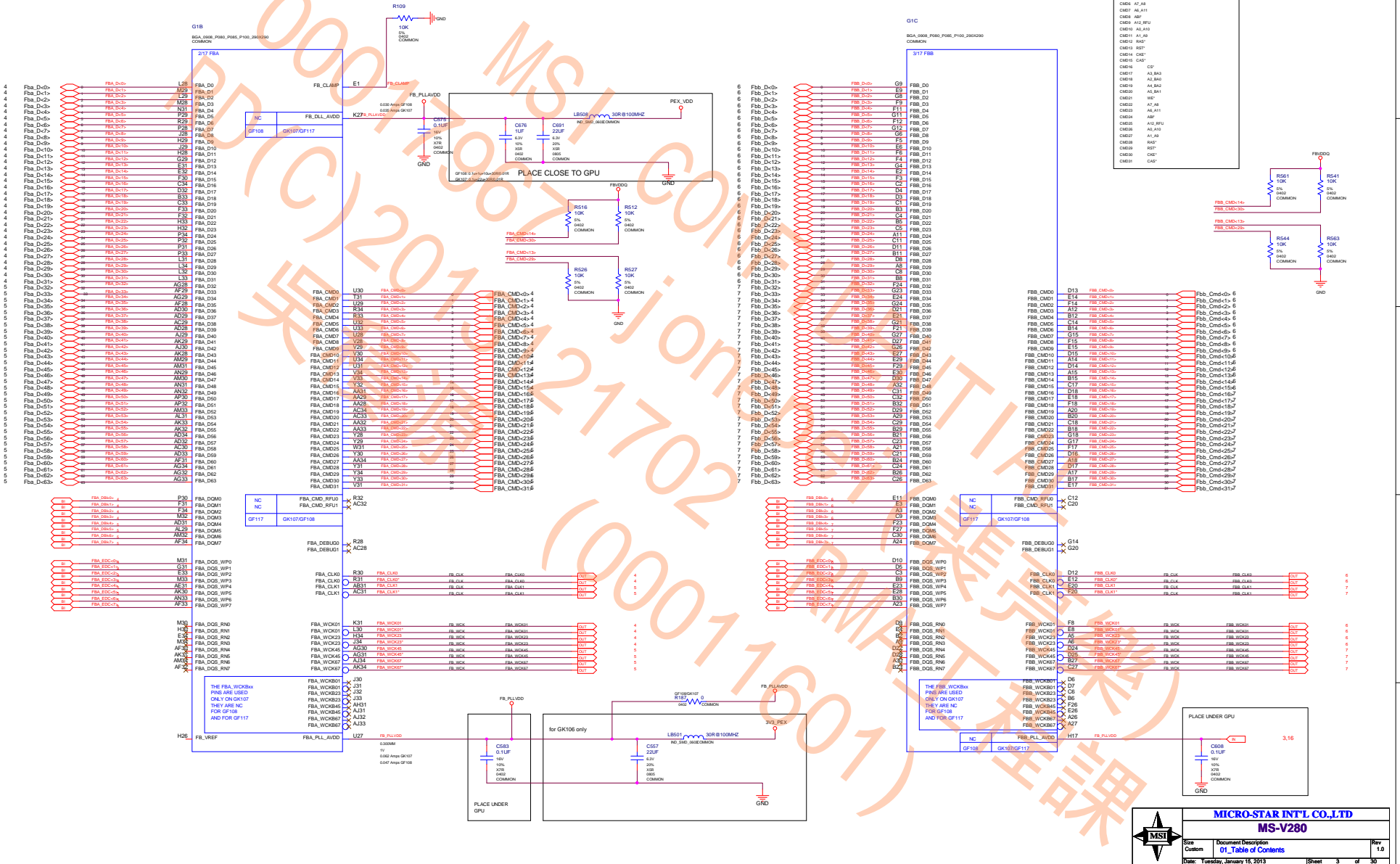
2013/01/15 MS-V280-2.3 changed list

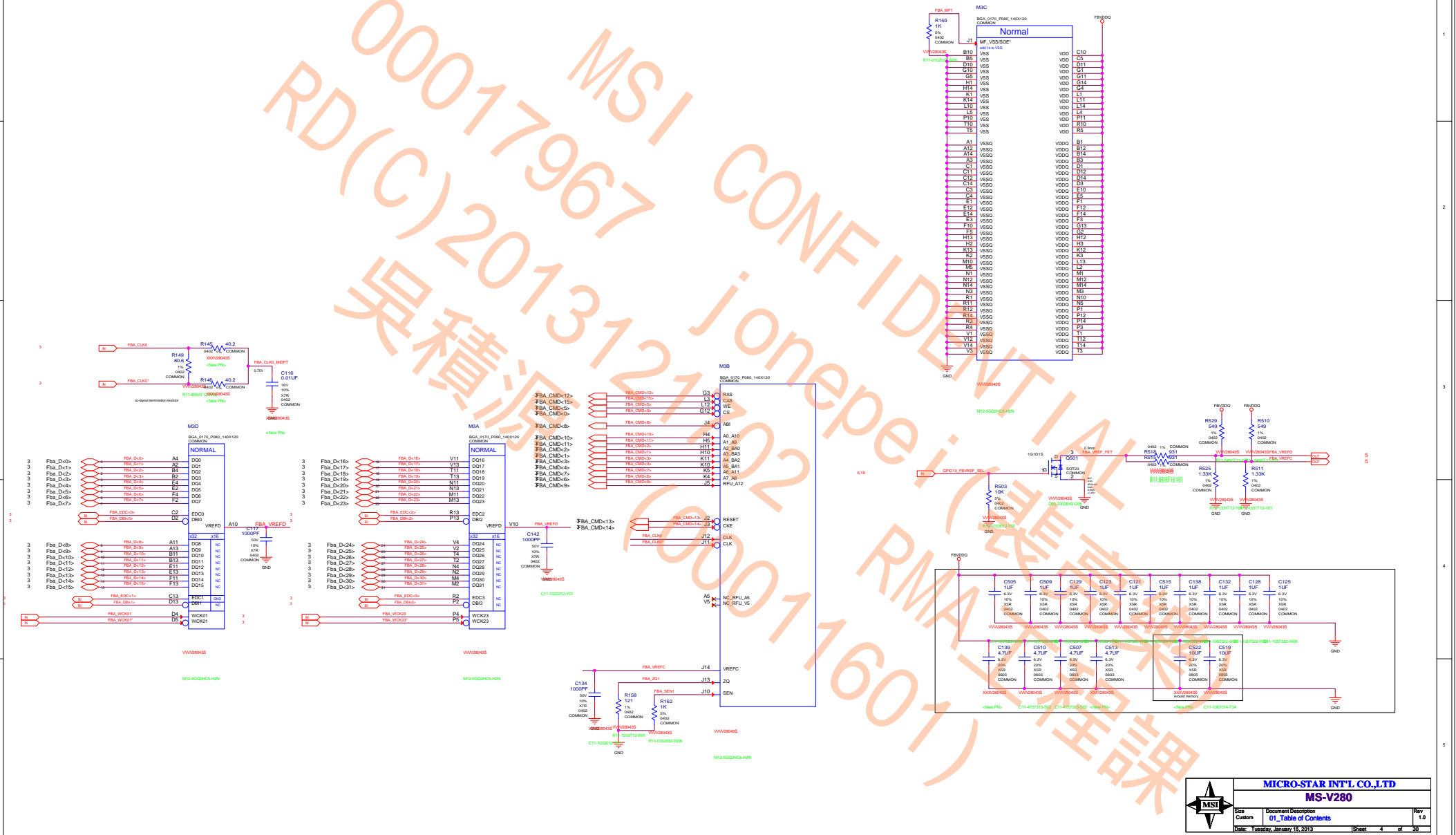
Page	Description
21	Base on V280-2.2 removed power seq
21	Add P2010 Power seq

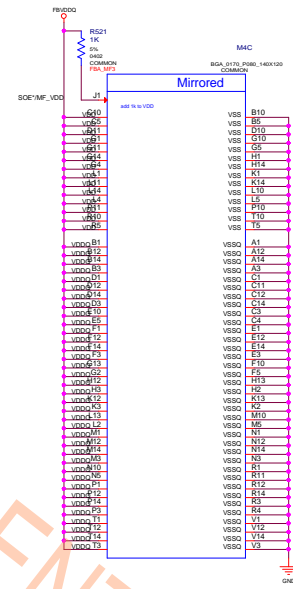
PCI Express

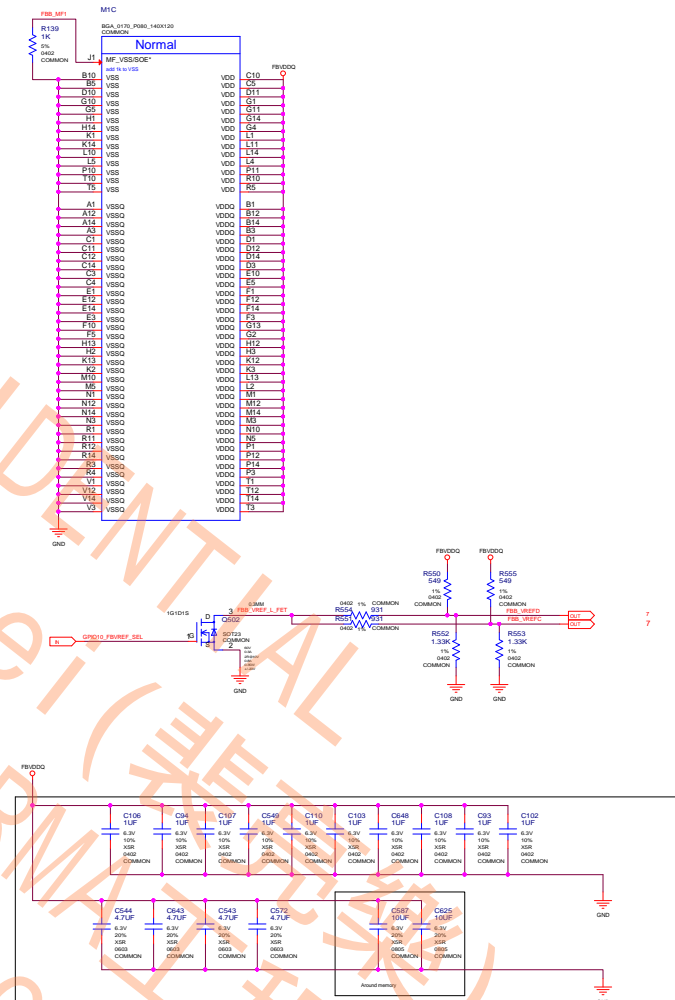


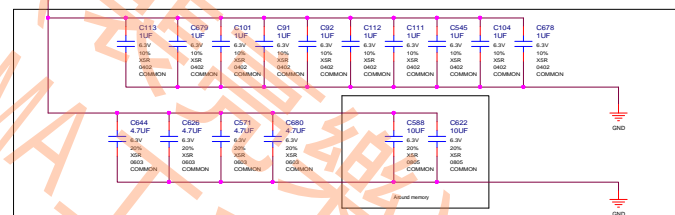
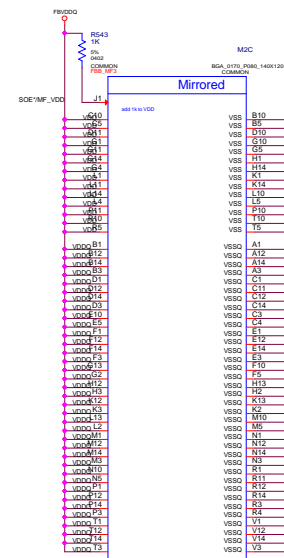
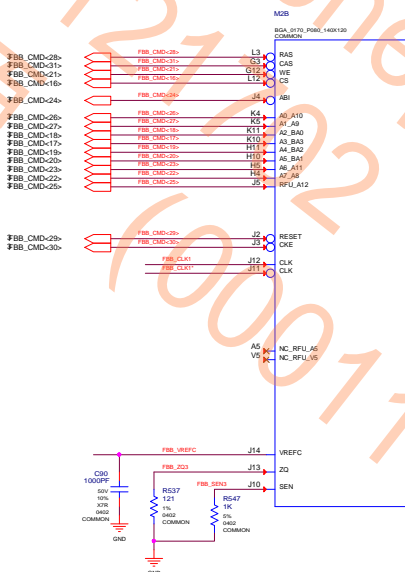
GPU Frame Buffer Partition A/B



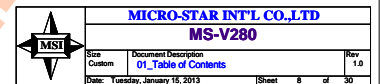
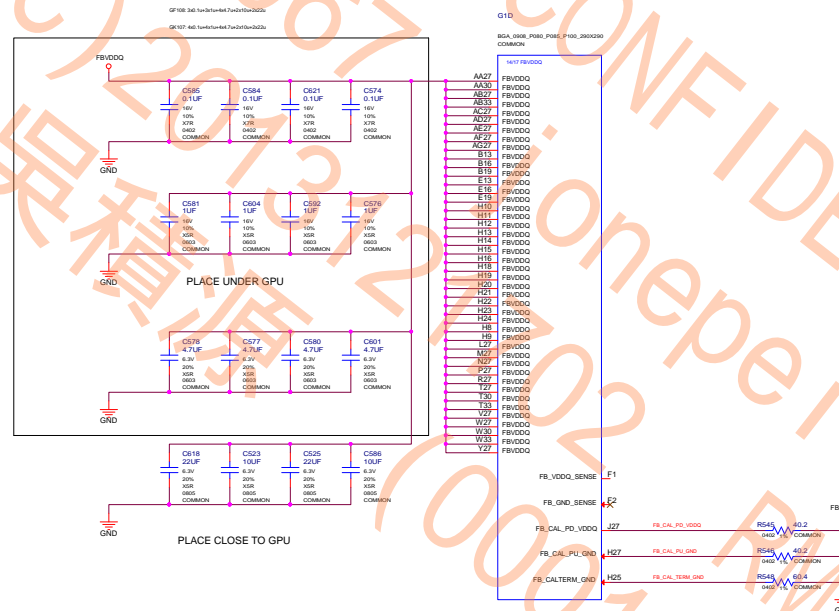




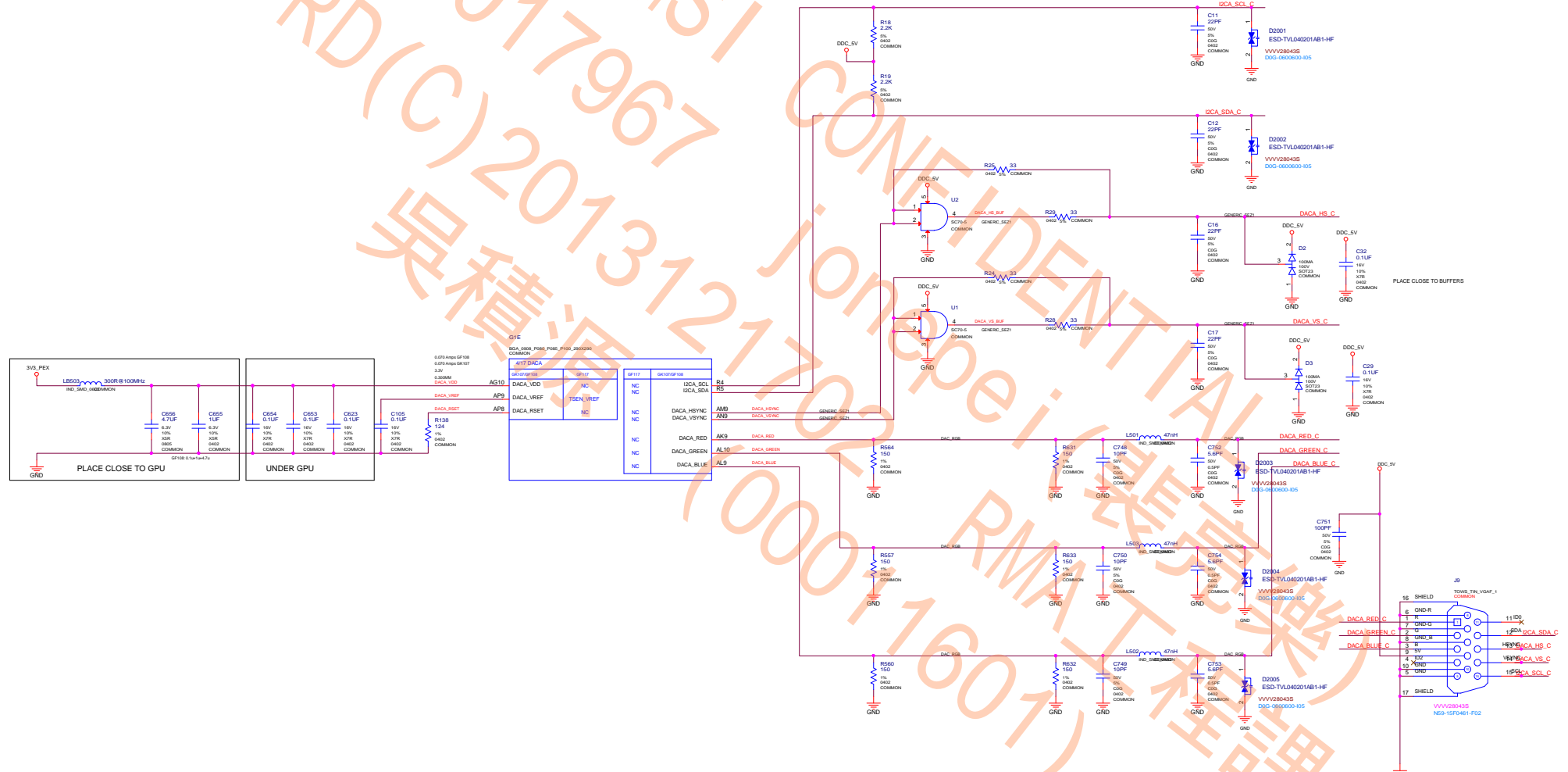




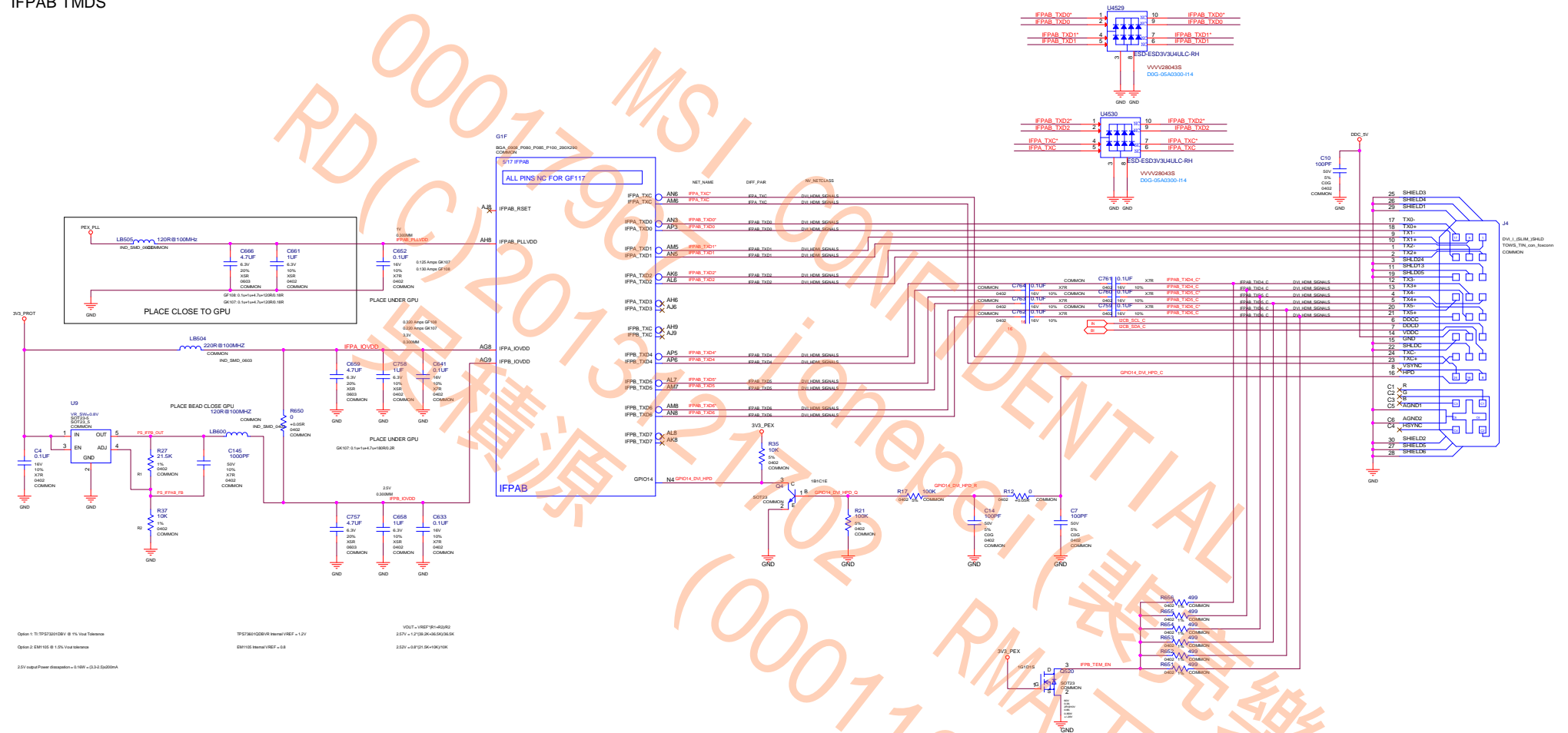
FB Decoupling



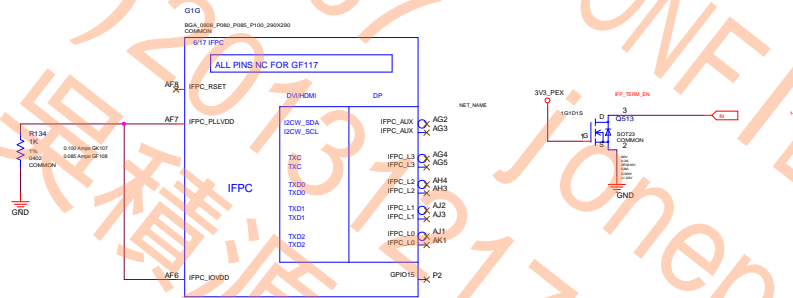
DACA VGA



IFPAB TMDS



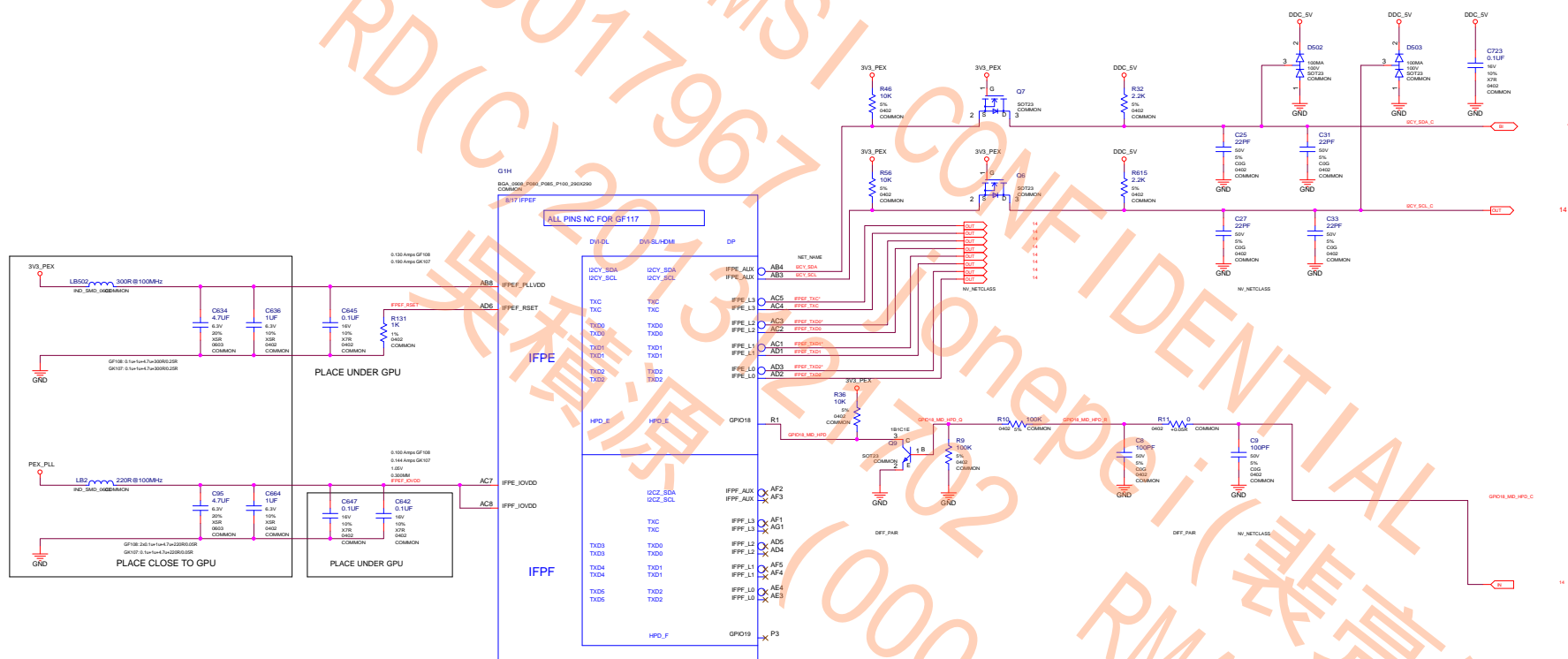
IFPC mHDMI



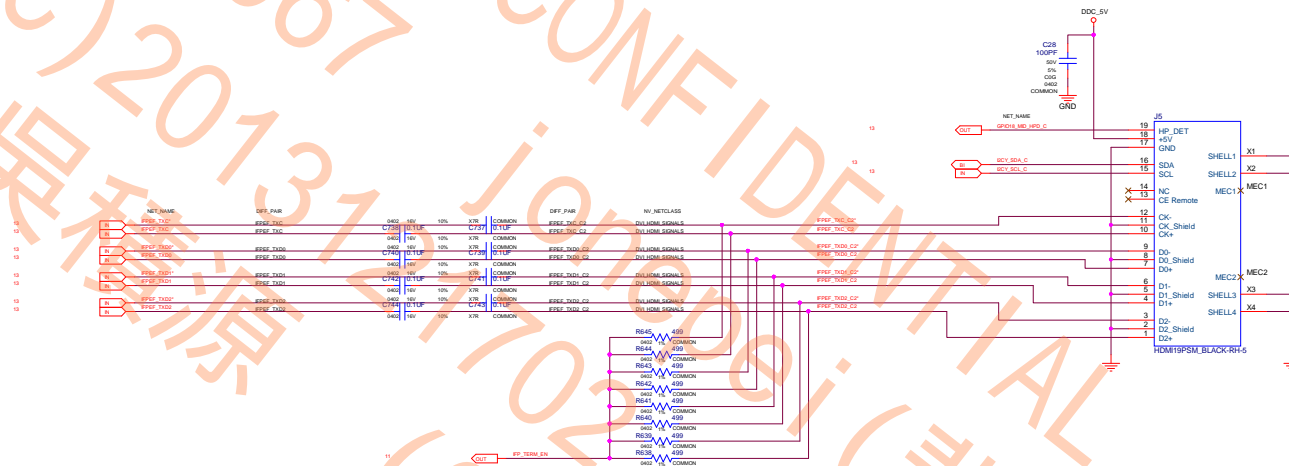
remove DP

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吳積源 (00011601)

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MS-V280		
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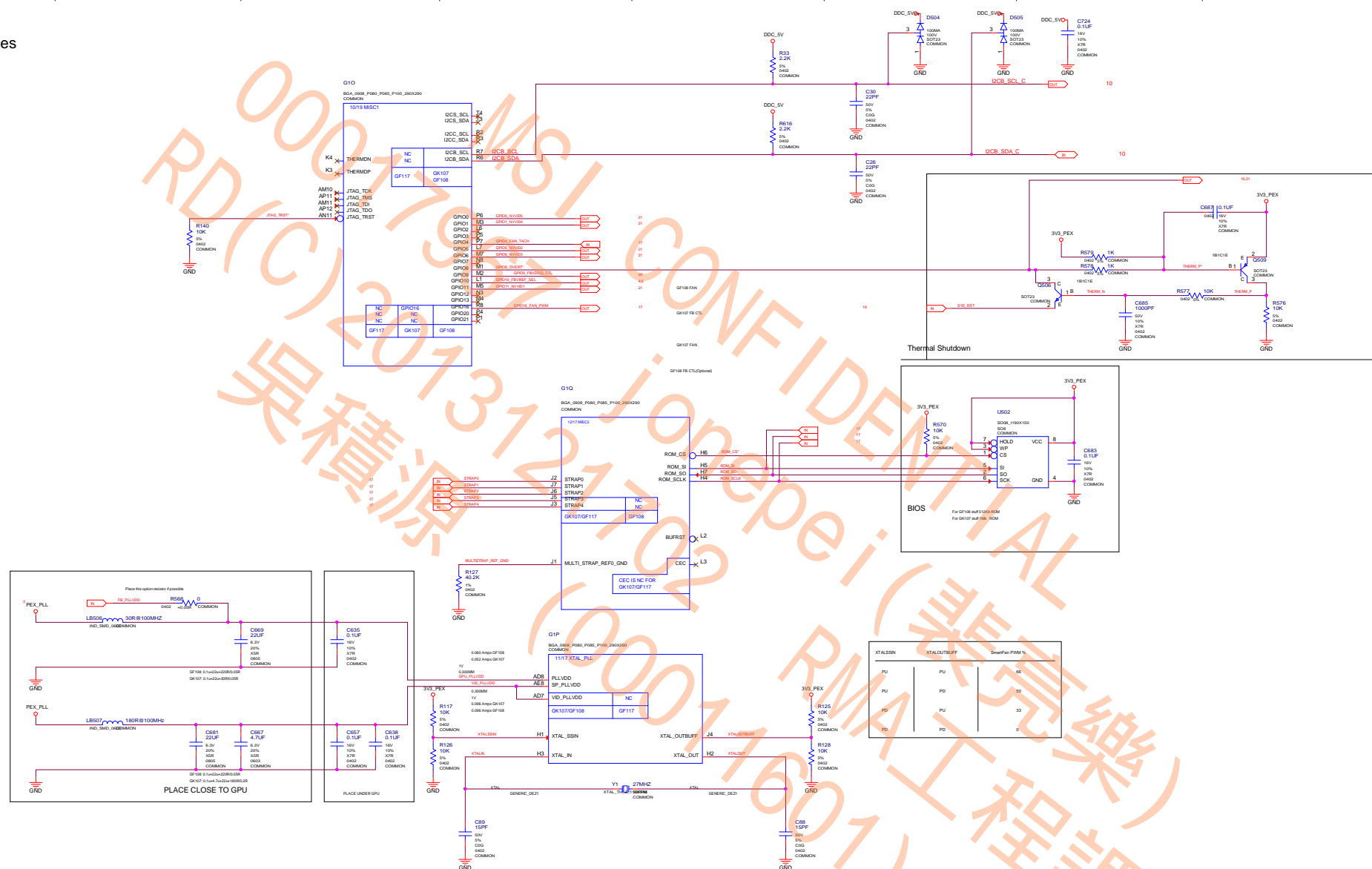


IFPE Long HDMI



[illegible]

Misc Interfaces



XTALSSN	XTALOUTBUFF	SmartFan PWM %
PU	PU	66
PU	PD	50
PD	PU	33
PD	PD	0

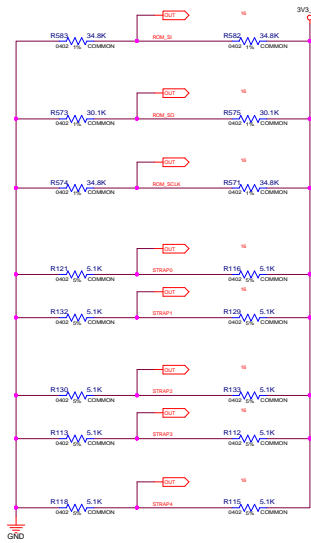
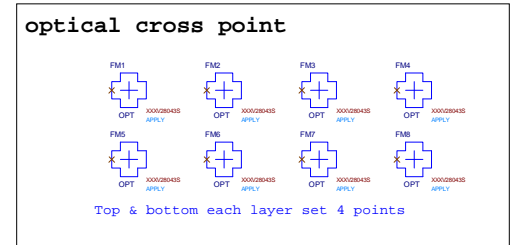
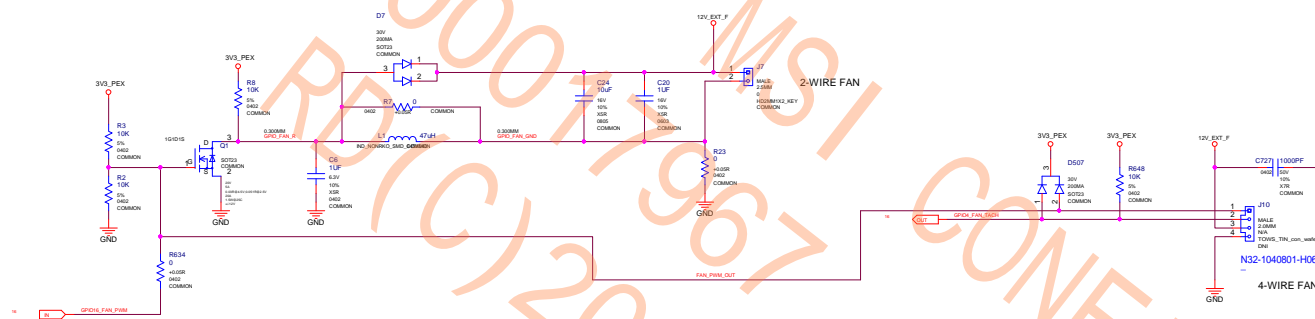


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Straps, Thermal, Mechanical



GK107 Straps

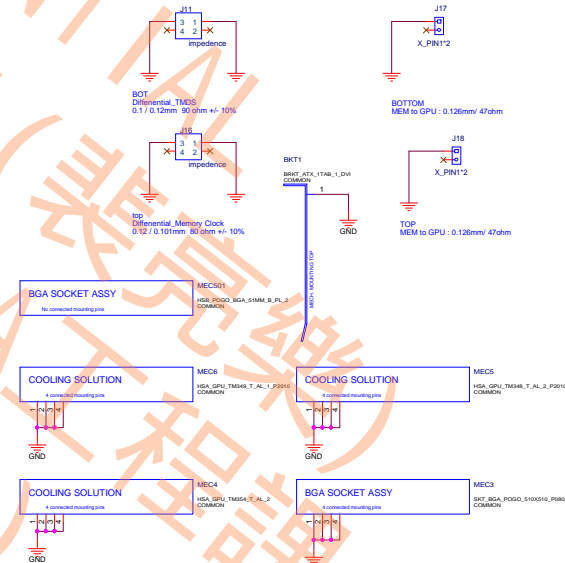
Bit Signal		Values	
00	MMIO_CFG	0000	MMIO_CFG_0000
01	MMIO_CFG	0001	MMIO_CFG_0001
02	MMIO_CFG	0010	MMIO_CFG_0010
03	MMIO_CFG	0011	MMIO_CFG_0011
04	MMIO_CFG	0100	MMIO_CFG_0100
05	MMIO_CFG	0101	MMIO_CFG_0101
06	MMIO_CFG	0110	MMIO_CFG_0110
07	MMIO_CFG	0111	MMIO_CFG_0111
08	IRQ	0	IRQ0
09	IRQ	1	IRQ1
10	IRQ_ALT_ADDR	0	DISABLED
11	IRQ_ALT_ADDR	1	ENABLED
12	PGA_DEVICE	0	DISABLED
13	PGA_DEVICE	1	ENABLED
14	PCI_DEV_ID_EXT	0	INT
15	PCI_DEV_ID_EXT	0	INT
16	PCI_DEV_ID_EXT	0	PCI_DEV_ID_EXT_0000
17	PCI_DEV_ID_EXT	1	PCI_DEV_ID_EXT_0001
18	PCI_DEV_ID_EXT	0	INT
19	PCI_DEV_ID_EXT	1	INT
20	PCI_DEV_ID_EXT	0	INT
21	PCI_DEV_ID_EXT	1	INT
22	PCI_DEV_ID_EXT	0	INT
23	PCI_DEV_ID_EXT	1	INT
24	PCI_DEV_ID_EXT	0	INT
25	PCI_DEV_ID_EXT	1	INT
26	PCI_DEV_ID_EXT	0	INT
27	PCI_DEV_ID_EXT	1	INT
28	PCI_DEV_ID_EXT	0	INT
29	PCI_DEV_ID_EXT	1	INT
30	PCI_DEV_ID_EXT	0	INT
31	PCI_DEV_ID_EXT	1	INT
32	PCI_DEV_ID_EXT	0	INT
33	PCI_DEV_ID_EXT	1	INT
34	PCI_DEV_ID_EXT	0	INT
35	PCI_DEV_ID_EXT	1	INT
36	PCI_DEV_ID_EXT	0	INT
37	PCI_DEV_ID_EXT	1	INT
38	PCI_DEV_ID_EXT	0	INT
39	PCI_DEV_ID_EXT	1	INT
40	PCI_DEV_ID_EXT	0	INT
41	PCI_DEV_ID_EXT	1	INT
42	PCI_DEV_ID_EXT	0	INT
43	PCI_DEV_ID_EXT	1	INT
44	PCI_DEV_ID_EXT	0	INT
45	PCI_DEV_ID_EXT	1	INT
46	PCI_DEV_ID_EXT	0	INT
47	PCI_DEV_ID_EXT	1	INT
48	PCI_DEV_ID_EXT	0	INT
49	PCI_DEV_ID_EXT	1	INT
50	PCI_DEV_ID_EXT	0	INT
51	PCI_DEV_ID_EXT	1	INT
52	PCI_DEV_ID_EXT	0	INT
53	PCI_DEV_ID_EXT	1	INT
54	PCI_DEV_ID_EXT	0	INT
55	PCI_DEV_ID_EXT	1	INT
56	PCI_DEV_ID_EXT	0	INT
57	PCI_DEV_ID_EXT	1	INT
58	PCI_DEV_ID_EXT	0	INT
59	PCI_DEV_ID_EXT	1	INT
60	PCI_DEV_ID_EXT	0	INT
61	PCI_DEV_ID_EXT	1	INT
62	PCI_DEV_ID_EXT	0	INT
63	PCI_DEV_ID_EXT	1	INT
64	PCI_DEV_ID_EXT	0	INT
65	PCI_DEV_ID_EXT	1	INT
66	PCI_DEV_ID_EXT	0	INT
67	PCI_DEV_ID_EXT	1	INT
68	PCI_DEV_ID_EXT	0	INT
69	PCI_DEV_ID_EXT	1	INT
70	PCI_DEV_ID_EXT	0	INT
71	PCI_DEV_ID_EXT	1	INT
72	PCI_DEV_ID_EXT	0	INT
73	PCI_DEV_ID_EXT	1	INT
74	PCI_DEV_ID_EXT	0	INT
75	PCI_DEV_ID_EXT	1	INT
76	PCI_DEV_ID_EXT	0	INT
77	PCI_DEV_ID_EXT	1	INT
78	PCI_DEV_ID_EXT	0	INT
79	PCI_DEV_ID_EXT	1	INT
80	PCI_DEV_ID_EXT	0	INT
81	PCI_DEV_ID_EXT	1	INT
82	PCI_DEV_ID_EXT	0	INT
83	PCI_DEV_ID_EXT	1	INT
84	PCI_DEV_ID_EXT	0	INT
85	PCI_DEV_ID_EXT	1	INT
86	PCI_DEV_ID_EXT	0	INT
87	PCI_DEV_ID_EXT	1	INT
88	PCI_DEV_ID_EXT	0	INT
89	PCI_DEV_ID_EXT	1	INT
90	PCI_DEV_ID_EXT	0	INT
91	PCI_DEV_ID_EXT	1	INT
92	PCI_DEV_ID_EXT	0	INT
93	PCI_DEV_ID_EXT	1	INT
94	PCI_DEV_ID_EXT	0	INT
95	PCI_DEV_ID_EXT	1	INT
96	PCI_DEV_ID_EXT	0	INT
97	PCI_DEV_ID_EXT	1	INT
98	PCI_DEV_ID_EXT	0	INT
99	PCI_DEV_ID_EXT	1	INT
100	PCI_DEV_ID_EXT	0	INT
101	PCI_DEV_ID_EXT	1	INT
102	PCI_DEV_ID_EXT	0	INT
103	PCI_DEV_ID_EXT	1	INT
104	PCI_DEV_ID_EXT	0	INT
105	PCI_DEV_ID_EXT	1	INT
106	PCI_DEV_ID_EXT	0	INT
107	PCI_DEV_ID_EXT	1	INT
108	PCI_DEV_ID_EXT	0	INT
109	PCI_DEV_ID_EXT	1	INT
110	PCI_DEV_ID_EXT	0	INT
111	PCI_DEV_ID_EXT	1	INT
112	PCI_DEV_ID_EXT	0	INT
113	PCI_DEV_ID_EXT	1	INT
114	PCI_DEV_ID_EXT	0	INT
115	PCI_DEV_ID_EXT	1	INT
116	PCI_DEV_ID_EXT	0	INT
117	PCI_DEV_ID_EXT	1	INT
118	PCI_DEV_ID_EXT	0	INT
119	PCI_DEV_ID_EXT	1	INT
120	PCI_DEV_ID_EXT	0	INT
121	PCI_DEV_ID_EXT	1	INT
122	PCI_DEV_ID_EXT	0	INT
123	PCI_DEV_ID_EXT	1	INT
124	PCI_DEV_ID_EXT	0	INT
125	PCI_DEV_ID_EXT	1	INT
126	PCI_DEV_ID_EXT	0	INT
127	PCI_DEV_ID_EXT	1	INT
128	PCI_DEV_ID_EXT	0	INT
129	PCI_DEV_ID_EXT	1	INT
130	PCI_DEV_ID_EXT	0	INT
131	PCI_DEV_ID_EXT	1	INT
132	PCI_DEV_ID_EXT	0	INT
133	PCI_DEV_ID_EXT	1	INT
134	PCI_DEV_ID_EXT	0	INT
135	PCI_DEV_ID_EXT	1	INT
136	PCI_DEV_ID_EXT	0	INT
137	PCI_DEV_ID_EXT	1	INT
138	PCI_DEV_ID_EXT	0	INT
139	PCI_DEV_ID_EXT	1	INT
140	PCI_DEV_ID_EXT	0	INT
141	PCI_DEV_ID_EXT	1	INT
142	PCI_DEV_ID_EXT	0	INT
143	PCI_DEV_ID_EXT	1	INT
144	PCI_DEV_ID_EXT	0	INT
145	PCI_DEV_ID_EXT	1	INT
146	PCI_DEV_ID_EXT	0	INT
147	PCI_DEV_ID_EXT	1	INT
148	PCI_DEV_ID_EXT	0	INT
149	PCI_DEV_ID_EXT	1	INT
150	PCI_DEV_ID_EXT	0	INT
151	PCI_DEV_ID_EXT	1	INT
152	PCI_DEV_ID_EXT	0	INT
153	PCI_DEV_ID_EXT	1	INT
154	PCI_DEV_ID_EXT	0	INT
155	PCI_DEV_ID_EXT	1	INT
156	PCI_DEV_ID_EXT	0	INT
157	PCI_DEV_ID_EXT	1	INT
158	PCI_DEV_ID_EXT	0	INT
159	PCI_DEV_ID_EXT	1	INT
160	PCI_DEV_ID_EXT	0	INT
161	PCI_DEV_ID_EXT	1	INT
162	PCI_DEV_ID_EXT	0	INT
163	PCI_DEV_ID_EXT	1	INT
164	PCI_DEV_ID_EXT	0	INT
165	PCI_DEV_ID_EXT	1	INT
166	PCI_DEV_ID_EXT	0	INT
167	PCI_DEV_ID_EXT	1	INT
168	PCI_DEV_ID_EXT	0	INT
169	PCI_DEV_ID_EXT	1	INT
170	PCI_DEV_ID_EXT	0	INT
171	PCI_DEV_ID_EXT	1	INT
172	PCI_DEV_ID_EXT	0	INT
173	PCI_DEV_ID_EXT	1	INT
174	PCI_DEV_ID_EXT	0	INT
175	PCI_DEV_ID_EXT	1	INT
176	PCI_DEV_ID_EXT	0	INT
177	PCI_DEV_ID_EXT	1	INT
178	PCI_DEV_ID_EXT	0	INT
179	PCI_DEV_ID_EXT	1	INT
180	PCI_DEV_ID_EXT	0	INT
181	PCI_DEV_ID_EXT	1	INT
182	PCI_DEV_ID_EXT	0	INT
183	PCI_DEV_ID_EXT	1	INT
184	PCI_DEV_ID_EXT	0	INT
185	PCI_DEV_ID_EXT	1	INT
186	PCI_DEV_ID_EXT	0	INT
187	PCI_DEV_ID_EXT	1	INT
188	PCI_DEV_ID_EXT	0	INT
189	PCI_DEV_ID_EXT	1	INT
190	PCI_DEV_ID_EXT	0	INT
191	PCI_DEV_ID_EXT	1	INT
192	PCI_DEV_ID_EXT	0	INT
193	PCI_DEV_ID_EXT	1	INT
194	PCI_DEV_ID_EXT	0	INT
195	PCI_DEV_ID_EXT	1	INT
196	PCI_DEV_ID_EXT	0	INT
197	PCI_DEV_ID_EXT	1	INT
198	PCI_DEV_ID_EXT	0	INT
199	PCI_DEV_ID_EXT	1	INT
200	PCI_DEV_ID_EXT	0	INT
201	PCI_DEV_ID_EXT	1	INT
202	PCI_DEV_ID_EXT	0	INT
203	PCI_DEV_ID_EXT	1	INT
204	PCI_DEV_ID_EXT	0	INT
205	PCI_DEV_ID_EXT	1	INT
206	PCI_DEV_ID_EXT	0	INT
207	PCI_DEV_ID_EXT	1	INT
208	PCI_DEV_ID_EXT	0	INT
209	PCI_DEV_ID_EXT	1	INT
210	PCI_DEV_ID_EXT	0	INT
211	PCI_DEV_ID_EXT	1	INT
212	PCI_DEV_ID_EXT	0	INT
213	PCI_DEV_ID_EXT	1	INT
214	PCI_DEV_ID_EXT	0	INT
215	PCI_DEV_ID_EXT	1	INT
216	PCI_DEV_ID_EXT	0	INT
217	PCI_DEV_ID_EXT	1	INT
218	PCI_DEV_ID_EXT	0	INT
219	PCI_DEV_ID_EXT	1	INT
220	PCI_DEV_ID_EXT	0	INT
221	PCI_DEV_ID_EXT	1	INT
222	PCI_DEV_ID_EXT	0	INT
223	PCI_DEV_ID_EXT	1	INT
224	PCI_DEV_ID_EXT	0	INT
225	PCI_DEV_ID_EXT	1	INT
226	PCI_DEV_ID_EXT	0	INT
227	PCI_DEV_ID_EXT	1	INT
228	PCI_DEV_ID_EXT	0	INT
229	PCI_DEV_ID_EXT	1	INT
230	PCI_DEV_ID_EXT	0	INT
231	PCI_DEV_ID_EXT	1	INT
232	PCI_DEV_ID_EXT	0	INT
233	PCI_DEV_ID_EXT	1	INT
234	PCI_DEV_ID_EXT	0	INT
235	PCI_DEV_ID_EXT	1	INT
236	PCI_DEV_ID_EXT	0	INT
237	PCI_DEV_ID_EXT	1	INT
238	PCI_DEV_ID_EXT	0	INT
239	PCI_DEV_ID_EXT	1	INT
240	PCI_DEV_ID_EXT	0	INT
241	PCI_DEV_ID_EXT	1	INT
242	PCI_DEV_ID_EXT	0	INT
243	PCI_DEV_ID_EXT	1	INT
244	PCI_DEV_ID_EXT	0	INT
245	PCI_DEV_ID_EXT	1	INT
246	PCI_DEV_ID_EXT	0	INT
247	PCI_DEV_ID_EXT	1	INT
248	PCI_DEV_ID_EXT	0	INT
249	PCI_DEV_ID_EXT	1	INT
250	PCI_DEV_ID_EXT	0	INT
251	PCI_DEV_ID_EXT	1	INT
252	PCI_DEV_ID_EXT	0	INT
253	PCI_DEV_ID_EXT	1	INT
254	PCI_DEV_ID_EXT	0	INT
255	PCI_DEV_ID_EXT	1	INT
256	PCI_DEV_ID_EXT	0	INT
257	PCI_DEV_ID_EXT	1	INT
258	PCI_DEV_ID_EXT	0	INT
259	PCI_DEV_ID_EXT	1	INT
260	PCI_DEV_ID_EXT	0	INT
261	PCI_DEV_ID_EXT	1	INT
262	PCI_DEV_ID_EXT	0	INT
263	PCI_DEV_ID_EXT	1	INT
264	PCI_DEV_ID_EXT	0	INT
265	PCI_DEV_ID_EXT	1	INT
266	PCI_DEV_ID_EXT	0	INT
267	PCI_DEV_ID_EXT	1	INT
268	PCI_DEV_ID_EXT	0	INT
269	PCI_DEV_ID_EXT	1	INT
270	PCI_DEV_ID_EXT	0	INT
271	PCI_DEV_ID_EXT	1	INT
272	PCI_DEV_ID_EXT	0	INT
273	PCI_DEV_ID_EXT	1	INT
274	PCI_DEV_ID_EXT	0	INT
275	PCI_DEV_ID_EXT	1	INT
276	PCI_DEV_ID_EXT	0	INT
277	PCI_DEV_ID_EXT	1	INT
278	PCI_DEV_ID_EXT	0	INT
279	PCI_DEV_ID_EXT	1	INT
280	PCI_DEV_ID_EXT	0	INT
281	PCI_DEV_ID_EXT	1	INT
282	PCI_DEV_ID_EXT	0	INT
283	PCI_DEV_ID_EXT	1	INT
284	PCI_DEV_ID_EXT	0	INT
285	PCI_DEV_ID_EXT	1	INT
286	PCI_DEV_ID_EXT	0	INT
287	PCI_DEV_ID_EXT	1	INT
288	PCI_DEV_ID_EXT	0	INT
289	PCI_DEV_ID_EXT	1	INT
290	PCI_DEV_ID_EXT	0	INT
291	PCI_DEV_ID_EXT	1	INT
292	PCI_DEV_ID_EXT	0	INT
293	PCI_DEV_ID_EXT	1	INT
294	PCI_DEV_ID_EXT	0	INT
295	PCI_DEV_ID_EXT	1	INT
296	PCI_DEV_ID_EXT	0	INT
297	PCI_DEV_ID_EXT	1	INT
298	PCI_DEV_ID_EXT	0	INT
299	PCI_DEV_ID_EXT	1	INT
300	PCI_DEV_ID_EXT	0	INT
301	PCI_DEV_ID_EXT	1	INT
302	PCI_DEV_ID_EXT	0	INT
303	PCI_DEV_ID_EXT	1	INT
304	PCI_DEV_ID_EXT	0	INT
305	PCI_DEV_ID_EXT	1	INT
306	PCI_DEV_ID_EXT	0	INT
307	PCI_DEV_ID_EXT	1	INT
308	PCI_DEV_ID_EXT	0	INT
309	PCI_DEV_ID_EXT	1	INT
310	PCI_DEV_ID_EXT	0	INT
311	PCI_DEV_ID_EXT	1	INT
312	PCI_DEV_ID_EXT	0	INT
313	PCI_DEV_ID_EXT	1	INT
314	PCI_DEV_ID_EXT	0	INT
315	PCI_DEV_ID_EXT	1	INT
316	PCI_DEV_ID_EXT	0	INT
317	PCI_DEV_ID_EXT	1	INT
318	PCI_DEV_ID_EXT	0	INT
319	PCI_DEV_ID_EXT	1	INT
320	PCI_DEV_ID_EXT	0	INT
321	PCI_DEV_ID_EXT	1	INT
322	PCI_DEV_ID_EXT	0	INT
323	PCI_DEV_ID_EXT	1	INT
324	PCI_DEV_ID_EXT	0	INT
325	PCI_DEV_ID_EXT	1	INT
326	PCI_DEV_ID_EXT	0	INT
327	PCI_DEV_ID_EXT	1	INT
328	PCI_DEV_ID_EXT	0	INT
329	PCI_DEV_ID_EXT	1	INT
330	PCI_DEV_ID_EXT	0	INT
331	PCI_DEV_ID_EXT	1	INT
332	PCI_DEV_ID_EXT	0	INT
333	PCI_DEV_ID_EXT	1	INT
334	PCI_DEV_ID_EXT	0	INT
335	PCI_DEV_ID_EXT	1	INT
336	PCI_DEV_ID_EXT	0	INT
337	PCI_DEV_ID_EXT	1	INT
338	PCI_DEV_ID_EXT	0	INT
339	PCI_DEV_ID_EXT	1	INT
340	PCI_DEV_ID_EXT	0	INT
341	PCI_DEV_ID_EXT	1	INT
342	PCI_DEV_ID_EXT	0	INT
343	PCI_DEV_ID_EXT	1	INT
344	PCI_DEV_ID_EXT	0	INT
345	PCI_DEV_ID_EXT	1	INT
346	PCI_DEV_ID_EXT	0	INT
347	PCI_DEV_ID_EXT	1	INT
348	PCI_DEV_ID_EXT	0	INT
349	PCI_DEV_ID_EXT	1	INT
350	PCI_DEV_ID_EXT	0	INT
351	PCI_DEV_ID_EXT	1	INT
352	PCI_DEV_ID_EXT	0	INT
353	PCI_DEV_ID_EXT	1	INT
354	PCI_DEV_ID_EXT	0	INT
355	PCI_DEV_ID_EXT	1	INT
356	PCI_DEV_ID_EXT	0	INT
357	PCI_DEV_ID_EXT	1	INT
358	PCI_DEV_ID_EXT	0	INT
359	PCI_DEV_ID_EXT	1	INT
360	PCI_DEV_ID_EXT	0	INT
361	PCI_DEV_ID_EXT	1	INT
362	PCI_DEV_ID_EXT	0	INT
363	PCI_DEV_ID_EXT	1	INT
364	PCI_DEV_ID_EXT	0	INT
365	PCI_DEV_ID_EXT	1	INT
366	PCI_DEV_ID_EXT	0	INT
367	PCI_DEV_ID_EXT	1	INT
368	PCI_DEV_ID_EXT	0	INT
369	PCI_DEV_ID_EXT	1	INT
370	PCI_DEV_ID_EXT	0	INT
371	PCI_DEV_ID_EXT	1	INT
372	PCI_DEV_ID_EXT	0	INT
373	PCI_DEV_ID_EXT	1	INT
374	PCI_DEV_ID_EXT	0	INT

GF108 Straps

Bit Signal		Values			
03	ARMV8C0	0000	ARMV8C0_128bit	1000	ARMV8C0_128bit
		0001	ARMV8C0_128bit Mono	1001	ARMV8C0_128bit Mono
		0010	ARMV8C0_128bit Dual	1010	ARMV8C0_128bit Dual
04	ARMV8C1C0	0000	ARMV8C1C0_128bit Streaming	1011	ARMV8C1C0_128bit Streaming
		0001	ARMV8C1C0_128bit Mono	1100	ARMV8C1C0_128bit Mono
		0010	ARMV8C1C0_128bit Dual	1101	ARMV8C1C0_128bit Dual
05	ARMV8C1C1	0000	ARMV8C1C1_128bit Mono	1110	ARMV8C1C1_128bit Mono
		0001	ARMV8C1C1_128bit Dual	1111	ARMV8C1C1_128bit Dual
		0010	ARMV8C1C1_128bit Streaming	1111	ARMV8C1C1_128bit Streaming
06	NEON_A67	0	DISABLED	1	ENABLED
07	TRIS	1	ENABLE		
08	DMB_ALT_ADDR	0	DISABLED	1	ENABLED
09	VSIM_DEVICE	0	DISABLED	1	ENABLED
10	POL_DEV_ID_A17	5	OF 10-450-A1		
11	SWB_VENDOR	0	NO_BUG	1	BUGS
11	BOOT_CLK_CTS0	0	DISABLED	1	ENABLED
12	PERF_PUL_ON_TERRAP	0	DISABLED	1	ENABLED
13	USBR0	0000	REF		
	USBR1				
	USBR2				
	USBR3				
14	GPIO_FUNC0_LUT_ADDR0	0000	GPIO_FUNC0_LUT_ADDR0	0000	GPIO_FUNC0_LUT_ADDR0
		0001	MOBILE_ITRHS_LAMP	0001	MOBILE_ITRHS_LAMP
		0010	MOBILE_ITRHS_LAMP	0010	MOBILE_ITRHS_LAMP
		0011	MOBILE_ITRHS_LAMP	0011	MOBILE_ITRHS_LAMP
15	GPIO_FUNC0_LUT_ADDR1	0000	MOBILE_ITRHS_LAMP	0100	MOBILE_ITRHS_LAMP
		0001	MOBILE_ITRHS_LAMP	0101	MOBILE_ITRHS_LAMP
		0010	MOBILE_ITRHS_LAMP	0110	MOBILE_ITRHS_LAMP
		0011	MOBILE_ITRHS_LAMP	0111	MOBILE_ITRHS_LAMP
16	GPIO_FUNC0_LUT_ADDR2	0000	MOBILE_ITRHS_LAMP	1000	MOBILE_ITRHS_LAMP
		0001	MOBILE_ITRHS_LAMP	1001	MOBILE_ITRHS_LAMP
		0010	MOBILE_ITRHS_LAMP	1010	MOBILE_ITRHS_LAMP
		0011	MOBILE_ITRHS_LAMP	1011	MOBILE_ITRHS_LAMP
17	POL_DEV_ID_A17	5	OF 10-450-A1		
18	POL_DEV_ID0				
19	POL_DEV_ID1				
20	POL_DEV_ID2				

Multilevel Straps

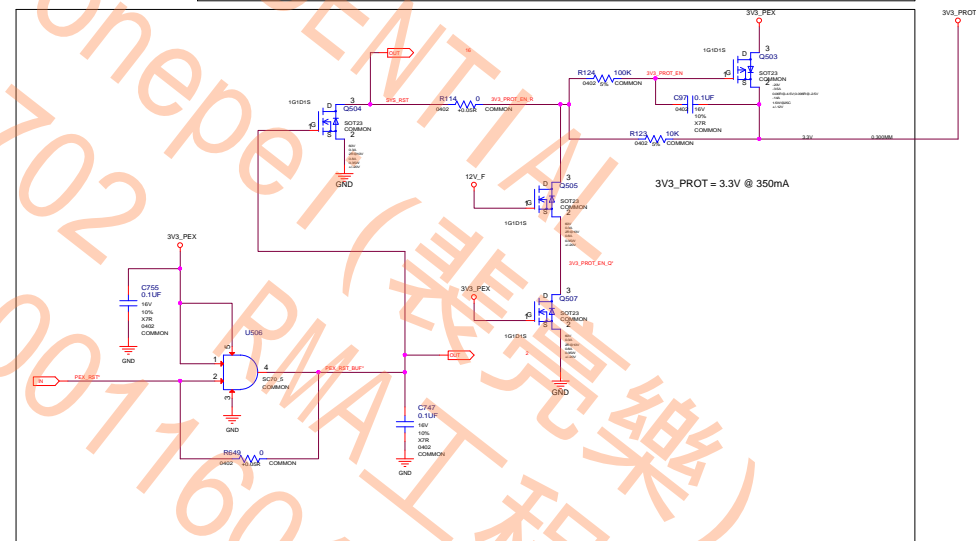
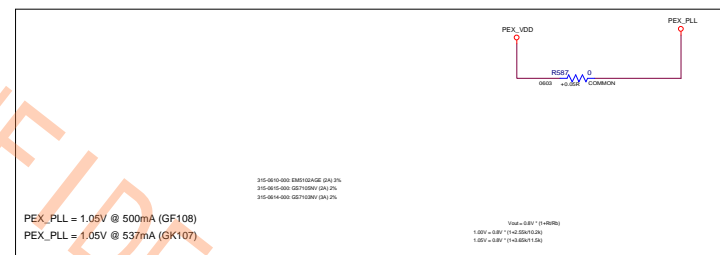
0000	5.1K to GND	1000	5.1K to VCC
0001	10K to GND	1001	10K to VCC
0010	15K to GND	1010	15K to VCC
0011	20K to GND	1011	20K to VCC
0100	24.9K to GND	1100	24.9K to VCC
0101	30.1K to GND	1101	30.1K to VCC
0110	34.8K to GND	1110	34.8K to VCC
0111	45.3K to GND	1111	45.3K to VCC




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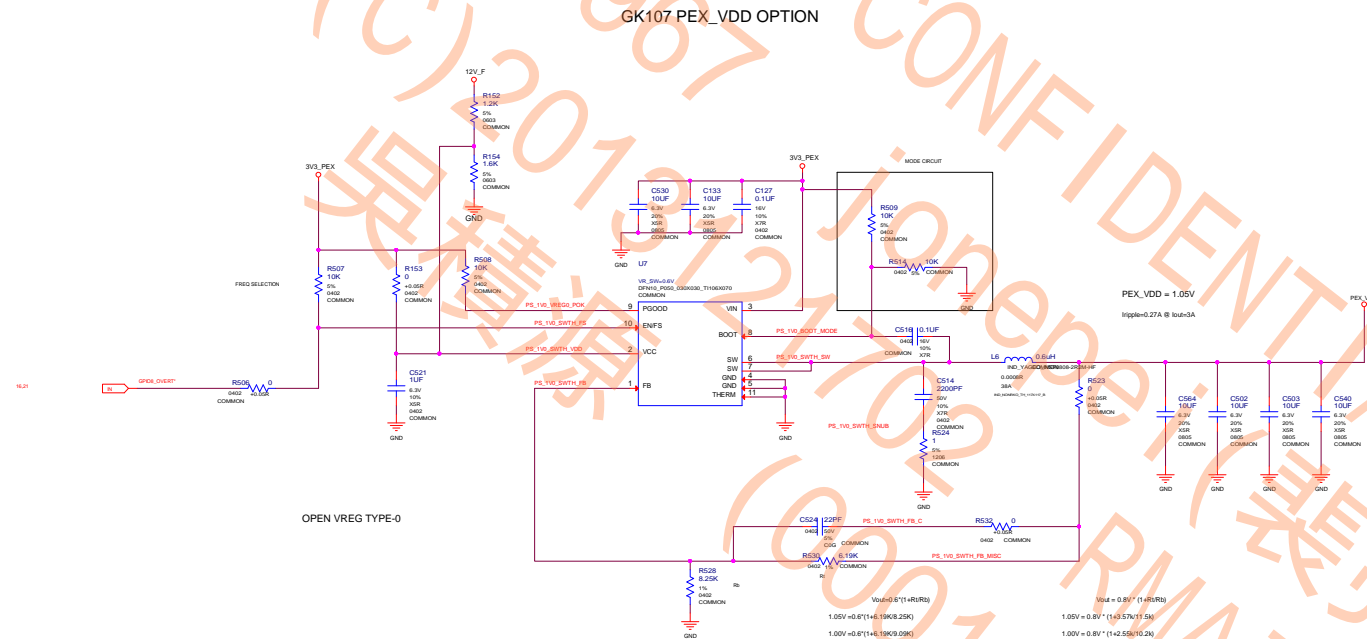
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Power Supply II: PEX_VDD

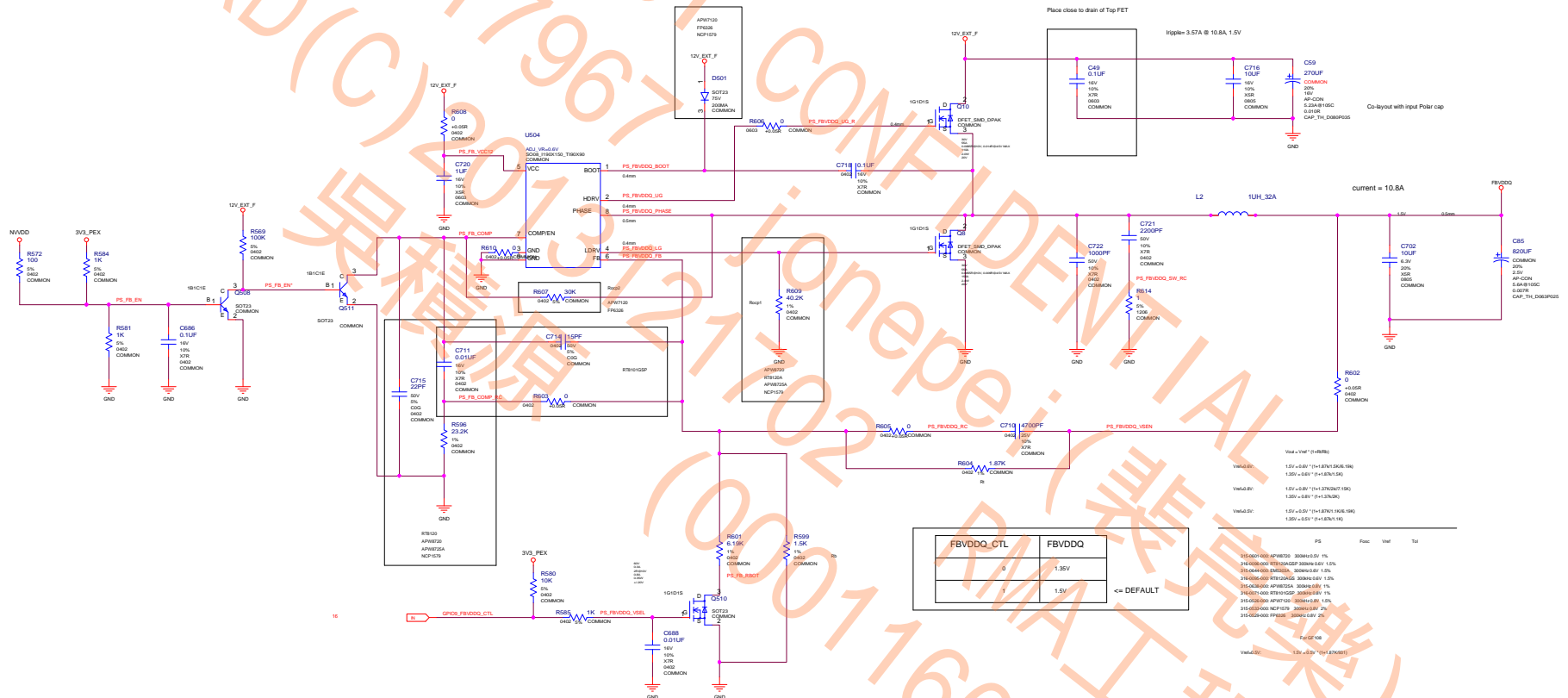


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FBVDDQ

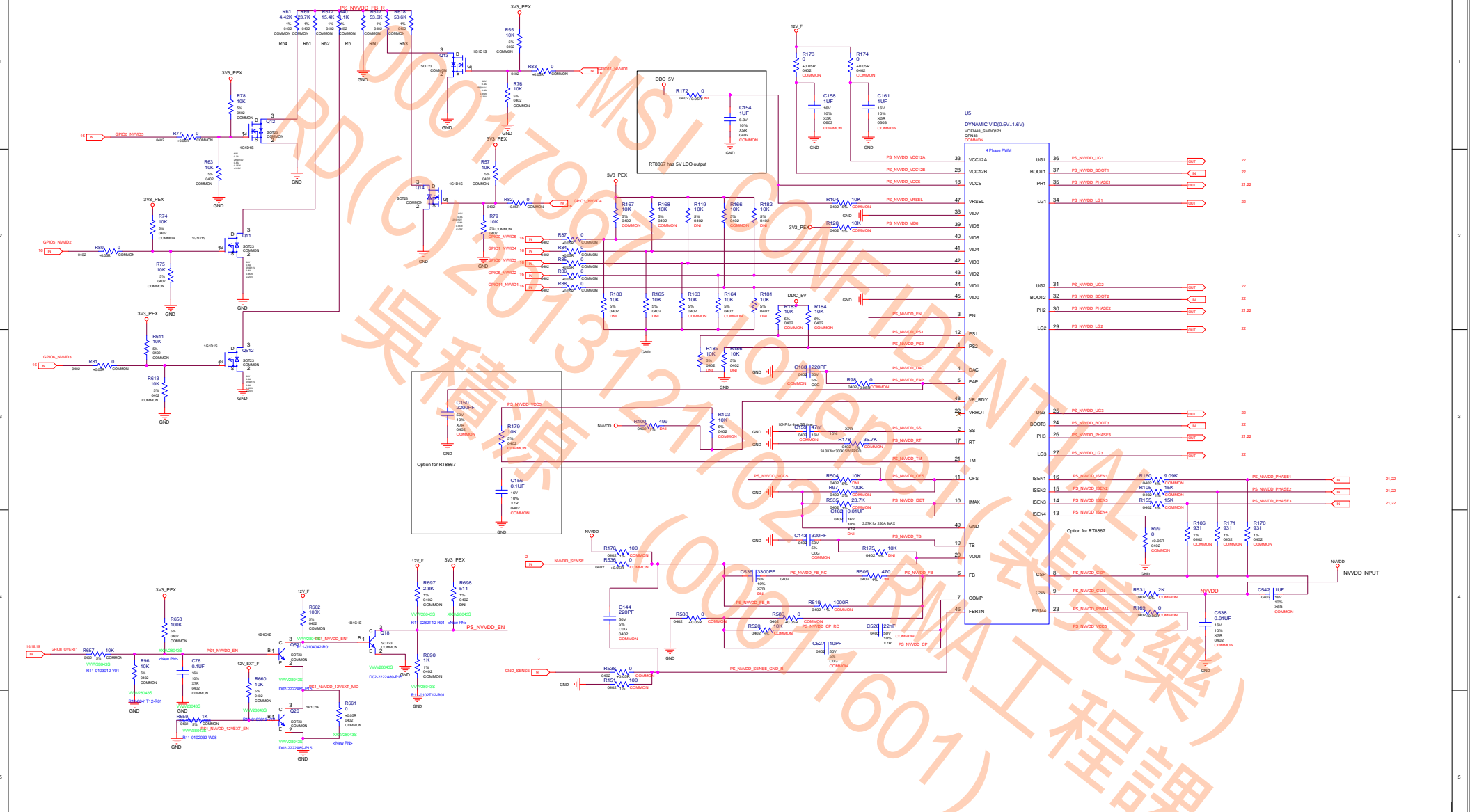


FBVDDQ_CTL	FBVDDQ
0	1.35V
1	1.5V

<= DEFAULT

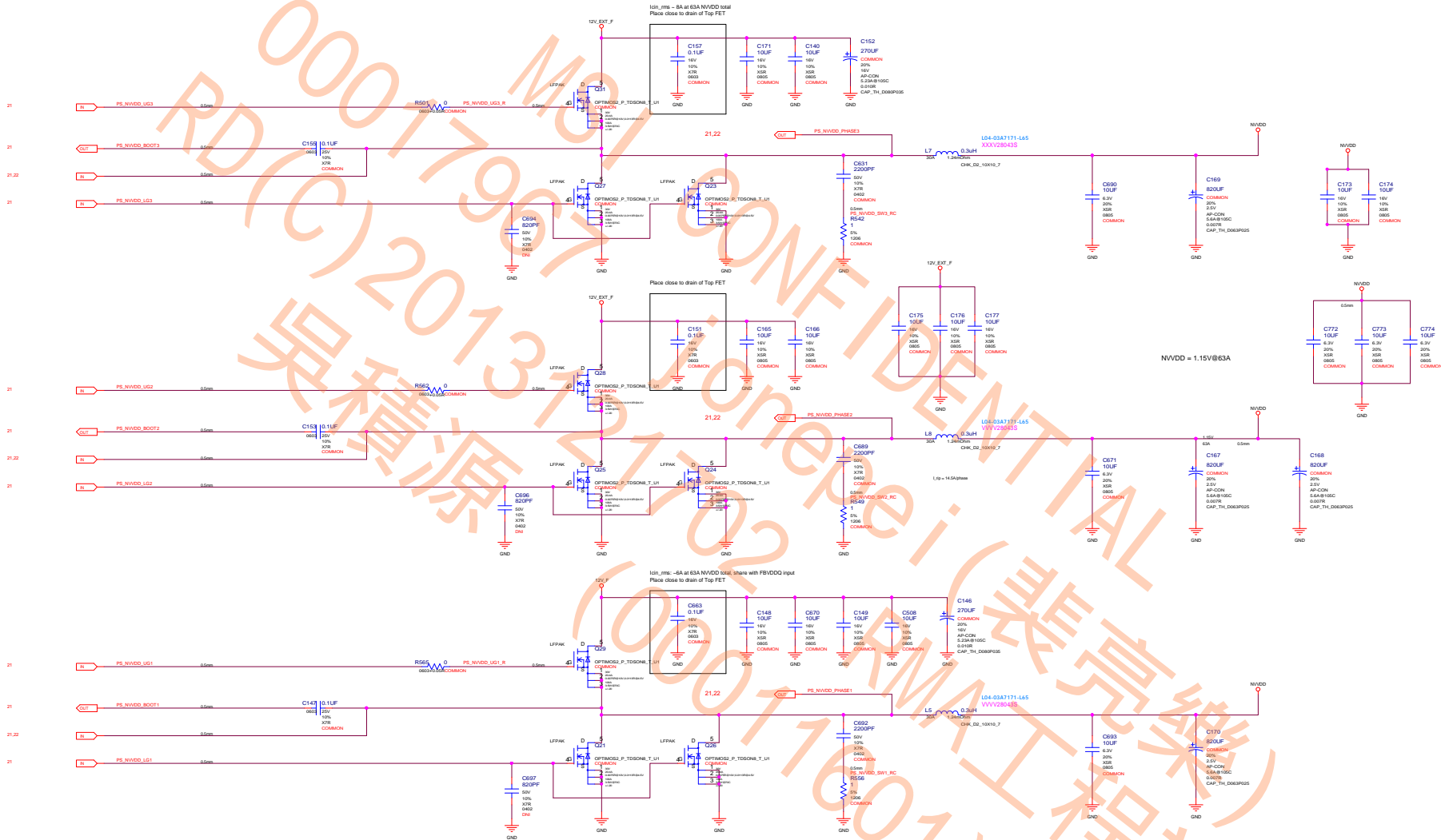
[illegible]

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1. Low Side MOSFET change to PowerPAK NIKO 0903 vs. 0403
2. Output Choke change to 0.3uH



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