

PG142-B00

DP + DP + DP + HDMI/DP

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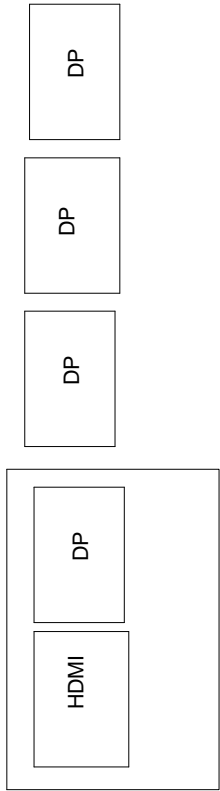
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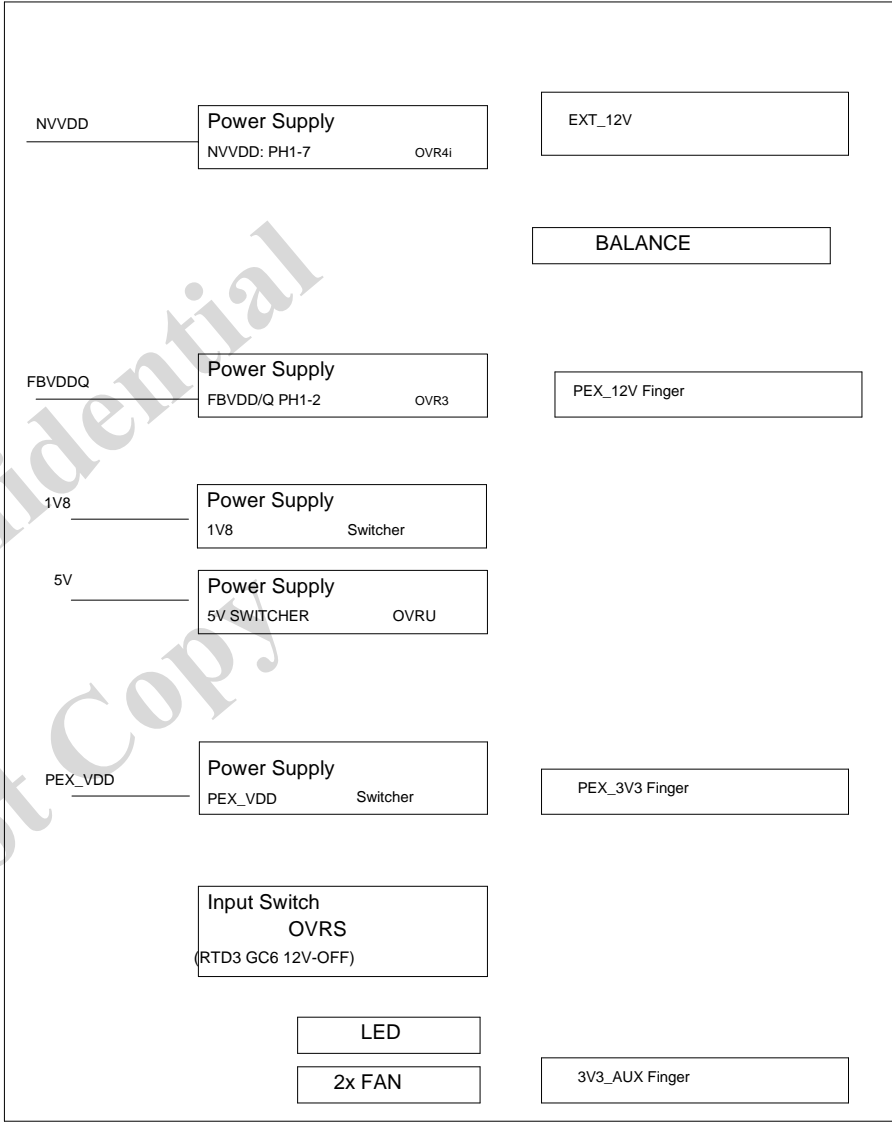
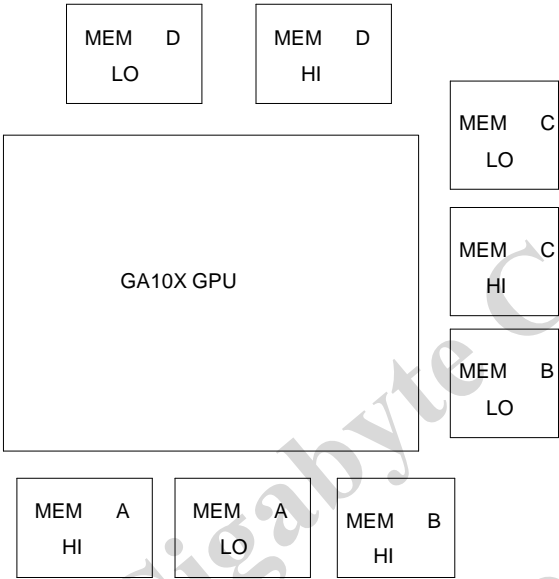
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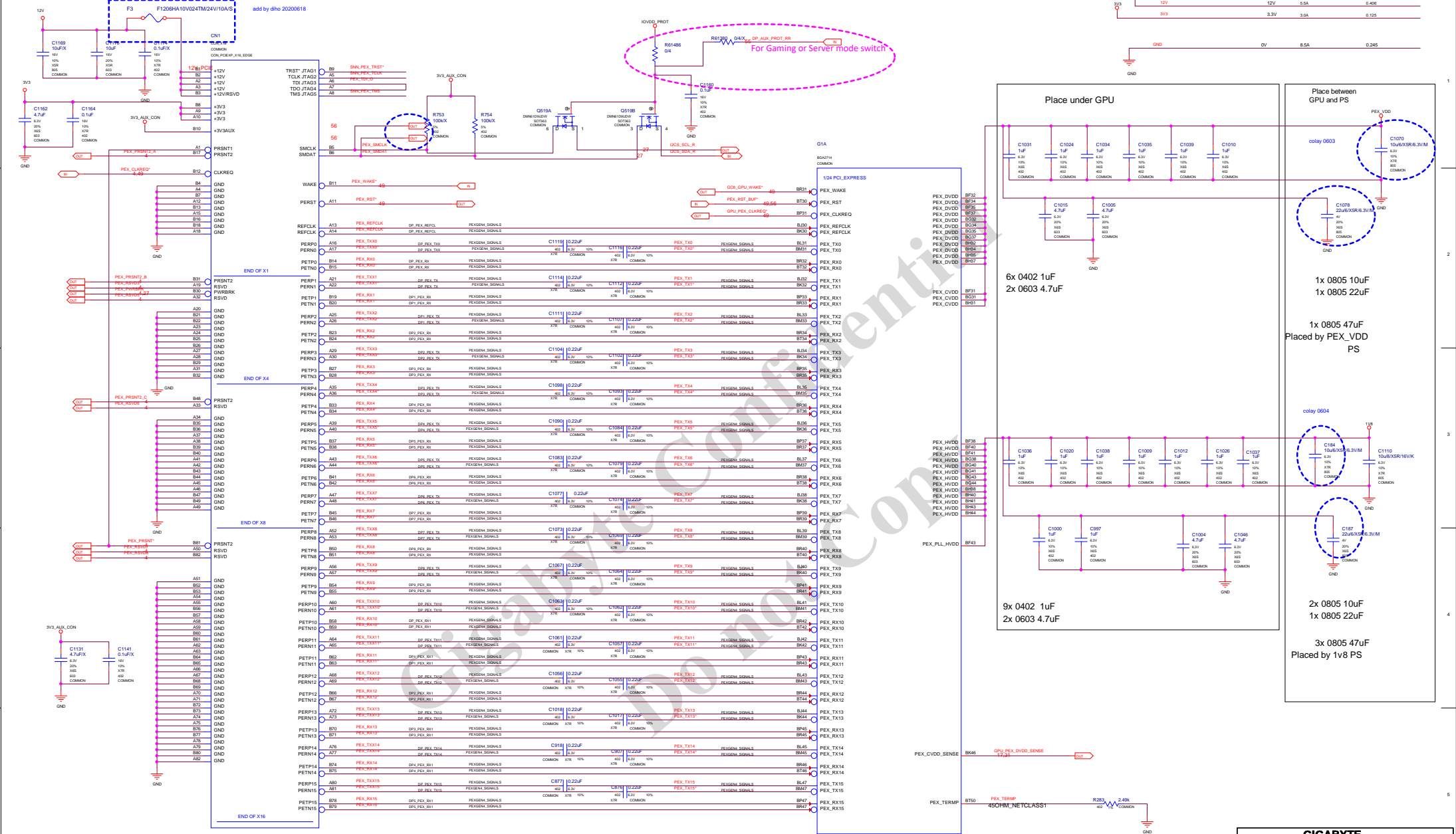
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PAGE DETAIL	PCI Express

**GIGABYTE**

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Size Custom	Document Number GV-N3070GAMING OC-8GD		Rev 1.0
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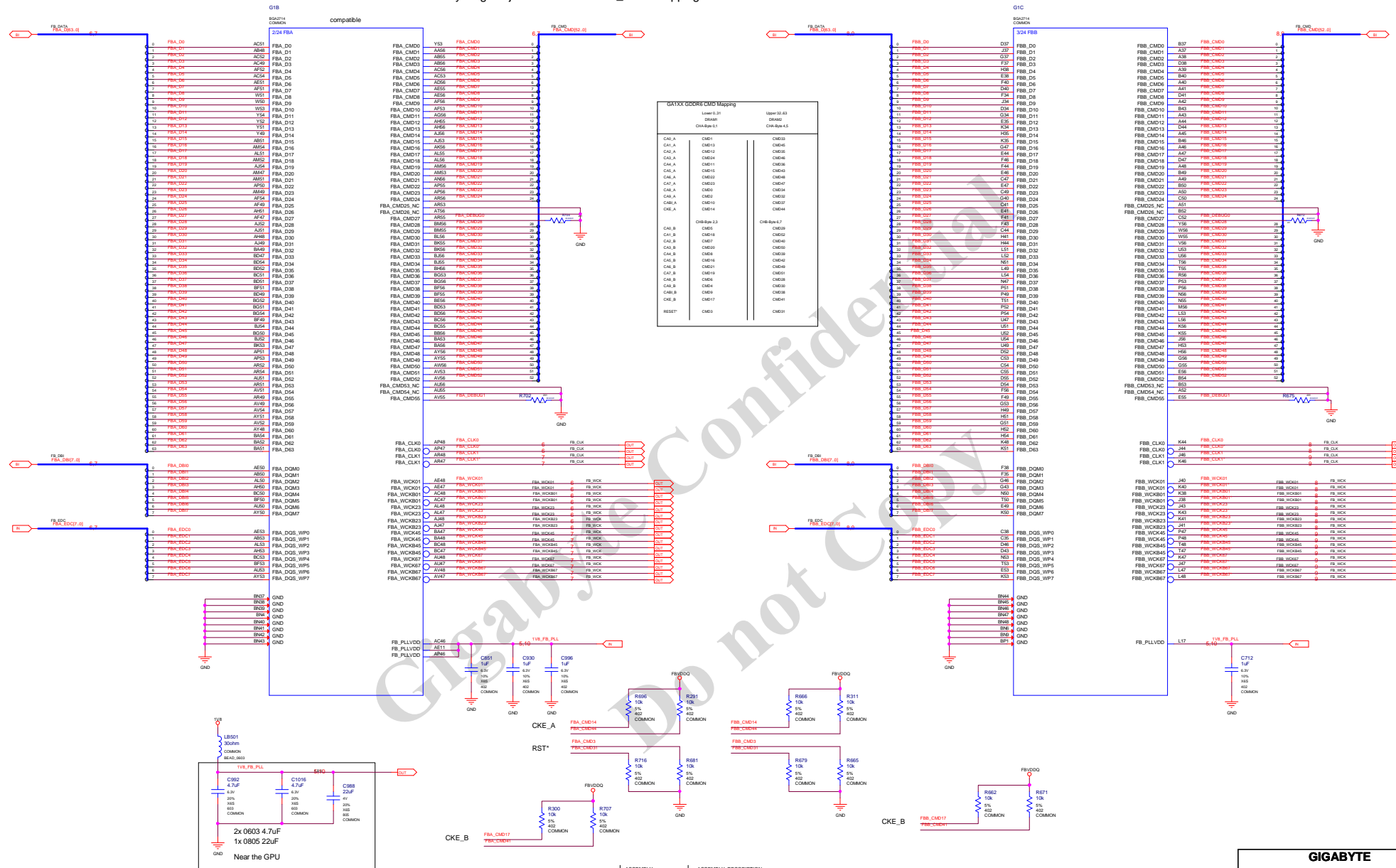


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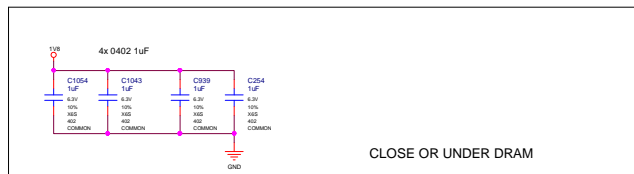
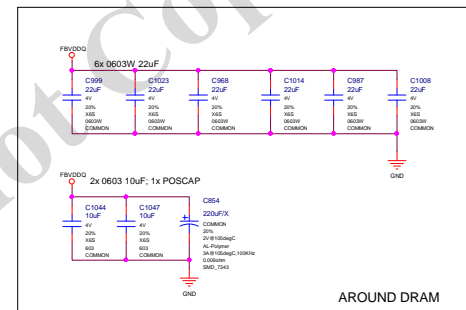
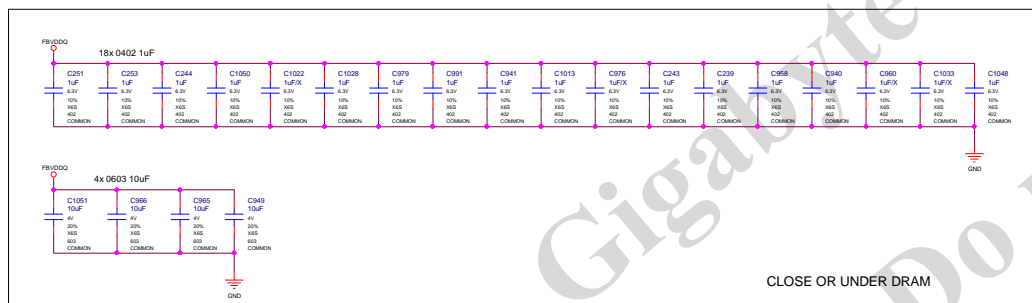
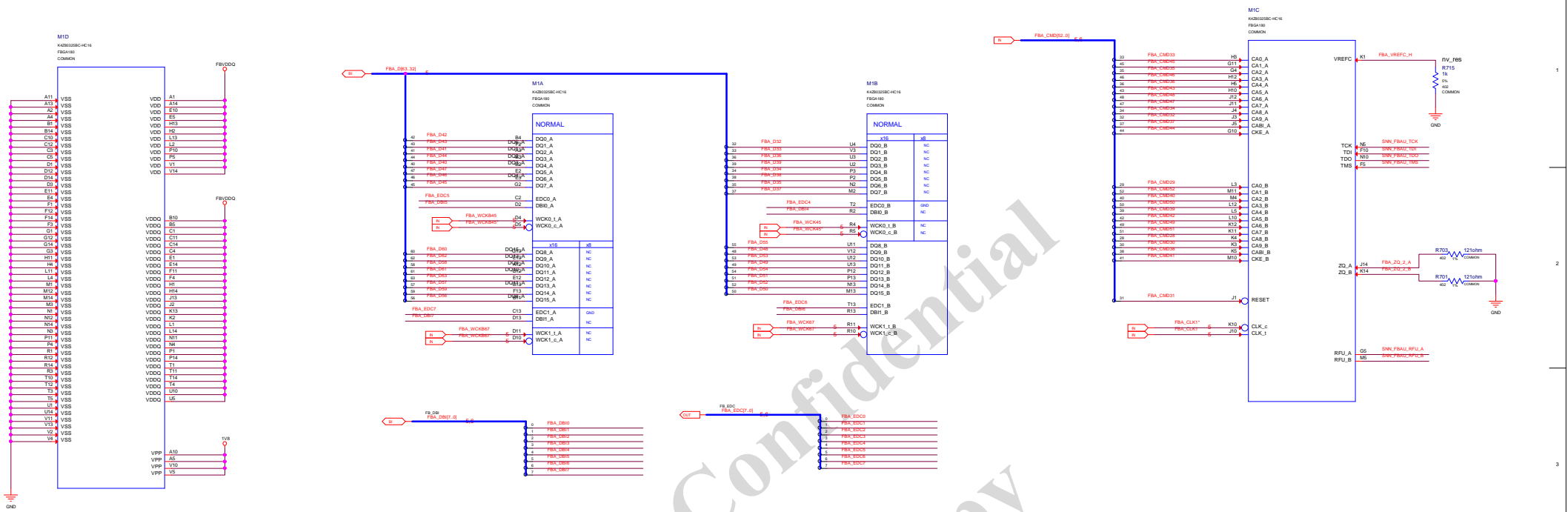
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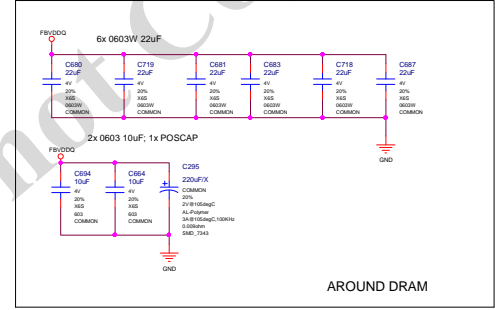
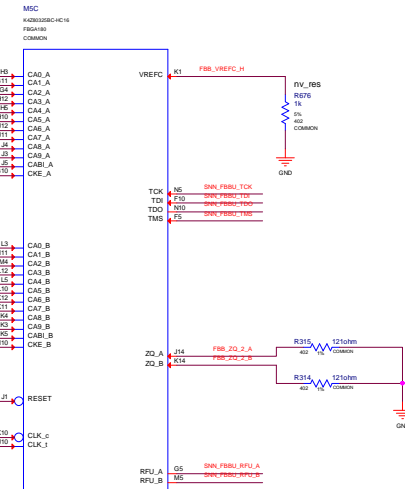
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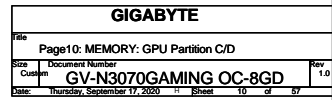
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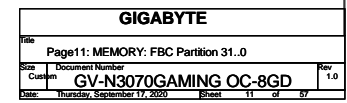
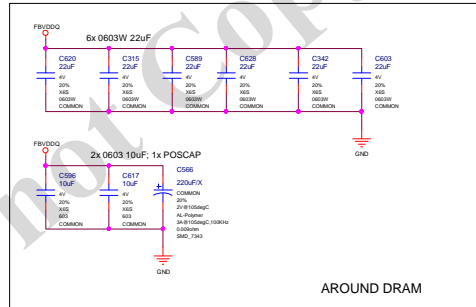


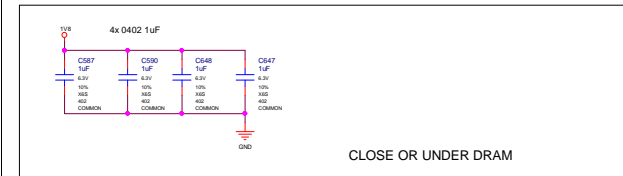
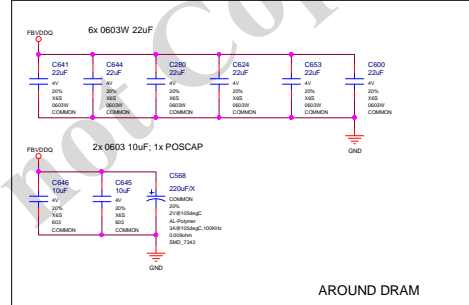
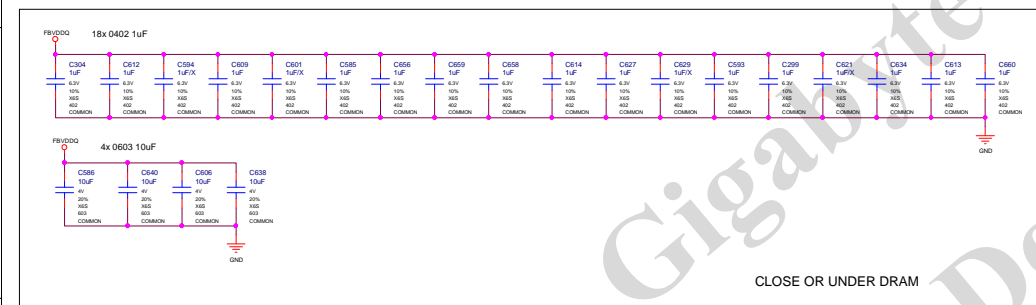
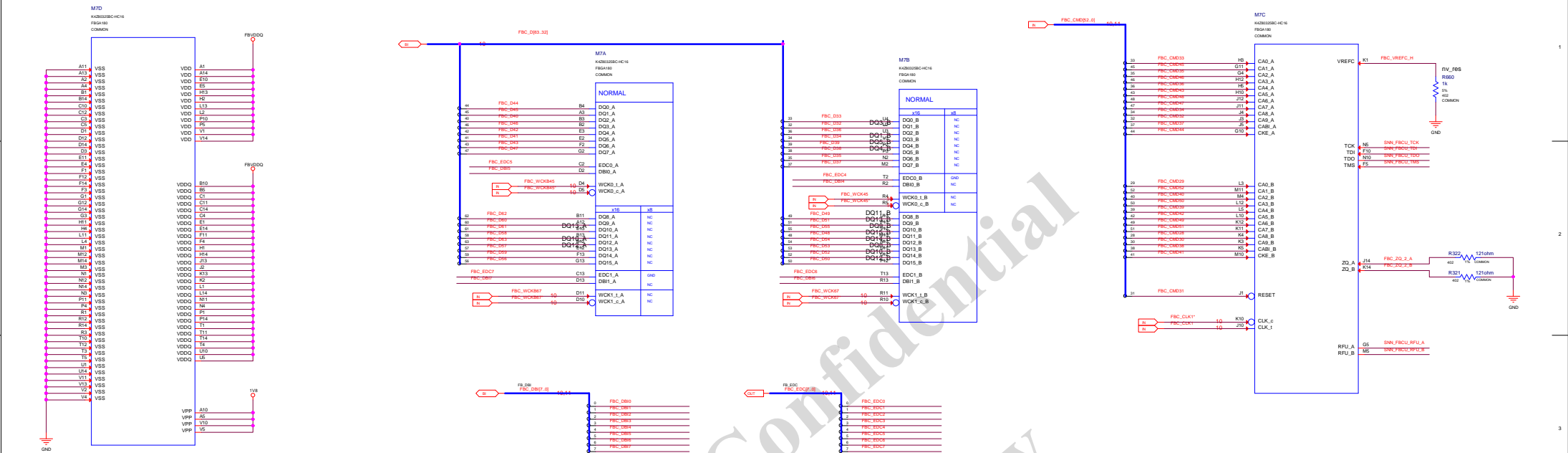






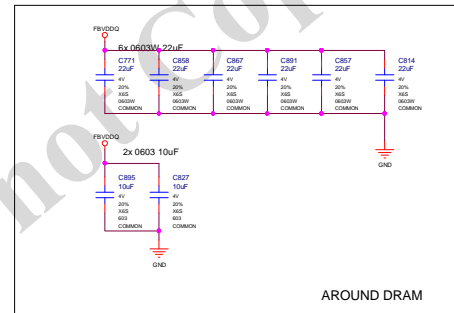
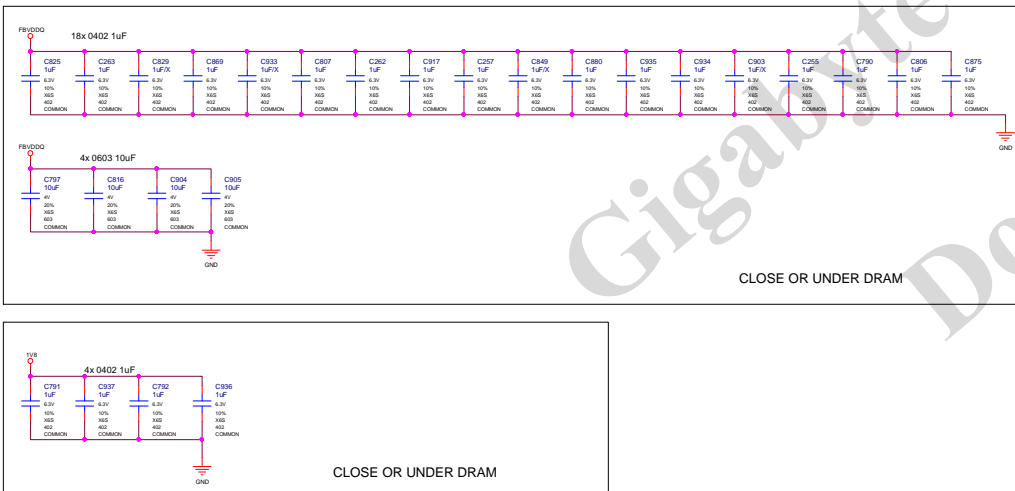


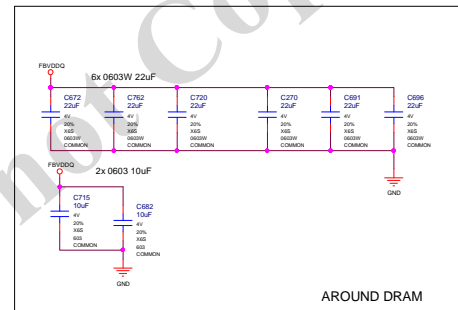
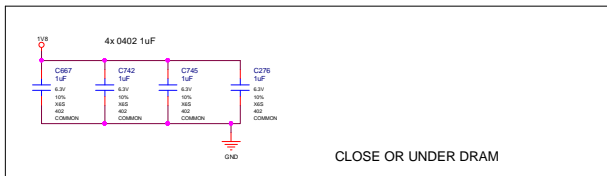
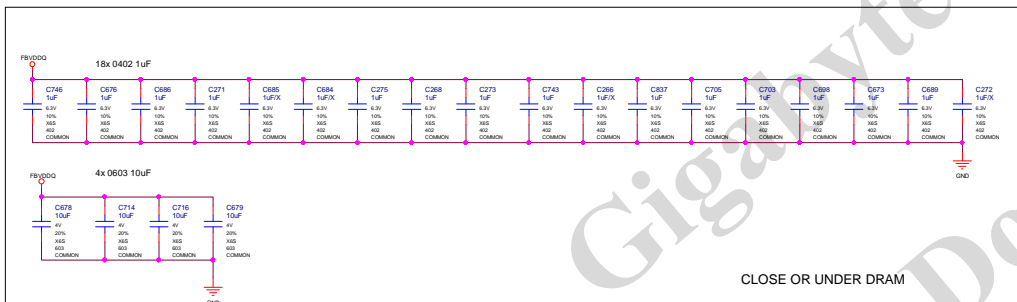
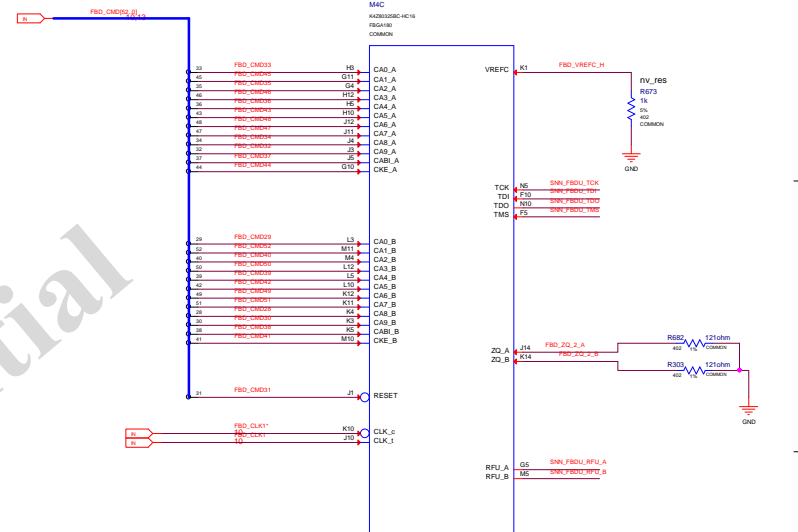
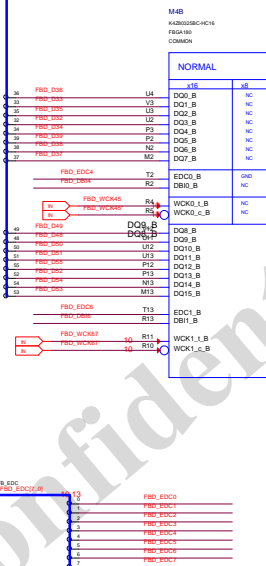
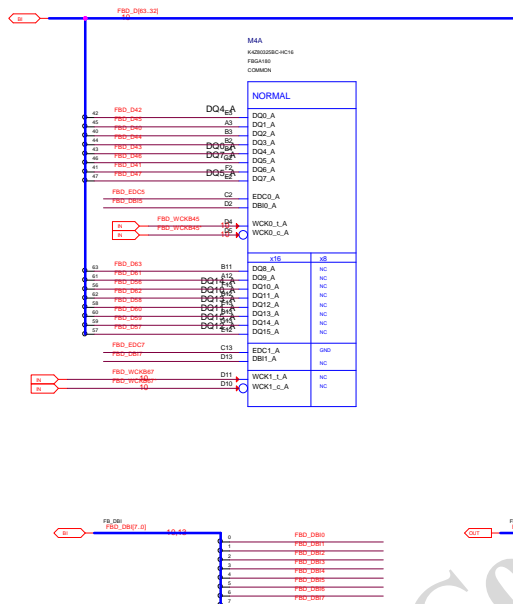
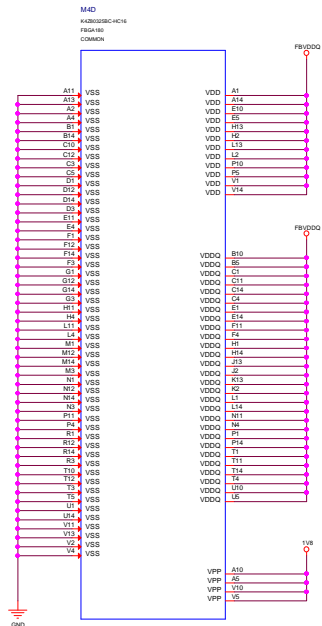




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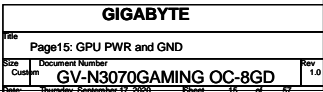




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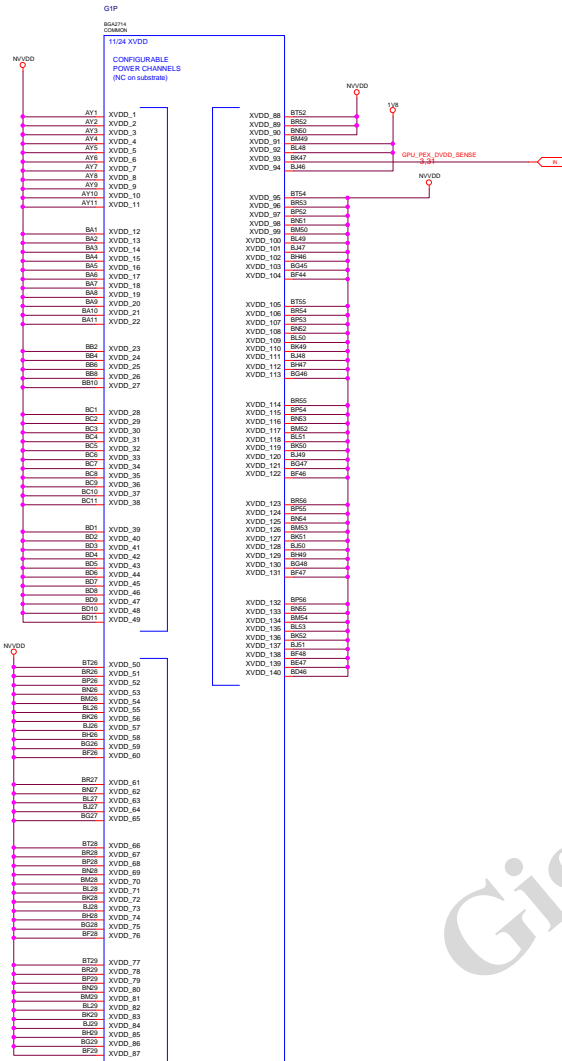
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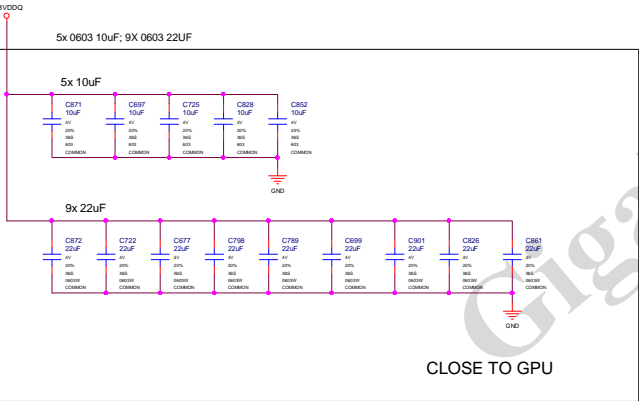
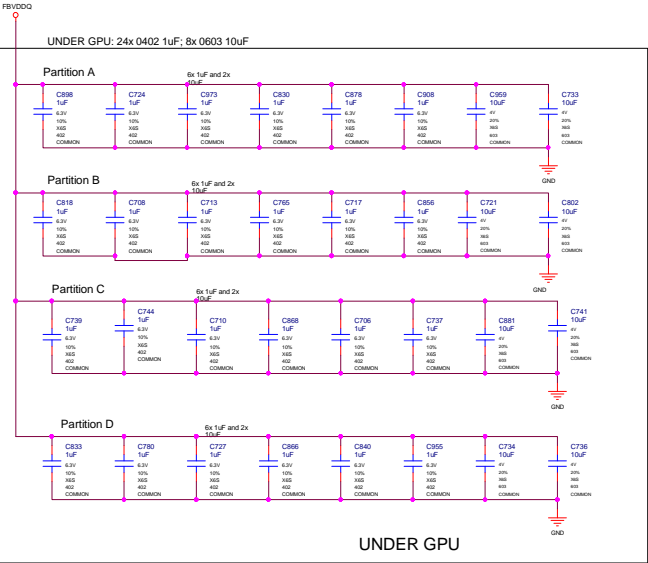


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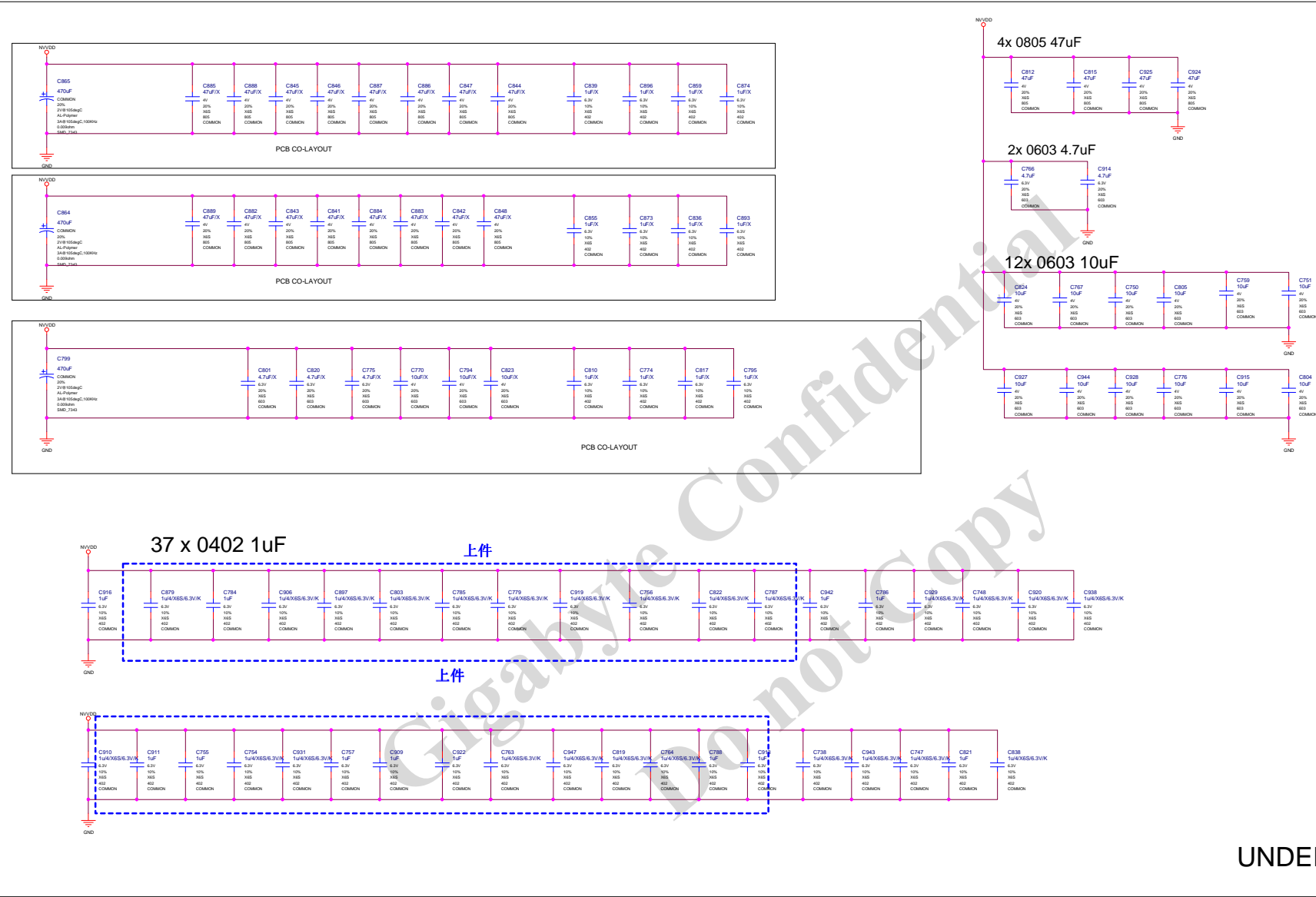
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GPU PWR & GND 3  
(XVDD)

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FBVDDQ



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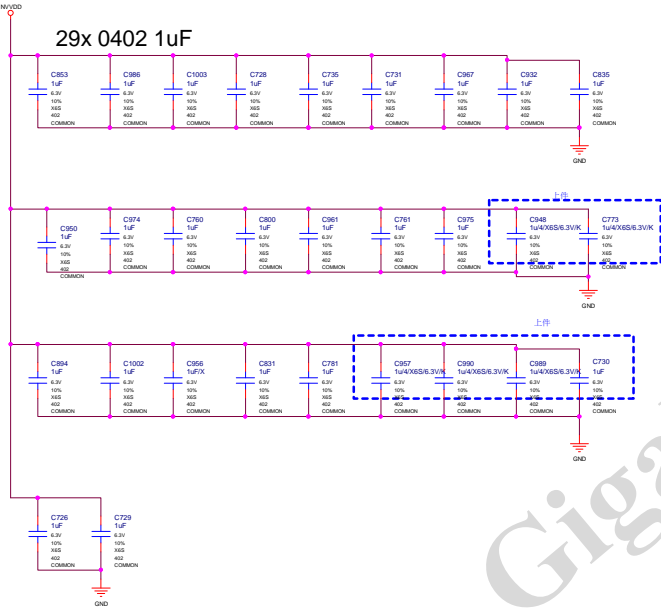
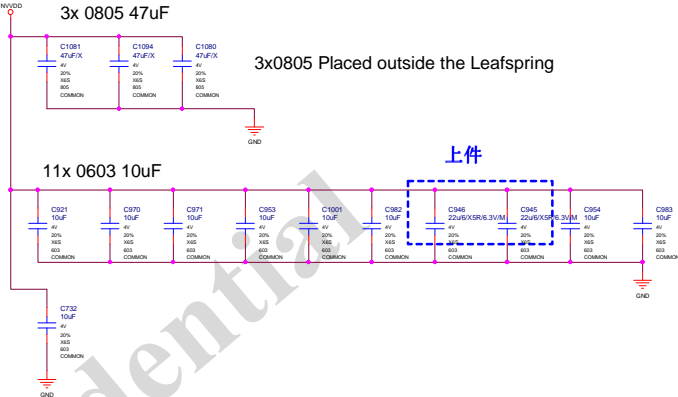
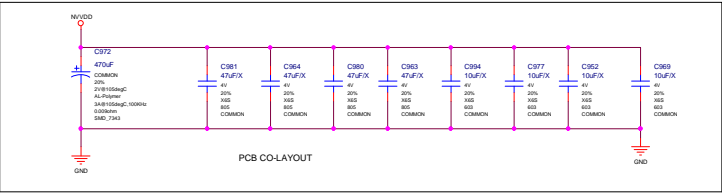


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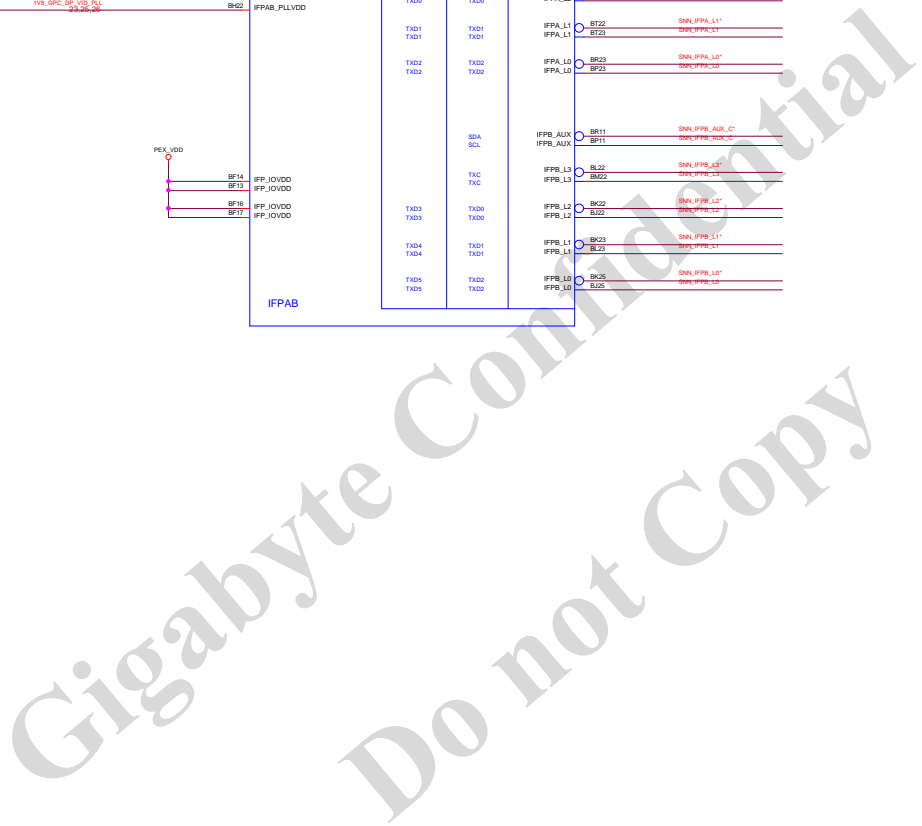


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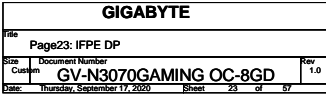
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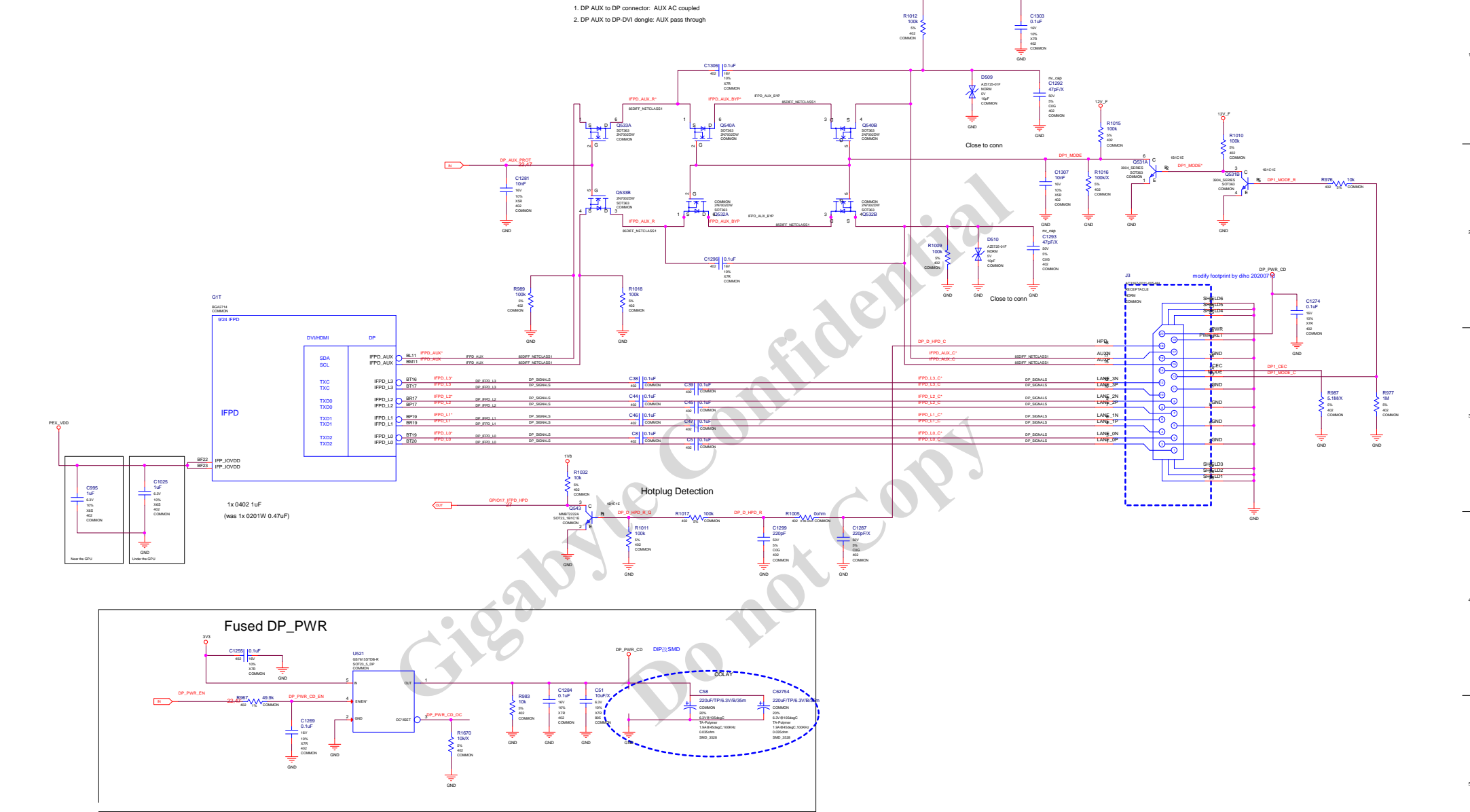
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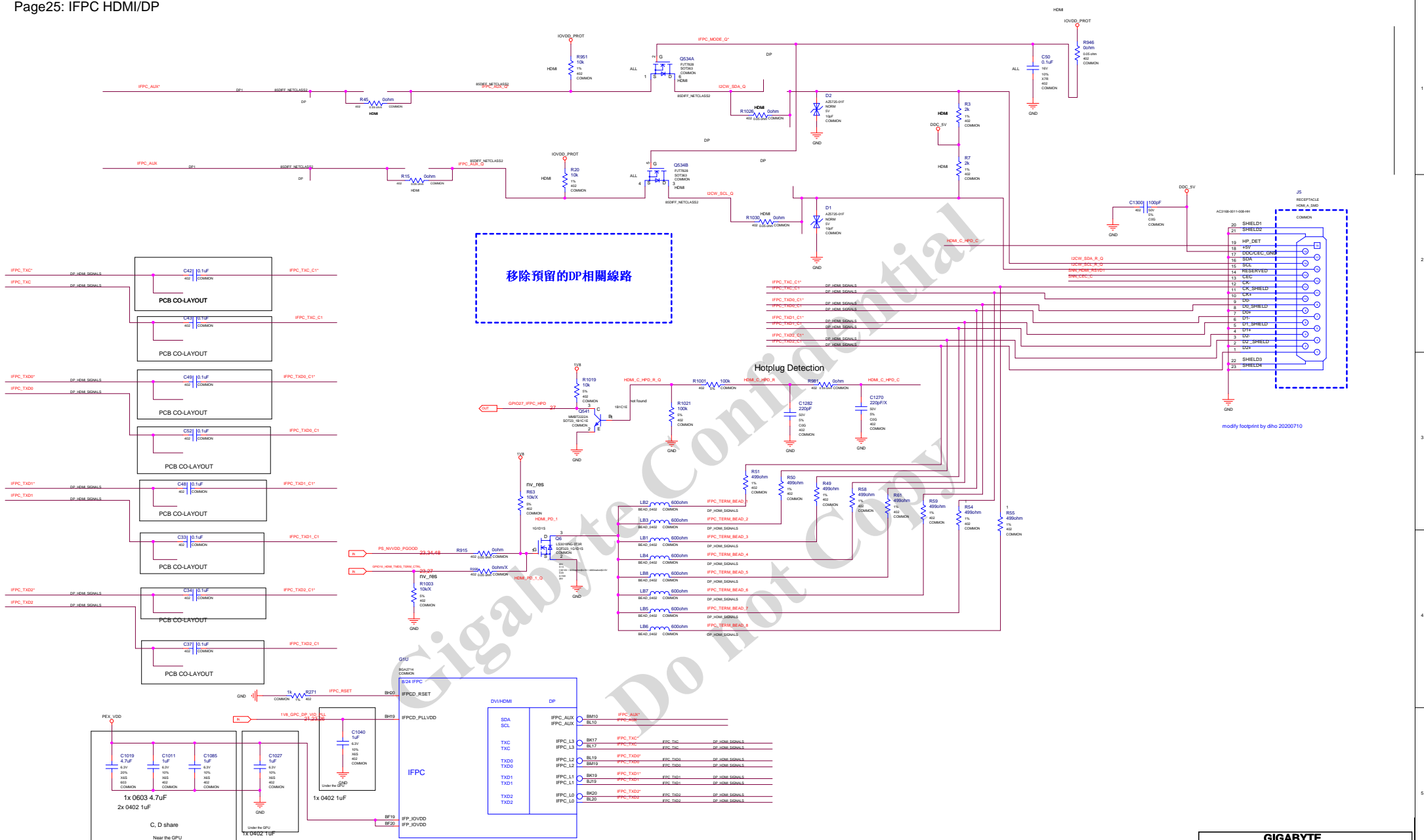


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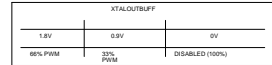
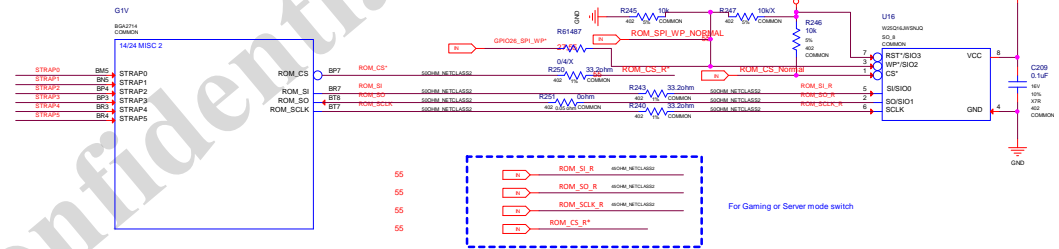
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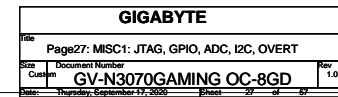
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1:PCIE\_CFG LOW POWER  
0:PCIE\_CFG HIGH POWER

1:VGA\_DEVICE ENABLE  
0:VGA\_DEVICE DISABLE





1: 12V  
2: BLUE  
3: GREEN  
4: RED  
5: WHITE

WHITE DRIVE

Del NV LED

LOGO LED WHITE

LOGO LED RED

RED DRIVE

GREEN DRIVE

BLUE DRIVE

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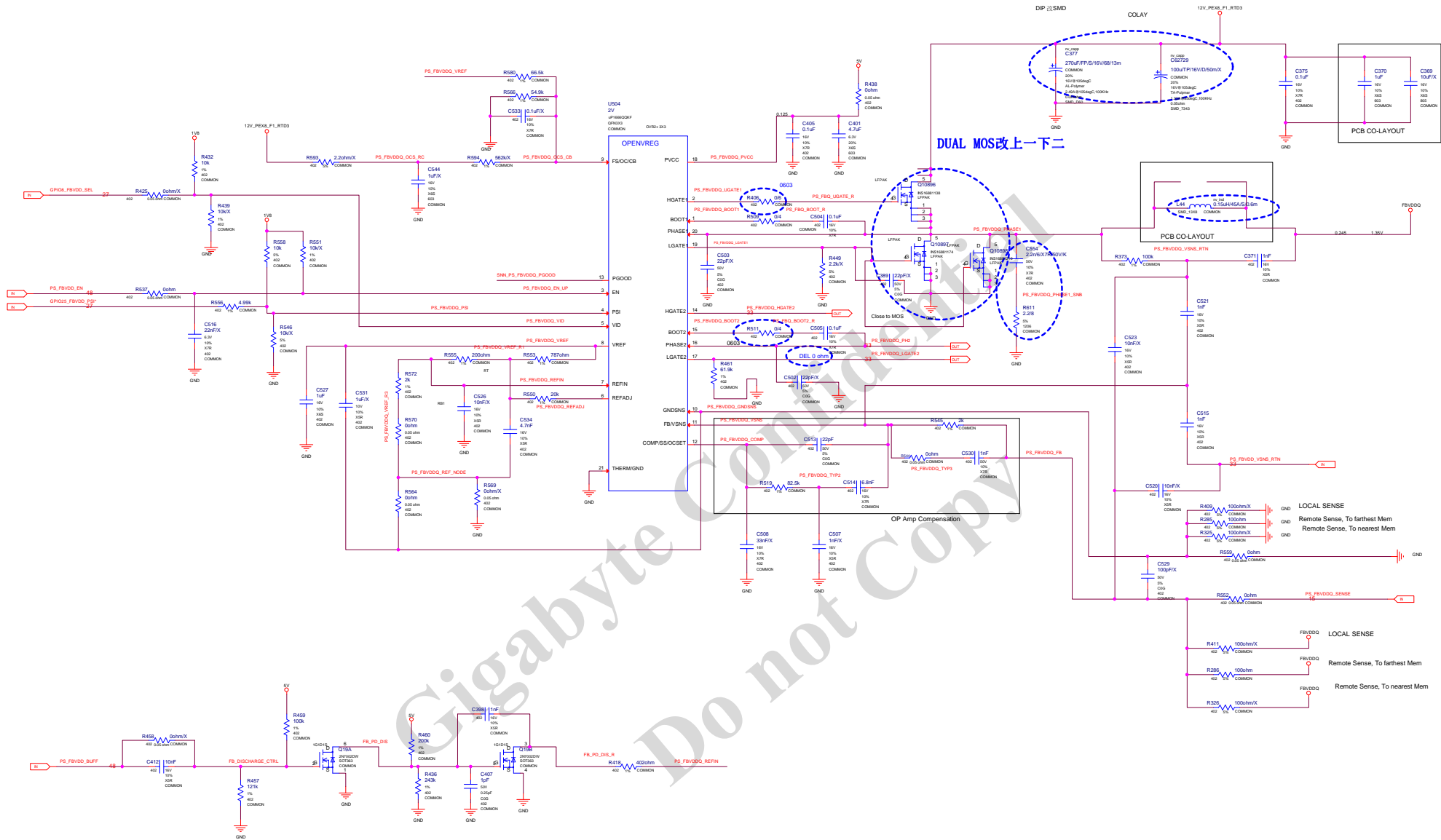
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ASSEMBLY  
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PS: FBVDDQ

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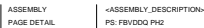
Page32: PS: FBVDDQ

Document Number  
GV-N3070GAMING OC-8GD

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1.0

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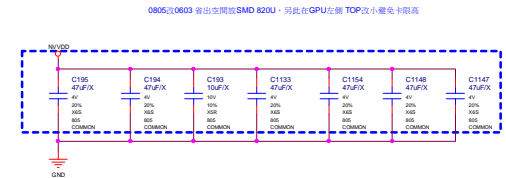
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Title			
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Custom	GV-N3070GAMING OC-8GD		1.0
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0805改0603 雷石空間於SMD B20U，另此在GPU左側 TOP改小電容半圈高

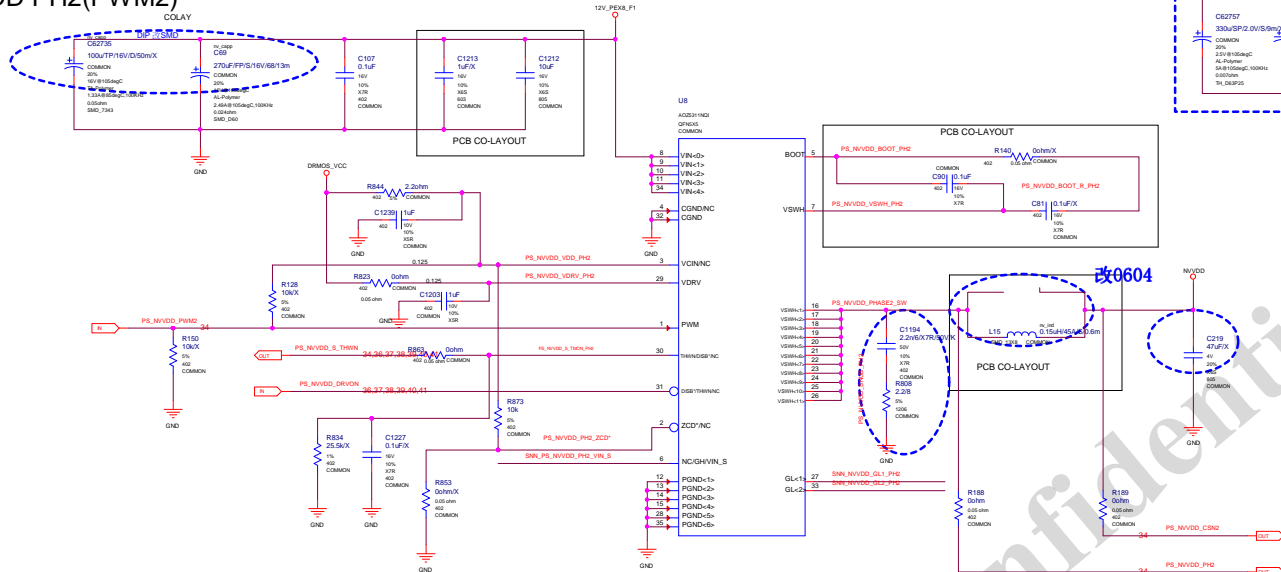
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COMMON  
4  
2.5V1687m  
COMMON  
C148  
820pF ±2.5V1687m  
COMMON  
C175  
47pF  
COMMON  
C166  
47pF  
COMMON  
C156  
47pF  
COMMON  
C136  
47pF  
COMMON  
C131  
47pF  
COMMON

CR2746  
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COMMON  
4  
2.5V1687m  
COMMON  
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C166  
47pF  
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C136  
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C131  
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COMMON

PCB CO-LAYOUT

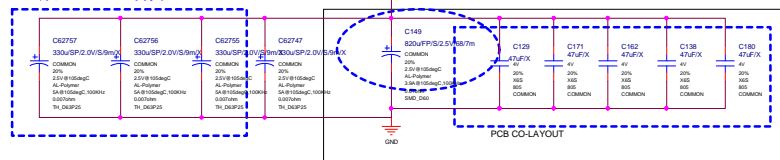
colayout only no self standing mlccs

## NVVDD PH2(PWM2)

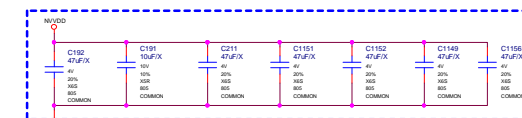


放BOTTOM 预留

DIP 2(SMD)

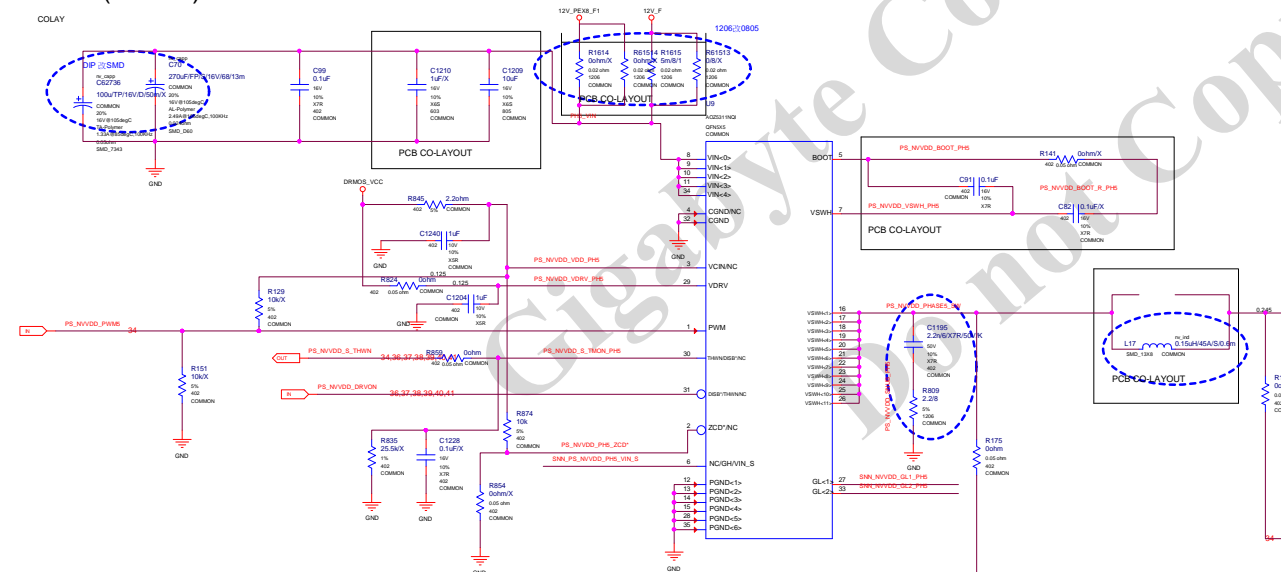


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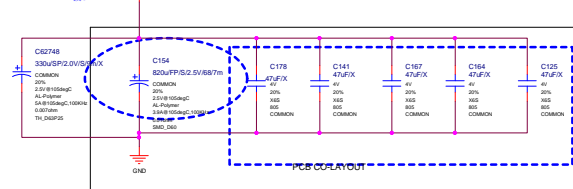


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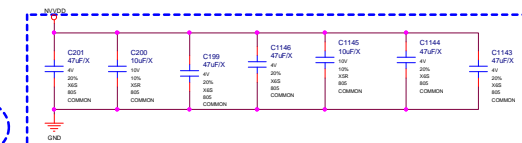
## NVVDD PH5(PWM5)



DIP 2(SMD)



PCB CO-LAYOUT



PCB CO-LAYOUT

ASSEMBLY  
PAGE DETAILASSEMBLY DESCRIPTION  
PS NVVDD PH2, PH5

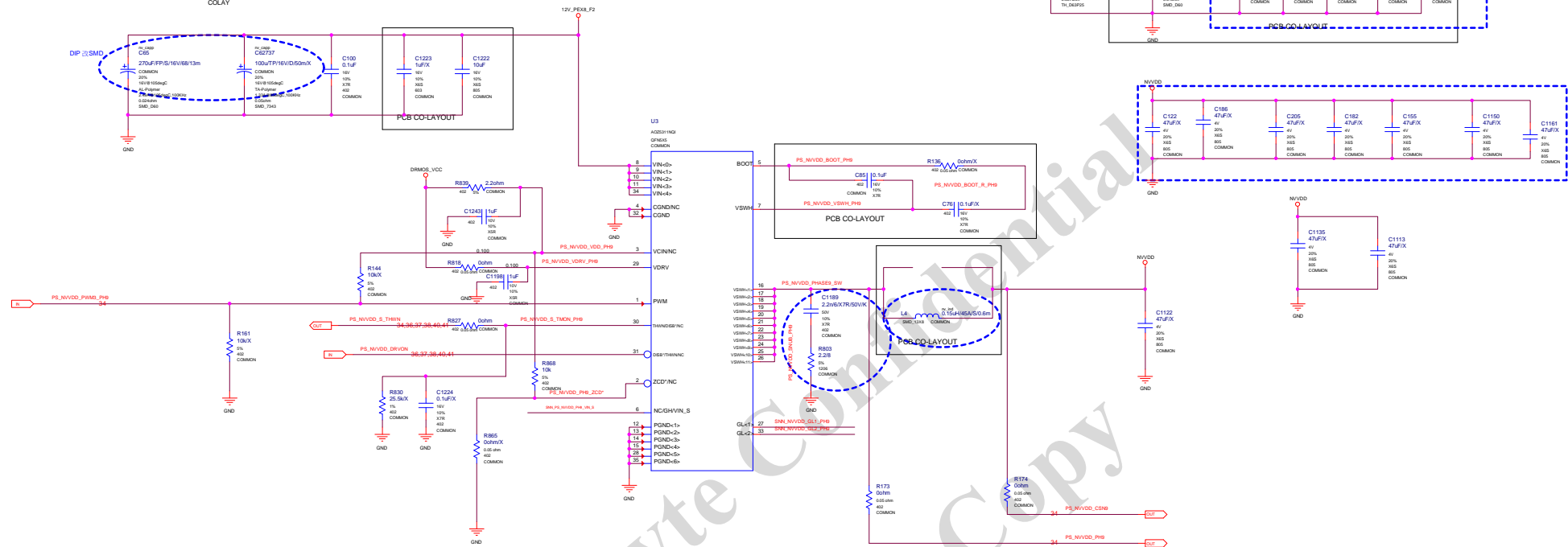
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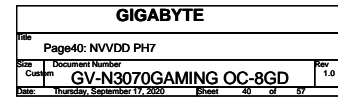
Page38: NVVDD PH2 & PH5			
Size	Document Number	Rev	1.0
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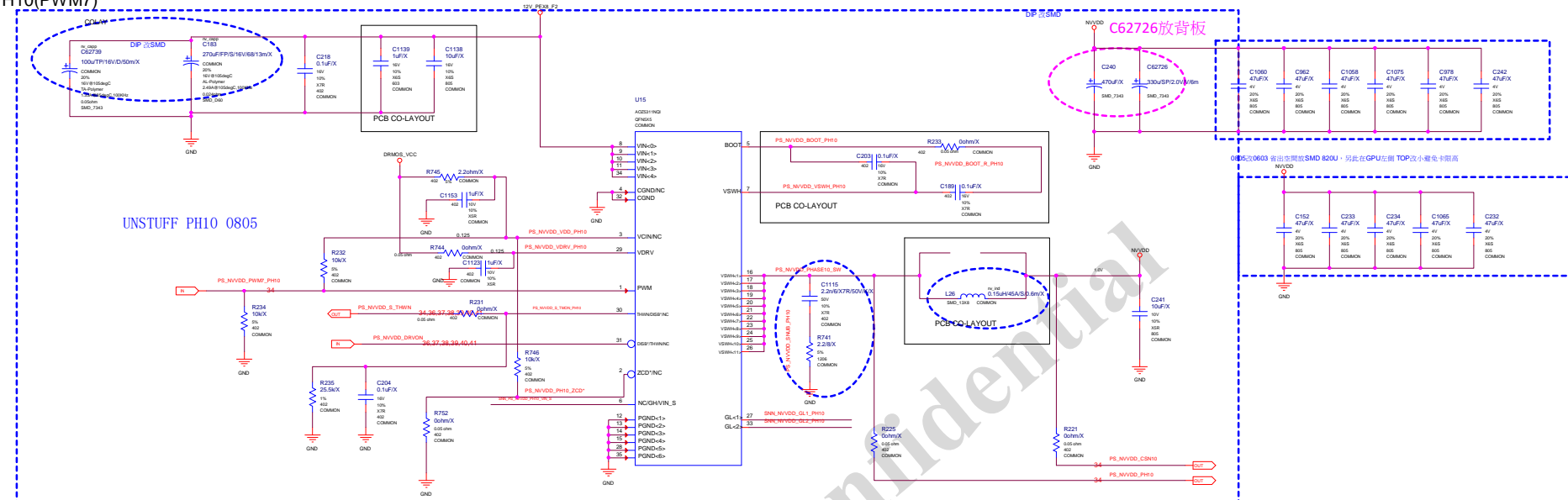
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COLAY

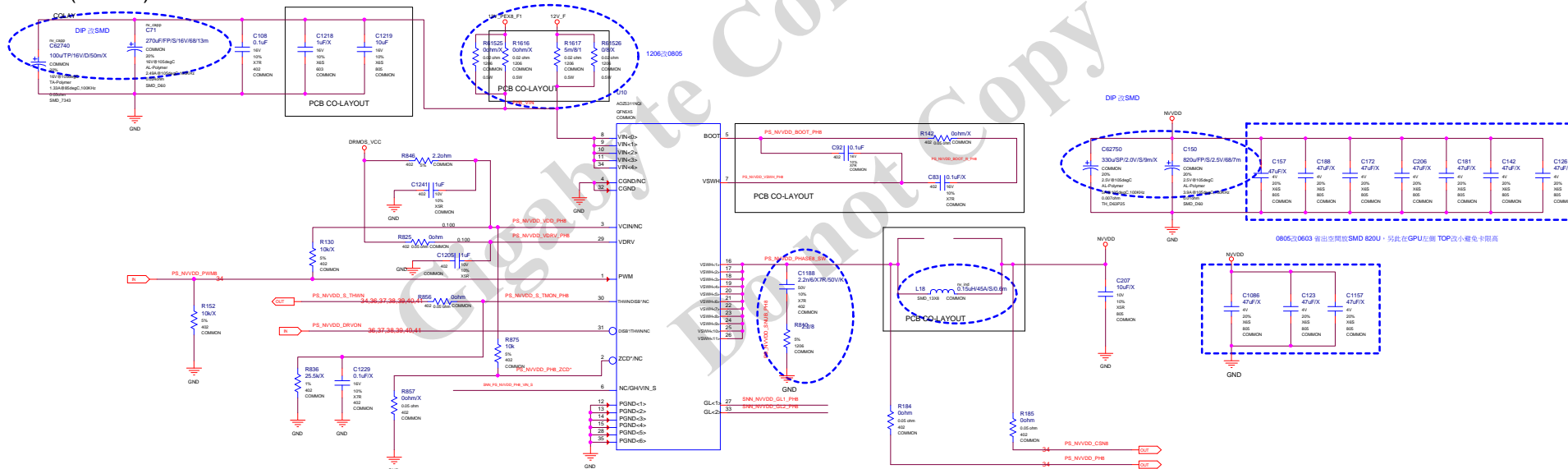


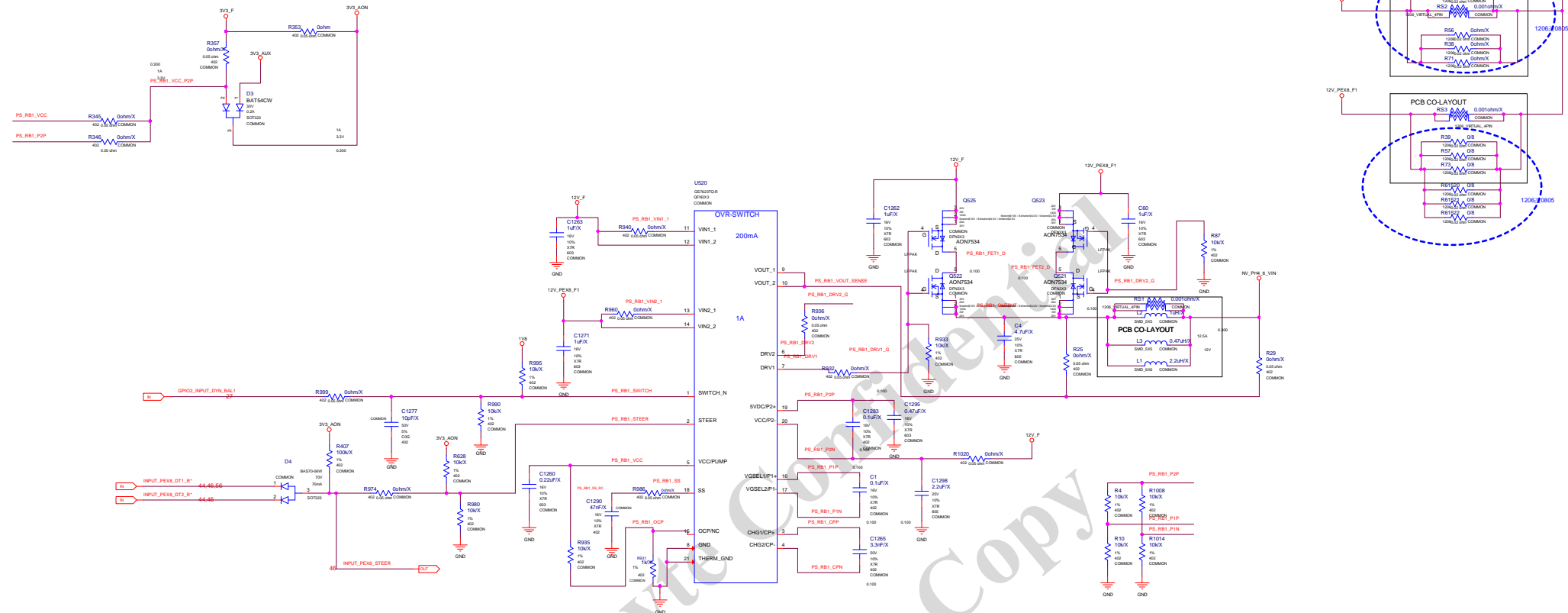






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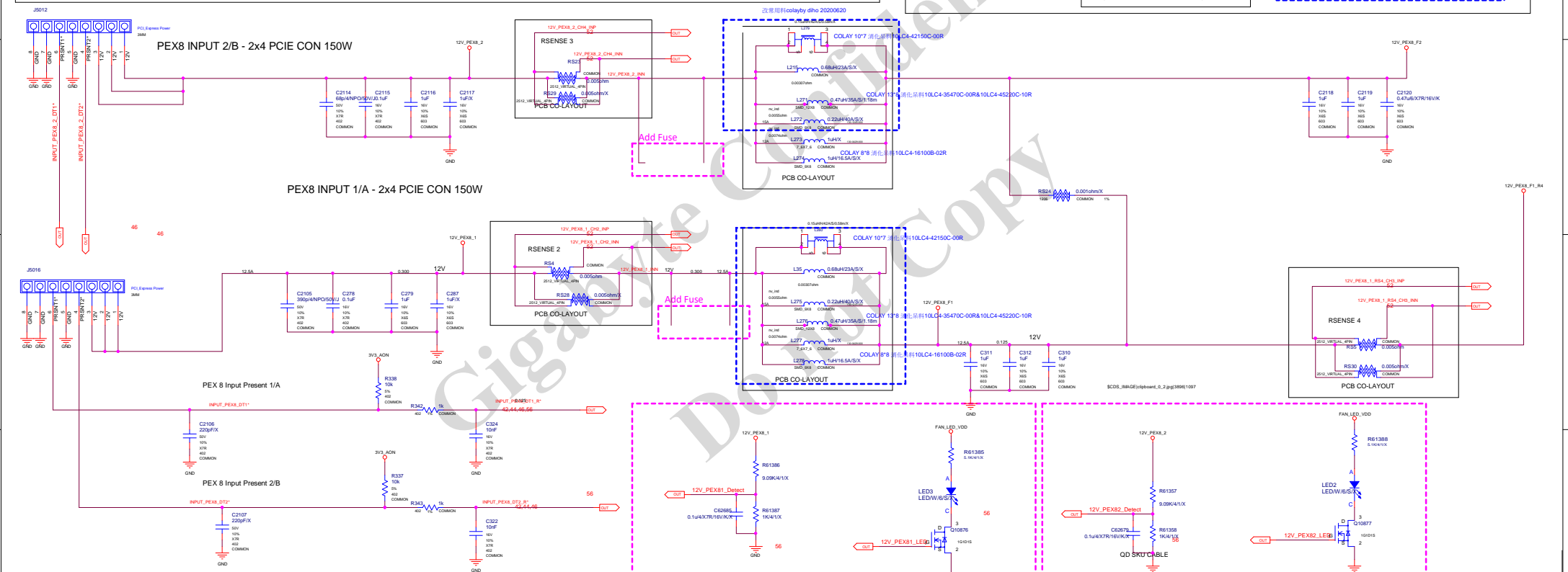
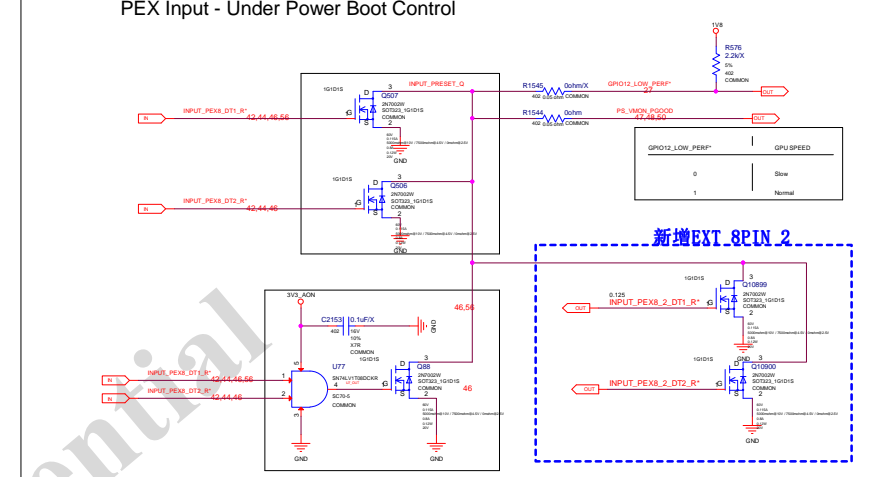
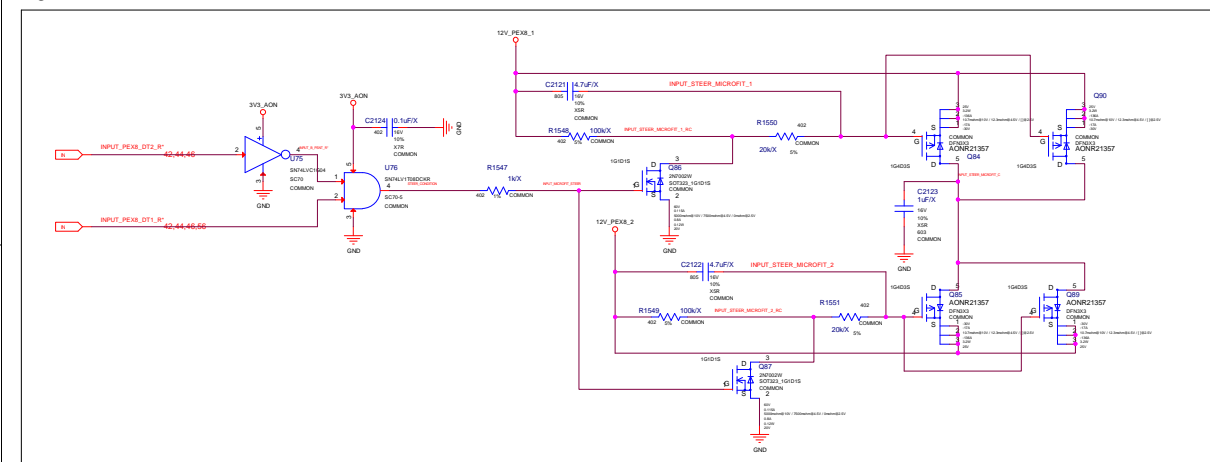
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PAGE DETAIL	PS: OVRS_INPUT POWER BALANCE

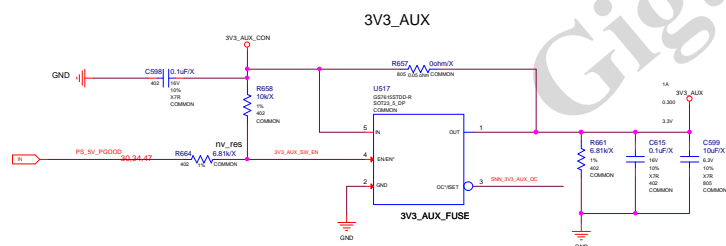
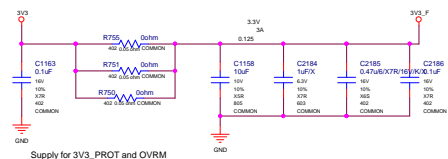
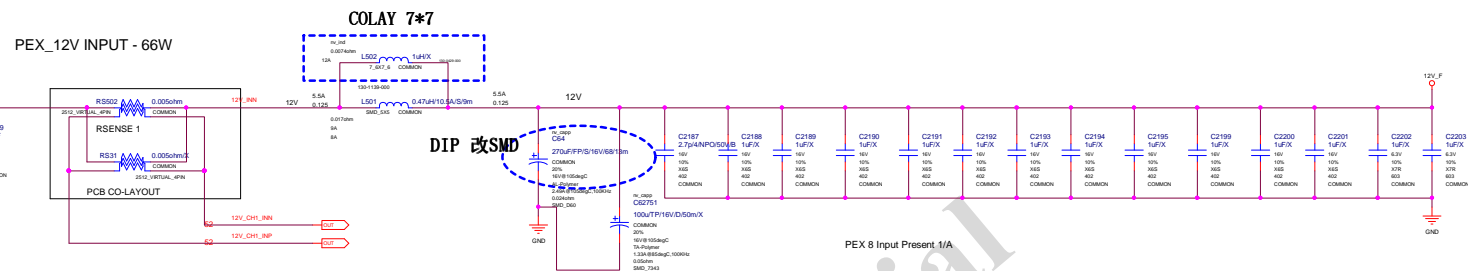
**GIGABYTE**

Title		Page42: Input Power Balancing Switcher	
Size	Document Number		
Custom	GV-N3070GAMING OC-8		
Date:	Thursday, September 17, 2020	Sheet	42





ASSEMBLY PAGE DETAIL			ASSEMBLY DESCRIPTION		
PEX INPUTS, FILTERING AND MONITORING			PEX INPUTS, FILTERING AND MONITORING		
PEX INPUT 2/B - 2x4 PCIE CON 150W			PEX INPUT 2/B - 2x4 PCIE CON 150W		
PEX INPUT 1/A - 2x4 PCIE CON 150W			PEX INPUT 1/A - 2x4 PCIE CON 150W		
PEX 8 Input Present 1/A			PEX 8 Input Present 1/A		
PEX 8 Input Present 2/B			PEX 8 Input Present 2/B		
PEX INPUT 2 - 2x4 PCIE CON 150W			PEX INPUT 2 - 2x4 PCIE CON 150W		



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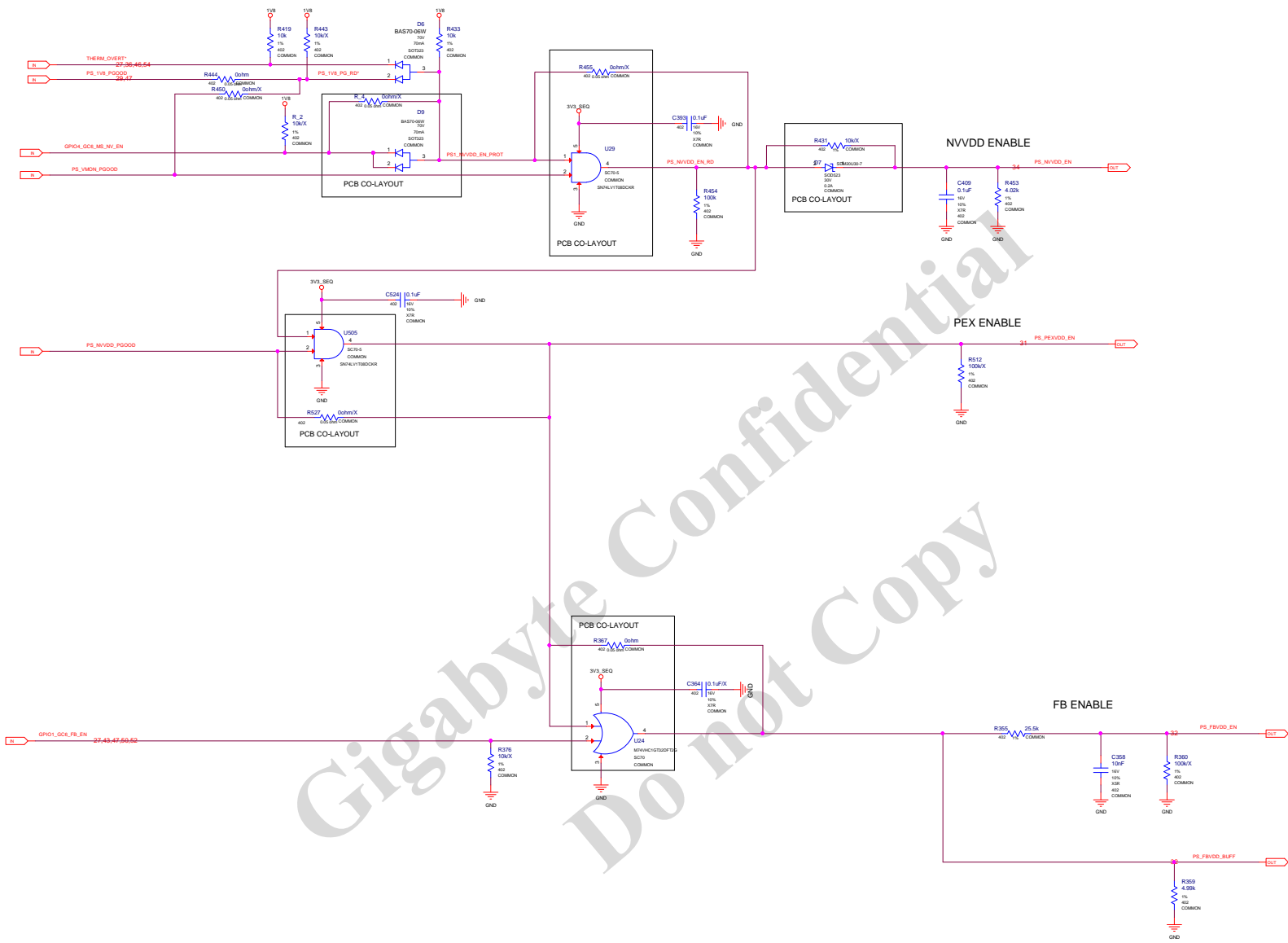
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PAGE DETAIL	PS: PEX_12V, PEX_3V3 AND A3V3_AU

<b>GIGABYTE</b>				
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PEX_INPUT & FILTERING, PEX_3V3, 3V3_AUX				
Size	Document Number			Rev
Custom	GV-N3070GAMING OC-8GD			1
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File			
SEQ: 5V, 1V8, 3V3_SEQ, DDC_5V, DP_AUX_PROT			
Size	Document Number		Rev
Custom	GV-N3070GAMING OC-8GD		1.0
Date:	Thursday, September 17, 2020	Sheet	47 of 57



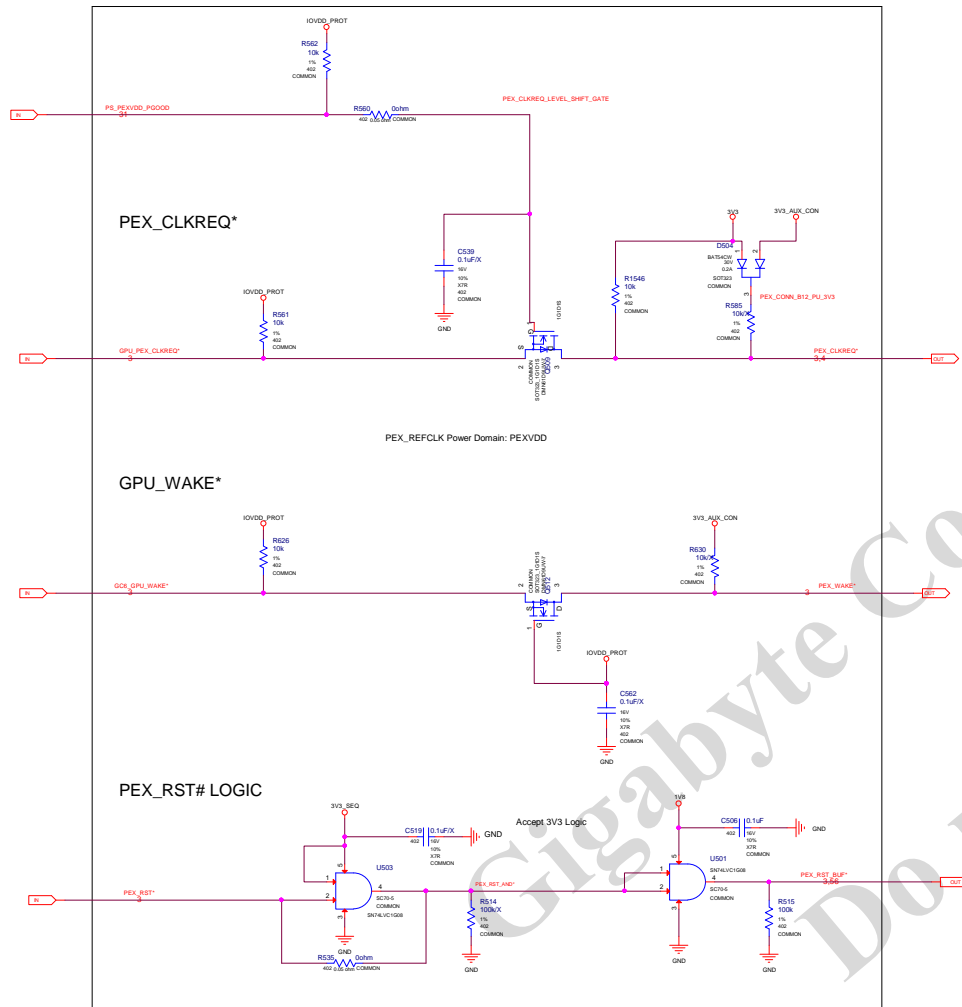
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ASSEMBLY  
PAGE DETAIL

«ASSEMBLY DESCRIPTION»  
SEQ: NVVDD, PEX, FBVDDQ ENABLE

GIGABYTE			
File: SEQ:NVVDD, PEX, FBVDDQ ENABLE			
Size	Document Number	Rev	
GV-N3070GAMING OC-8GD		1.0	
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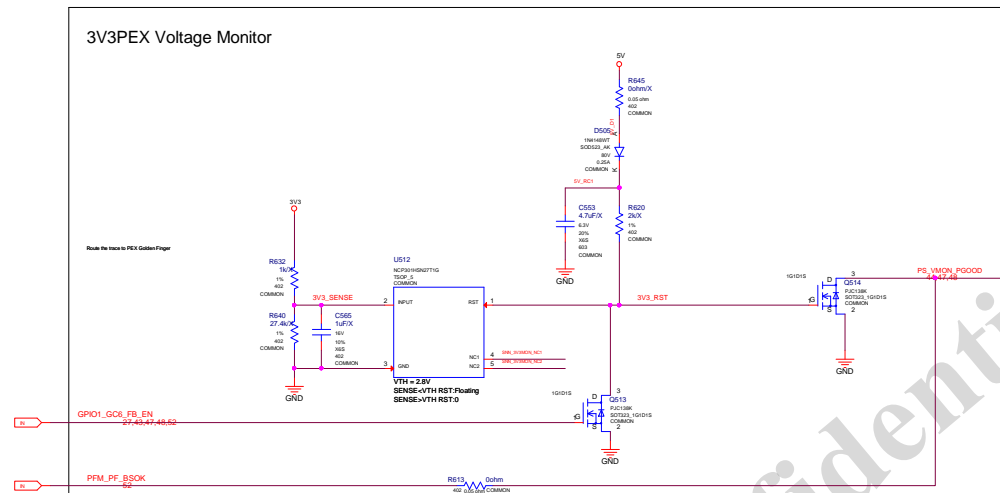


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ASSEMBLY  
PAGE DETAIL

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SEQ: MISC

GIGABYTE			
File: SEQ:MISC			
Size	Document Number		Rev
GV-N3070GAMING OC-8GD			1.0
Date	Thursday, September 17, 2020		Sheet 49 of 67



## BOM Configuration

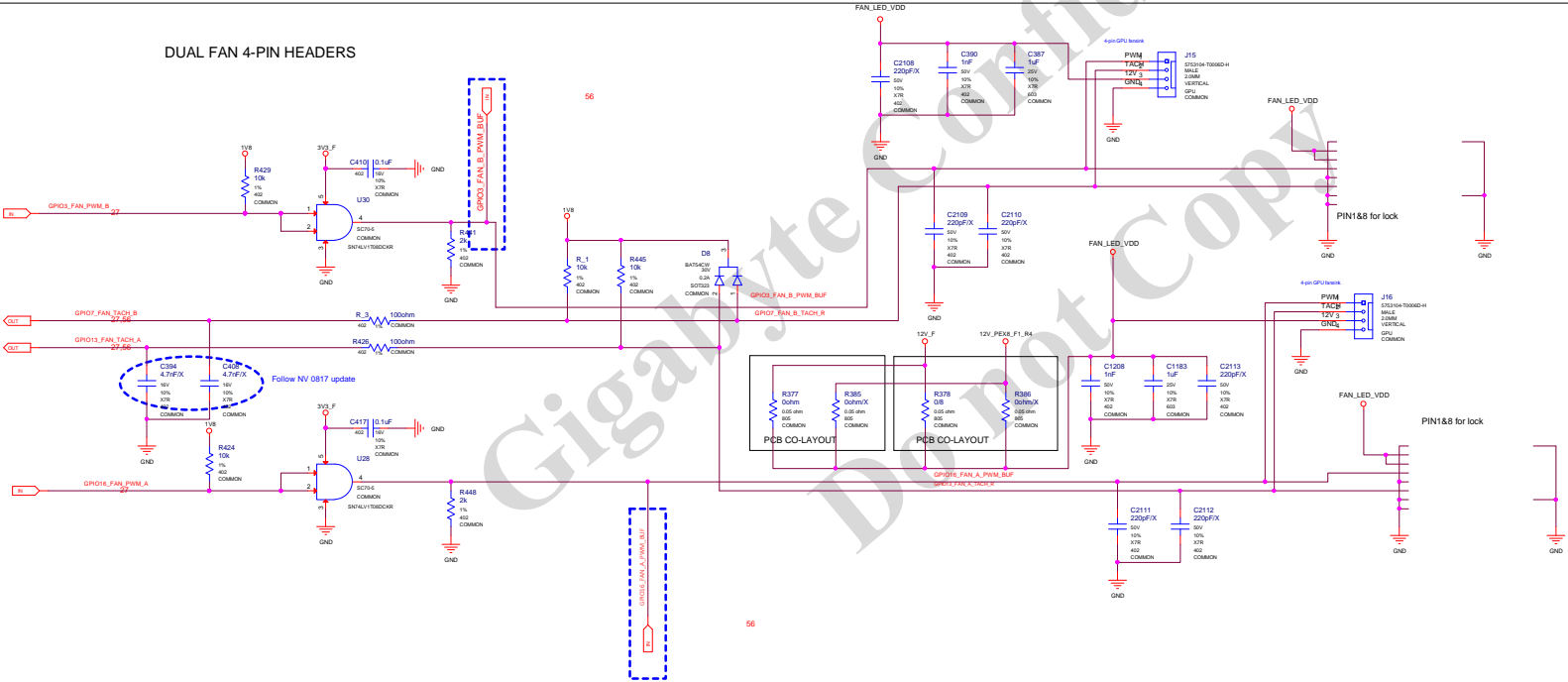
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Use Pre-Filter	Pre-Filter	Pre-Filter	Pre-Filter
NO Pre-Filter	Voltage_Monitor	Voltage_Monitor	N/A

Signal	Direction	Function
3V3	INPUT	Sense the 3V3 Voltage from PCIE golden finger
12V	INPUT	Sense the 12V Voltage from PCIE golden finger
PS_VMON_PGOOD	OPEN-DRAIN	Floating(t <sub>H</sub> ) once both 3V3 and 12V reach V <sub>th</sub>
GC6_FB_EN	INPUT	Indicator for RTD3/GC6 resistance, Use to Mask the VMON_PGOOD
PS_PF_SKIP	INPUT	From INA3221(VPU) or Pre-filter(SKIP)
PS_PF_BSKOK	INPUT	From INA3221(PV) or Pre-filter(BS_OK)

2-PIN LED HEADER



DUAL FAN 4-PIN HEADERS



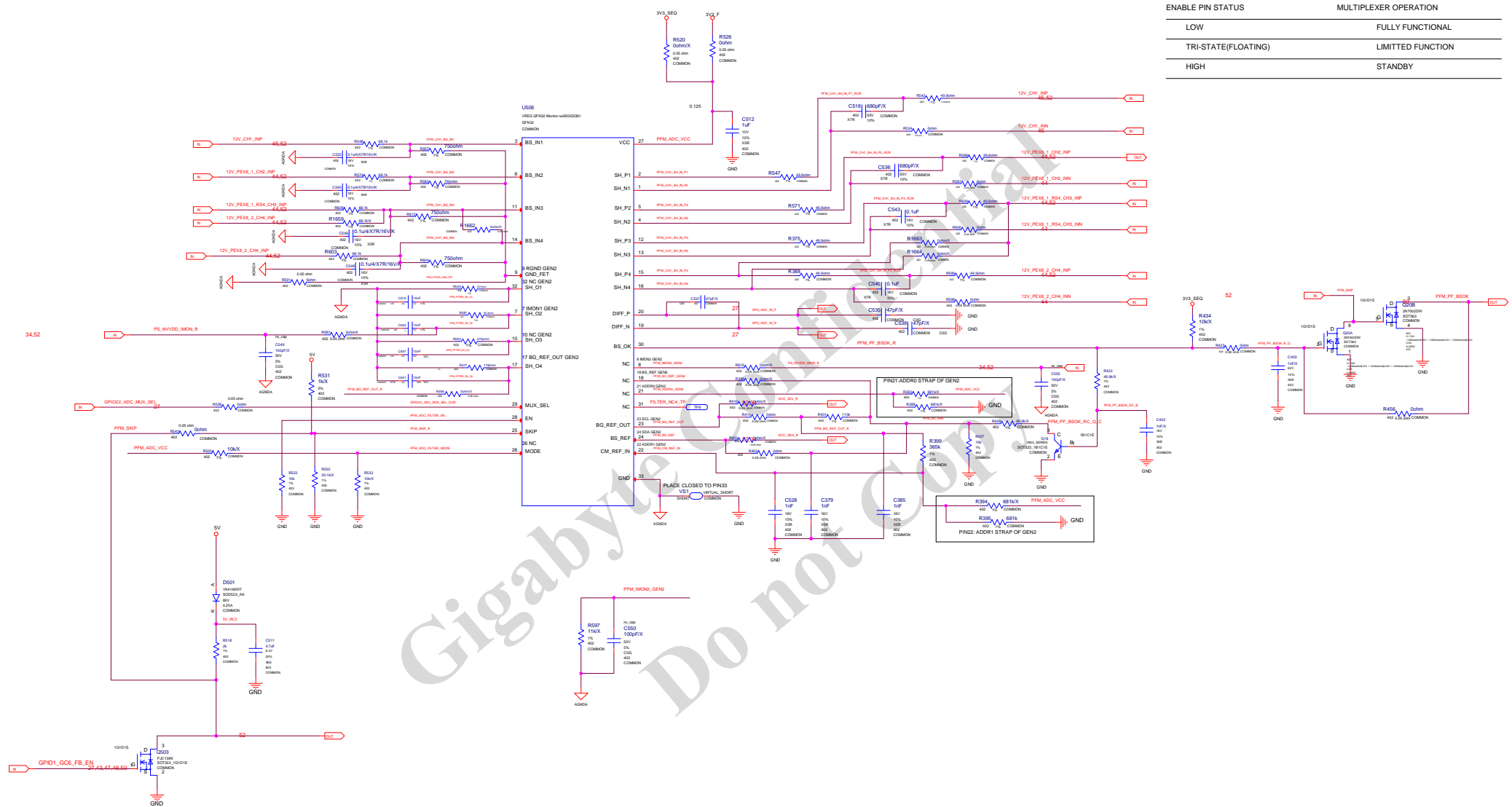
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ASSEMBLY  
PAGE DETAIL

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FAN & LED

GIGABYTE

File	FAN&LED	Rev	1.0
Docu	Document Number		
Customer	GV-N3070GAMING OC-8GD		
Date	Thursday, September 17, 2020	Sheet	51 of 57

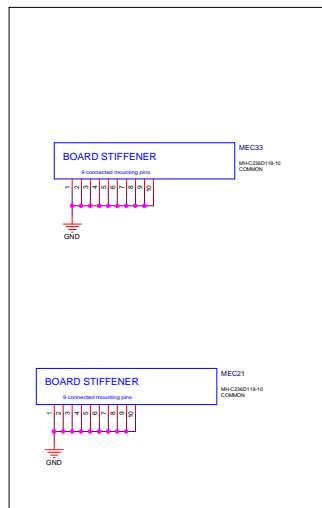


MODE PIN STATUS	MULTIPLEXER OPERATION
LOW	DEVICE A
TRI-STATE(FLOATING)	STAND-ALONE
HIGH	DEVICE B

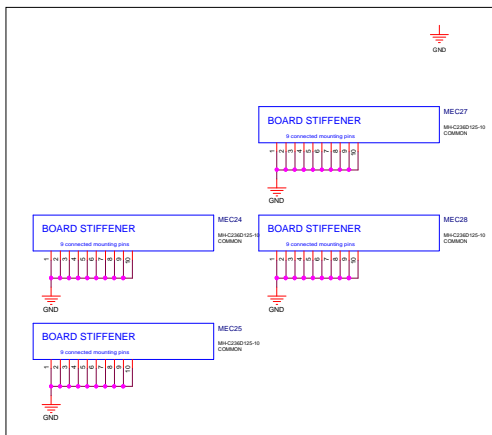
  

ENABLE PIN STATUS	MULTIPLEXER OPERATION
LOW	FULLY FUNCTIONAL
TRI-STATE(FLOATING)	LIMITED FUNCTION
HIGH	STANDBY

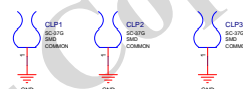
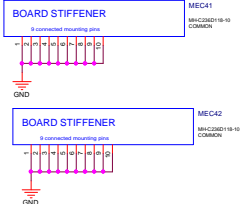
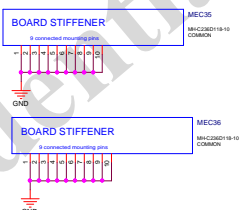
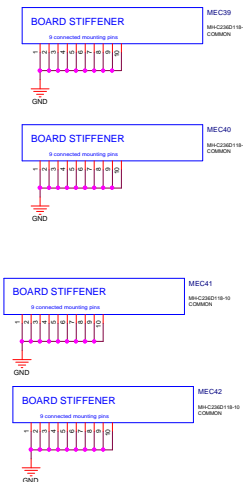
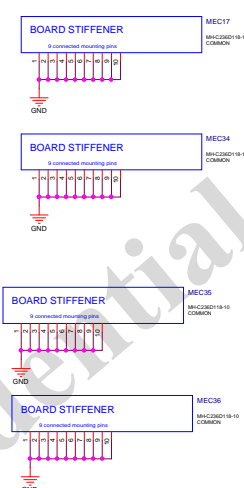
BKT SINK



GPU SINK



MOS  
螺絲孔



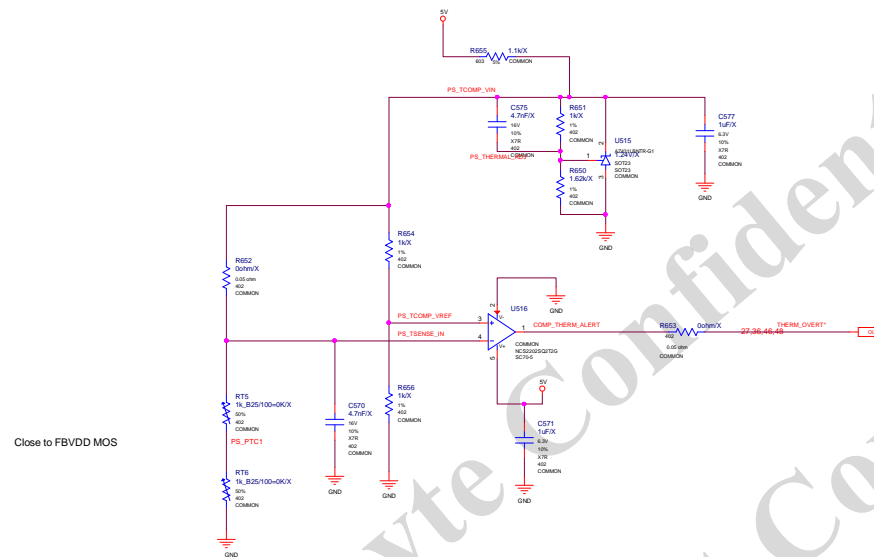
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ASSEMBLY  
PAGE DETAIL

ASSEMBLY DESCRIPTION  
MECH

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Rev	Customer	Document Number	Rev
		GV-N3070GAMING OC-8GD	1.0
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Close to FBVDD MOS

ASSEMBLY  
PAGE DETAIL

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Title			
PTC			
Size	Document Number		Rev
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WINFORCE LED

0805 COLAY 0603 dha 2018.12.02

FAN\_LED\_VDD

FAN Stop

C62714

10uF X6S 25V

LB009 120V/600mA 50X120/6/600mA/50

LB010 COMMON BEAD 3845

LB011 25V/8A 50X120/6/600mA/50

R51470 050-Y 50X120/6/600mA/50

Q501 50X120/6/600mA/50

Q502 50X120/6/600mA/50

Q503 50X120/6/600mA/50

Q504 50X120/6/600mA/50

Q505 50X120/6/600mA/50

Q506 50X120/6/600mA/50

Q507 50X120/6/600mA/50

Q508 50X120/6/600mA/50

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Q585 50X120/6/600mA/50

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