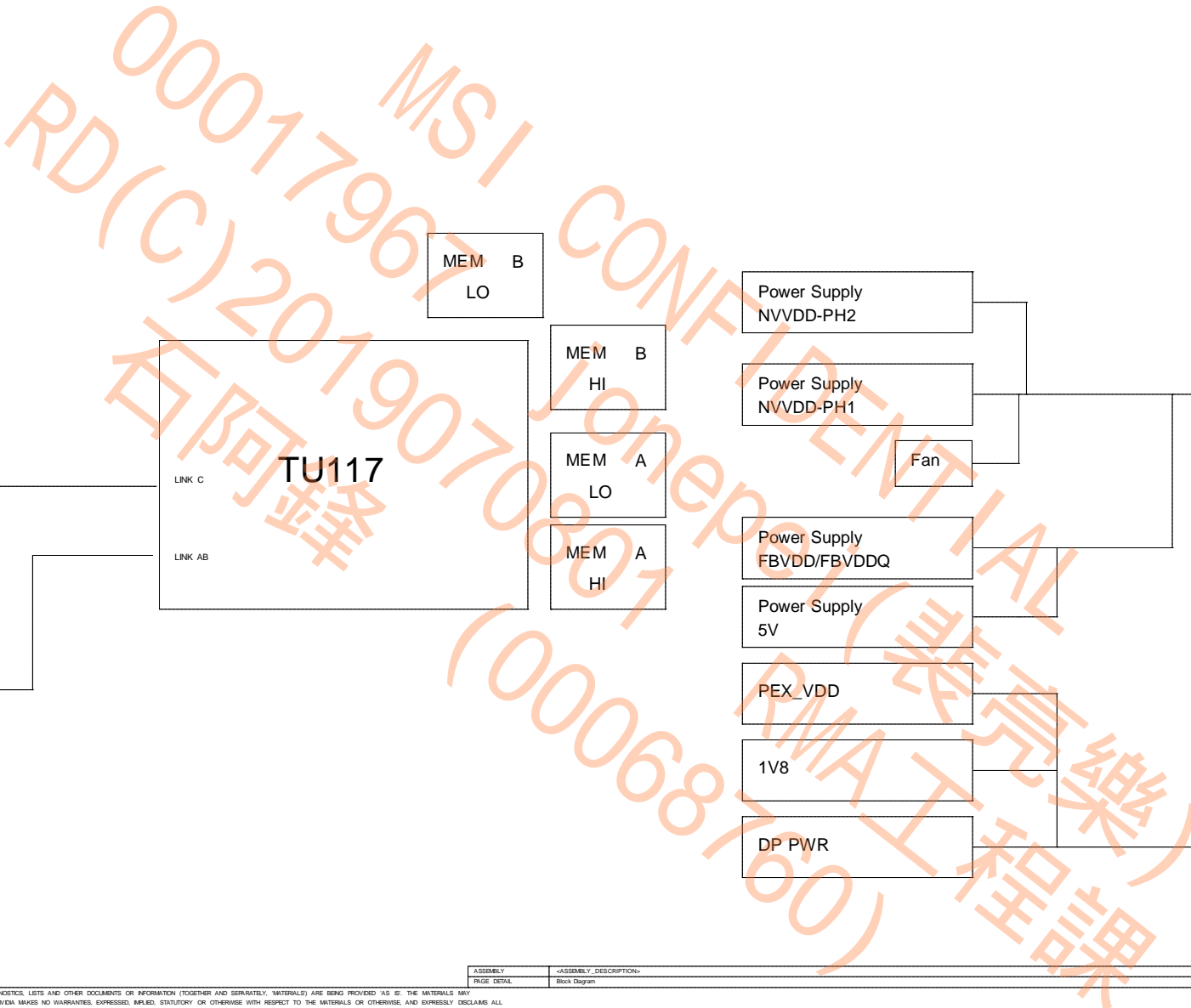
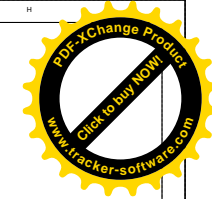


TABLE OF CONTENTS

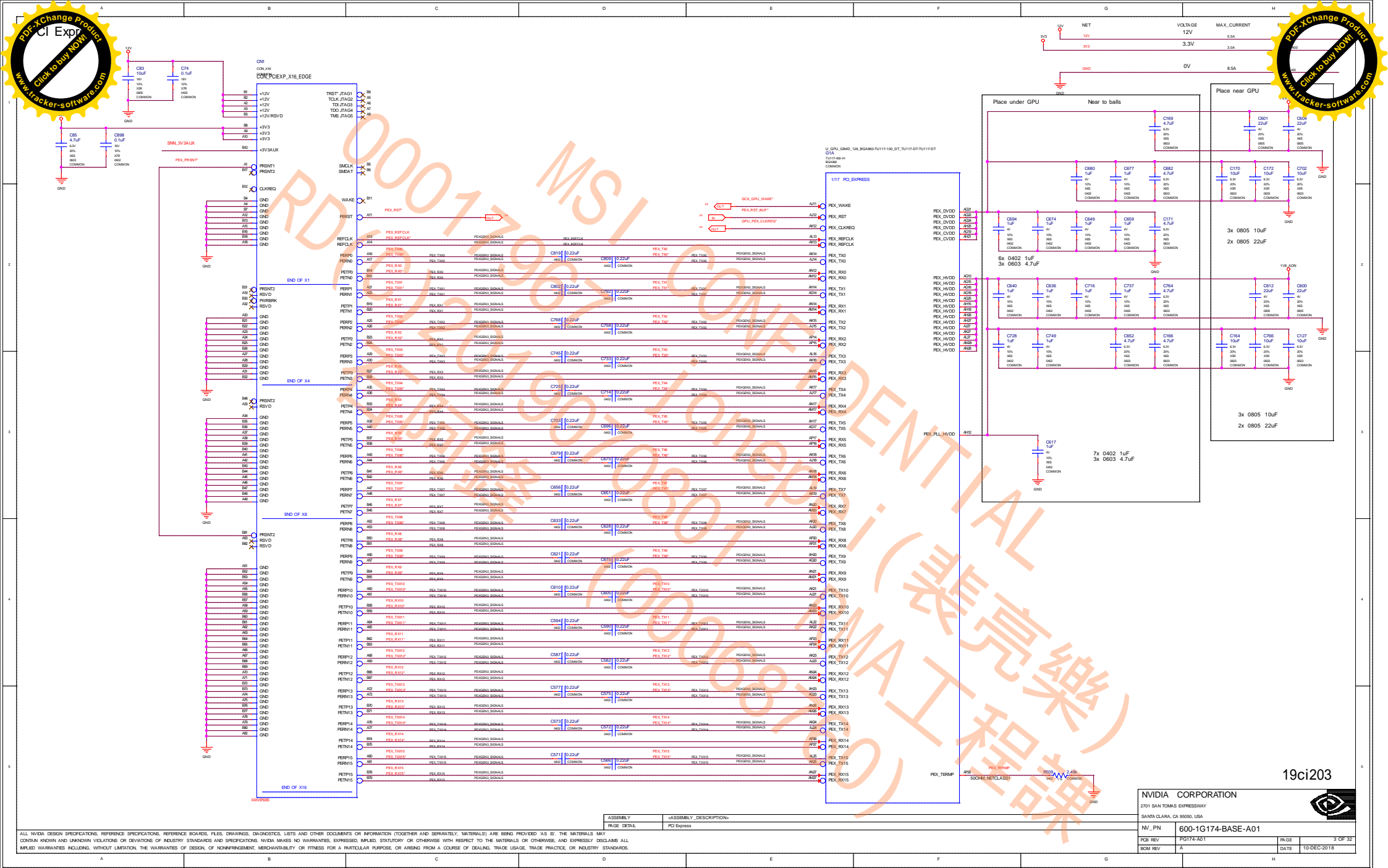
Page	Description	Page	Description
1	Table of Contents	26	PS: 5V
2	Block Diagram	27	PS: 1V8
3	PCI Express	28	PS: PEXVDD
4	MEMORY: GPU Partition A/B	29	PS: FBVDDQ
5	MEMORY: FBA[31:0]	30	PS: NVVDD OVR2+1
6	MEMORY: FBA[63:32]	31	PS: NVVDD Phase 1,2
7	MEMORY: FBB[31:0]	32	MECH: Bracket/Thermal
8	MEMORY: FBB[63:32]		
9	GPU FBVDDQ & 1V8 Decoupling		
10	GPU NVVDD DECOUPLING		
11	IFPAB DVI-DL		
12	IFPA DP		
13	IFPB DP		
14	IFPC		
15	IFPC HDMI		
16	IFPDEF UNUSED		
17	MISC1: Fan, Thermal, JTAG, GPIO		
18	MISC2: ROM, XTAL, Straps		
19	Power Inputs and Filtering		
20	SEQ: 5V, 1V8, NV3V3 ENABLE		
21	SEQ: NV, PEX, FB ENABLE		
22	SEQ: VOLTAGE MONITOR		
23	SEQ: DISCHARGE		
24	SEQ: MISC		
25	PS: Pre-filter		

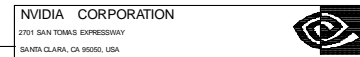
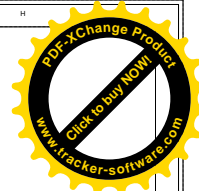
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PCB REV	PG174-A01	FRGE	2 OF 3
BOM REV	A	DATE	10-DEC-2018

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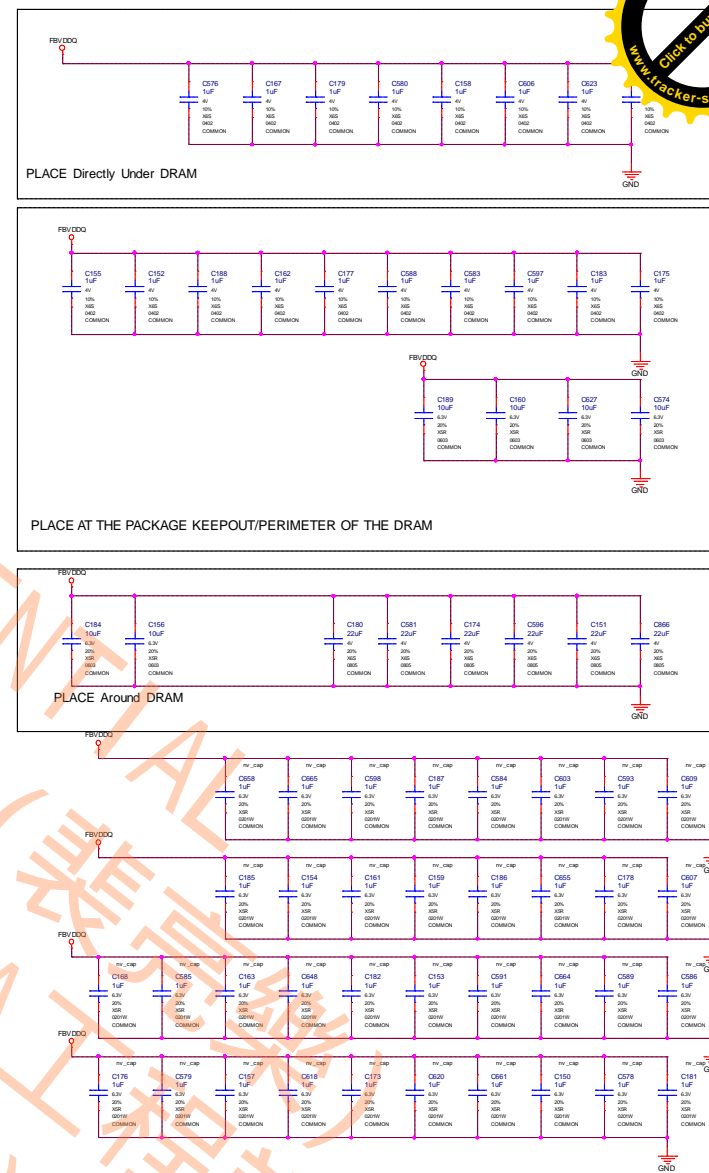
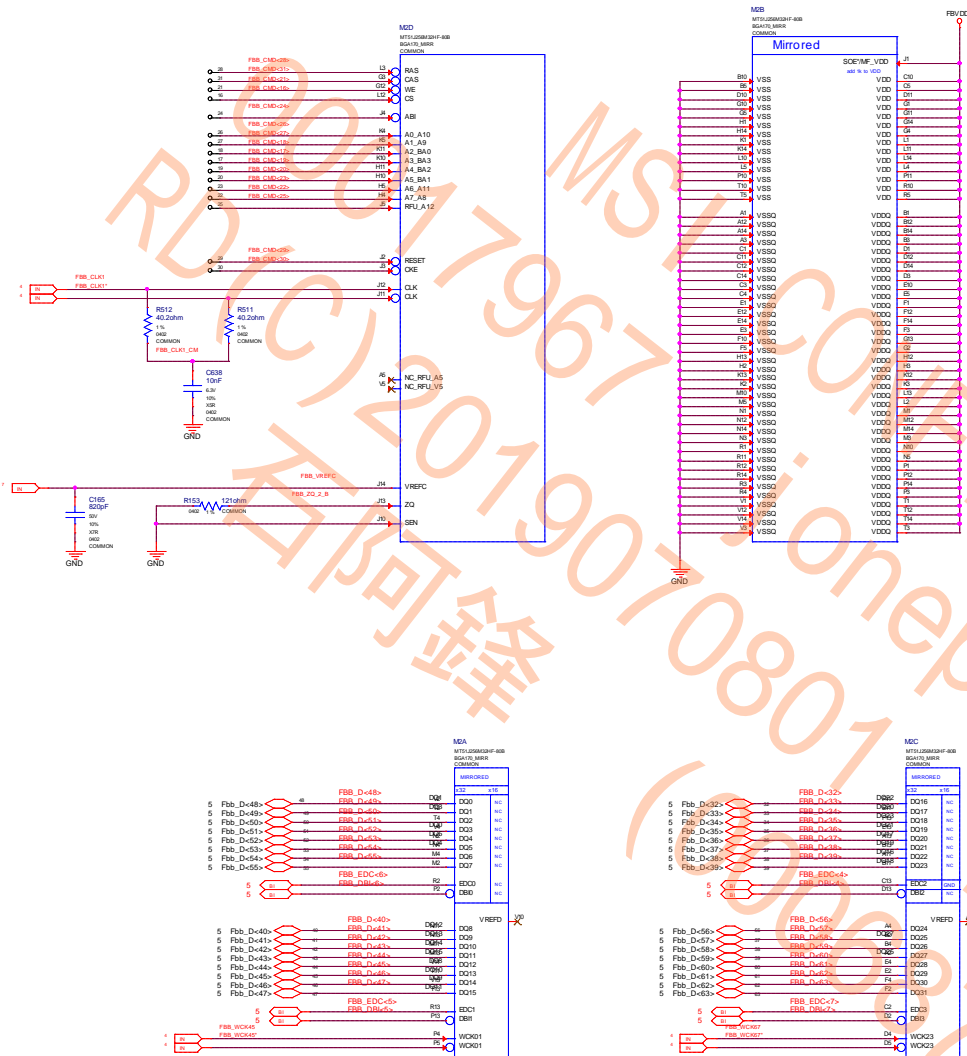



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BOM_REV	A	DATE	10-DEC-2018

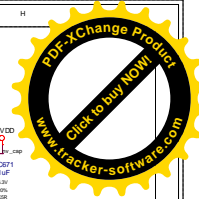
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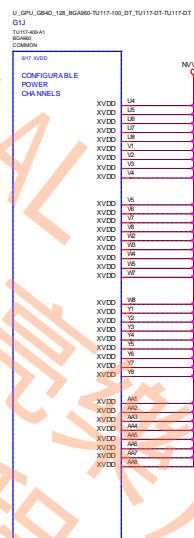
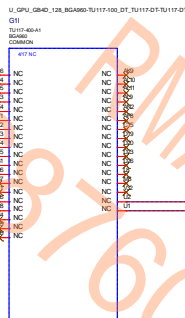
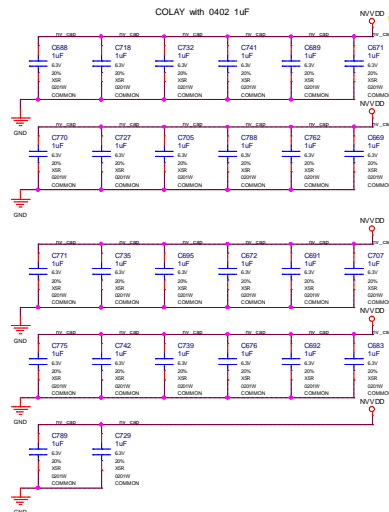
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CMD1	A3_BA3	
CMD2	A2_BA2	
CMD3	A4_BA2	
CMD4	A5_BA1	
CMD5	WE*	
CMD6	AF_A8	
CMD7	AE_A11	
CMD8	AB*	
CMD9	A12_RFU	
CMD10	AO_A19	
CMD11	A1_BA5	
CMD12	RA5*	
CMD13	RS1*	
CMD14	OK*	
CMD15	CA3*	
CMD16		
CMD17	A3_BA3	
CMD18	CS*	
CMD19	A2_BA2	
CMD20	A4_BA2	
CMD21	A5_BA1	
CMD22	WE*	
CMD23	AF_A8	
CMD24	AE_A11	
CMD25	AB*	
CMD26	A12_RFU	
CMD27	AO_A19	
CMD28	RA5*	
CMD29	RS1*	
CMD30	OK*	
CMD31	CA3*	



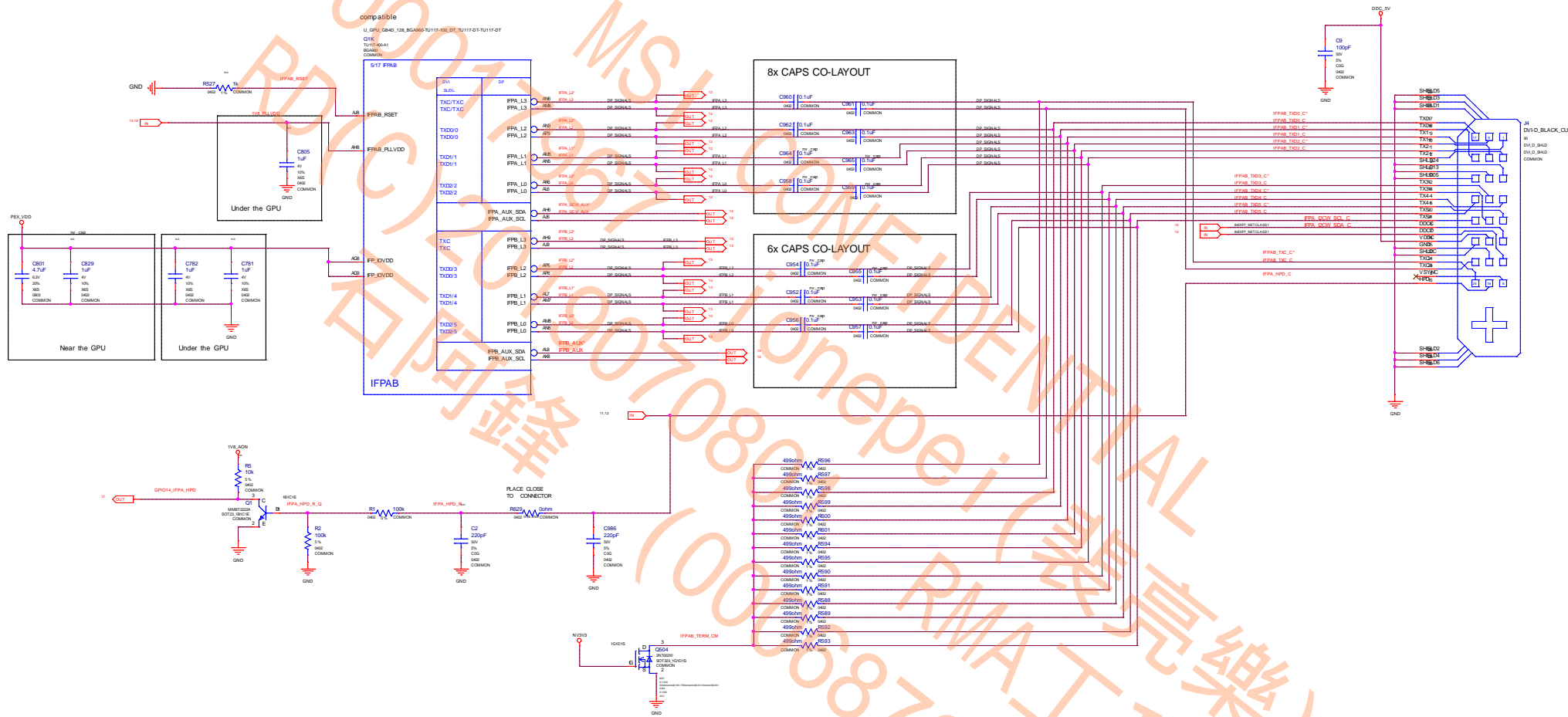
NVIDIA CORPORATION			
2701 SAN TOMAS EXPRESSWAY			
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NV_PN	600-1G174-BASE-A01		
PCB REV	PG174-A01	PRGE	8 OF 32
BOM REV	A	DATE	10-DEC-2018



Place Near GPU



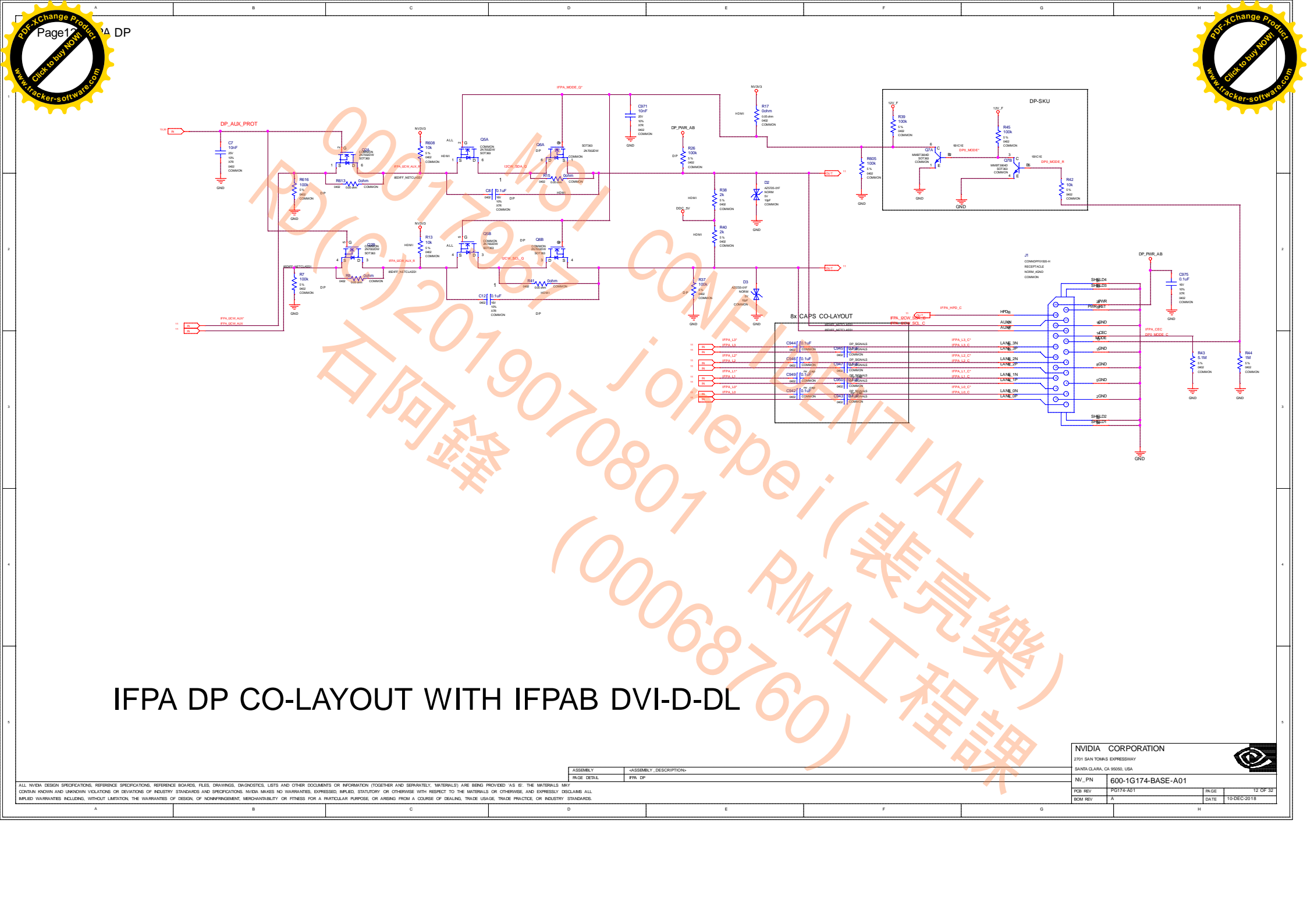
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ASSEMBLY	ASSEMBLY DESCRIPTION
IFPAB DVI-DL	

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NV_PN	600-1G174-BASE-A01
PCB REV	PG174-A01
DATE	10-DEC-2018
11 OF 32	

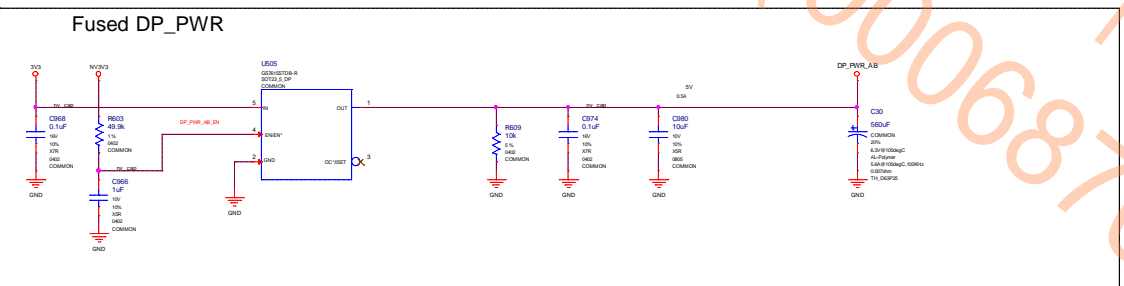


IFPA DP CO-LAYOUT WITH IFPAB DVI-D-DL

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NV_PN	600-1G174-BASE-A01
PCB REV	PG174-A01
ICM REV	A
PRGE	12 OF 32
DATE	10-DEC-2018

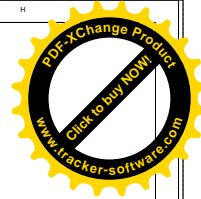
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FPB DP

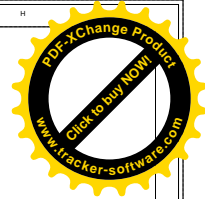
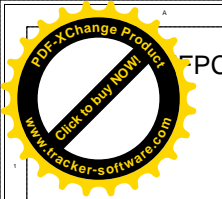


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		BOM REV	A
		DATE	10-DEC-2018
		PAGE	13 OF 32

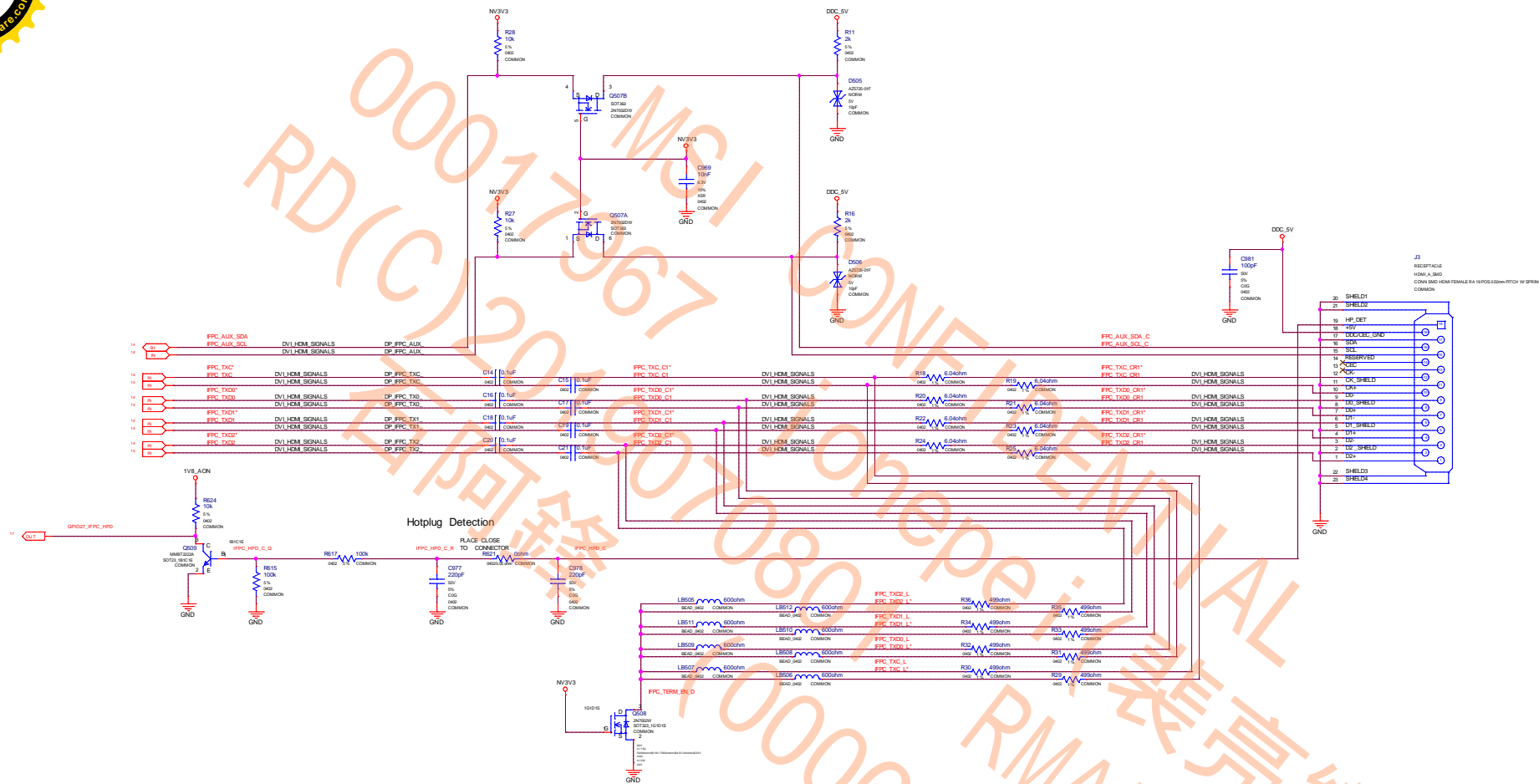
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NV_PN		600-1G174-BASE-A01		
PCB REV	PG174-A01	PAGE	14 OF 32	
BOIM REV	A	DATE	10-DEC-2018	



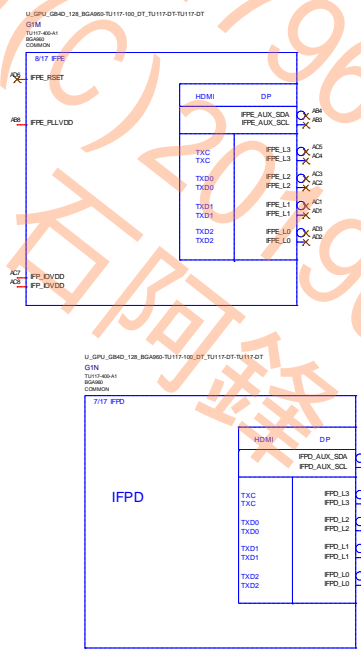
TPC HDMI



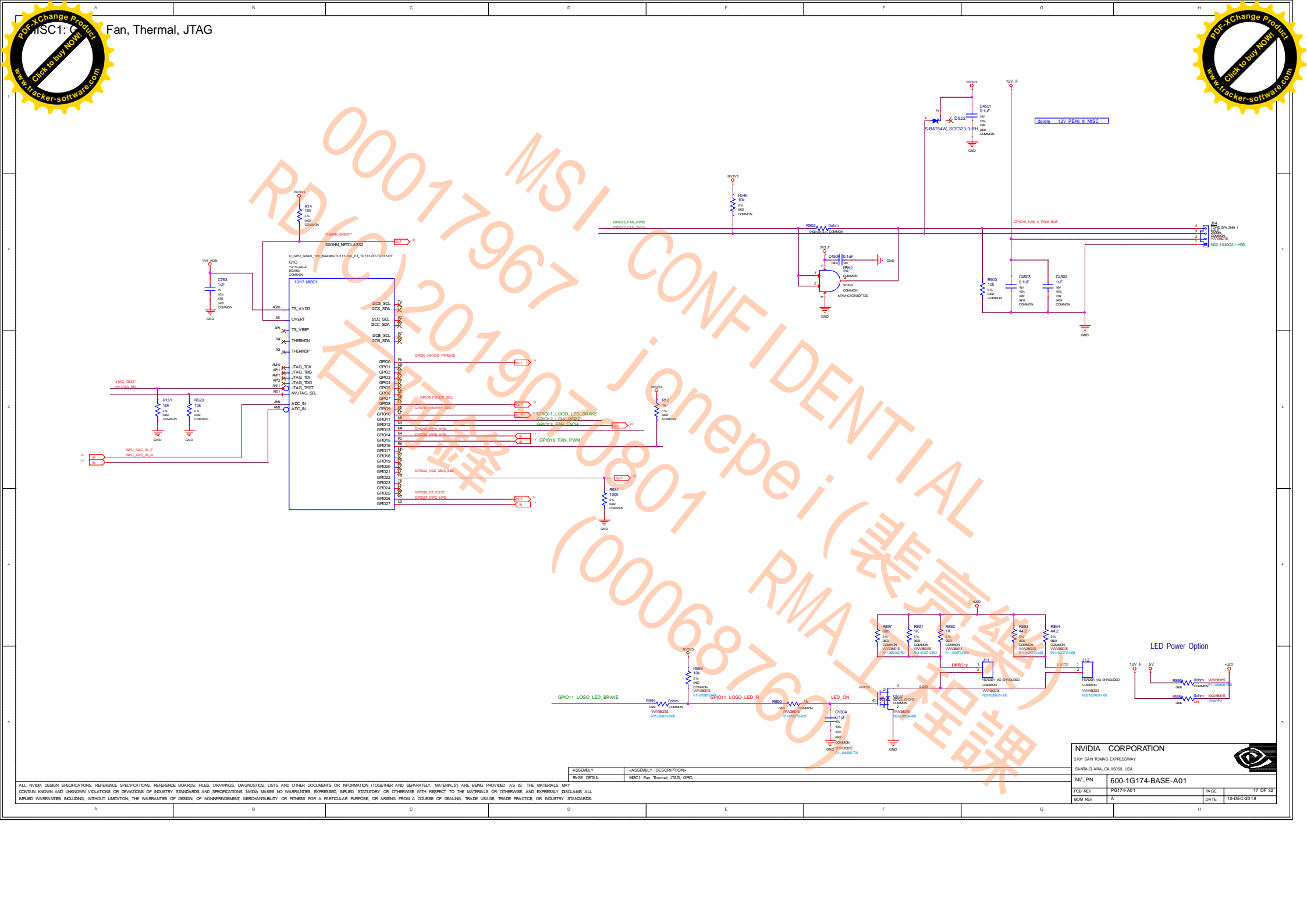
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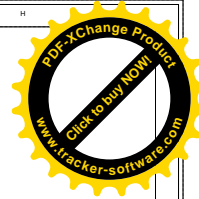
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PCB DETAIL	TPC HDMI


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NV_PN	600-1G174-BASE-A01
PCB REV	PG174-A01
ICM REV	A
PRGE	15 OF 32
DATE	10-DEC-2018



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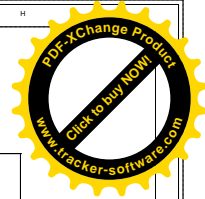
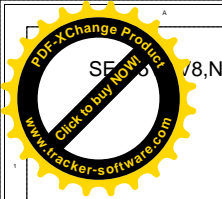




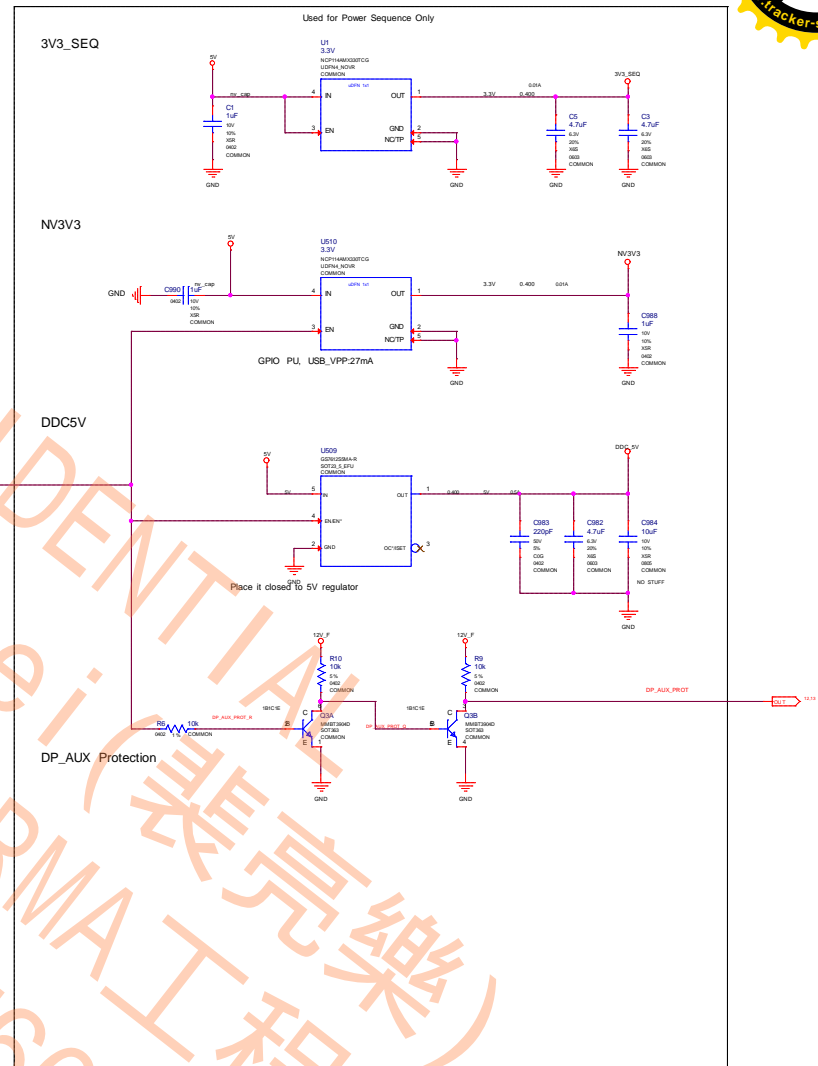
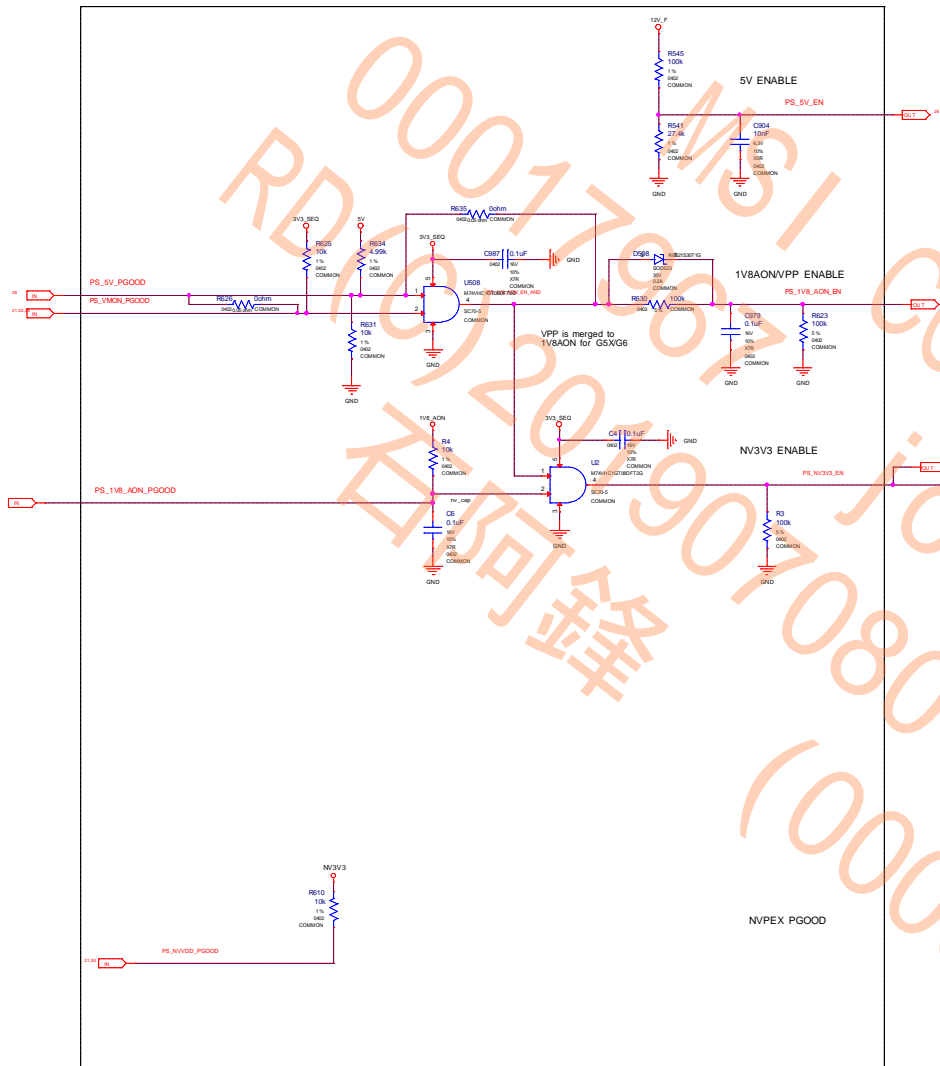
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NV_PN		600-1G174-BASE-A01		
POB REV	P0174-A01	PAGE	19 OF 32	
BOM REV	A	DATE	10-DEC-2018	

ASSEMBLY P/RGE DETAIL	«ASSEMBLY DESCRIPTION» Power inputs and Filtering	SANTA CLARA, CA 95050, USA	
		600-1G174-BASE-A01	
CLAIM ALL STANDARDS.		P0174-A01	15 OF 32
		A	10-DEC-2018

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8,NV3V3 ENABLE



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ASSEMBLY
PAGE DETAIL

ASSEMBLY DESCRIPTION
SEQ SV 1V8 NV3V3 ENABLE

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NV_PN 600-1G174-BASE-A01

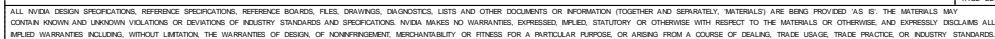
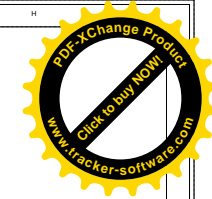
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ROM REV A

PRGE 20 OF 32

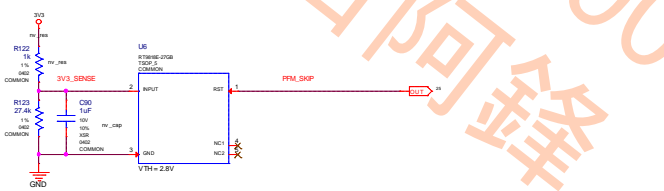
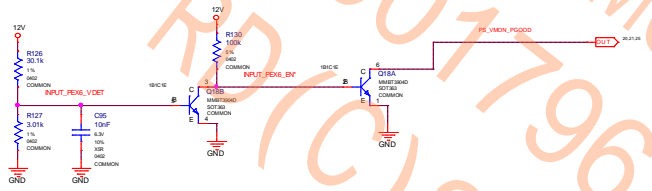
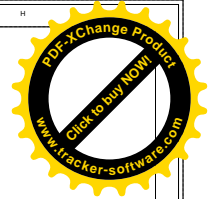
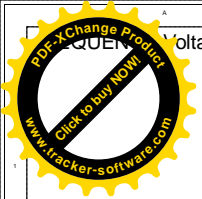
DATE 10-DEC-2018





NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA			
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PCB REV	PG174-A01	PAGE	21 OF 32
BOM REV	A	DATE	10-DEC-2018

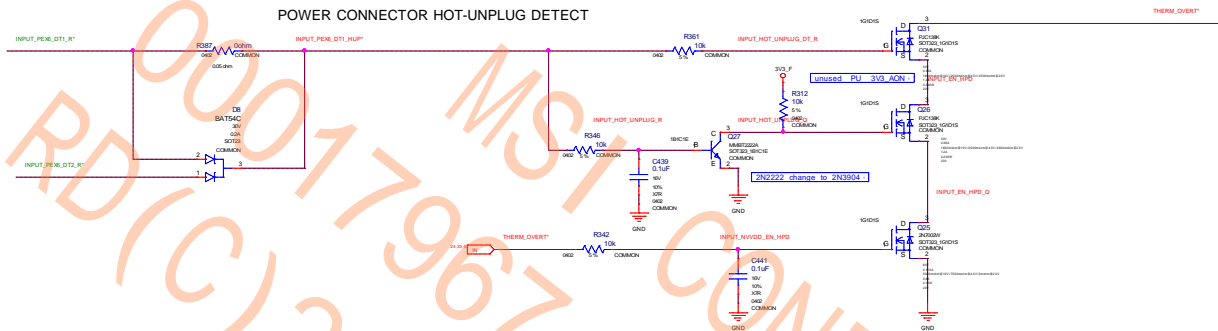




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PCB REV	PG174-A01	PRGE	22 OF 32
ROM REV	A	DATE	10-DEC-2018

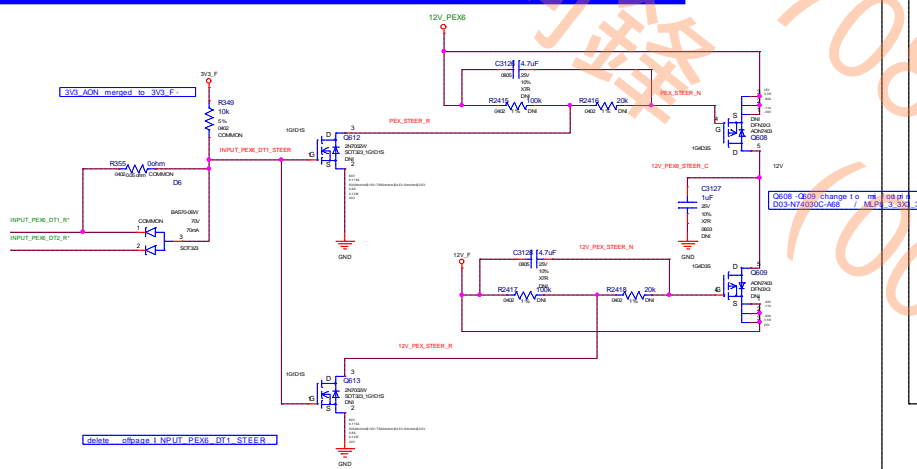
POWER CONNECTOR HOT-UNPLUG DETECT



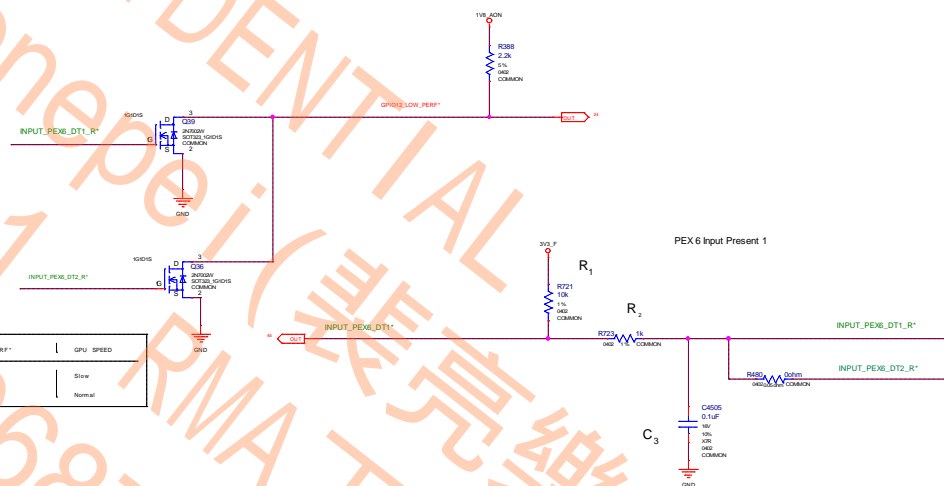
12V CURRENT STEERING (UNDER POWER BOOT):

GUIDES CURRENT FROM PEX EDGE TO PEX 6 PIN INPUT AREA

copied 12V CURRENT STEERING from V373-2.0 and BOM stuff ed



PEX Input - Power Level/PSI* Control



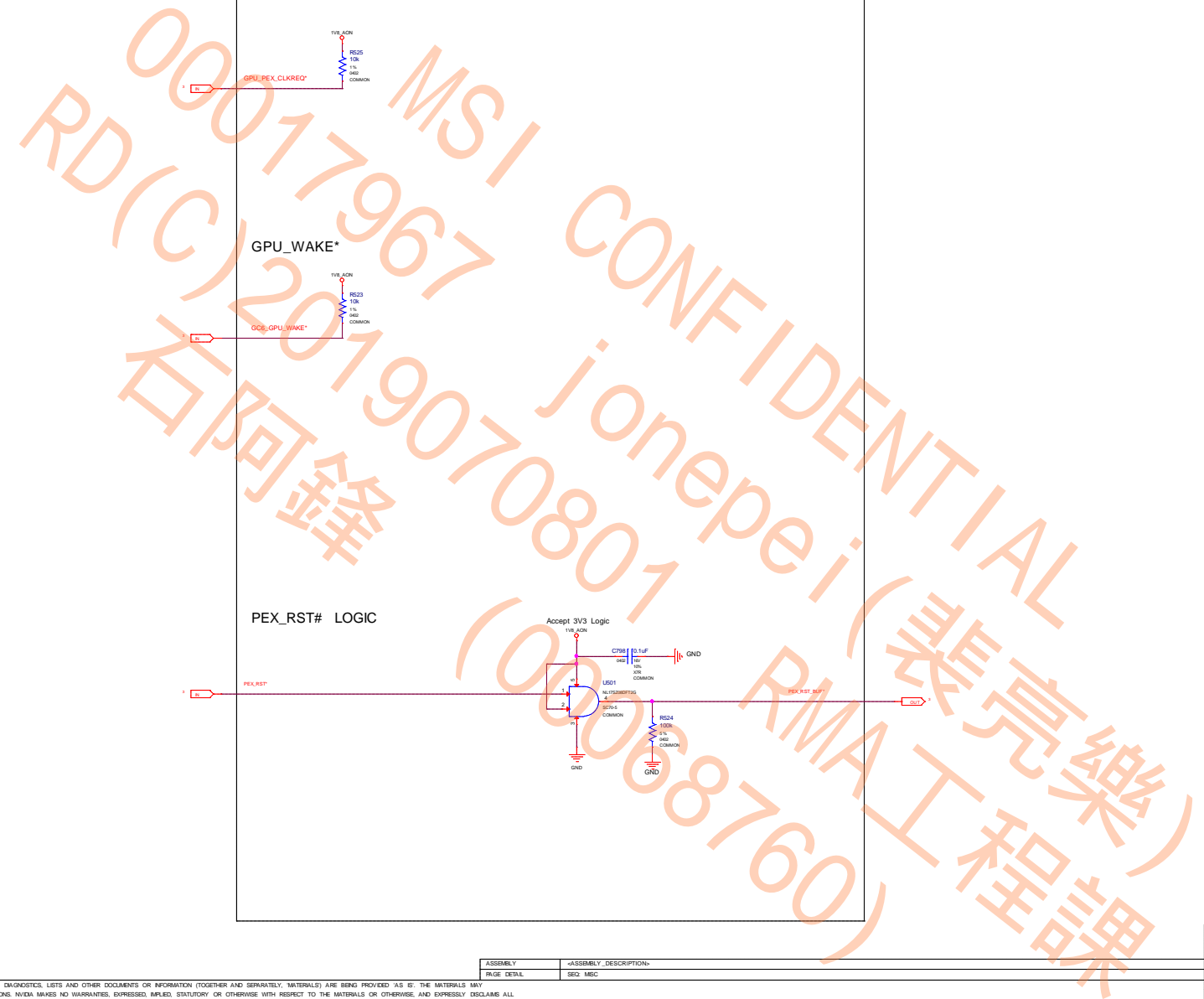
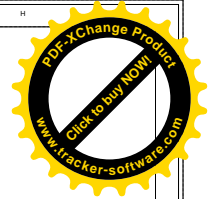
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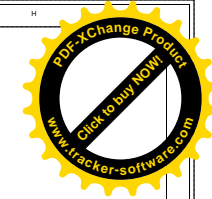


NV_PN	600-1G174-BASE-A01		
PCB REV	PG174-A01	FRGE	23 OF 3
BOM REV	A	DATE	10-DEC-2018

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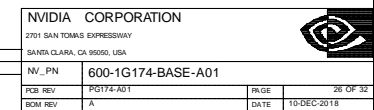
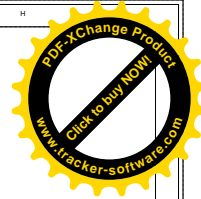
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LOW	FULLY FUNCTIONAL
TRI-STATE(FLOATING)	LIMITED FUNCTION
HIGH	STANDBY

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PCB REV	PG174-A01
BOM REV	A

PAGE	25 OF 32
DATE	10-DEC-2018

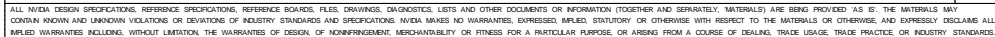
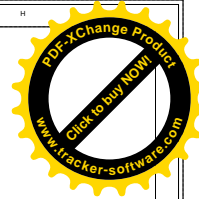
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PCB REV	PG174-A01	PAGE	26 OF 32
BOM REV	A	DATE	10-DEC-2018



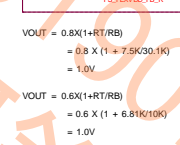
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BOM REV	A	DATE	10-DEC-2018



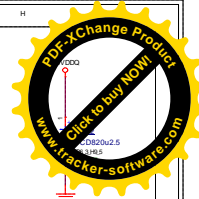
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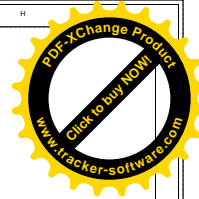
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PCB REV	PG174-A01	PAGE	28 OF 32	
BOM REV	A	DATE	10-DEC-2018	

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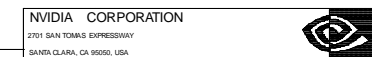


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NV_PN	600-1G174-BASE-A01		
PCB REV	PG174-A01	PAGE	29 OF 32
BOM REV	A	DATE	10-DEC-2018



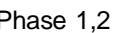



NCP81273	EN VIH >1.5V, VIL < 0.7V
UP9509/10	EN VIH >1.2V, VIL < 0.6V
RT8817A	EN VIH >1.2V, VIL < 0.6V



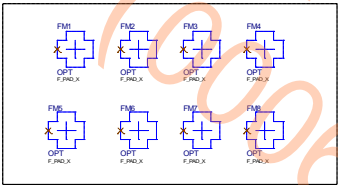
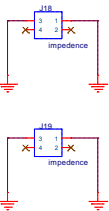
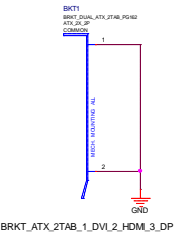
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PCB REV	PG174-A01	PAGE	30 OF 32
BOM REV	A	DATE	10-DEC-2018

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PCB REV	PG174-A01	PRGE	31 OF 32
BOM REV	A	DATE	10-DEC-2018

Brackets:



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BOM REV	A	DATE	10-DEC-2018