

C116-B, NV18B/NV34, 8MX16DDR, 64MB, VIDEO OUT, VGA

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HISTORY:

0B

- A. Change R534 from 2.43K_1% to 2.55K_1% (R11-2551T13-Y01), R533 change from 1.05K_1% to 1.18K_1% (R11-1181T13-Y01) to get FBVDDQ=2.528V.
- B. Add R591 (2.2 ohm_0603_5%) and C409 (2200p_0603) to reduce VRM noise on L1 pin1.

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MS-8917-A00

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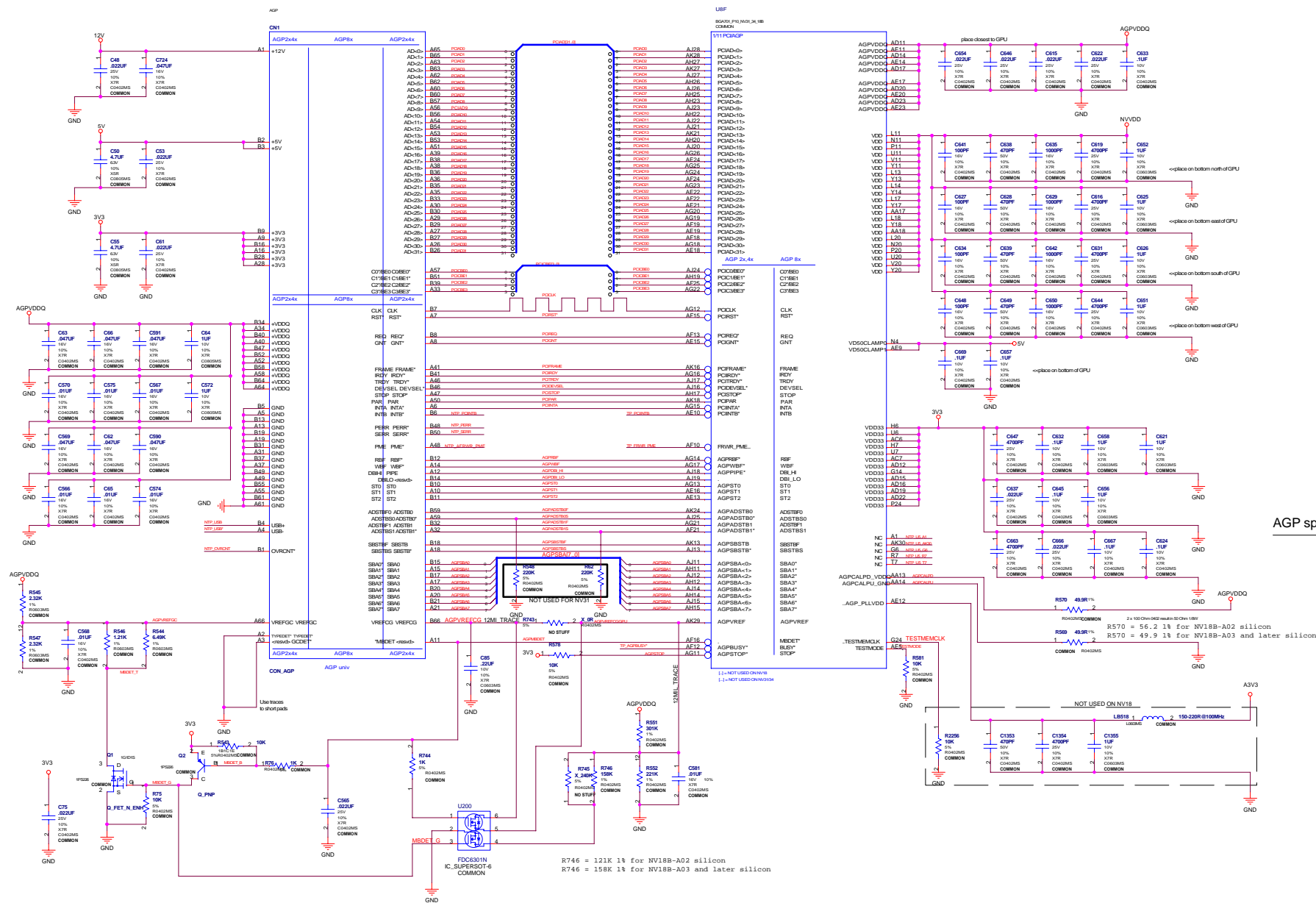
Document Number: TOP PAGE

Date: Sunday, March 16, 2003

Sheet: 1 of 11

Rev: A00

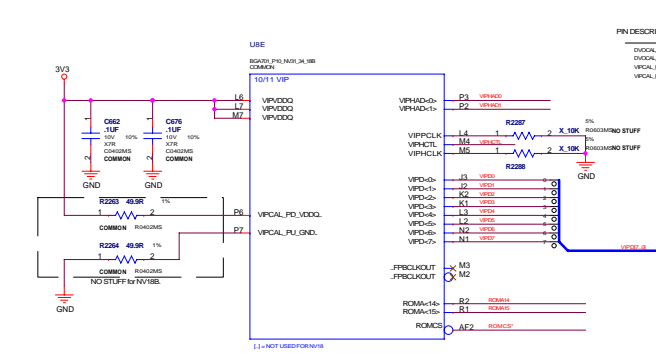
NV18 AGP SECTION AND AGP CONNECTOR



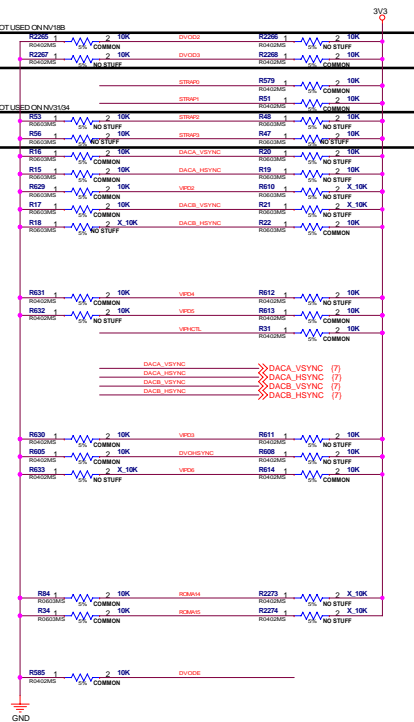
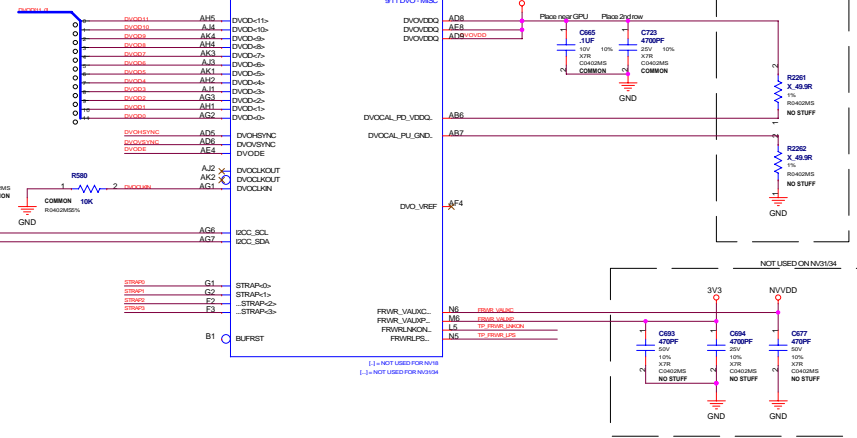
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NV18 STRAPPING, BIOS, FAN CONNECTOR



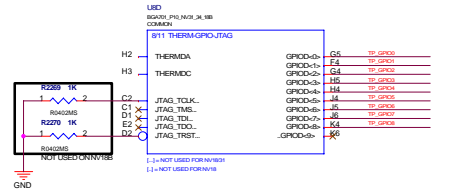
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DVCCAL_PU_GND	NOT USED	50 OHM 1% TO DVCCALDDQ	NOT USED
VPICAL_PD_VDDQ	NOT USED	50 OHM 1% TO GND	NOT USED
VPICAL_PU_GND	NOT USED	50 OHM 1% TO VPICDDQ	NOT USED



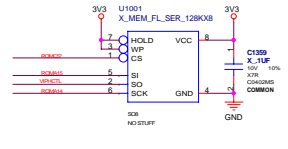
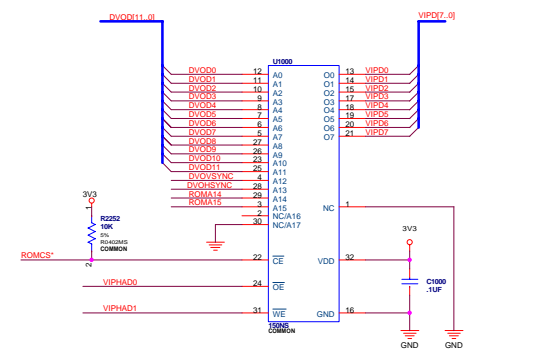
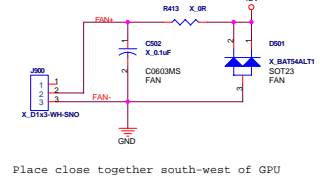
STRAPPING OPTIONS

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NV18 GPIO



EEPROM

FAN
Connector

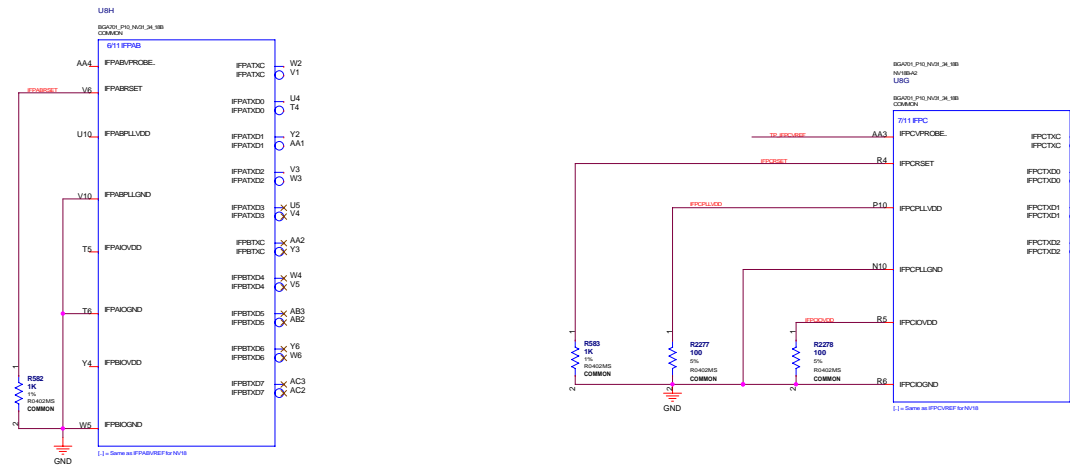
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EVOLVEING	100M	



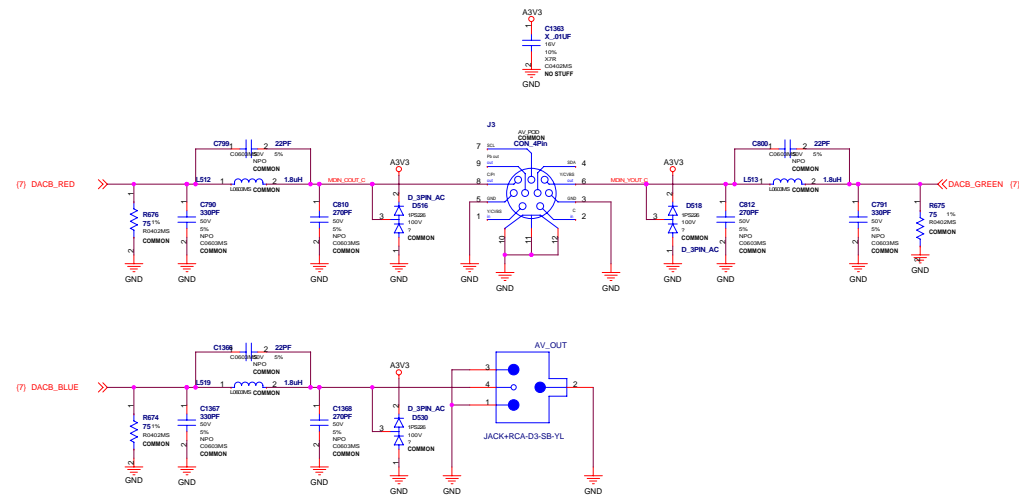
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Document Number		Rev
NV18B STRAPPING, BIOS		A00

INTERNAL TMDS POWER AND DECOUPLING



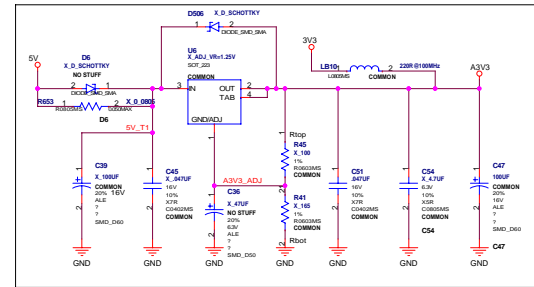
VIDEO OUT CONNECTOR



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POWER SUPPLY

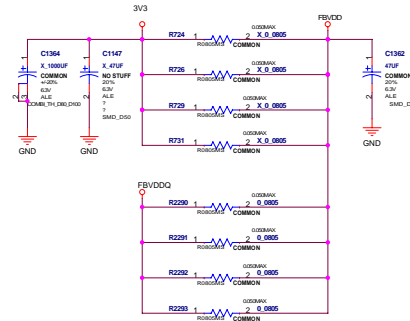
ANALOG 3V3



$$V_{out} = V_{Ref} * (1 + R_{bot}/R_{top})$$

$$3.31V = 1.25V * (1 + (165/100))$$

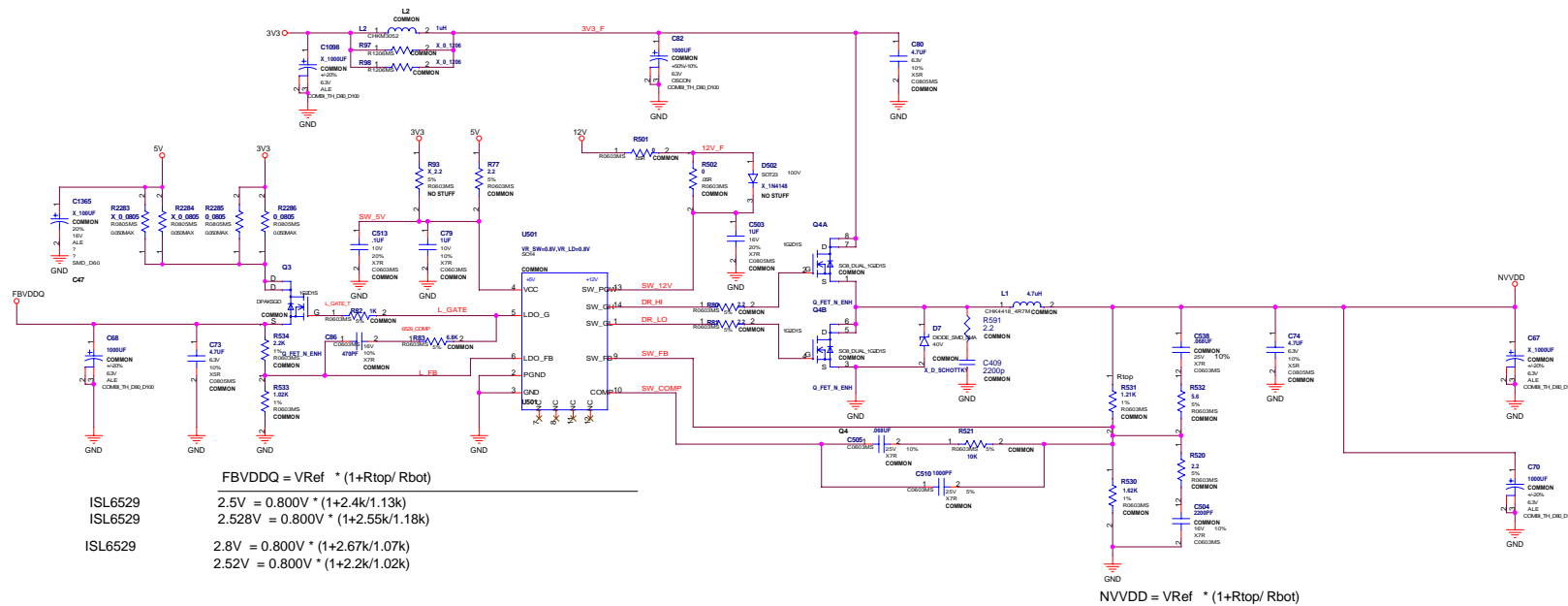
FBVDD 3.3V



For SAMSUNG DDR FBVDD=FBVDDQ=2.6
HYNIX DDR FBVDD=3V3=3.3V

	NET	NET_PHYSICAL_TYPE	VOLTAGE
12V	12V	12M_TRACK	12V
5V	5V	5M_TRACK	5V
NVDD0	NVDD0	12M_TRACK	12V
A3V3	A3V3	12M_TRACK	3.3V
3V3	3V3	12M_TRACK	3.3V
	AVDD0	12M_TRACK	3.3V
	NVDD0	12M_TRACK	12V
	5V	5M_TRACK	5V
	12V	12M_TRACK	12V
	IOVDD	12M_TRACK	3.3V
	IOV_F	12M_TRACK	12V
	IOV_PUDD	12M_TRACK	5V
	IOVDC_2V	12M_TRACK	5V
	IOV_OV	12M_TRACK	12V
	IOV_5V	12M_TRACK	5V
	IOV_1A	12M_TRACK	
	IOV_1A5	12M_TRACK	
	IOV_FB	12M_TRACK	
	IOV_GNDP	12M_TRACK	
	A3V3_OV	12M_TRACK	
	IOVDD0	12M_TRACK	3.3V

NVDD-SWITCHER / FBVDD-LDO CONTROLER ISL6529



$$FBVDDQ = V_{Ref} * (1 + R_{top} / R_{bot})$$

$$\text{ISL6529} \quad 2.5\text{V} = 0.800\text{V} * (1 + 2.4\text{k}/1.13\text{k})$$

$$\text{ISL6529} \quad 2.528\text{V} = 0.800\text{V} * (1+2.55\text{k}/1.18\text{k})$$

ISL6529 $2.8V = 0.800V * (1+2.67k/1.07k)$

$$2.52V = 0.800V * (1 + 2.2k/1.02k)$$

$$NVDD = V_{Ref} * (1 + R_{top} / R_{bot})$$

$$1.656V = 0.800V * (1 + 1070/1000)$$

$$1.45V = 0.800V * (1 + 820/1000)$$
$$1.4V = 0.800V * (1 + 1.21K/1.62K)$$

ISL6529

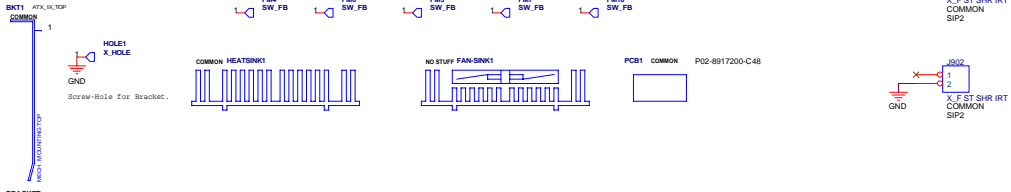
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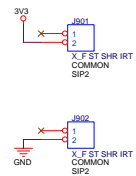
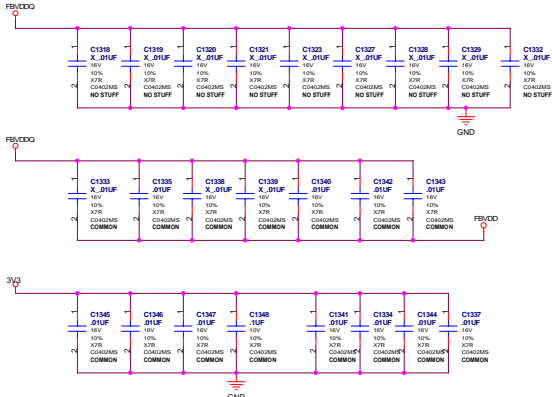
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MECHANICS




E22-0000541-C22
Mech. Card Bracket, SPCC3, V067, with M3x0.5 screw*1, For MS-8878
Bracket Same as MS-8847



NET	NET_PHYSICAL_TYPE	VOLTAGE
3V3	3V3	3.3V
FBVDD	10A_TRACE	3.3V
5V	5V	5.0V

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