NV11,4Mx16 DDR, RGB, INTERNAL DVI-I, AGP.

PCI DEVICE ID 0X0=0X110 FOR NV11.

STRAPS: AGP4X, SIDEBAND DIS, FAST WRITE ENA, ADP BIOS, NORMAL PCIAD, 14.318MHZ
STRAP_RAM_TYPE [5:2]=1010.4MX16 DDR SDRAM

HISTORY REV. A00:

1) A00: Imported P0039-C00
2) A00: Changed FB Memory to 4Mx16 DDR Ram
3) A00: Added 2.5V Power Reg for DDR RAM
4) A00: Changed Strap resistors.

HISTORY REV. A01:

- Changed R212 to 110R value
(RSET)

602-10044-0000-A01

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Size	CAGE Code	DWG NO	Rev
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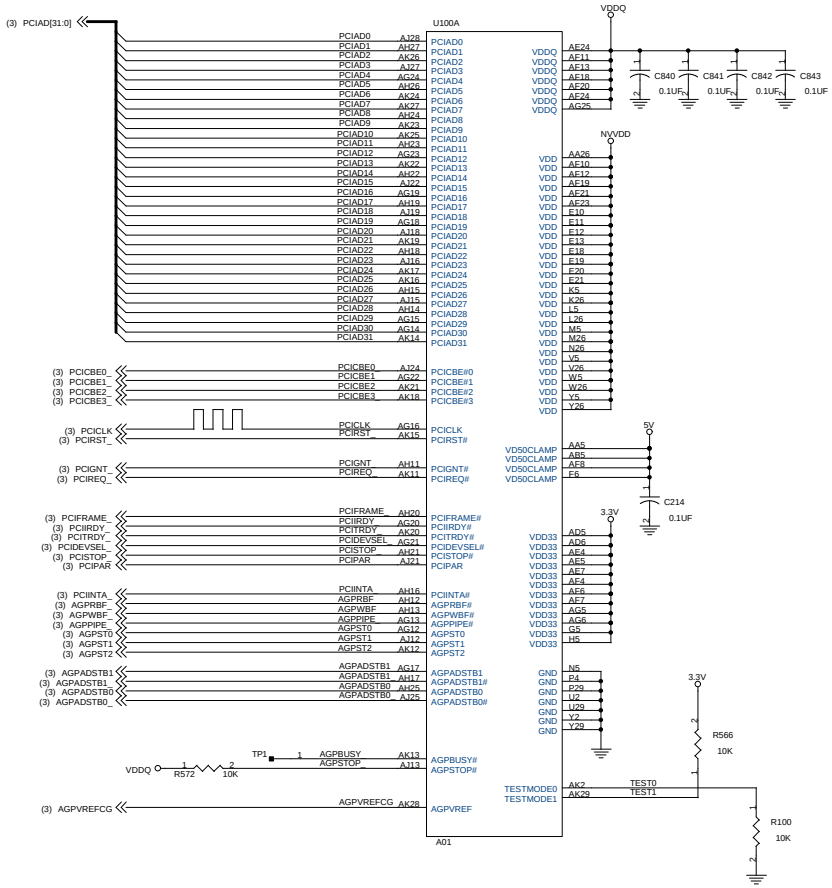
Saturday, March 09, 2002

Scale

Sheet 1 of 12

{Item}\t{Quantity}\t{Reference}\t{Source Package}\t{Value}\t{Assembly}
{Assembly}{Source Package} {Value}{PN}{AVL1}{AVL2}{AVL3}{AVL4}{AVL5}



1a. NVXX HOST

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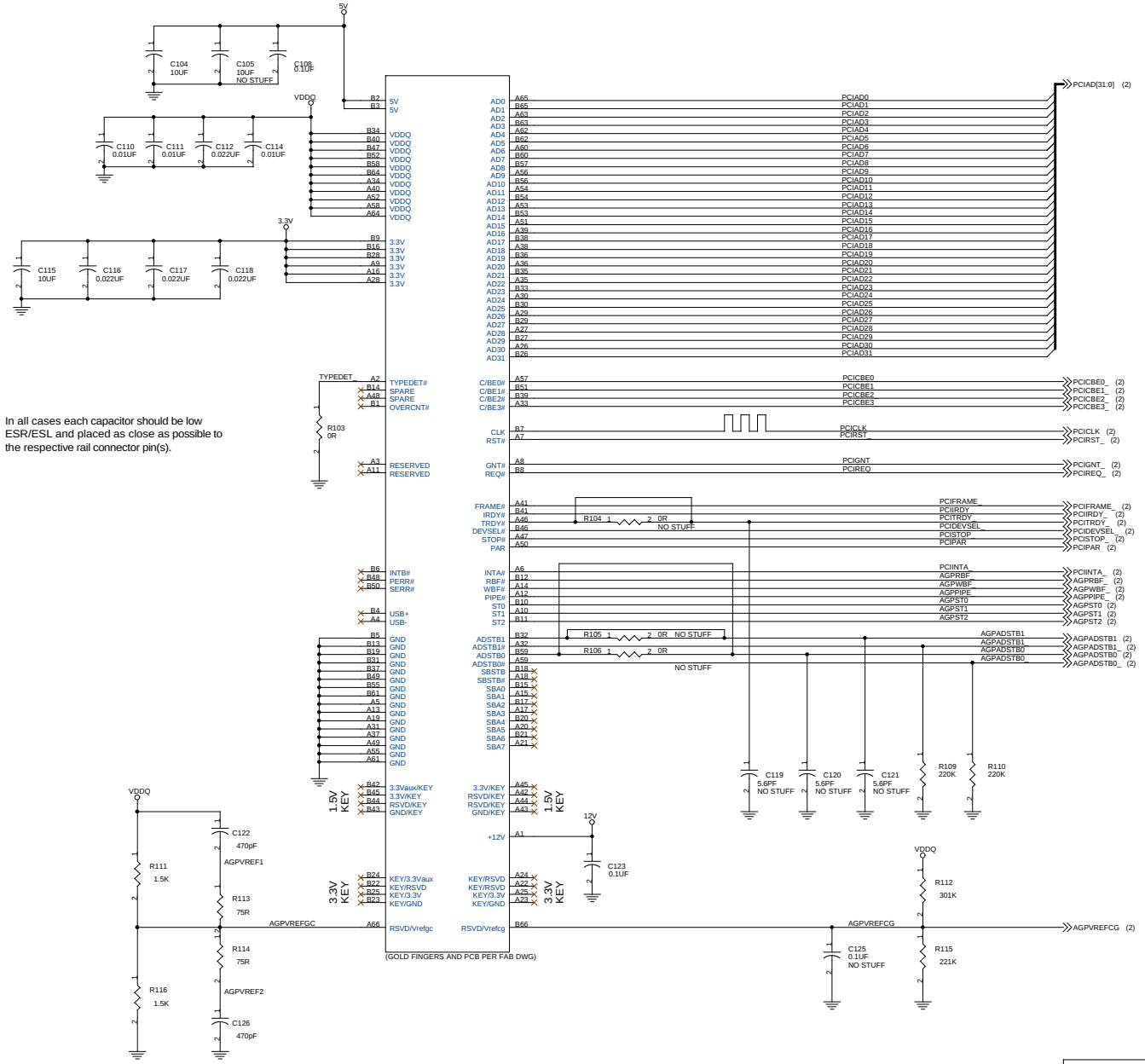
Size	CAGE Code	DWG NO	Rev
C			

Monday, March 11, 2002

Scale

Sheet 2 of 12



1b1. AGP I/O

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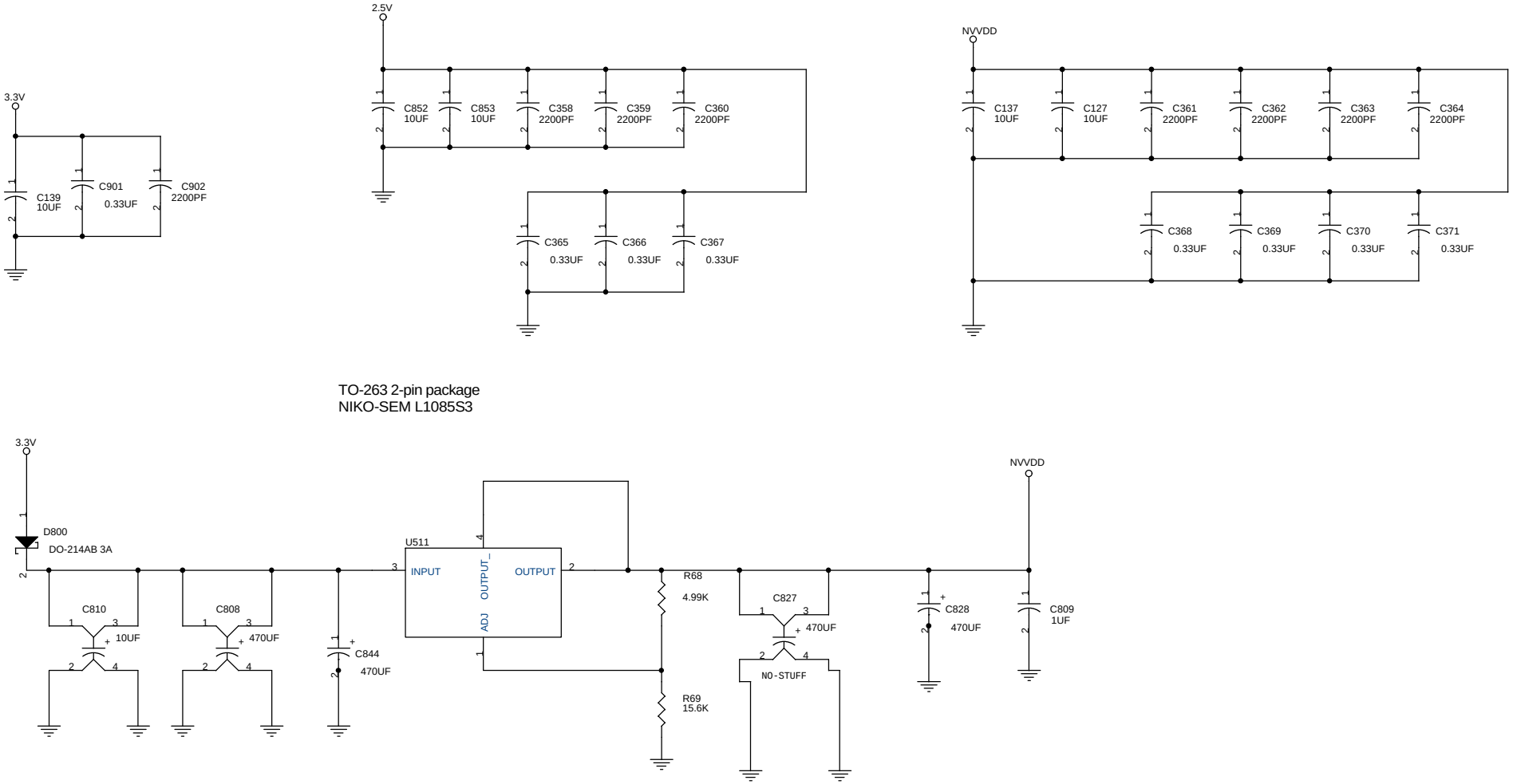
Size	CAGE Code	DWG NO	Rev
C			

Monday, March 11, 2002

Scale

Sheet 3 of 12



TO-263 2-pin package
NIKO-SEM L1085S3

1y. NVXX DECOUPLING

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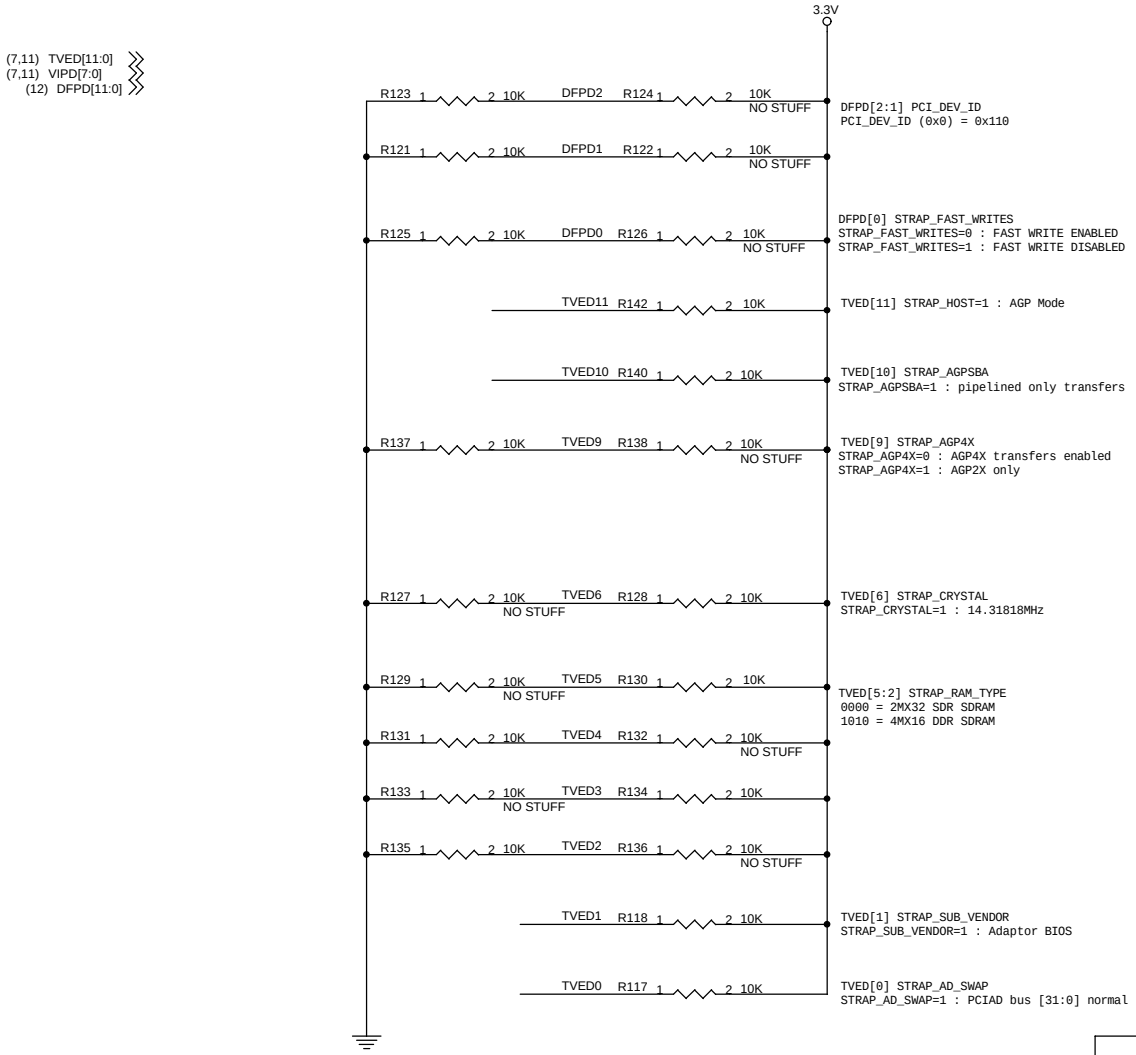
B			

Monday, March 11, 2002

Scale

Sheet 4 of 12



1z. NVXX HOST STRAPS

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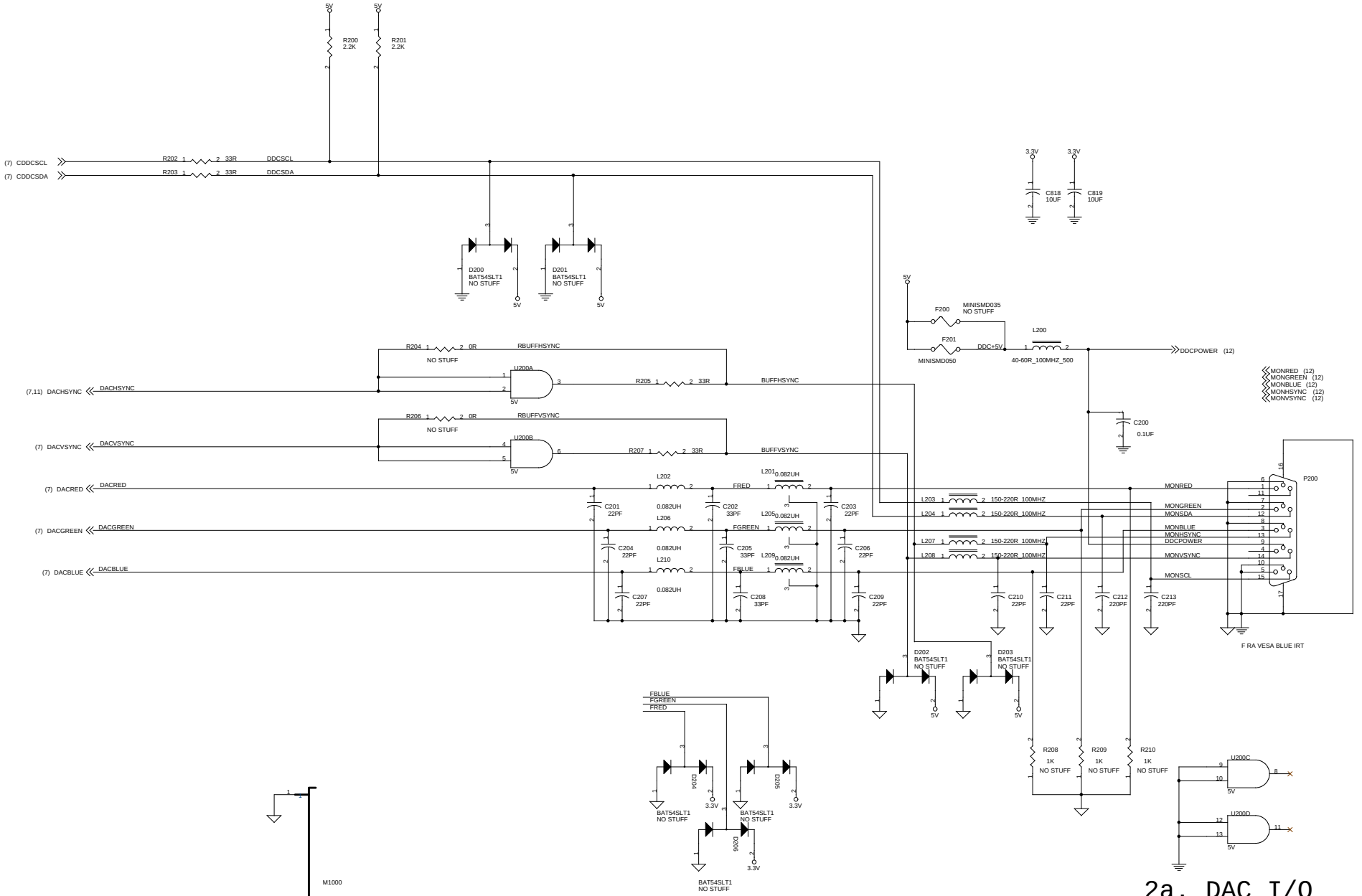
Santa Clara, CA 95051, USA

Size	CAGE Code	DWG NO	Rev
B			

Monday, March 11, 2002

Scale

Sheet 5 of 12



2a. DAC I/O

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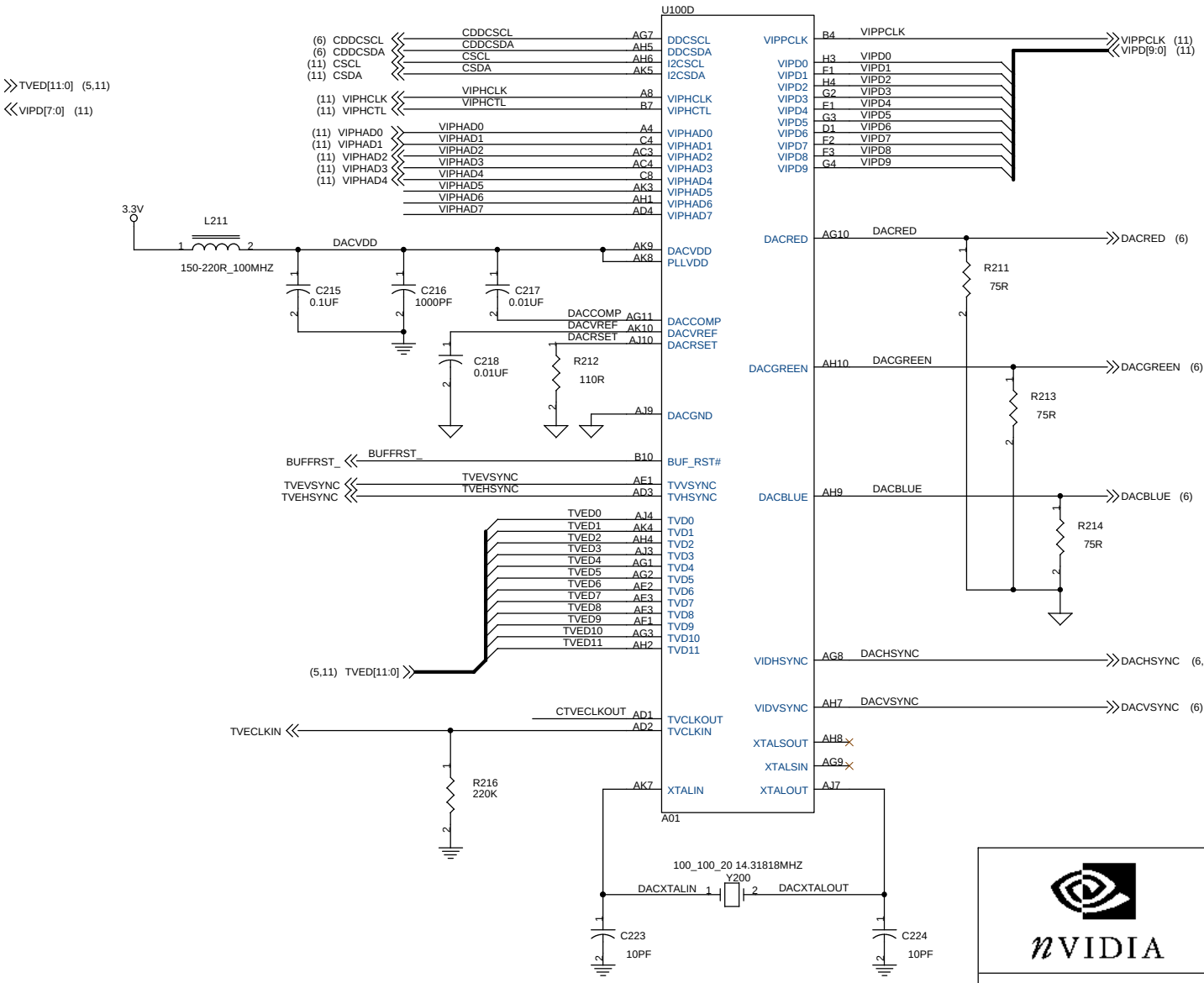
Size	CAGE Code	DWG NO	Rev
C			

Monday, March 11, 2002

Scale

Sheet 6 of 12



Use thick (non-impedance controlled) traces on XTALIN/OUT

2b. NVXX DAC

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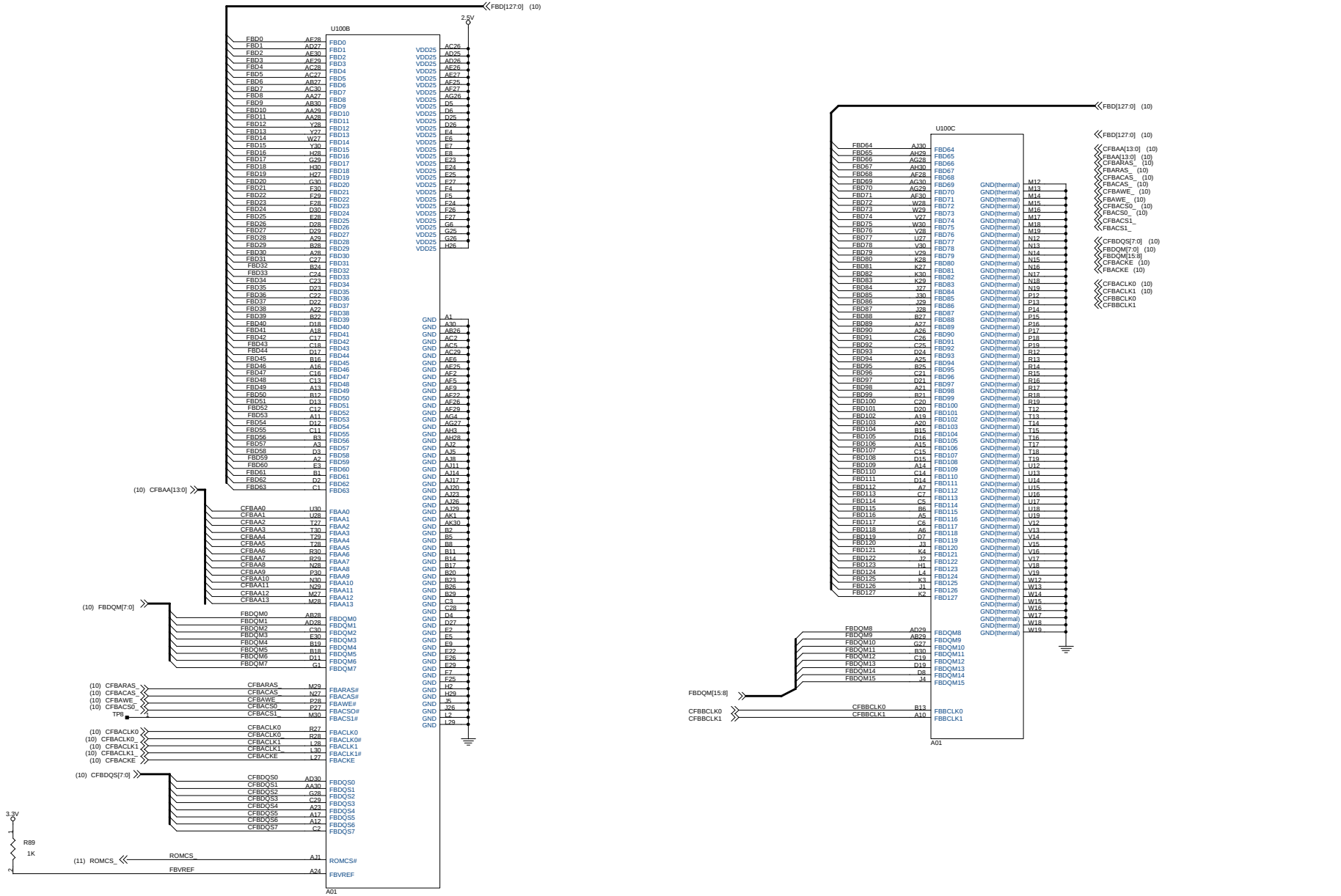
Size	CAGE Code	DWG NO	Rev
B			

Monday, March 11, 2002

Scale

Sheet 7 of 12



3a. NVXX FBA/FBB

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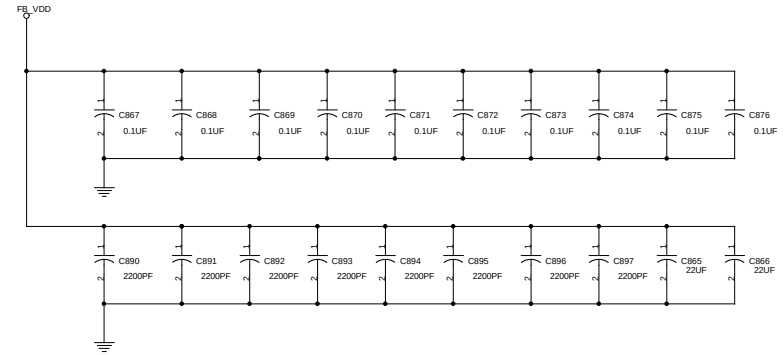
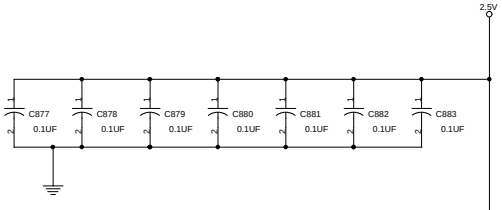
Size	CAGE Code	DWG NO	Rev
C			

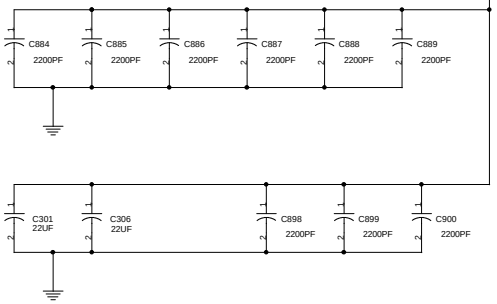
Monday, March 11, 2002

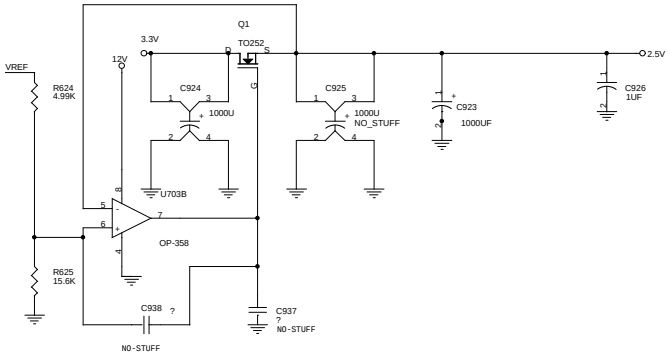
Scale

Sheet 8 of 12

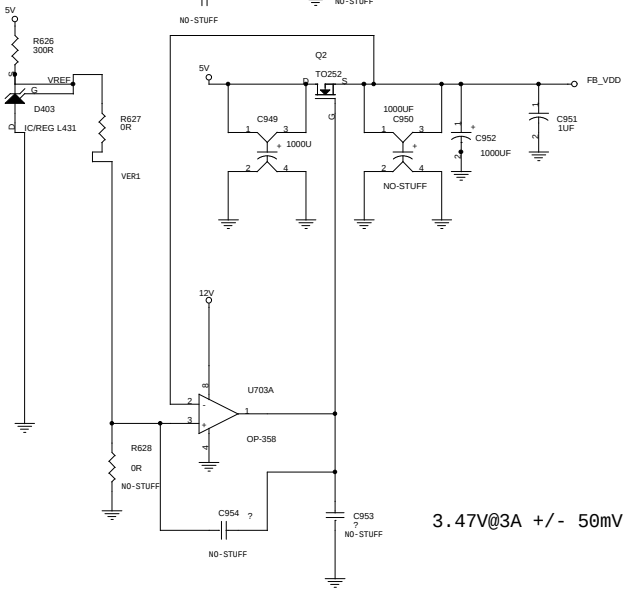




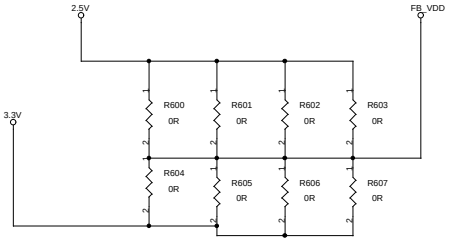




(3V)



3.47V@3A +/- 50mV



3b. FB DECOUPLING

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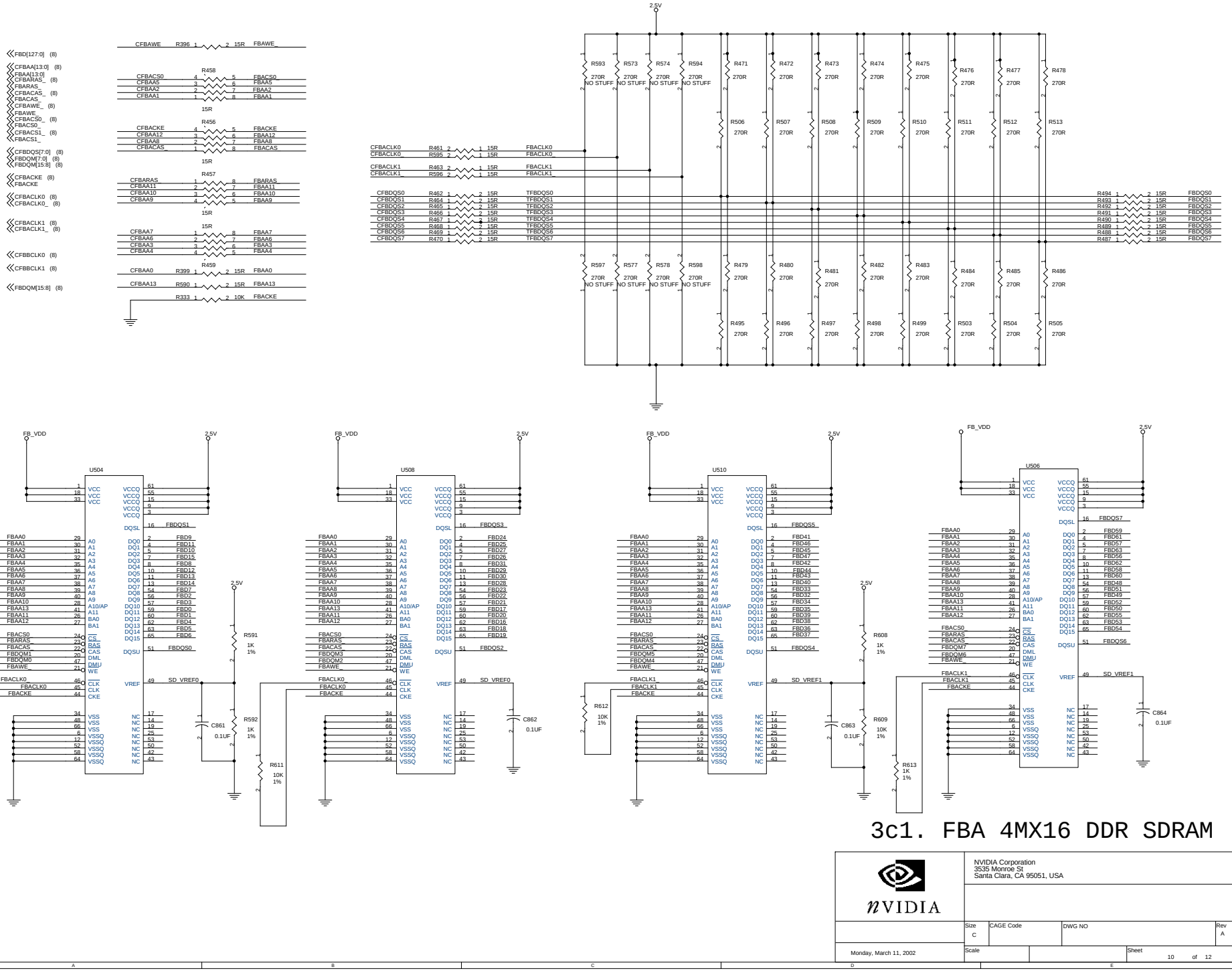
Size	CAGE Code	DWG NO	Rev
C			

Monday, March 11, 2002

Scale

Sheet 9 of 12



3c1. FBA 4MX16 DDR SDRAM

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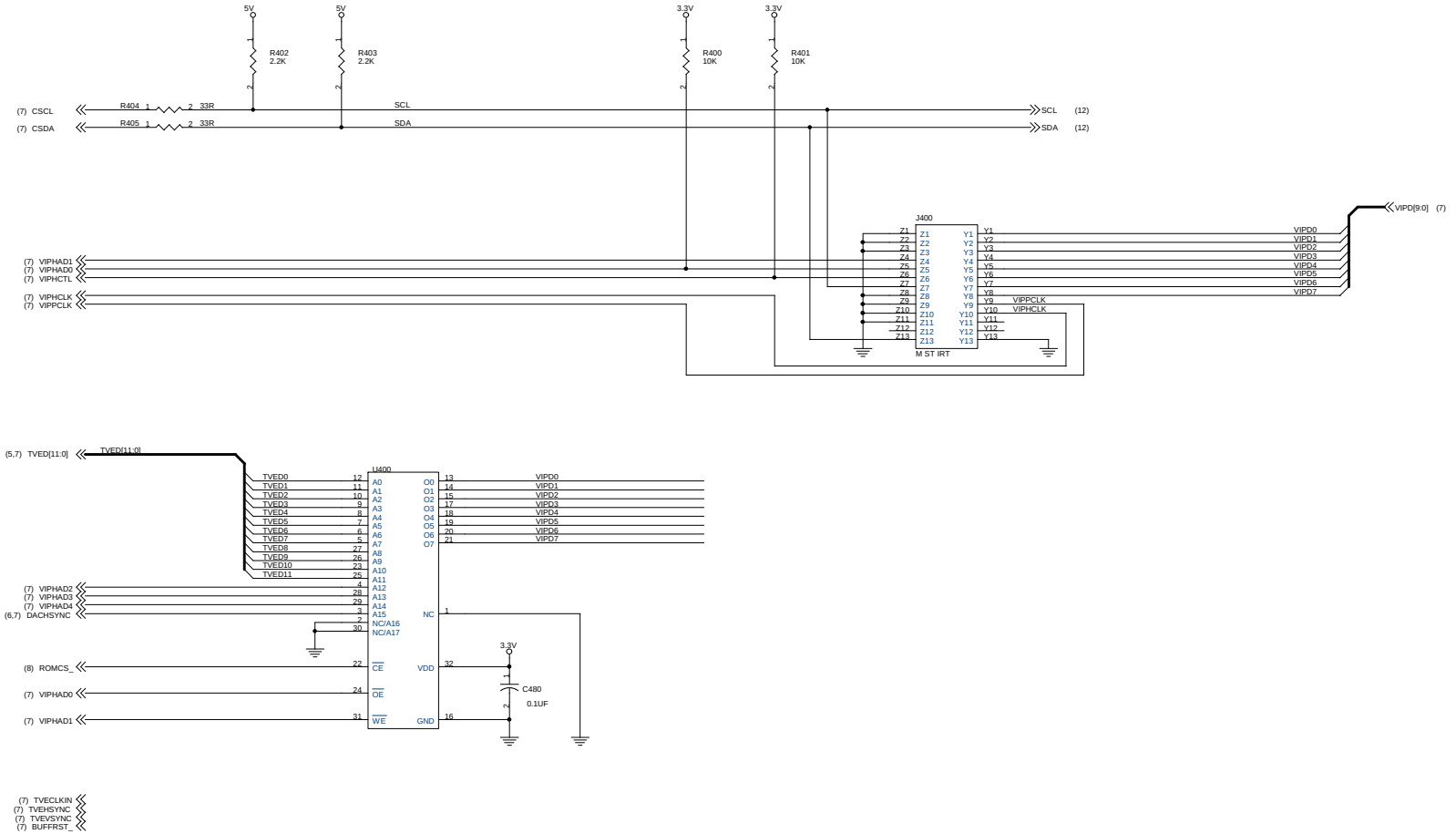
Size	CAGE Code	DWG NO	Rev
C			A

Monday, March 11, 2002

Scale

Sheet 10 of 12



4a. ROM VIP & VIDEO IF

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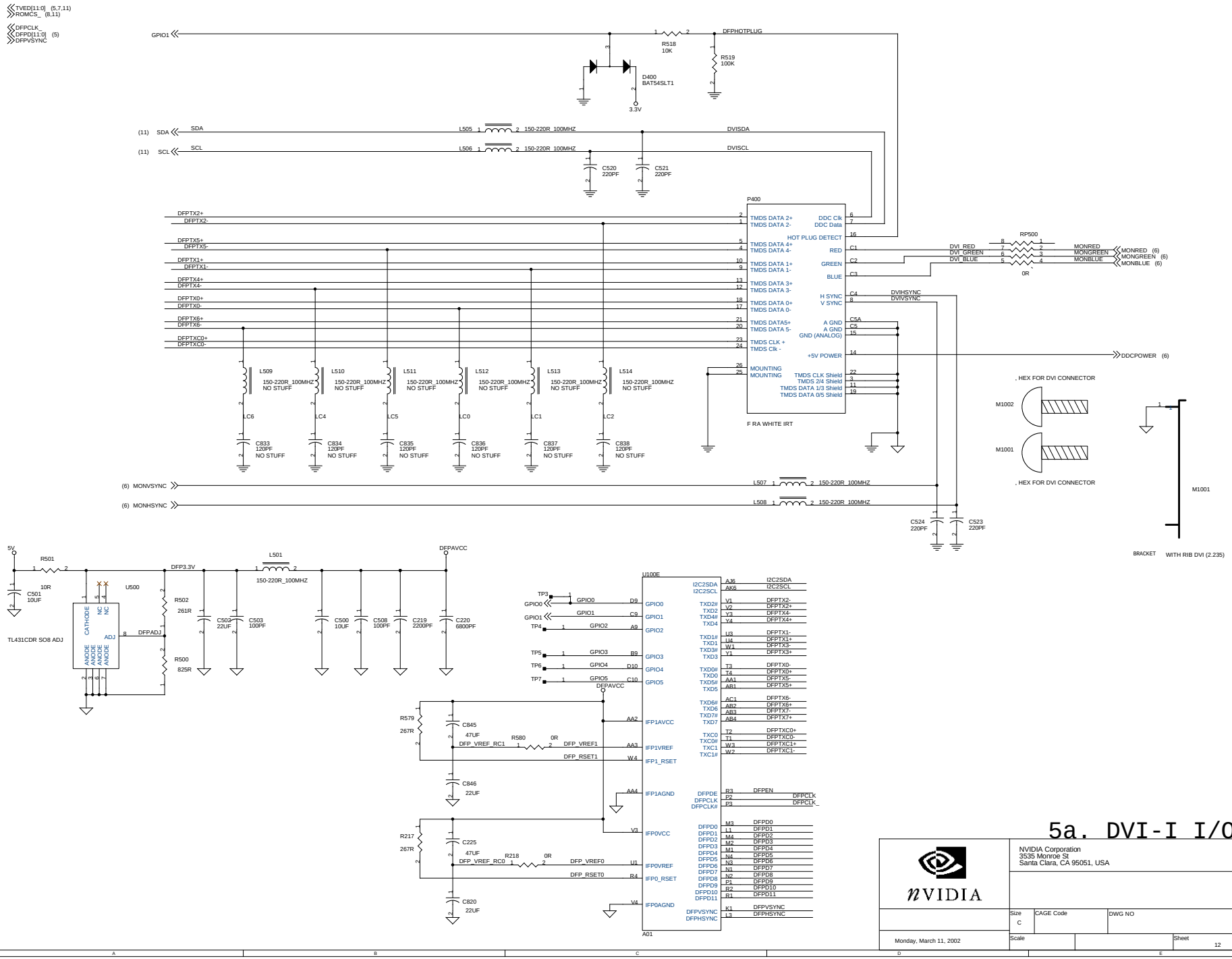
Size	CAGE Code	DWG NO	Rev
C			

Monday, March 11, 2002

Scale

Sheet 11 of 12



5a. DVI-I I/O

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3535 Monroe St
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Size	CAGE Code	DWG NO	Rev
C			A

Monday, March 11, 2002

Scale

Sheet 12 of 12