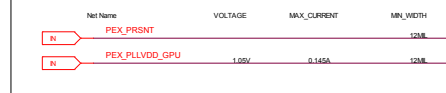
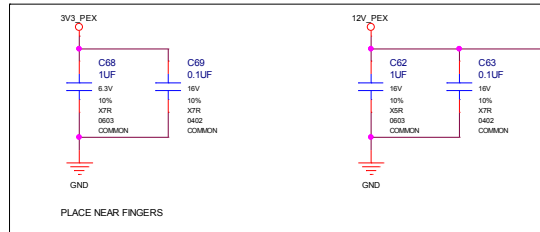
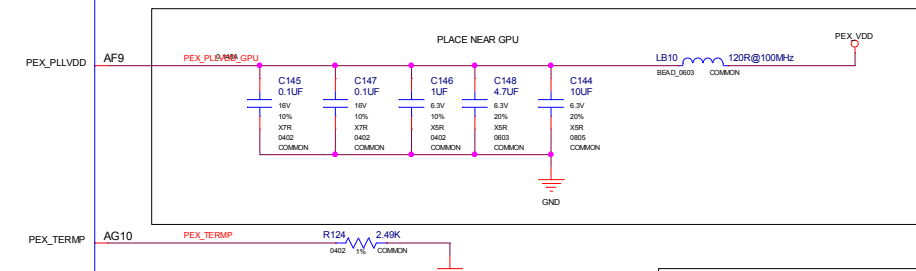
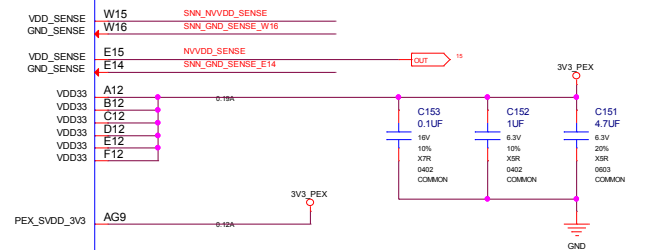
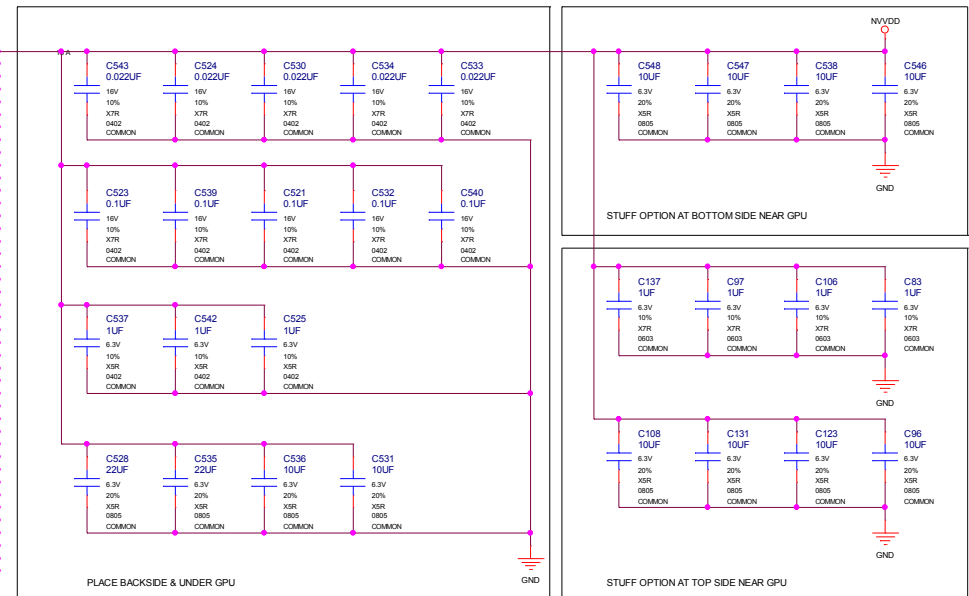
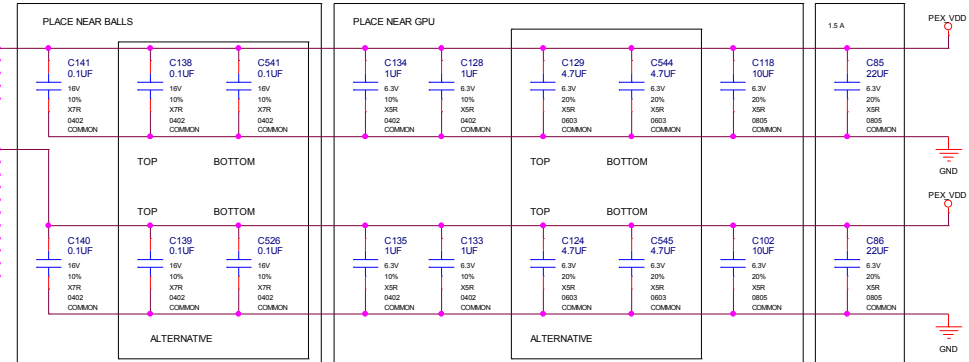
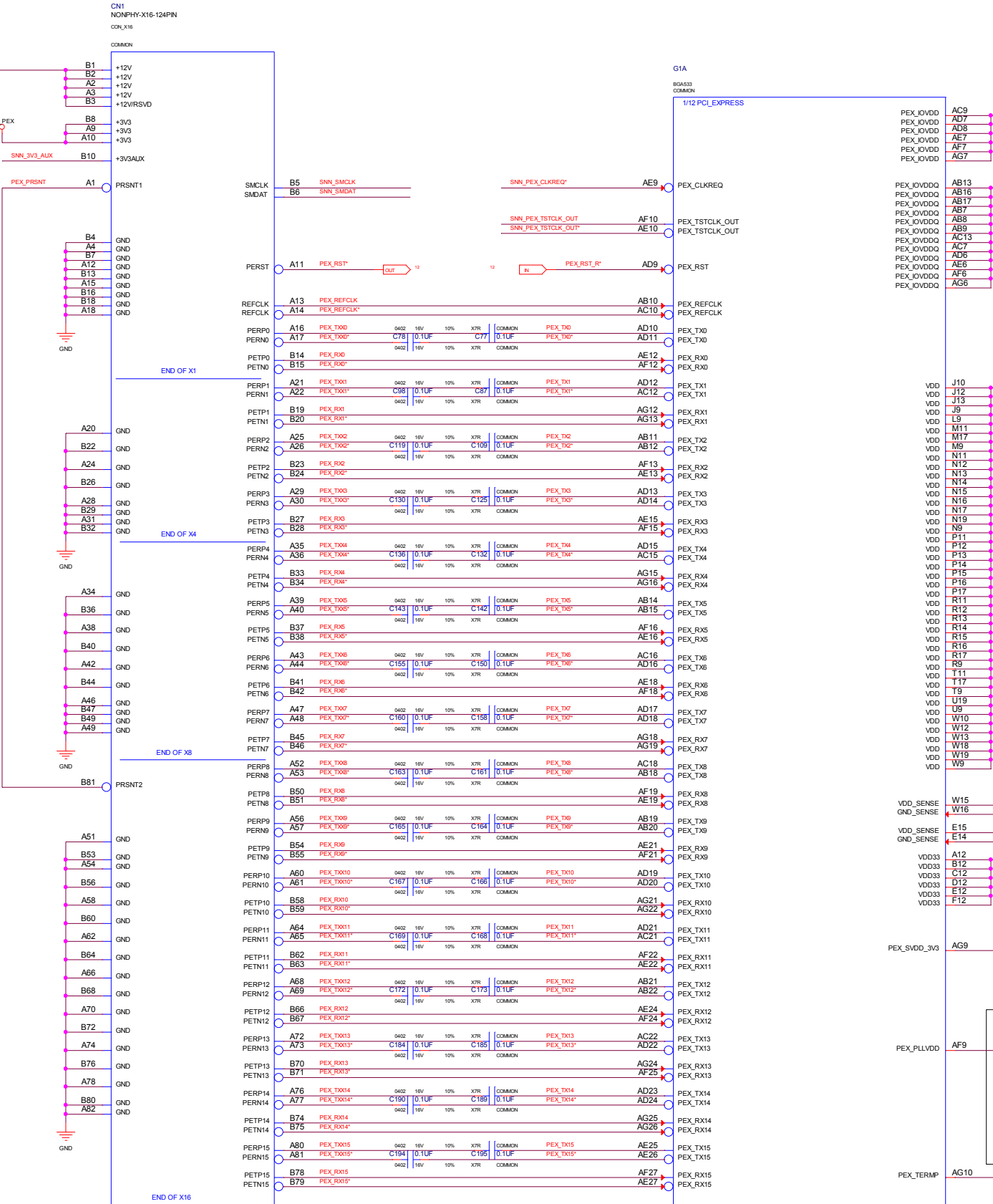


A	B	C	D	E	F	G	H

PCI Express Interface



Net Name		DIFF_PAIR	CRITICAL	IMPEDANCE
N	PEX_D0	PEX_D00	1	500K
N	PEX_D0*	PEX_D00	1	500K
N	PEX_D1	PEX_D01	1	500K
N	PEX_D1*	PEX_D01	1	500K
N	PEX_D2	PEX_D02	1	500K
N	PEX_D2*	PEX_D02	1	500K
N	PEX_D3	PEX_D03	1	500K
N	PEX_D3*	PEX_D03	1	500K
N	PEX_D4	PEX_D04	1	500K
N	PEX_D4*	PEX_D04	1	500K
N	PEX_D5	PEX_D05	1	500K
N	PEX_D5*	PEX_D05	1	500K
N	PEX_D6	PEX_D06	1	500K
N	PEX_D6*	PEX_D06	1	500K
N	PEX_D7	PEX_D07	1	500K
N	PEX_D7*	PEX_D07	1	500K
N	PEX_D8	PEX_D08	1	500K
N	PEX_D8*	PEX_D08	1	500K
N	PEX_D9	PEX_D09	1	500K
N	PEX_D9*	PEX_D09	1	500K
N	PEX_D10	PEX_D100	1	500K
N	PEX_D10*	PEX_D100	1	500K
N	PEX_D11	PEX_D101	1	500K
N	PEX_D11*	PEX_D101	1	500K
N	PEX_D12	PEX_D102	1	500K
N	PEX_D12*	PEX_D102	1	500K
N	PEX_D13	PEX_D103	1	500K
N	PEX_D13*	PEX_D103	1	500K
N	PEX_D14	PEX_D104	1	500K
N	PEX_D14*	PEX_D104	1	500K
N	PEX_D15	PEX_D105	1	500K
N	PEX_D15*	PEX_D105	1	500K
N	PEX_D16	PEX_D106	1	500K
N	PEX_D16*	PEX_D106	1	500K
N	PEX_D17	PEX_D107	1	500K
N	PEX_D17*	PEX_D107	1	500K
N	PEX_D18	PEX_D108	1	500K
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N	PEX_D20	PEX_D1000	1	500K
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N	PEX_D21	PEX_D1010	1	500K
N	PEX_D21*	PEX_D1010	1	500K
N	PEX_D22	PEX_D1011	1	500K
N	PEX_D22*	PEX_D1011	1	500K
N	PEX_D23	PEX_D1012	1	500K
N	PEX_D23*	PEX_D1012	1	500K
N	PEX_D24	PEX_D1013	1	500K
N	PEX_D24*	PEX_D1013	1	500K
N	PEX_D25	PEX_D1014	1	500K
N	PEX_D25*	PEX_D1014	1	500K
N	PEX_D26	PEX_D1015	1	500K
N	PEX_D26*	PEX_D1015	1	500K
N	PEX_R00	PEX_R000	1	500K
N	PEX_R00*	PEX_R000	1	500K
N	PEX_R01	PEX_R001	1	500K
N	PEX_R01*	PEX_R001	1	500K
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N	PEX_R02*	PEX_R002	1	500K
N	PEX_R03	PEX_R003	1	500K
N	PEX_R03*	PEX_R003	1	500K
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N	PEX_R06	PEX_R006	1	500K
N	PEX_R06*	PEX_R006	1	500K
N	PEX_R07	PEX_R007	1	500K
N	PEX_R07*	PEX_R007	1	500K
N	PEX_R08	PEX_R008	1	500K
N	PEX_R08*	PEX_R008	1	500K
N	PEX_R09	PEX_R009	1	500K
N	PEX_R09*	PEX_R009	1	500K
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N	PEX_R13	PEX_R013	1	500K
N	PEX_R13*	PEX_R013	1	500K
N	PEX_R14	PEX_R014	1	500K
N	PEX_R14*	PEX_R014	1	500K
N	PEX_R15	PEX_R015	1	500K
N	PEX_R15*	PEX_R015	1	500K
N	PEX_REFCLK	PEX_REFCLK	1	500K
N	PEX_REFCLK*	PEX_REFCLK	1	500K



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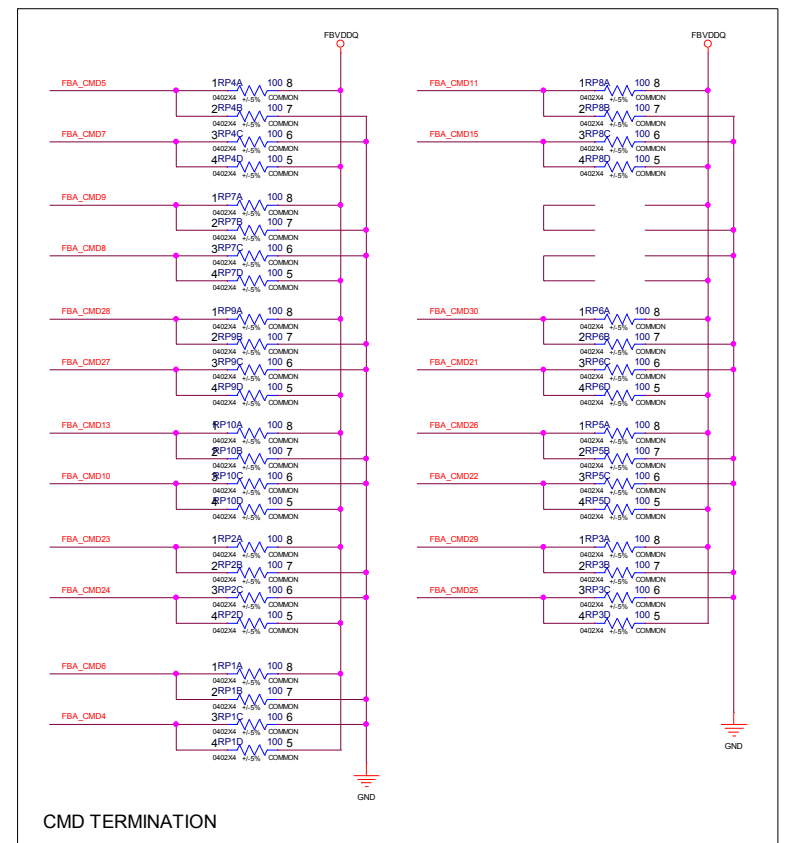
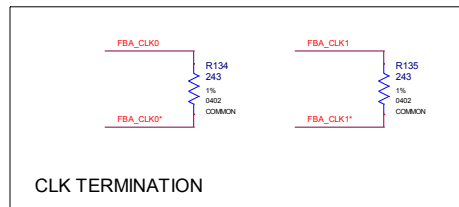
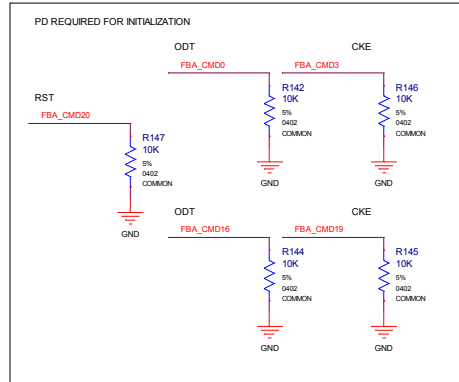
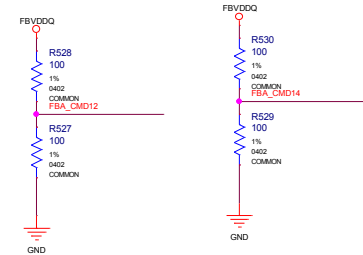
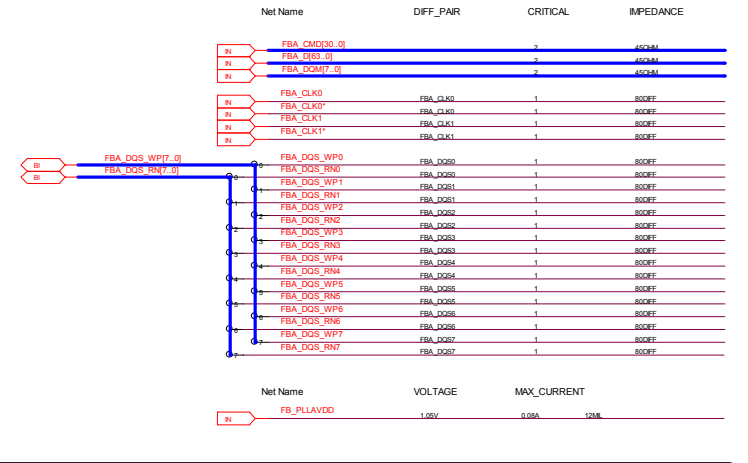
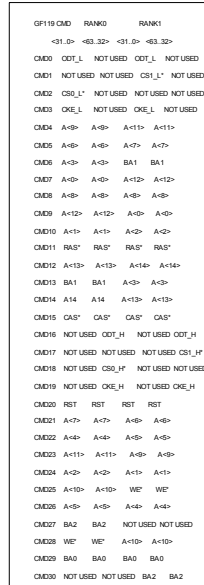
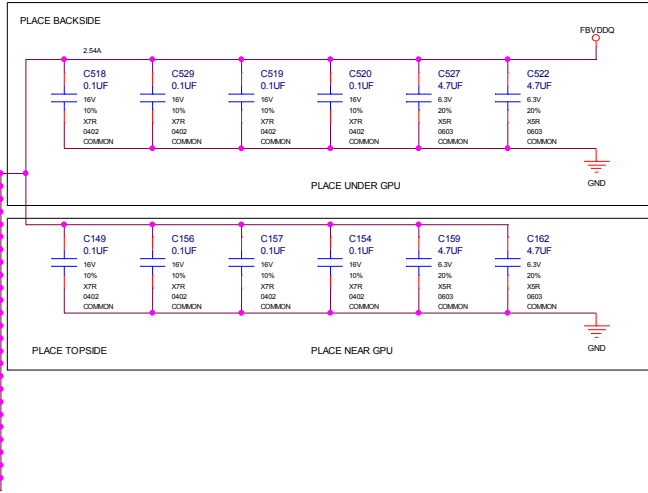
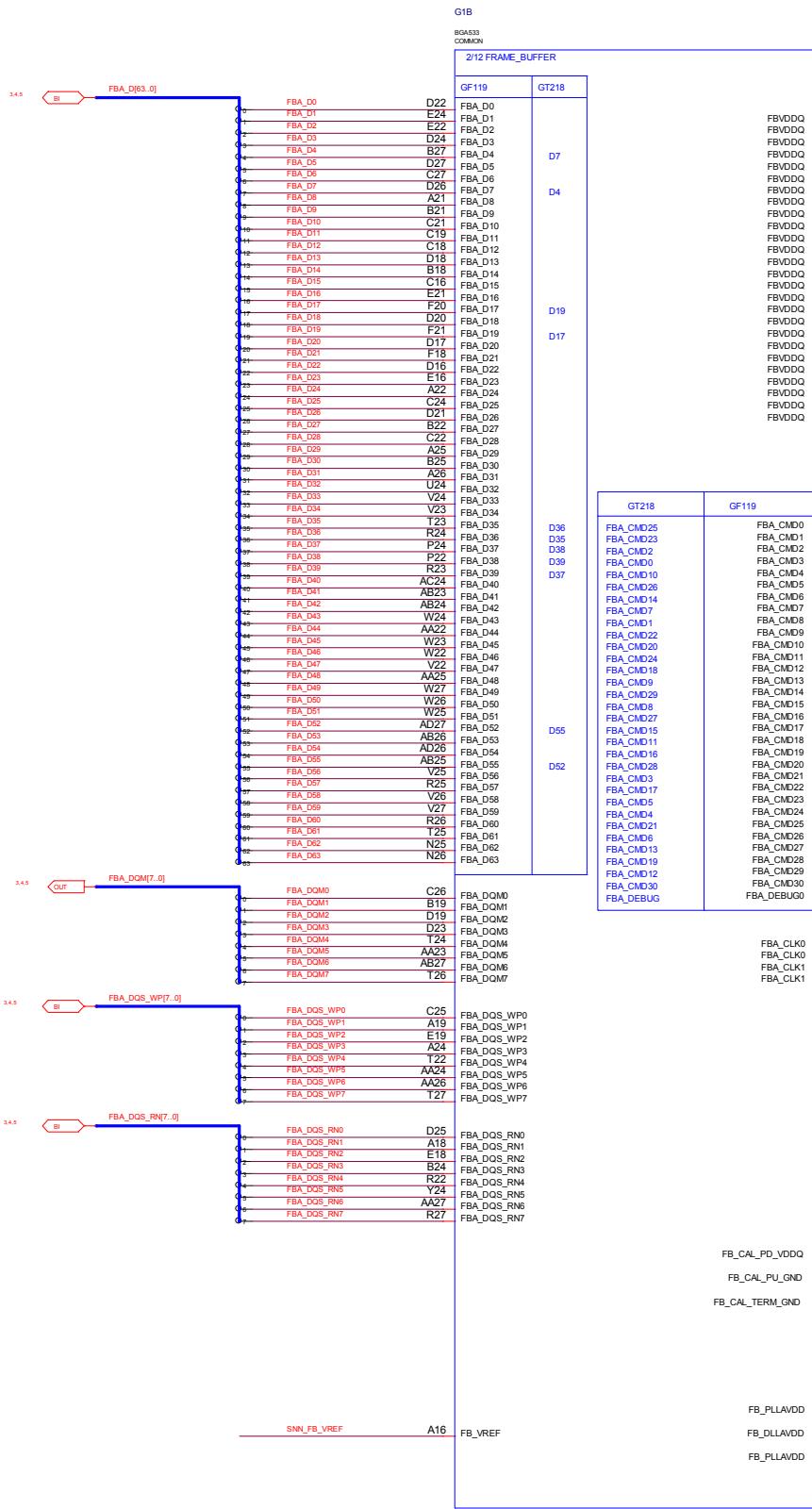


NV_PN	600-11310-BASE-100		
PCB REV	P1310-A01	PAGE	
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	PCI Express Interface

Frame Buffer Interface



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NV_PN	600-11310-BASE-100
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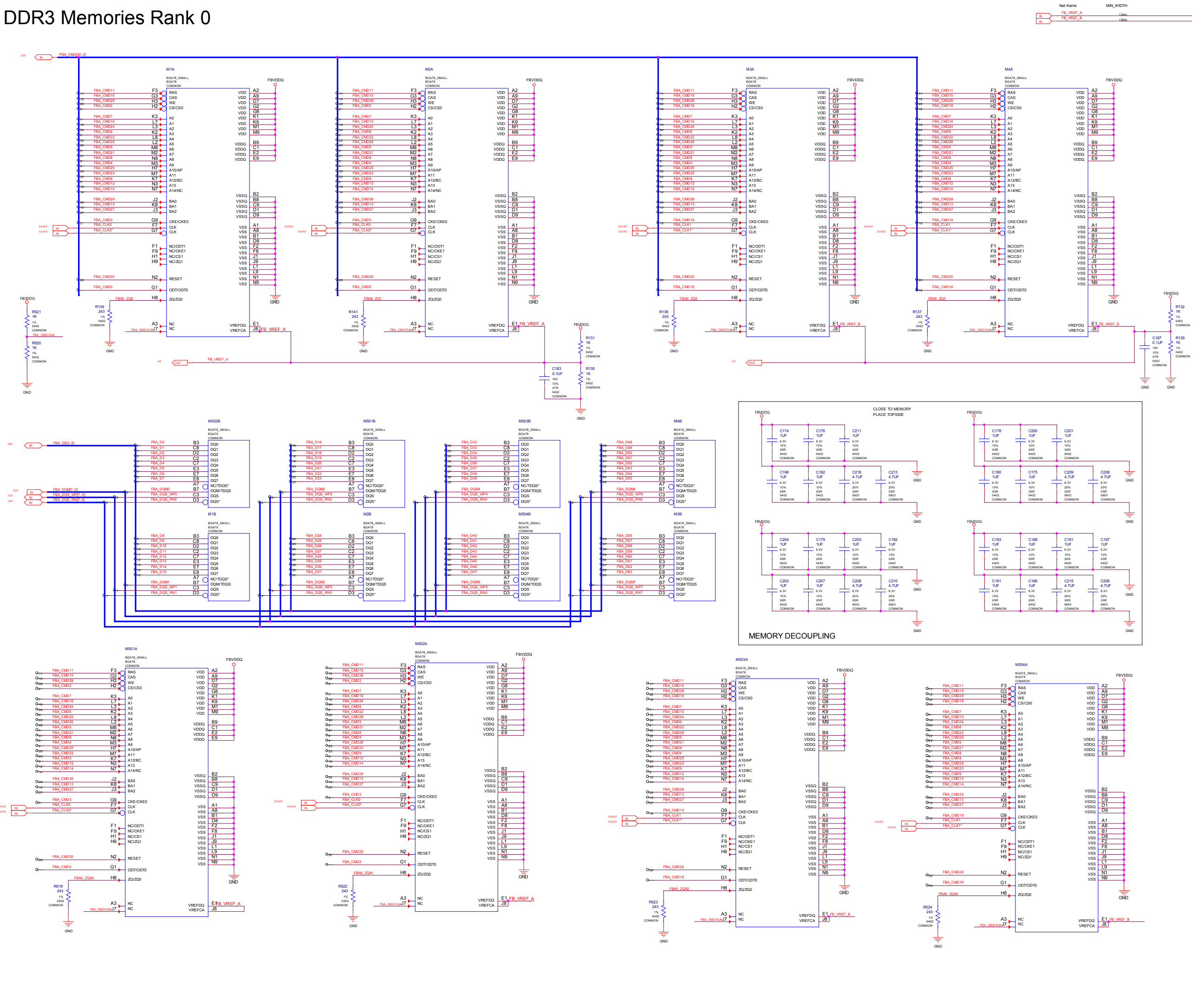
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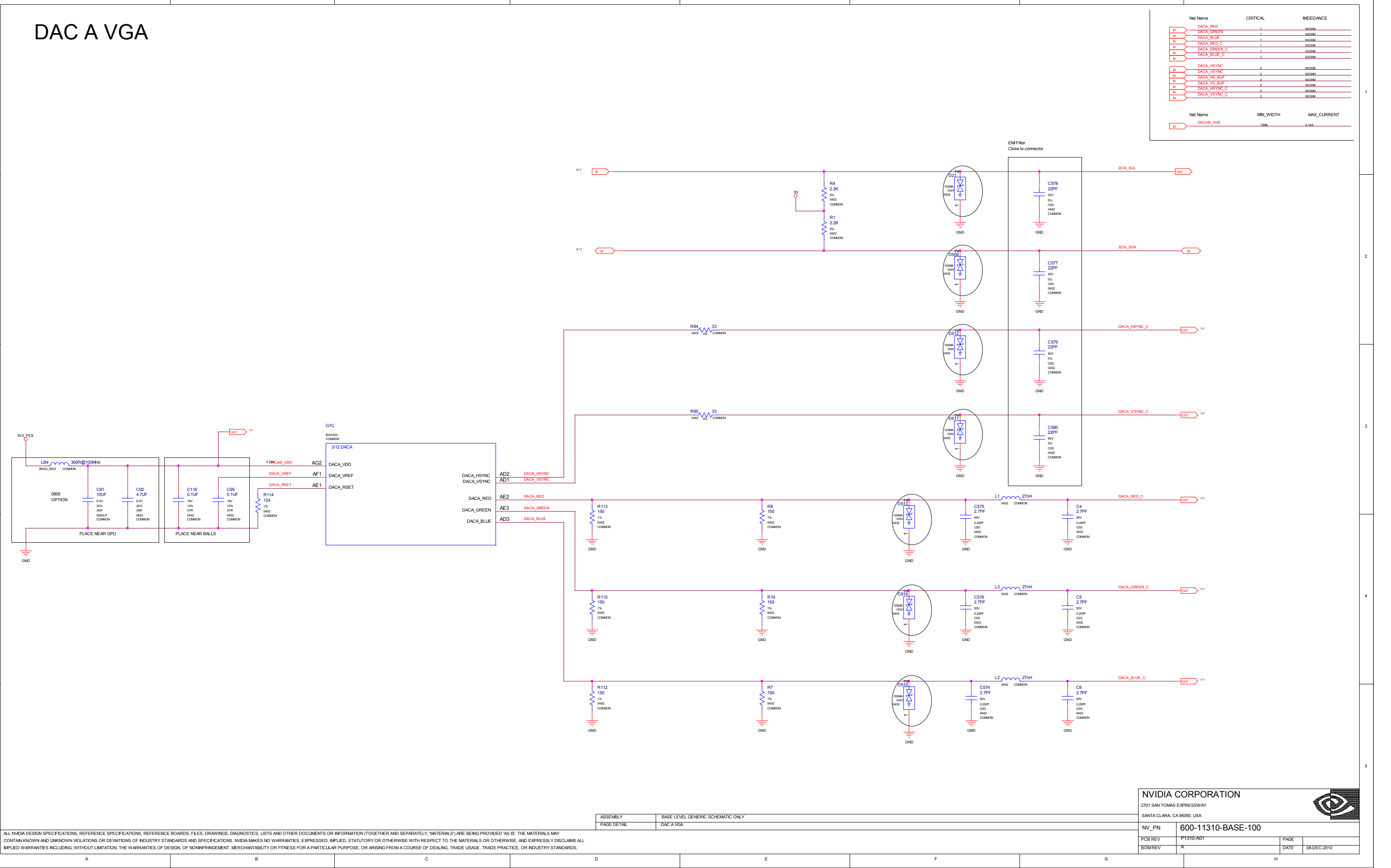
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	Frame Buffer Interface

DDR3 Memories Rank 0

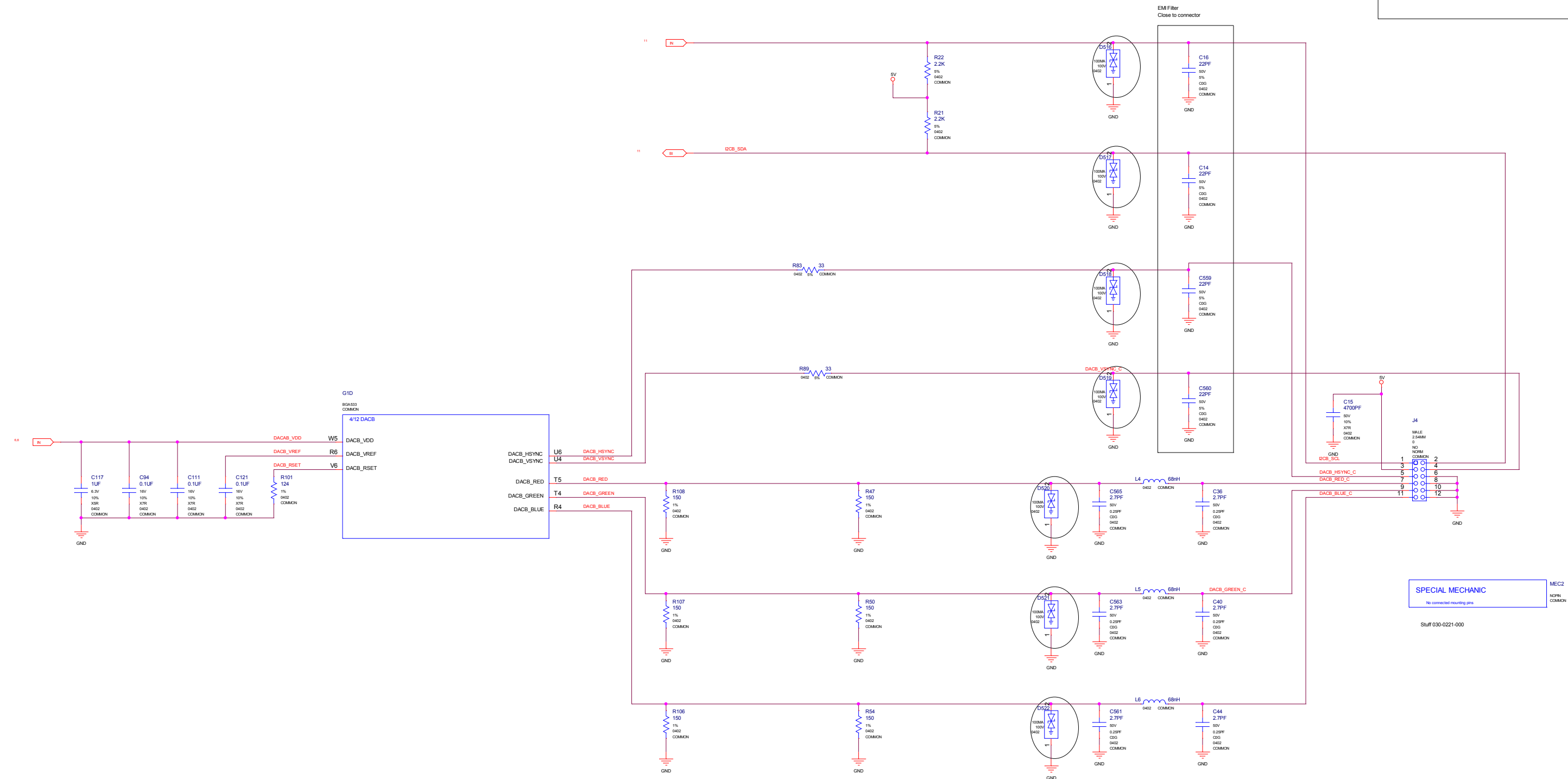


DAC A VGA



DAC B VGA Header

Net Name	CRITICAL	IMPEDANCE
N DADB_RED	1	50ΩIN
N DADB_GREEN	1	50ΩIN
N DADB_BLUE	1	50ΩIN
N DADB_RED_C	1	50ΩIN
N DADB_GREEN_C	1	50ΩIN
N DADB_BLUE_C	1	50ΩIN
N DADB_VSYN	2	50ΩIN
N DADB_VSYN_C	2	50ΩIN
N DADB_VSYN_C	2	50ΩIN
N DADB_HS_BUF	2	50ΩIN
N DADB_VS_BUF	2	50ΩIN



SPECIAL MECHANIC

No connected mounting pins

Stuff 030-0221-000

MEC2

NON-COMMON

COMMON

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	DAC B VGA Header

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MM PN	600 112
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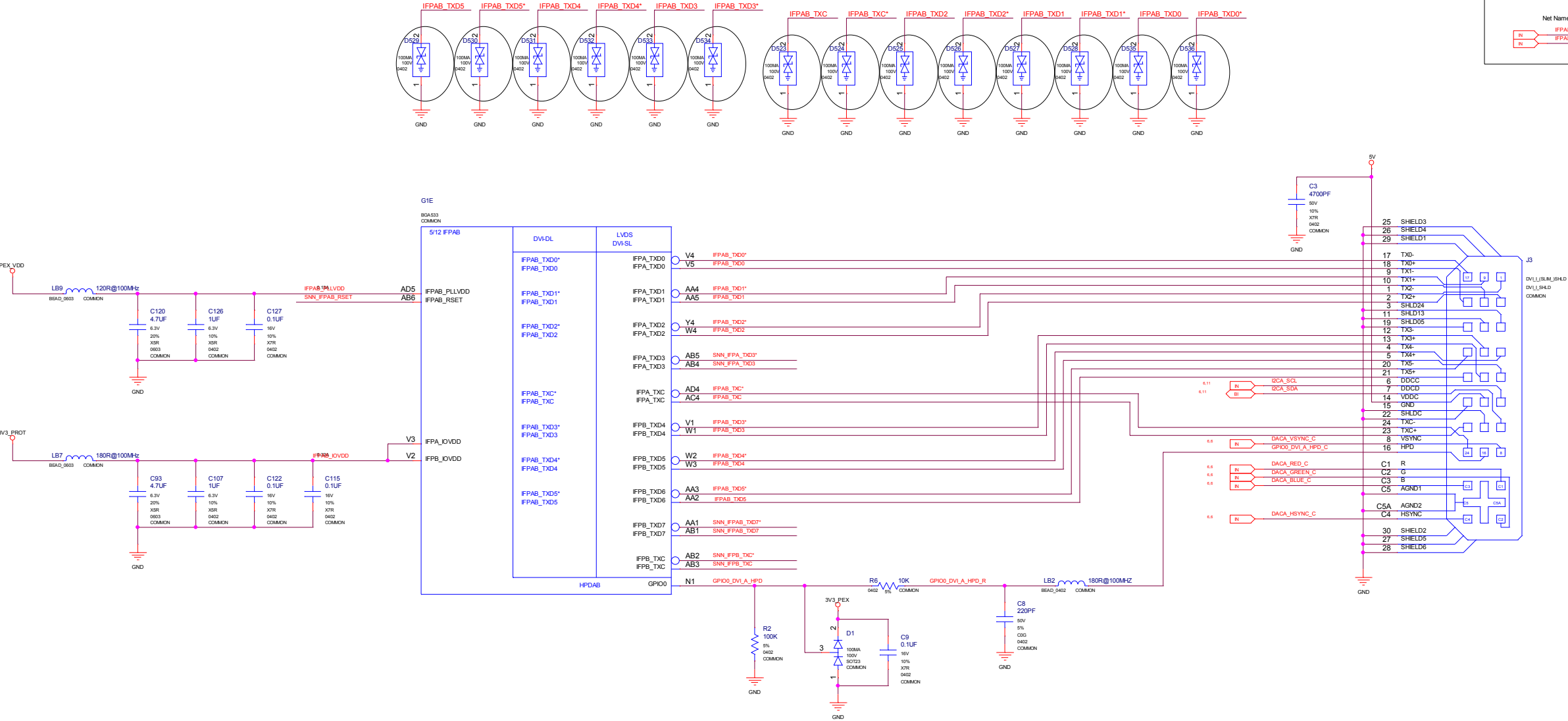
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PCB REV	P1310-A01	PAGE	
BOMREV	A	DATE	08-DEC-2010

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IFPAB TMD5 Interface

Net Name	DIFF_PAIR	CRITICAL	IMPEDANCE
IFPAB_TXD0*	IFPAB_TXD0	1	SECRET
IFPAB_TXD0*	IFPAB_TXD0	1	SECRET
IFPAB_TXD1*	IFPAB_TXD1	1	SECRET
IFPAB_TXD1*	IFPAB_TXD1	1	SECRET
IFPAB_TXD2*	IFPAB_TXD2	1	SECRET
IFPAB_TXD2*	IFPAB_TXD2	1	SECRET
IFPAB_TXD3*	IFPAB_TXD3	1	SECRET
IFPAB_TXD3*	IFPAB_TXD3	1	SECRET
IFPAB_TXD4*	IFPAB_TXD4	1	SECRET
IFPAB_TXD4*	IFPAB_TXD4	1	SECRET
IFPAB_TXD5*	IFPAB_TXD5	1	SECRET
IFPAB_TXD5*	IFPAB_TXD5	1	SECRET
IFPAB_TXC*	IFPAB_TXC	1	SECRET
IFPAB_TXC*	IFPAB_TXC	1	SECRET
IFPAB_TXD0	IFPAB_TXD0	1	SECRET
IFPAB_TXD0	IFPAB_TXD0	1	SECRET
IFPAB_TXD1	IFPAB_TXD1	1	SECRET
IFPAB_TXD1	IFPAB_TXD1	1	SECRET
IFPAB_TXD2	IFPAB_TXD2	1	SECRET
IFPAB_TXD2	IFPAB_TXD2	1	SECRET
IFPAB_TXD3	IFPAB_TXD3	1	SECRET
IFPAB_TXD3	IFPAB_TXD3	1	SECRET
IFPAB_TXD4	IFPAB_TXD4	1	SECRET
IFPAB_TXD4	IFPAB_TXD4	1	SECRET
IFPAB_TXD5	IFPAB_TXD5	1	SECRET
IFPAB_TXD5	IFPAB_TXD5	1	SECRET
IFPAB_TXC	IFPAB_TXC	1	SECRET
IFPAB_TXC	IFPAB_TXC	1	SECRET

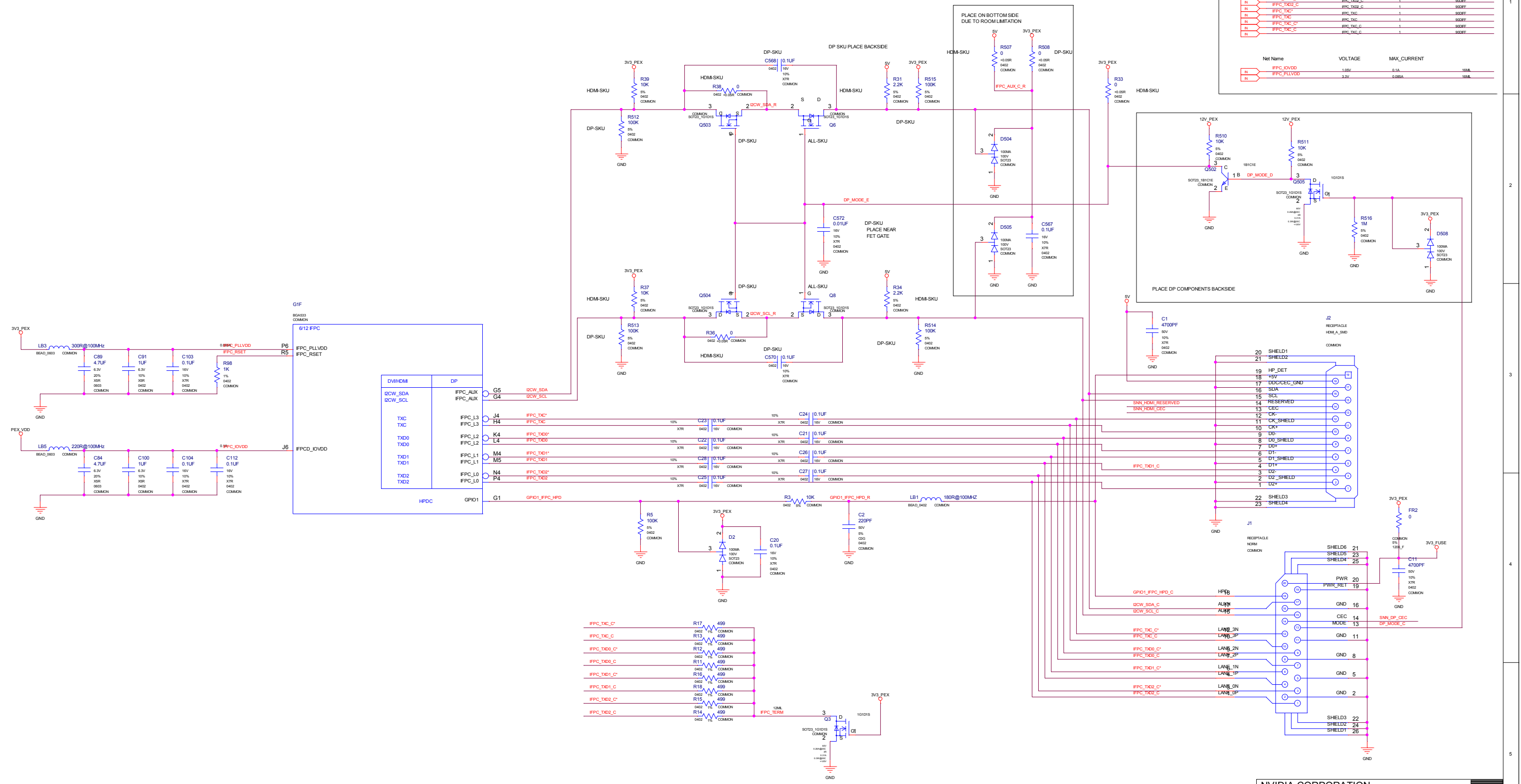
Net Name	VOLTAGE	MAX_CURRENT
IFPAB_PLLVDD	1.05V	0.13A 100ML
IFPAB_IOVDD	3.3V	0.32A 100ML




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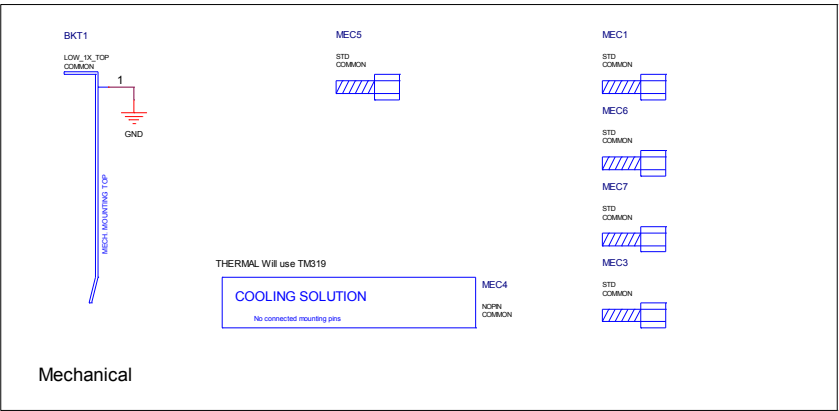
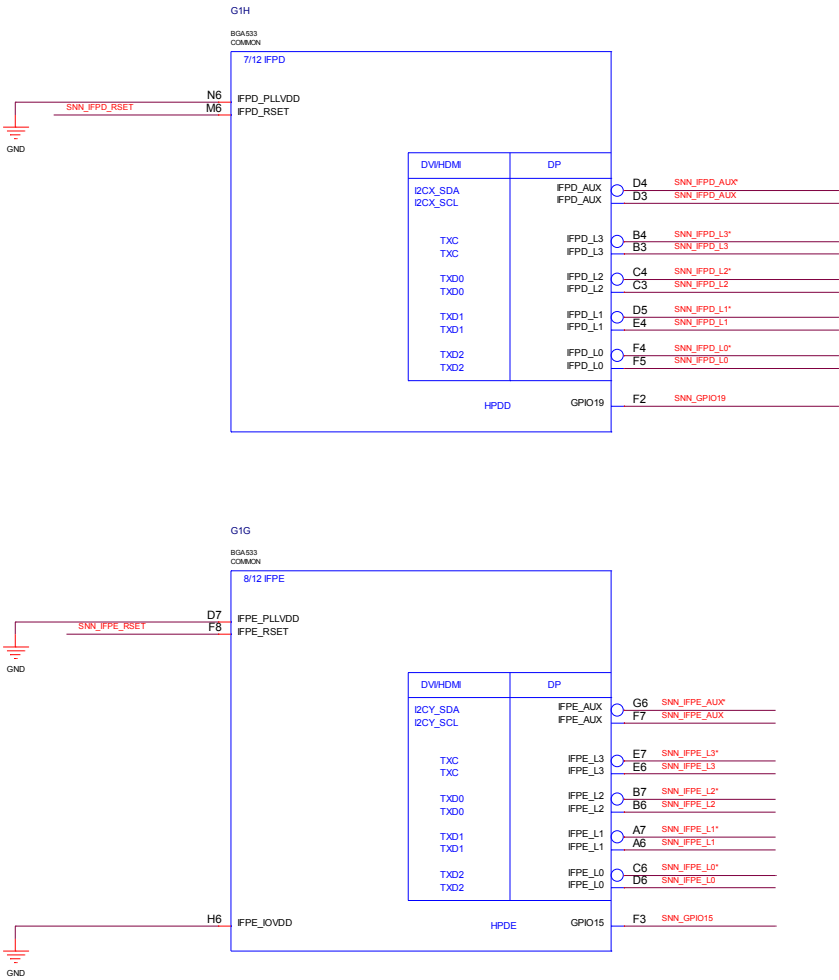
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NV_PN	600-11310-BASE-100		
PCB REV	P1310-A01	PAGE	
BOM REV	A	DATE	08-DEC-2010

IFPC HDMI/DP Connector



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NV_PN 600-11310-BASE-100			
PCB REV	P1310-A01	PAGE	
BOM REV	A	DATE	08-DEC-2010

IFPD, IFPE Interface(Not used), Mechanical parts



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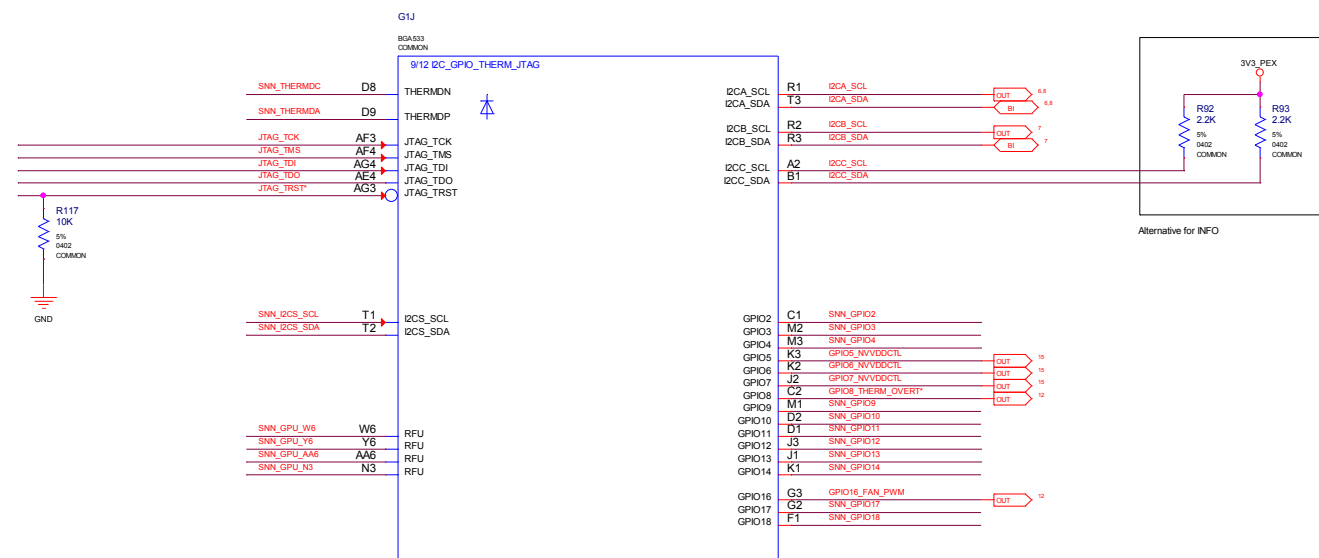
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	IFPD, IFPE Interface(Not used), Mechanical parts

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NV_PN	600-11310-BASE-100		
PCB REV	P1310-A01	PAGE	
BOM REV	A	DATE	08 DEC 2010

XTAL, ROM, INFO ROM,JTAG

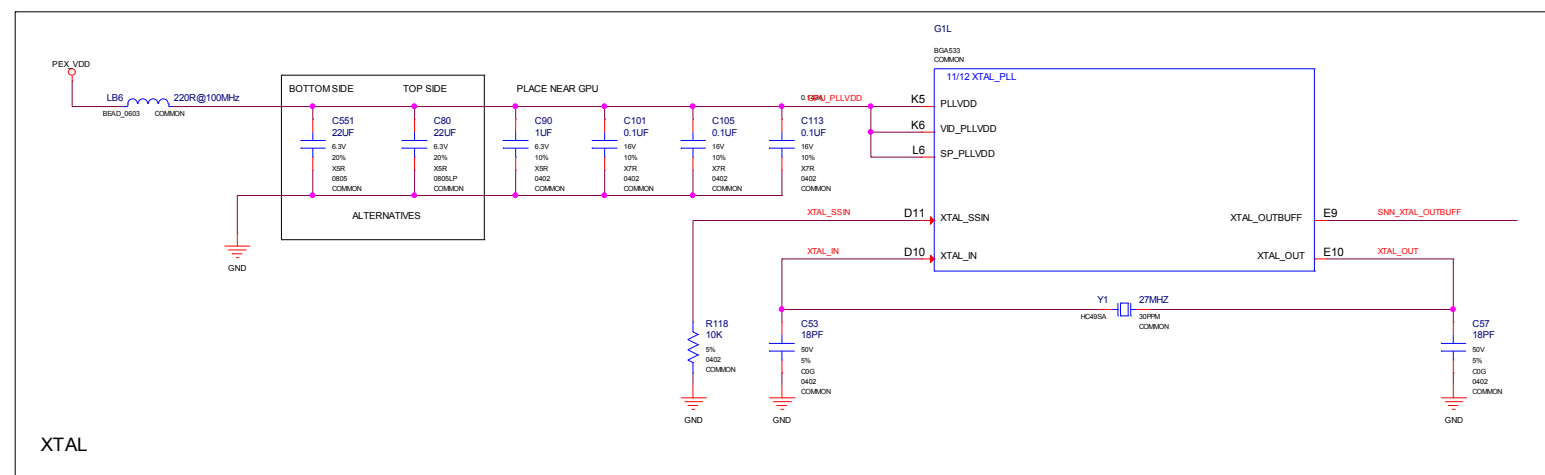
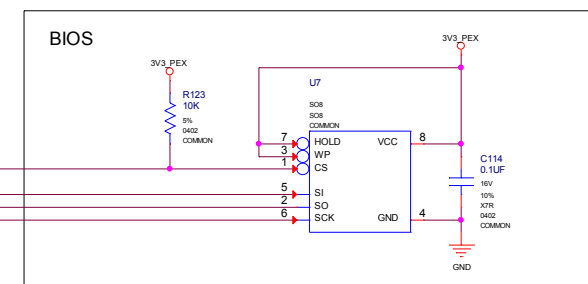
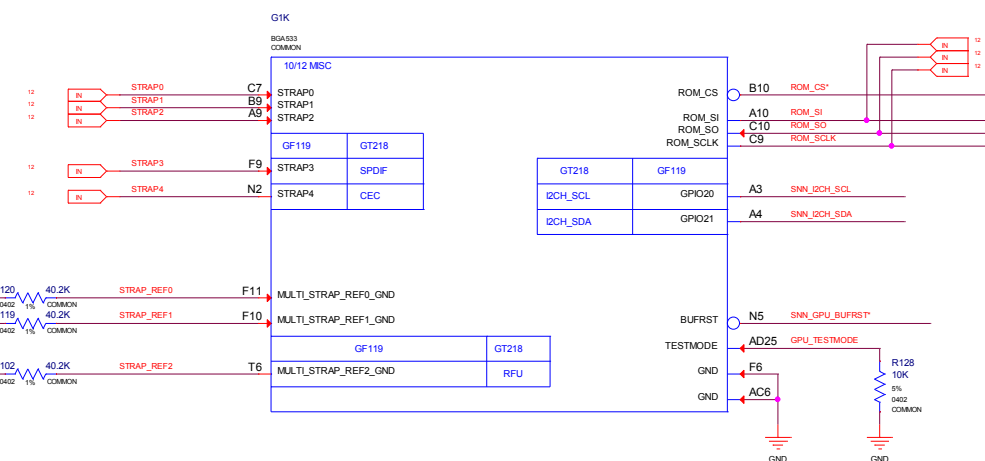
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N	XTAL_OUT	XTAL	1	50OHM
N	XTAL_IN	XTAL	1	50OHM

Net Name		VOLTAGE		MAX_CURRENT
N	GPU_PL1VDD	1.05V	0.143A	150ML
N	SV3_D			120ML



Alternative for INFO

PLACE BACKSIDE, NOSTUFF ON PRODUCTION



XTAL

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	XTAL, ROM, INFO ROM, JTAG

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	NV_PN	600-11310-BASE-100
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PCB REV	P1310-A01
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PAGE	
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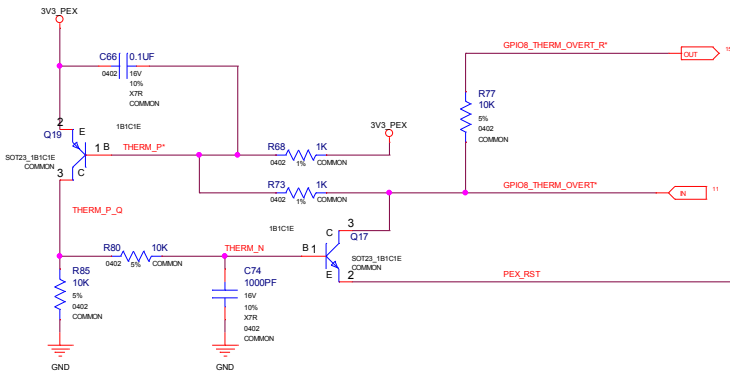
BOMREV

DATE	08-DEC-2010
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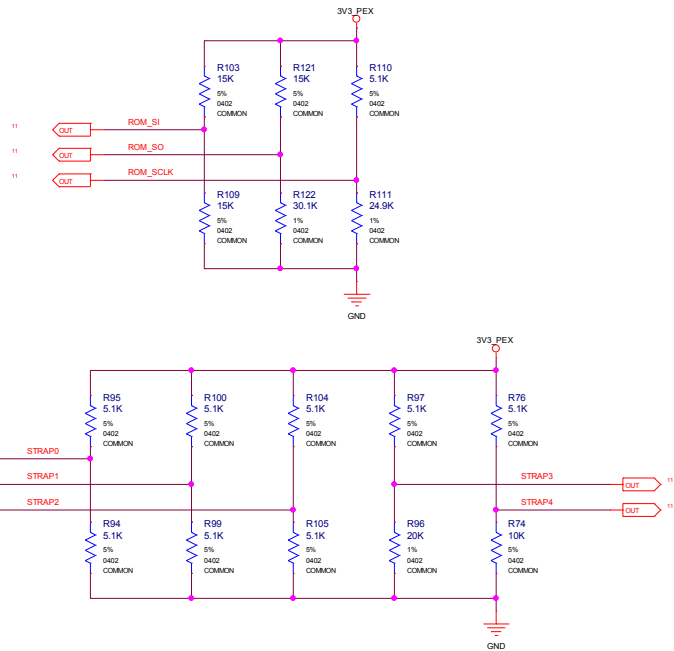
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Thermal Protection, Protected 3V3, Straps, Fan

Thermal Latch



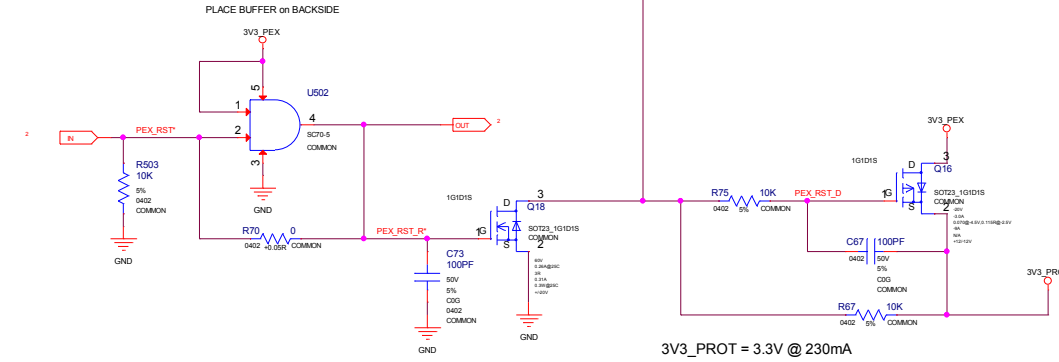
STRAPS



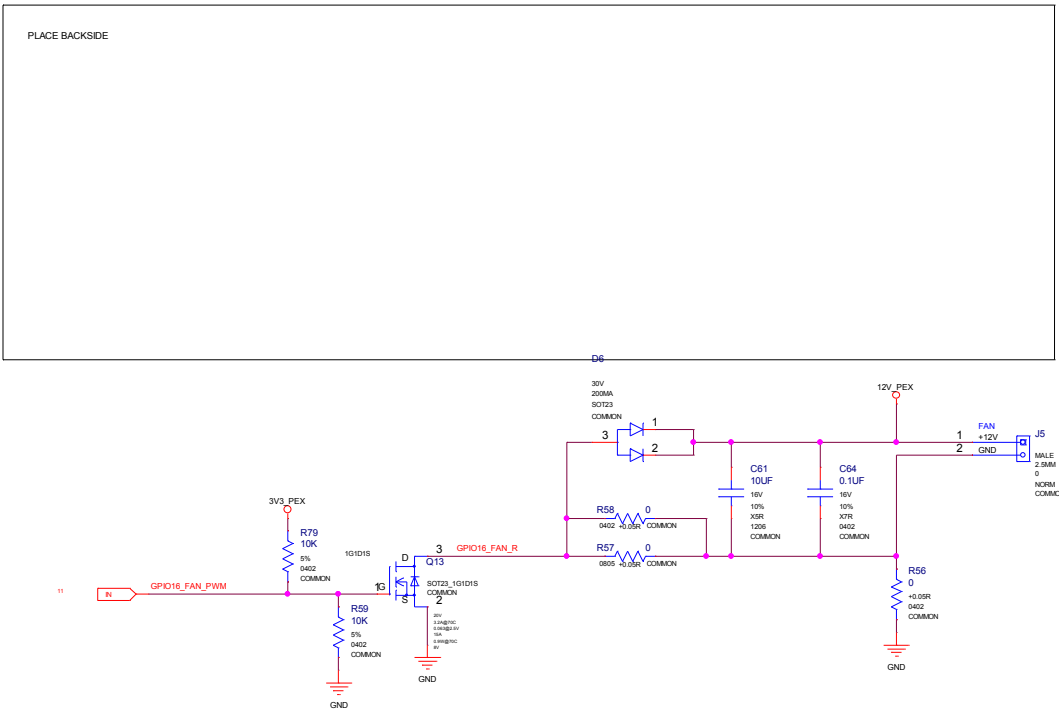
STRAP PIN MODE TABLE

STRAP PIN MODE TABLE				
	PIN NAME	MULTI-LEVEL bit [3:0]	BINARY PRODUCTION	BINARY BRINGUP
GF119	STRAP0	USER[3:0]	RAMCFG0	3GIO_PADCFG_LUT_ADR0
	STRAP1	3GIO_PADCFG_ADR[3:0]	RAMCFG1	3GIO_PADCFG_LUT_ADR1
	STRAP2	PCI_DEVID[3:0]	RAMCFG2	3GIO_PADCFG_LUT_ADR2
	STRAP3	SOR[3:0]_EXPOSED	TBD	TBD
GF119	STRAP4	RESERVED, RESERVED, PCIE_MAX_SPEED, DP_PLL_VDD33V	TBD	TBD
	ROM_SCLK	PCIDEVID[4], SUB_VENDOR, SLOT_CLK, PEX_PLL_EN_TERM	PCI_DEVID3	3GIO_PADCFG_LUT_ADR3
	ROM_SCLK	PCI_DEVID[4], SUB_VENDOR, PCI_DEVID[5], PEX_PLL_EN_TERM	TBD	TBD
	ROM_SI	RAMCFG[3:0]	PCI_DEVID_EXT	SUB_VENDOR
GF119	ROM_SO	XCLK_417, FB0_BAR_SIZE, SMB_ALT_ADDR, VGA_DEVICE	XCLK_417	XCLK_277
	ROM_SO	FB[1], FB[0], SMB_ALT_ADDR, VGA_DEVICE	TBD	TBD

Backdrive Prevention



FAN CIRCUIT



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PCB REV P1310-A01

BOM REV A

PAGE

DATE

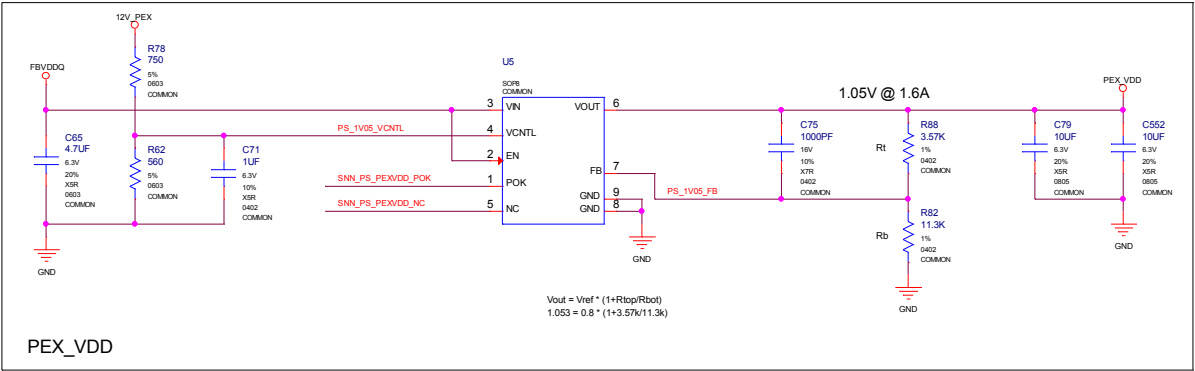
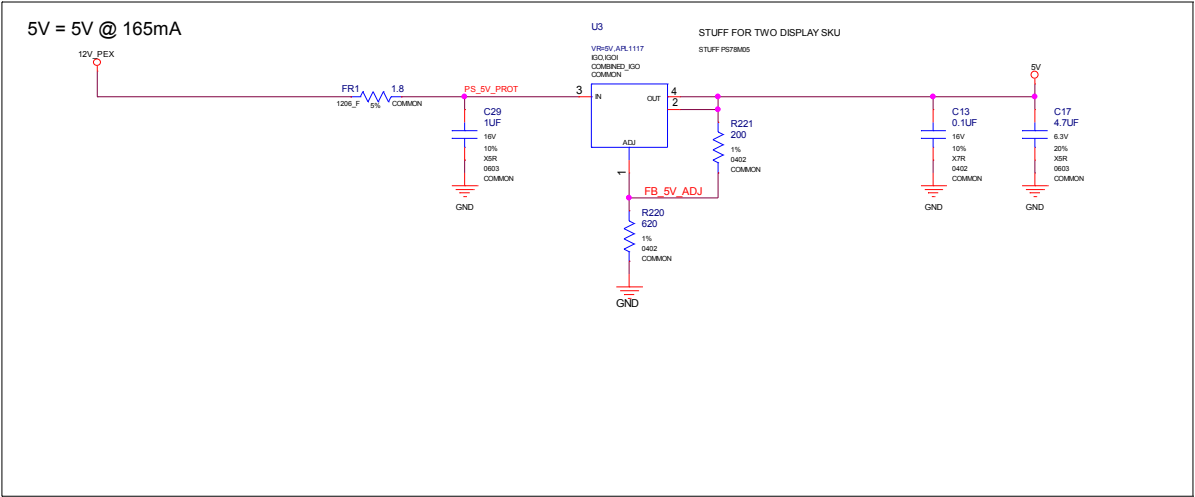
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Power Supply I: 3V3_FUSE,5V,PEXVDD

Net Name		MIN_WIDTH	
N	PS_5V_PROT	120M	
	PS_5V_PROT_D	120M	
N	PS_1V05_VCNTRL	120M	
	PS_1V05_FB	120M	

Net Name		VOLTAGE	MAX_CURRENT	
5V	5V	5V	0.365A	105M
PEX_VDD	PEX_VDD	1.05V	1.6A	205M
3V3_PEX	3V3_PEX	3.3V	3A	105M

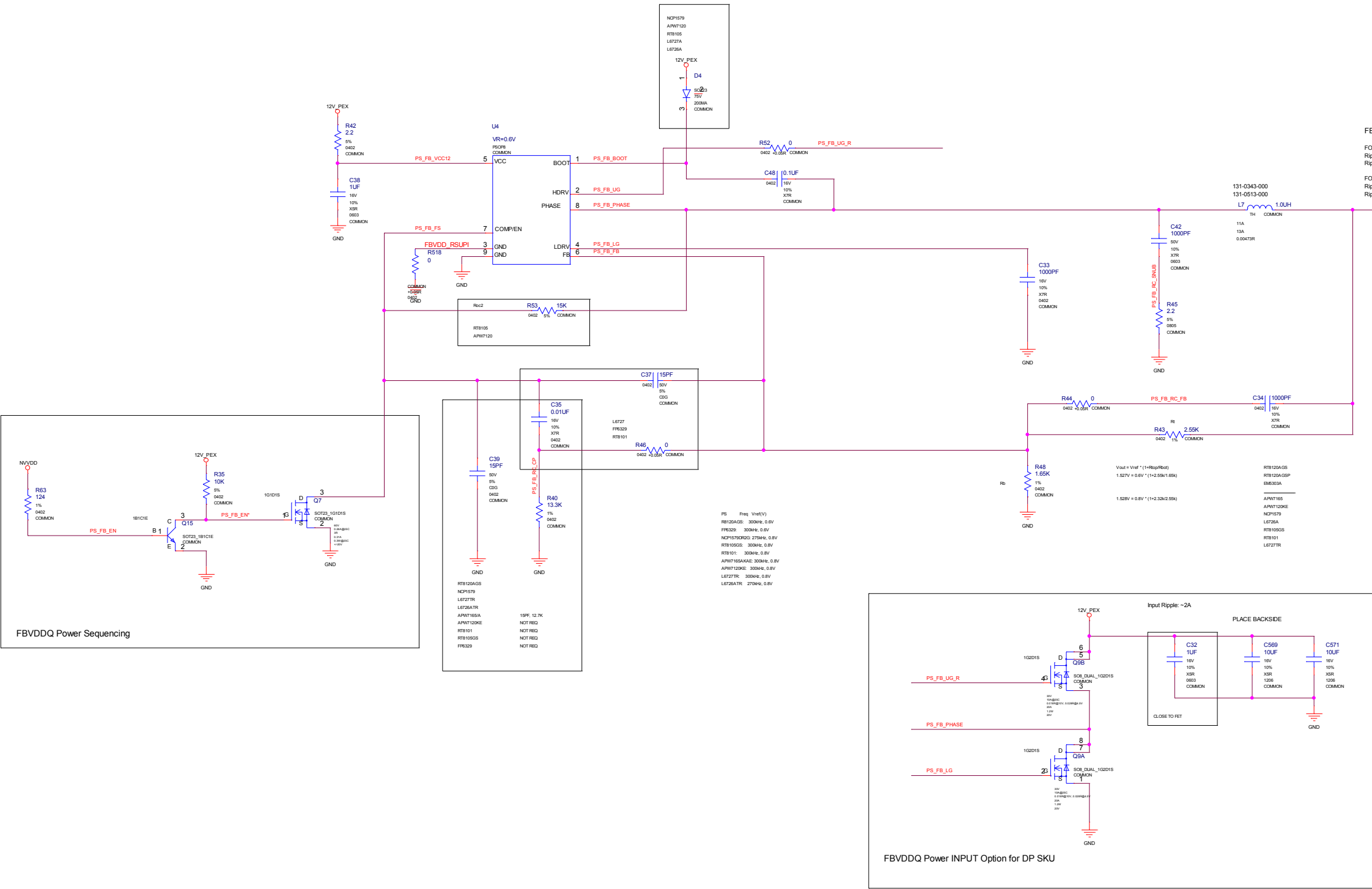


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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	Power Supply I: 3V3_FUSE,5V,PEXVDD

NVIDIA CORPORATION			
2701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95050, USA			
NV_PN	600-11310-BASE-100		
PCB REV	P1310-A01	PAGE	
BOM REV	A	DATE	08 DEC 2010

Power Supply II: FBVDDQ

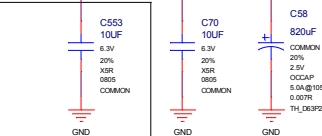


Net Name	MIN_LINE_WIDTH	VOLTAGE	NV_NET_MAX_CURRENT
FBVDDQ	12M	1.5V	6A
PS_FB_VCC12	10M		
PS_FB_FB	10M		
PS_FB_UG	10M		
PS_FB_PHASE	10M		
PS_FB_LG	10M		
PS_FB_FB	10M		
PS_FB_RC_FB	10M		
PS_FB_BOOT	10M		
PS_FB_UG	10M		
PS_FB_LG	10M		
PS_FB_FB	10M		
PS_FB_RC_FB	10M		
PS_FB_BOOT	10M		

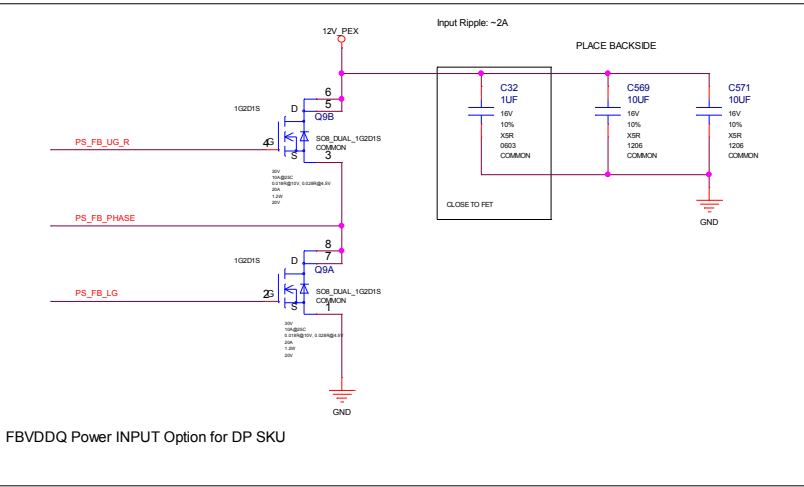
FBVDDQ = 6A @ 1.5V

FOR 3V3 INPUT:
Ripple Current: ~2.7A
Ripple Voltage: ~20mV

FOR 12V INPUT:
Ripple Current: ~4.4A
Ripple Voltage: ~30mV



PLACE BACKSIDE



FBVDDQ Power INPUT Option for DP SKU

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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	Power Supply II: FBVDDQ

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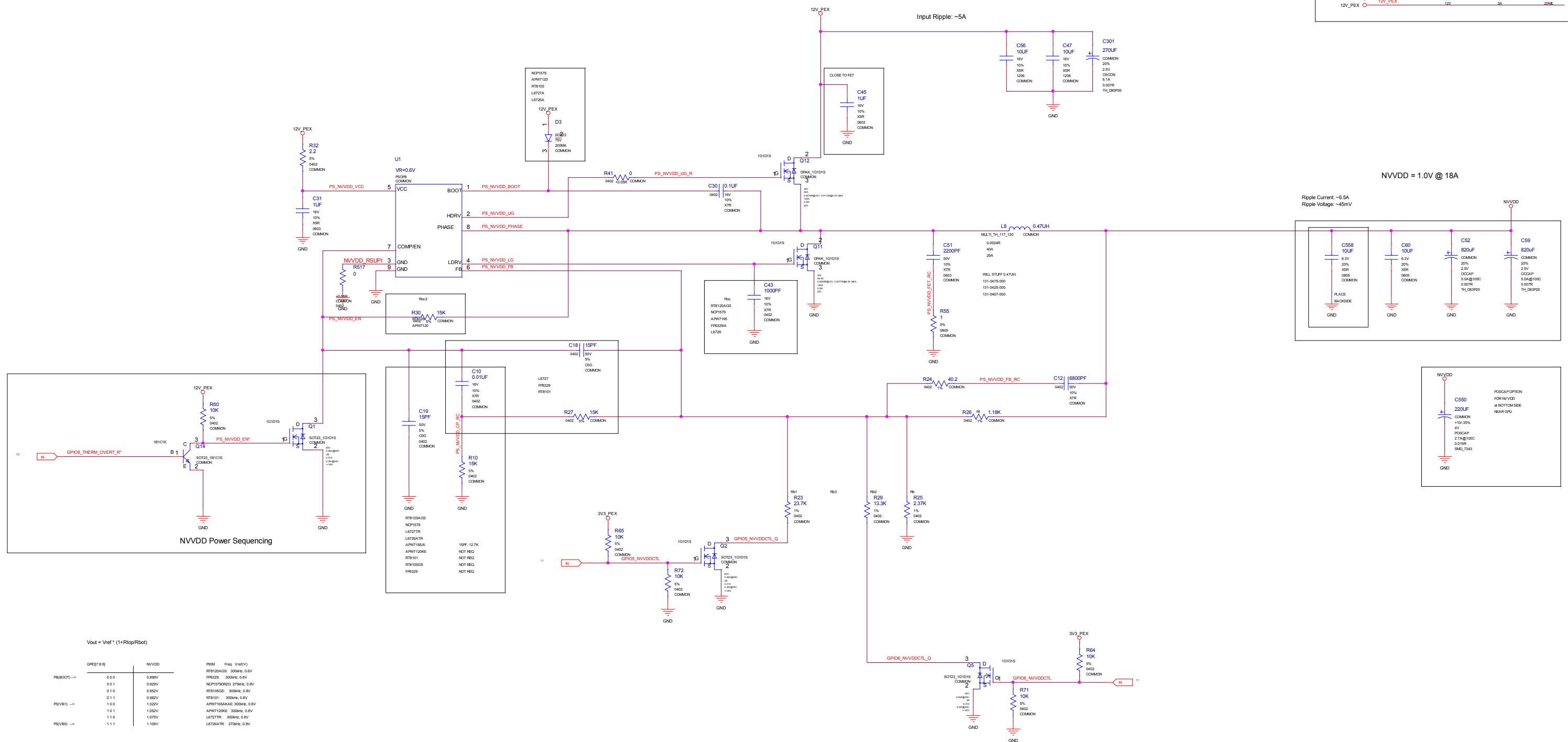
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Power Supply III: NVVDD



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