


| Page | Description |
|------|-------------|
|------|-------------|

| SNO | VARIANT | NYPN | ASSEMBLY |
|-----|-------------|--------------------|---|
| B | BASE | 600-11062 base-100 | P1062 BASE LEVEL GENERIC SCHEMATIC ONLY; COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| 1 | SKU0001 | 600-11062-0001-100 | GF100-300 153MM 6AM16 BGAl100 sDOR3, HCMV/VGA/DVI/H |
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| SANTA CLARA, CA 95050, USA | | | |
| NV_PN | 600-11062-base-100 | | |
| PCB REV | P1062-B00 | PAGE | |
| BOM REV | A | DATE | 06-APR-2010 |

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3V3

R73 20K

C73 4.7UF

R74 20K

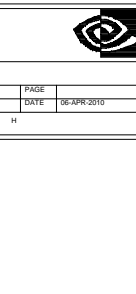
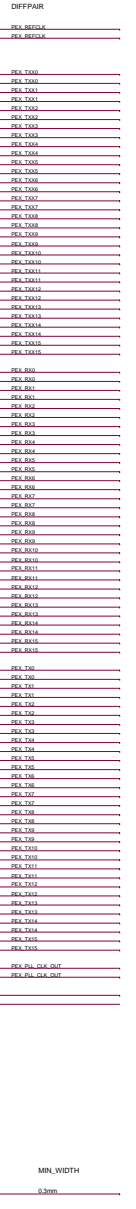
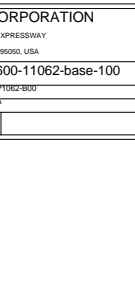
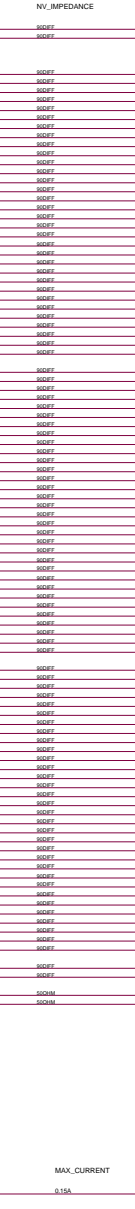
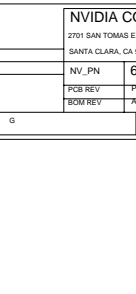
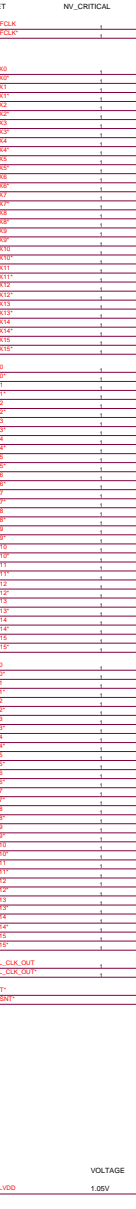
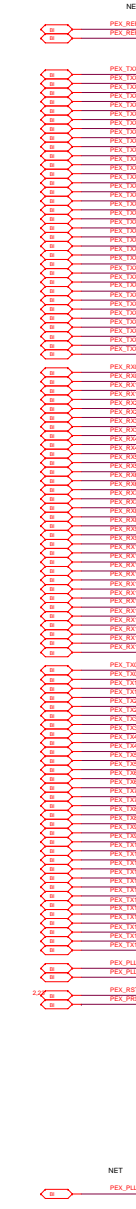
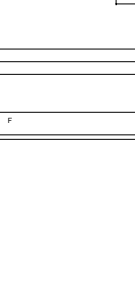
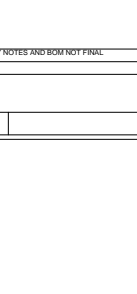
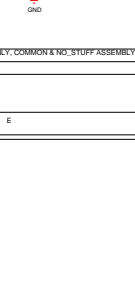
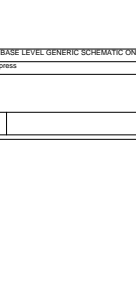
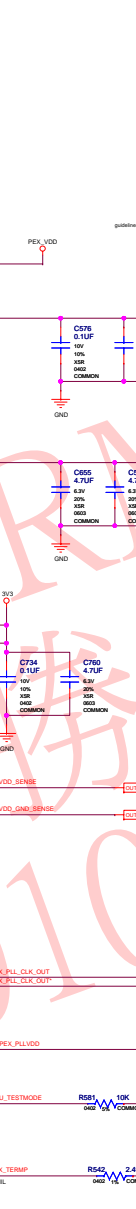
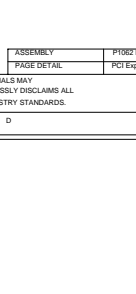
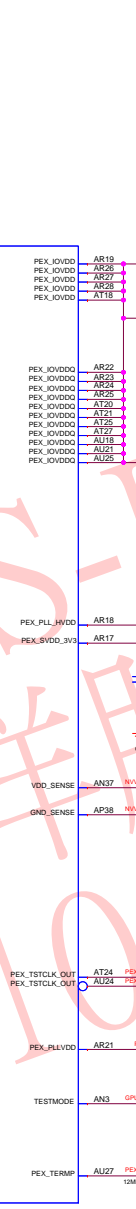
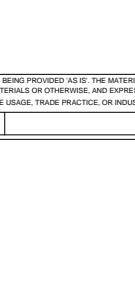
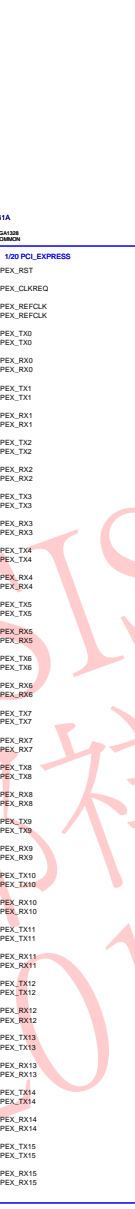
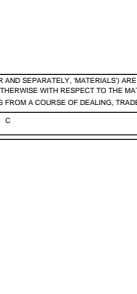
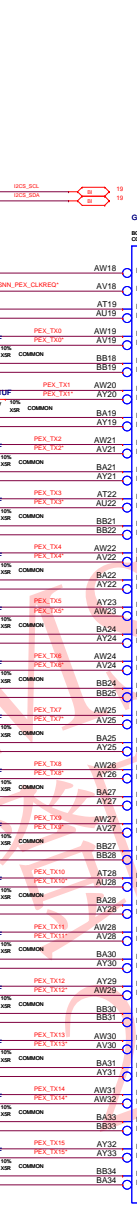
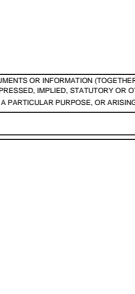
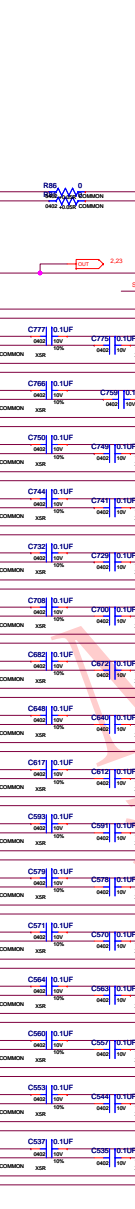
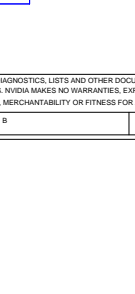
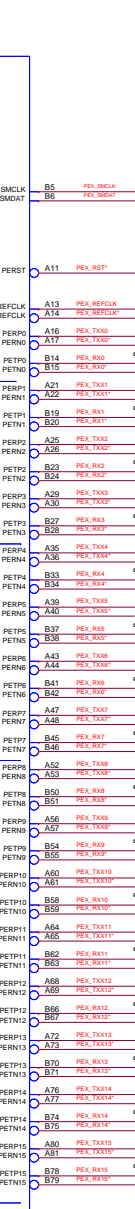
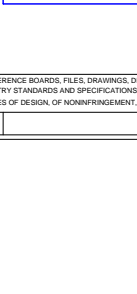
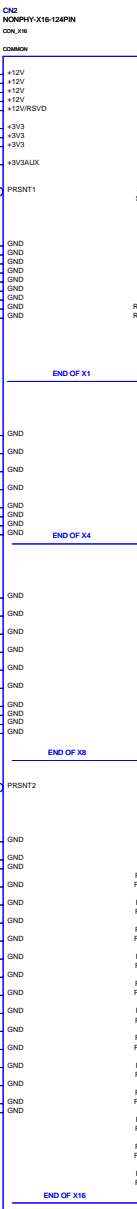
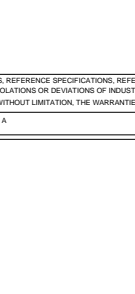
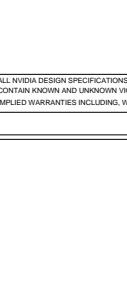
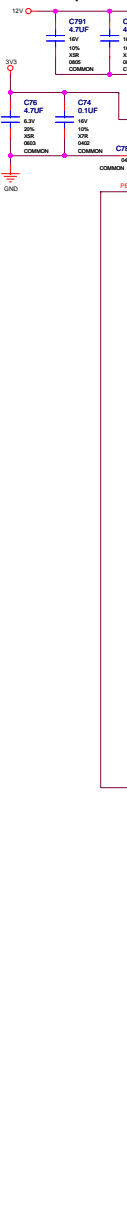
C74 5.1UF

R75 20K

C75 4.7UF

GND

3V3



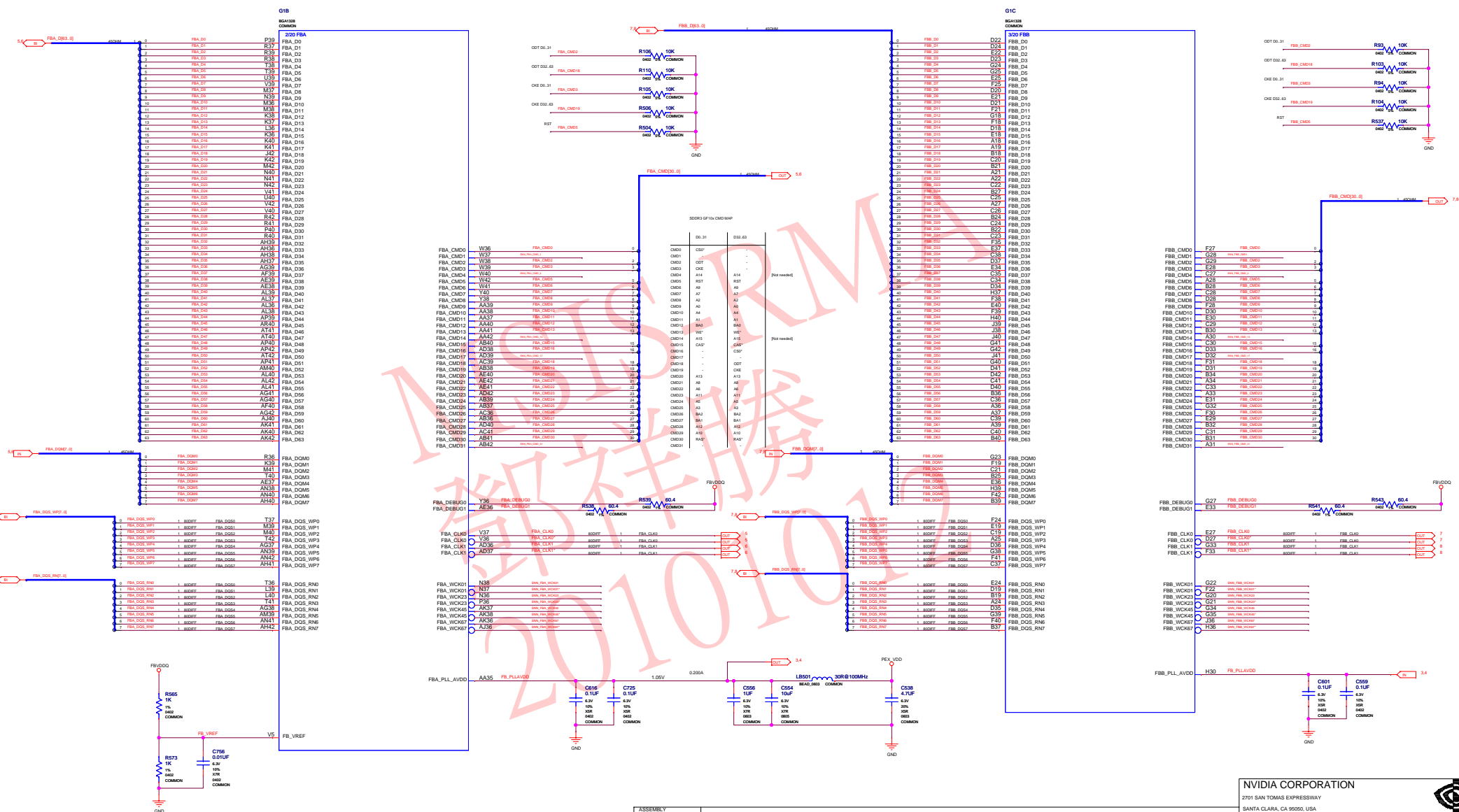
| A | B | C | D |
|---|---|---|---|
|---|---|---|---|

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STRY STANDARDS.

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| | H |
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MEMORY: GPU FB Partition A and B



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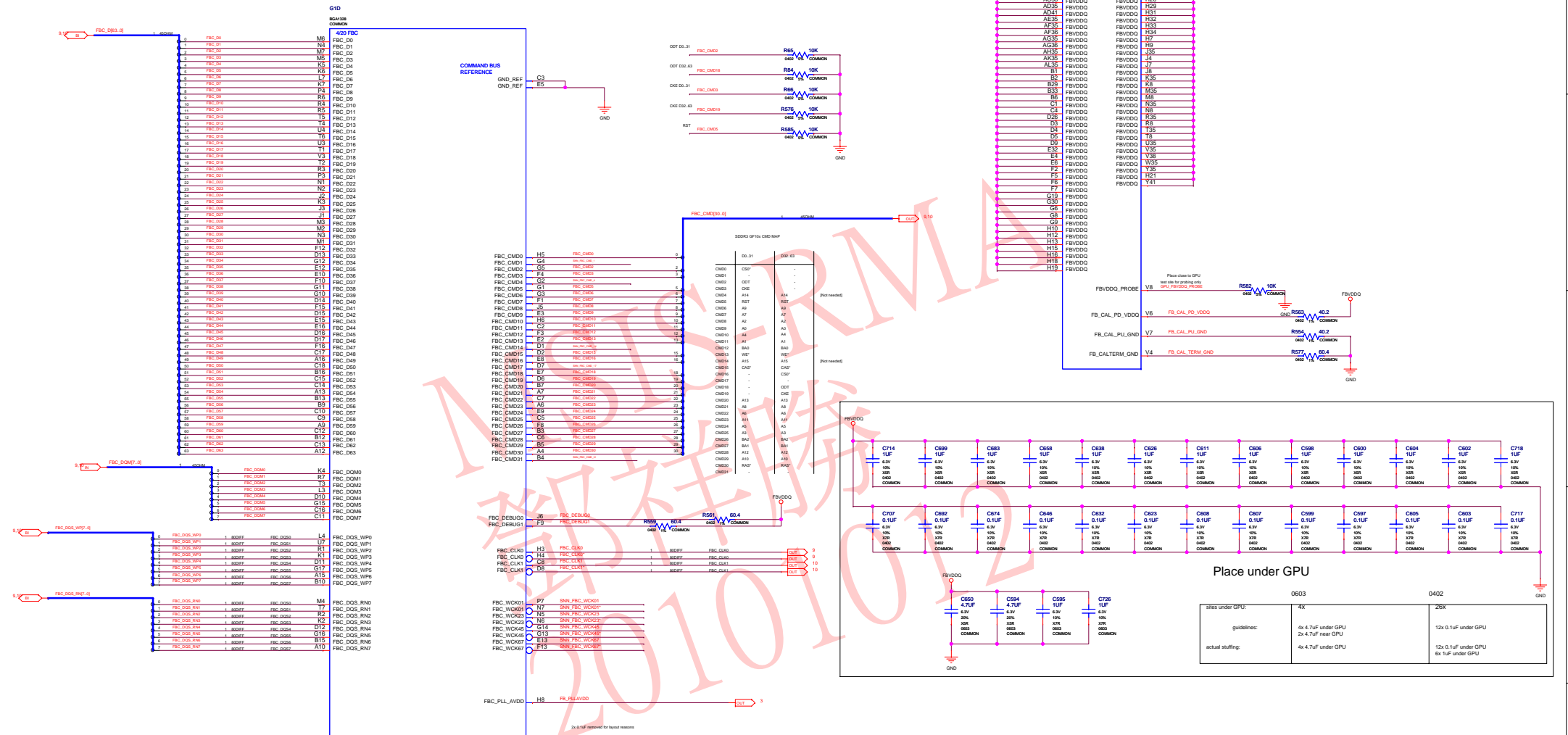


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|-------------|--------------------------|
| ASSEMBLY | |
| PAGE DETAIL | GPU FB PARTITION A AND B |

| | | | |
|---------|--------------------|------|-------------|
| NV_PN | 600-11062-base-100 | | |
| PCB REV | P1062-B00 | PAGE | |
| BOM REV | A | DATE | 06-APR-2010 |


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MEMORY: GPU FB Partition C AND GPU FB Decap



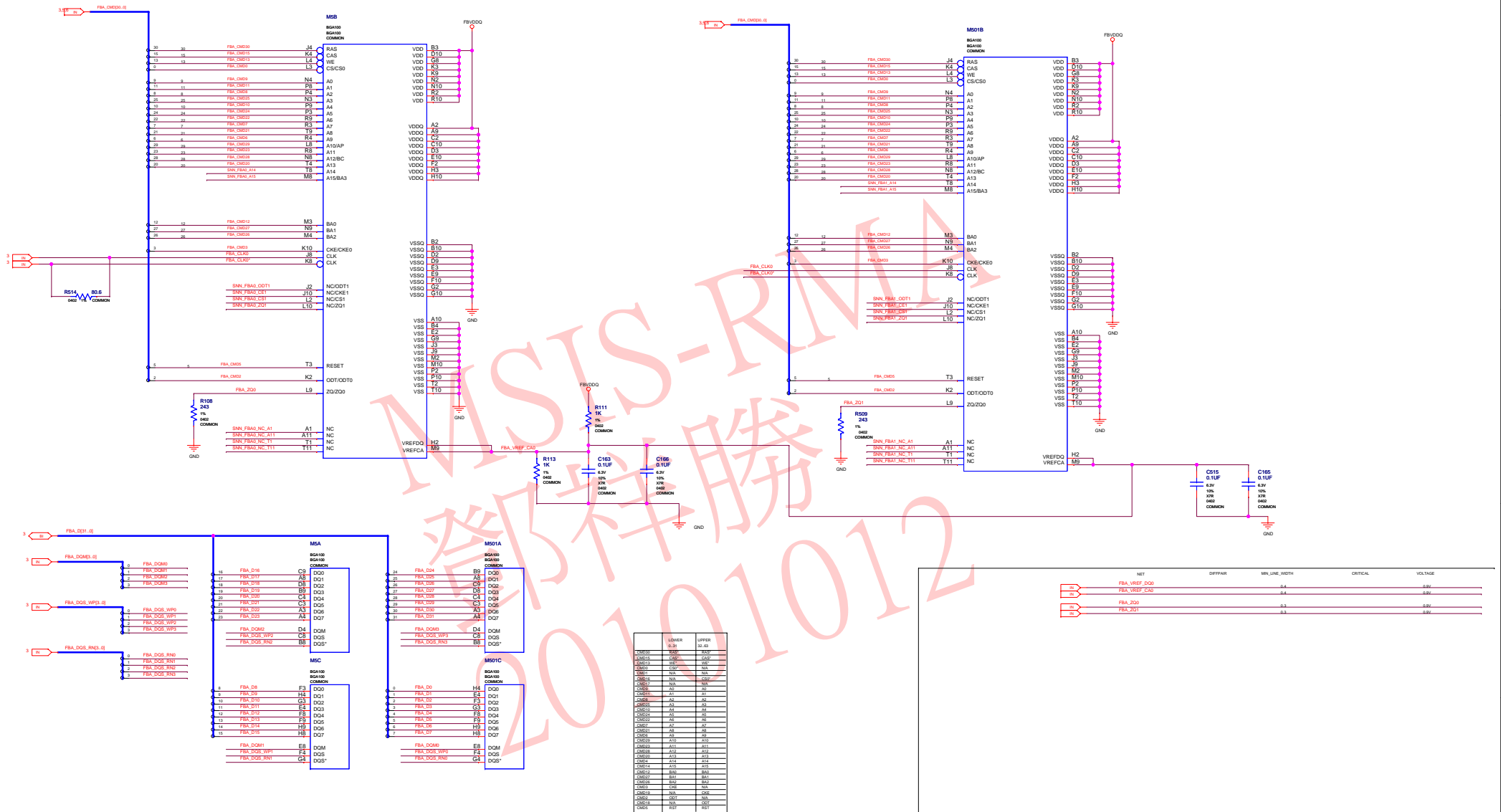
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| | |
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| ASSEMBLY | P1062 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| PAGE DETAIL | GPU FB Partition C and GPU FB Decap |

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| PCB REV | P1062-B00 | | PAGE | |
| BOM REV | A | | DATE | 06-APR-2010 |



Memory Lower Partition A




| | |
|-------------|---|
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| PAGE DETAIL | Memory Lower Partition A |

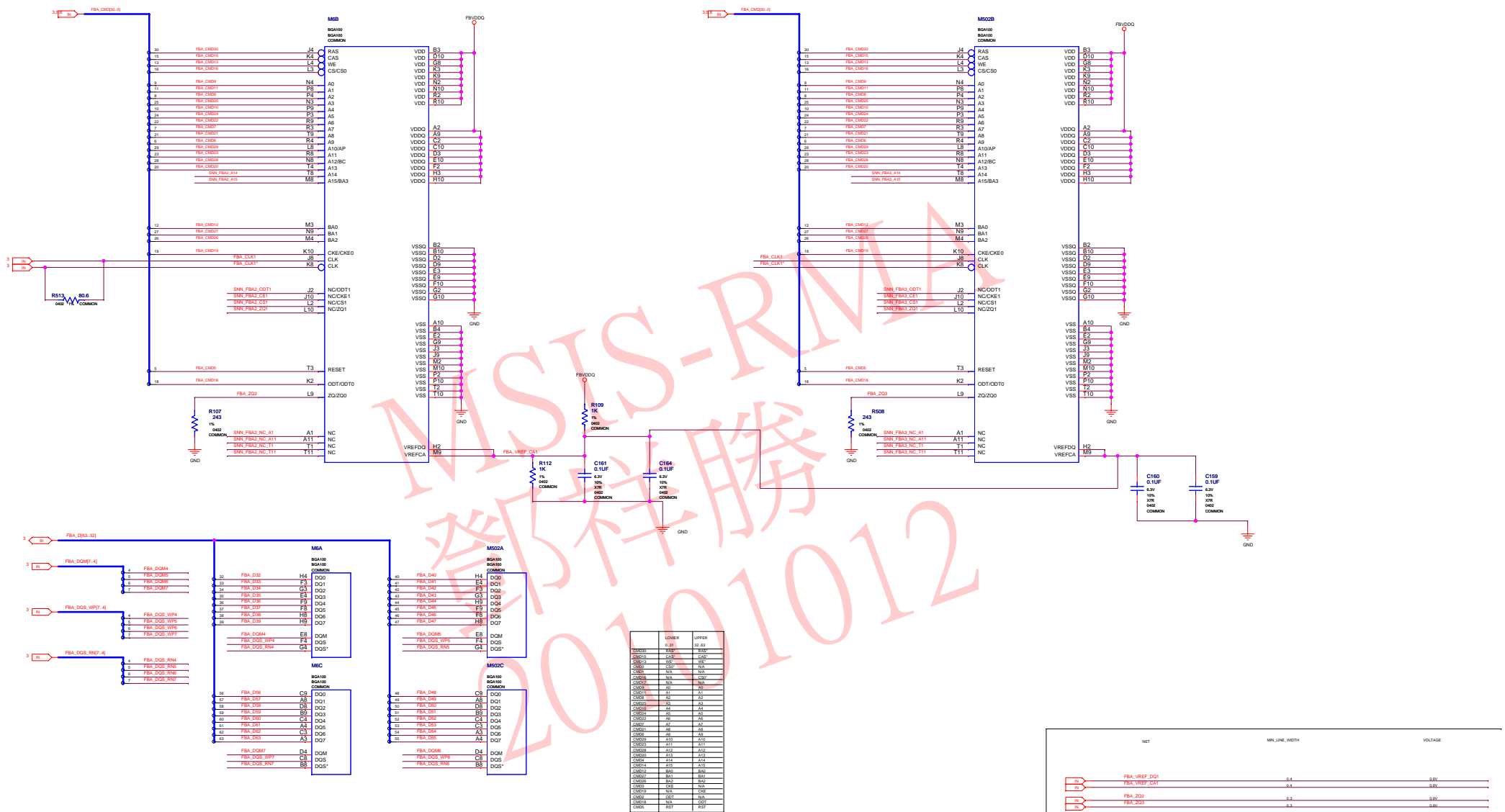
IN USE ON

| | |
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| PAGE DETAIL | Memory Lower Partition A |

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| NV_PN | 600-11062-base-100 | | |
| PCB REV | P1062-B00 | PAGE | |
| BOM REV | A | DATE | 06-APR-2010 |

Memory Upper Partition A



| | LOWER | UPPER |
|--------|-------|-------|
| CAD006 | 6-21 | 32-43 |
| CAD005 | CAS | CAS |
| CAD013 | AS | WCT |
| CAD010 | C2P | N/A |
| CAD004 | N/A | C000 |
| CAD007 | N/A | N/A |
| CAD008 | A | A |
| CAD001 | A1 | A1 |
| CAD009 | A2 | A2 |
| CAD002 | A3 | A3 |
| CAD003 | A4 | A4 |
| CAD012 | A5 | A5 |
| CAD011 | A6 | A6 |
| CAD007 | A7 | A7 |
| CAD006 | A8 | A8 |
| CAD005 | A9 | A9 |
| CAD009 | A10 | A10 |
| CAD008 | A11 | A11 |
| CAD010 | A12 | A12 |
| CAD013 | A13 | A13 |
| CAD012 | A14 | A14 |
| CAD014 | A15 | A15 |
| CAD015 | B0 | B0 |
| CAD016 | B1 | B1 |
| CAD017 | B2 | B2 |
| CAD018 | C0 | N/A |
| CAD019 | C1 | C0E |
| CAD020 | C2 | C0T |
| CAD021 | R0T | R1T |

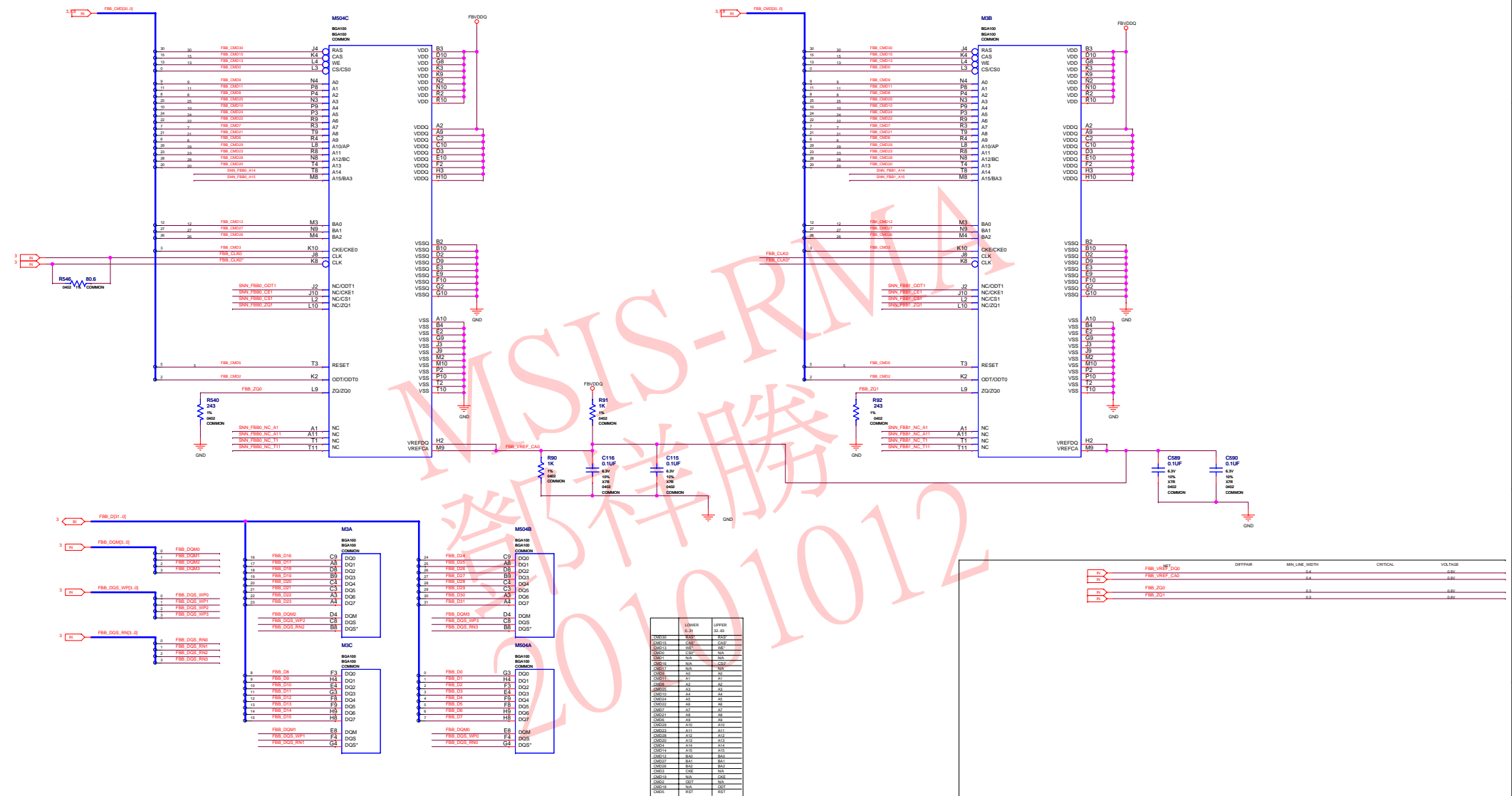
| NET | MIN_LINE_WIDTH | VOLTAGE |
|--------------|----------------|---------|
| FBA_VREF_D01 | 0.4 | 0.0V |
| FBA_VREF_CLK | 0.4 | 0.0V |
| FBA_D02 | 0.3 | 0.0V |
| FBA_D03 | 0.3 | 0.0V |

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Memory Lower Partition B

IN USE ON

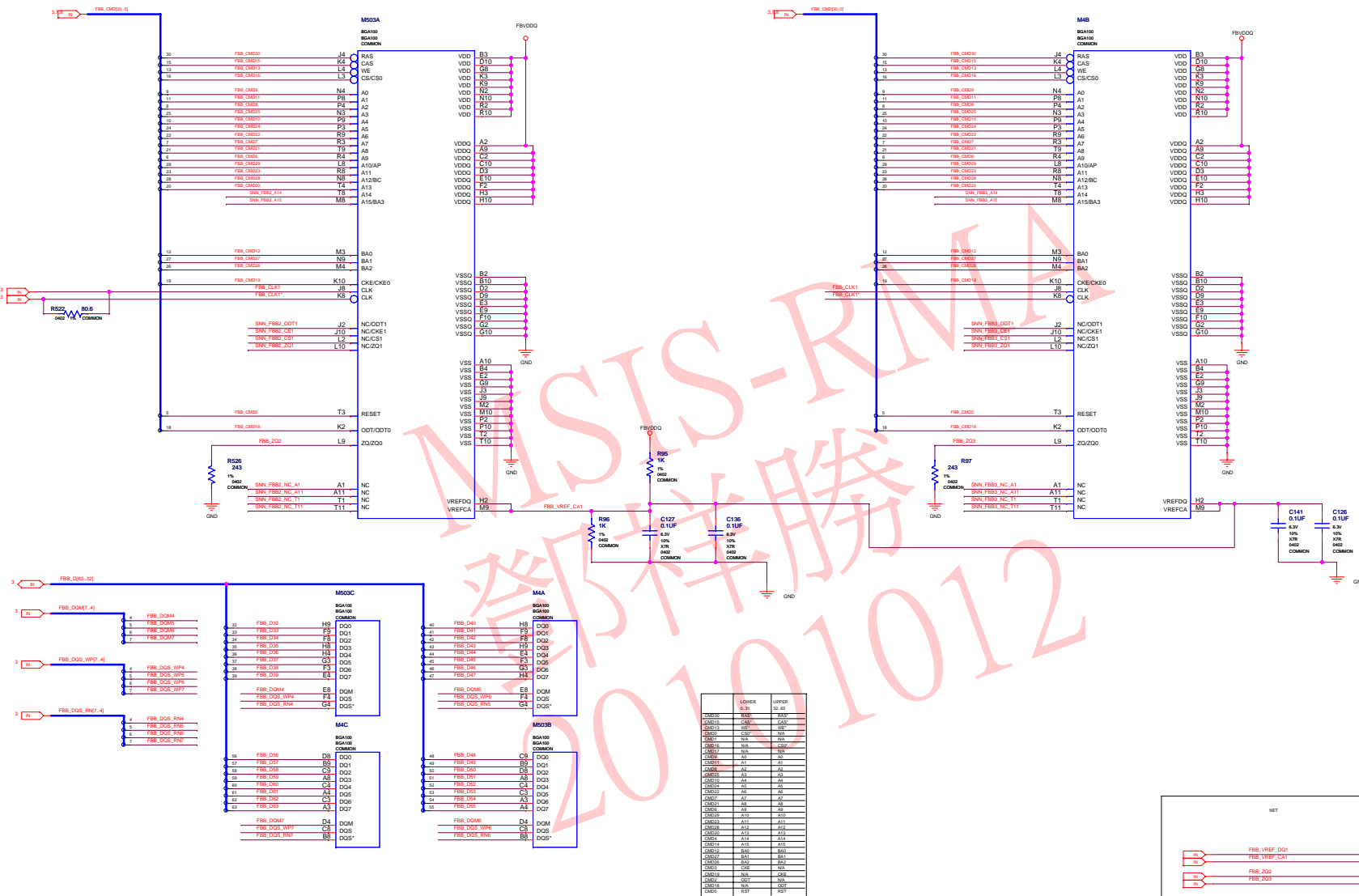
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| PAGE DETAIL | Memory Lower Partition B |

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| PCB REV | P1062-B00 | PAGE | |
| BOM REV | A | DATE | 06-APR-2010 |



Memory Upper Partition B



| | LOWER | UPPER |
|--------|-------|-------|
| MEM01 | 0-31 | 32-63 |
| MEM02 | 0-31 | 32-63 |
| MEM03 | 0-31 | 32-63 |
| MEM04 | 0-31 | 32-63 |
| MEM05 | 0-31 | 32-63 |
| MEM06 | 0-31 | 32-63 |
| MEM07 | 0-31 | 32-63 |
| MEM08 | 0-31 | 32-63 |
| MEM09 | 0-31 | 32-63 |
| MEM10 | 0-31 | 32-63 |
| MEM11 | 0-31 | 32-63 |
| MEM12 | 0-31 | 32-63 |
| MEM13 | 0-31 | 32-63 |
| MEM14 | 0-31 | 32-63 |
| MEM15 | 0-31 | 32-63 |
| MEM16 | 0-31 | 32-63 |
| MEM17 | 0-31 | 32-63 |
| MEM18 | 0-31 | 32-63 |
| MEM19 | 0-31 | 32-63 |
| MEM20 | 0-31 | 32-63 |
| MEM21 | 0-31 | 32-63 |
| MEM22 | 0-31 | 32-63 |
| MEM23 | 0-31 | 32-63 |
| MEM24 | 0-31 | 32-63 |
| MEM25 | 0-31 | 32-63 |
| MEM26 | 0-31 | 32-63 |
| MEM27 | 0-31 | 32-63 |
| MEM28 | 0-31 | 32-63 |
| MEM29 | 0-31 | 32-63 |
| MEM30 | 0-31 | 32-63 |
| MEM31 | 0-31 | 32-63 |
| MEM32 | 0-31 | 32-63 |
| MEM33 | 0-31 | 32-63 |
| MEM34 | 0-31 | 32-63 |
| MEM35 | 0-31 | 32-63 |
| MEM36 | 0-31 | 32-63 |
| MEM37 | 0-31 | 32-63 |
| MEM38 | 0-31 | 32-63 |
| MEM39 | 0-31 | 32-63 |
| MEM40 | 0-31 | 32-63 |
| MEM41 | 0-31 | 32-63 |
| MEM42 | 0-31 | 32-63 |
| MEM43 | 0-31 | 32-63 |
| MEM44 | 0-31 | 32-63 |
| MEM45 | 0-31 | 32-63 |
| MEM46 | 0-31 | 32-63 |
| MEM47 | 0-31 | 32-63 |
| MEM48 | 0-31 | 32-63 |
| MEM49 | 0-31 | 32-63 |
| MEM50 | 0-31 | 32-63 |
| MEM51 | 0-31 | 32-63 |
| MEM52 | 0-31 | 32-63 |
| MEM53 | 0-31 | 32-63 |
| MEM54 | 0-31 | 32-63 |
| MEM55 | 0-31 | 32-63 |
| MEM56 | 0-31 | 32-63 |
| MEM57 | 0-31 | 32-63 |
| MEM58 | 0-31 | 32-63 |
| MEM59 | 0-31 | 32-63 |
| MEM60 | 0-31 | 32-63 |
| MEM61 | 0-31 | 32-63 |
| MEM62 | 0-31 | 32-63 |
| MEM63 | 0-31 | 32-63 |
| MEM64 | 0-31 | 32-63 |
| MEM65 | 0-31 | 32-63 |
| MEM66 | 0-31 | 32-63 |
| MEM67 | 0-31 | 32-63 |
| MEM68 | 0-31 | 32-63 |
| MEM69 | 0-31 | 32-63 |
| MEM70 | 0-31 | 32-63 |
| MEM71 | 0-31 | 32-63 |
| MEM72 | 0-31 | 32-63 |
| MEM73 | 0-31 | 32-63 |
| MEM74 | 0-31 | 32-63 |
| MEM75 | 0-31 | 32-63 |
| MEM76 | 0-31 | 32-63 |
| MEM77 | 0-31 | 32-63 |
| MEM78 | 0-31 | 32-63 |
| MEM79 | 0-31 | 32-63 |
| MEM80 | 0-31 | 32-63 |
| MEM81 | 0-31 | 32-63 |
| MEM82 | 0-31 | 32-63 |
| MEM83 | 0-31 | 32-63 |
| MEM84 | 0-31 | 32-63 |
| MEM85 | 0-31 | 32-63 |
| MEM86 | 0-31 | 32-63 |
| MEM87 | 0-31 | 32-63 |
| MEM88 | 0-31 | 32-63 |
| MEM89 | 0-31 | 32-63 |
| MEM90 | 0-31 | 32-63 |
| MEM91 | 0-31 | 32-63 |
| MEM92 | 0-31 | 32-63 |
| MEM93 | 0-31 | 32-63 |
| MEM94 | 0-31 | 32-63 |
| MEM95 | 0-31 | 32-63 |
| MEM96 | 0-31 | 32-63 |
| MEM97 | 0-31 | 32-63 |
| MEM98 | 0-31 | 32-63 |
| MEM99 | 0-31 | 32-63 |
| MEM100 | 0-31 | 32-63 |

| NET | MIN_LEN_WIDTH | VOLTAGE |
|--------------|---------------|---------|
| FBS_VREF_DCH | 0.4 | 0.9V |
| FBS_VREF_DCH | 0.4 | 0.9V |
| FBS_VREF_DCH | 0.4 | 0.9V |
| FBS_VREF_DCH | 0.4 | 0.9V |
| FBS_VREF_DCH | 0.4 | 0.9V |

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NV_PN: 600-11062-base-100

PCB REV: P1062-2000

BOM REV: A

PAGE: 1

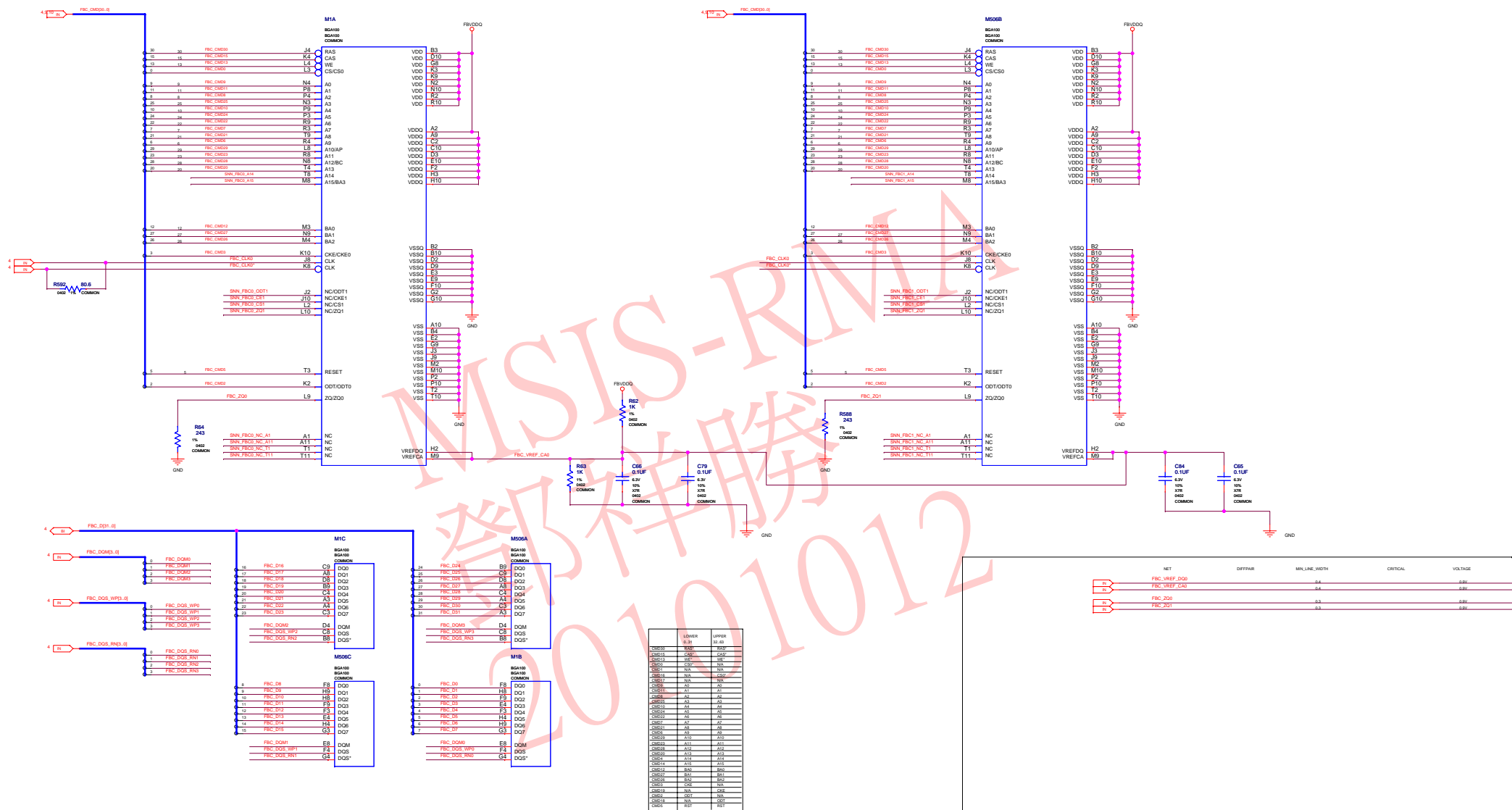
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ASSEMBLY: P1062 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO.310FF ASSEMBLY NOTES AND BOM NOT FINAL

PAGE DETAIL: Memory Upper Partition B

Memory Lower Partition C




| | LOWER | UPPER |
|-------|-------|-------|
| | 8-31 | 30-31 |
| C0000 | NOV | NOV |
| C0001 | CAS | CAS |
| C0002 | VE1 | VE1 |
| C0003 | CAS | CAS |
| C0004 | NOV | NOV |
| C0005 | NOV | NOV |
| C0006 | NOV | NOV |
| C0007 | A1 | A1 |
| C0008 | A2 | A2 |
| C0009 | A3 | A3 |
| C0010 | A4 | A4 |
| C0011 | A5 | A5 |
| C0012 | A6 | A6 |
| C0013 | A7 | A7 |
| C0014 | A8 | A8 |
| C0015 | A8 | A8 |
| C0016 | A9 | A9 |
| C0017 | A10 | A10 |
| C0018 | A11 | A11 |
| C0019 | A12 | A12 |
| C0020 | A13 | A13 |
| C0021 | A14 | A14 |
| C0022 | A15 | A15 |
| C0023 | BA0 | BA0 |
| C0024 | BA1 | BA1 |
| C0025 | BA2 | BA2 |
| C0026 | CSE | CSE |
| C0027 | NOV | NOV |
| C0028 | CO1 | CO1 |
| C0029 | NOV | NOV |
| C0030 | RS1 | RS1 |

IN USE ON

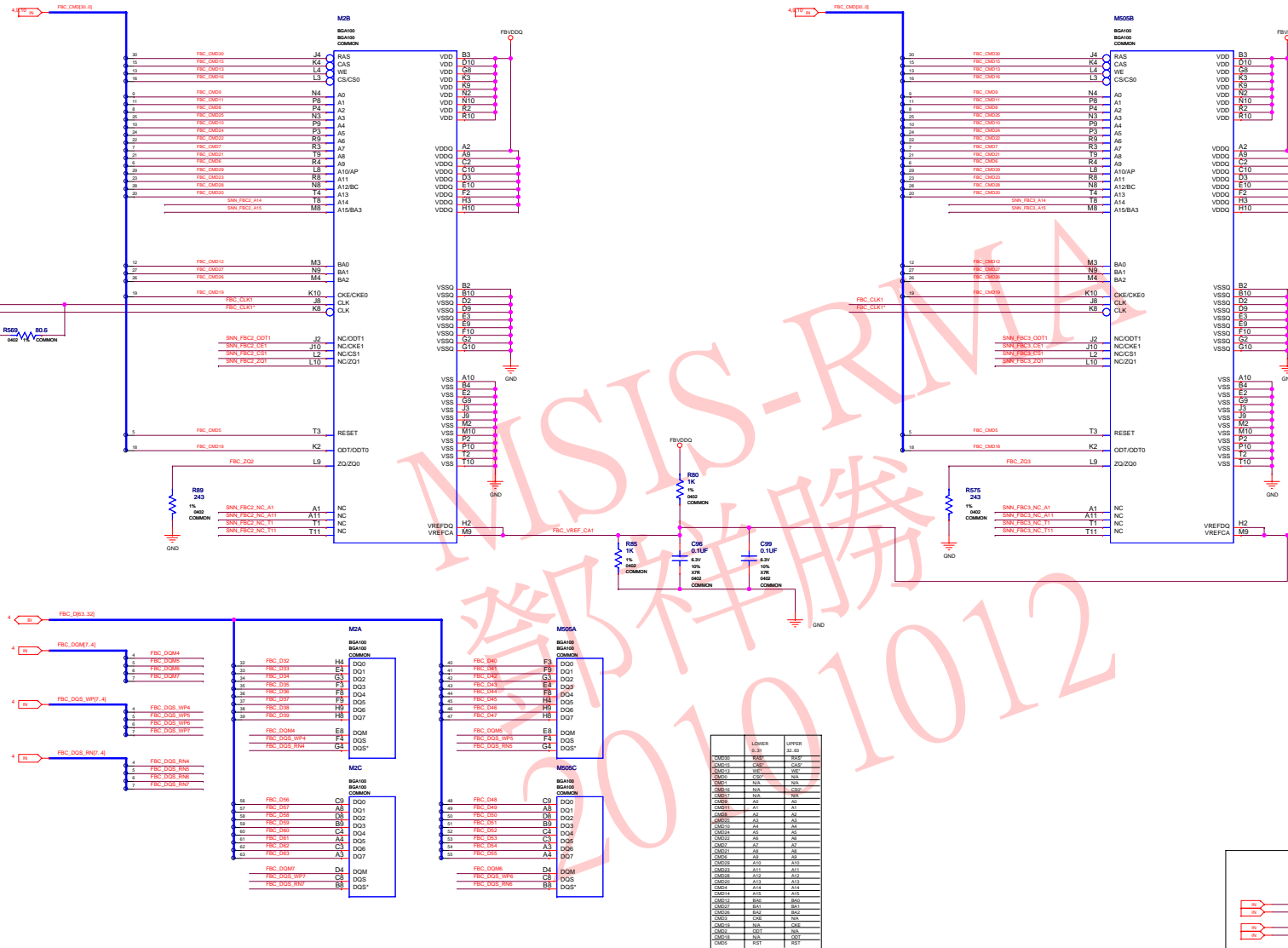
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| ASSEMBLY | P1062 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| PAGE DETAIL | Memory Lower Partition C |

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| NV_PN | 600-11062-base-100 | | |
| PCB REV | P1062-B00 | PAGE | |
| BOM REV | A | DATE | 06-APR-2010 |



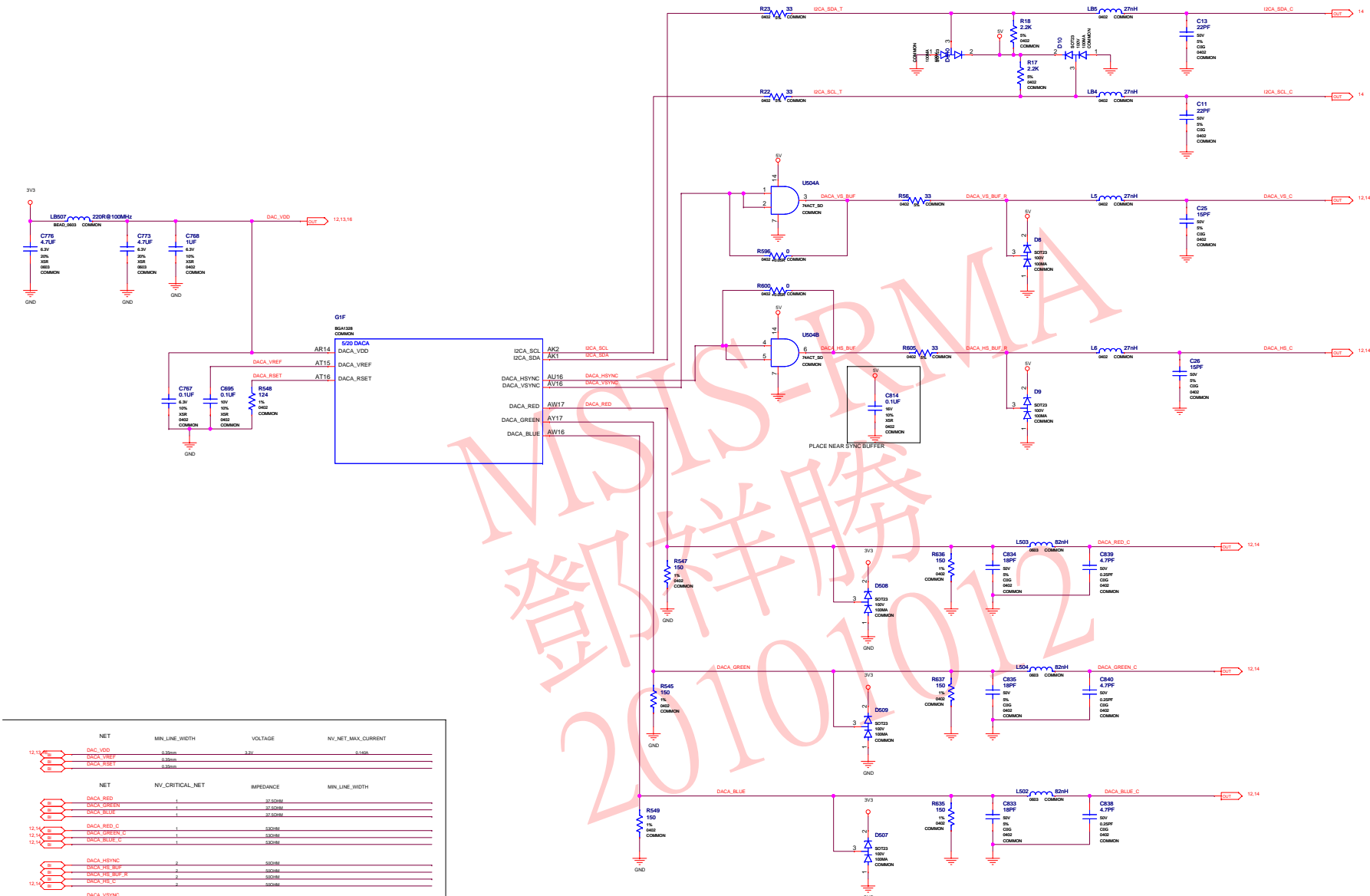
Memory Upper Partition C



Memory FBVDD/Q Decoupling

DACA for South DVI Interface


DACA RGB-FILTER



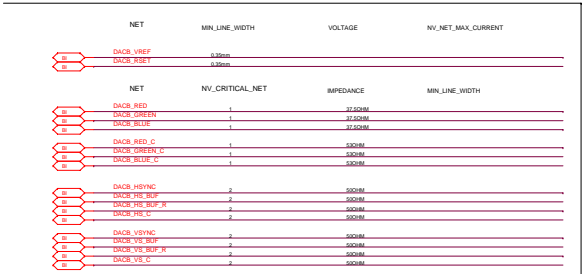
| | NET | MIN_LENGTH | VOLTAGE | IV_NET_MAX_CURRENT |
|--------|----------------|-----------------|-----------|--------------------|
| 12, 14 | DAC_VDD | 0.50mm | 3.3V | 0.50A |
| | DAC_VREF | 0.50mm | | |
| | DAC_PURE1 | 0.50mm | | |
| | | | | |
| | NET | IV_CRITICAL_NET | RAPIDANCE | MIN_LENGTH |
| | DACA_RED | 1 | 37.500M | |
| | DACA_GREEN | 1 | 37.500M | |
| | DACA_BLUE | 1 | 37.500M | |
| | | | | |
| 12, 14 | DACA_RED_C | 1 | 5000M | |
| 12, 14 | DACA_GREEN_C | 1 | 5000M | |
| 12, 14 | DACA_BLUE_C | 1 | 5000M | |
| | | | | |
| | DACA_HEYMC | 2 | 5000M | |
| | DACA_PIE_BUF | 2 | 5000M | |
| | DACA_PIE_BUF_W | 2 | 5000M | |
| 12, 14 | DACA_PIE_C | 2 | 5000M | |
| | | | | |
| | DACA_VENMC | 2 | 5000M | |
| | DACA_PIE_BUF | 2 | 5000M | |
| | DACA_VIE_BUF_W | 2 | 5000M | |
| 12, 14 | DACA_VIE_C | 2 | 5000M | |


| | |
|-------------|---|
| ASSEMBLY | PT062 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| PAGE DETAIL | DACA Interface for DVI |

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| PCB REV | P1062-B00 | PAGE | |
| BOM REV | A | DATE | 06-APR-2010 |

DACB RGB-FILTER



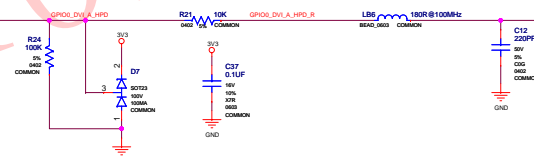
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| NV_PN | 600-11062-base-100 | | |
| PCB REV | P1062-B00 | PAGE | |
| BOM REV | A | DATE | 06-APR-2010 |

IFP A/B Interface -- DVI Connector, South

| NET | MIN_LINE_WIDTH | VOLTAGE | IV_NET_MAX_CURRENT |
|-------------|----------------|---------|--------------------|
| FPAB_PLYVDD | 120ML | 1.05V | 0.130A |
| FPAB_I0VDD | 120ML | 3.3V | 0.320A |
| FPAB_RSET | 120ML | | |



DVIAB Hotplug Detection



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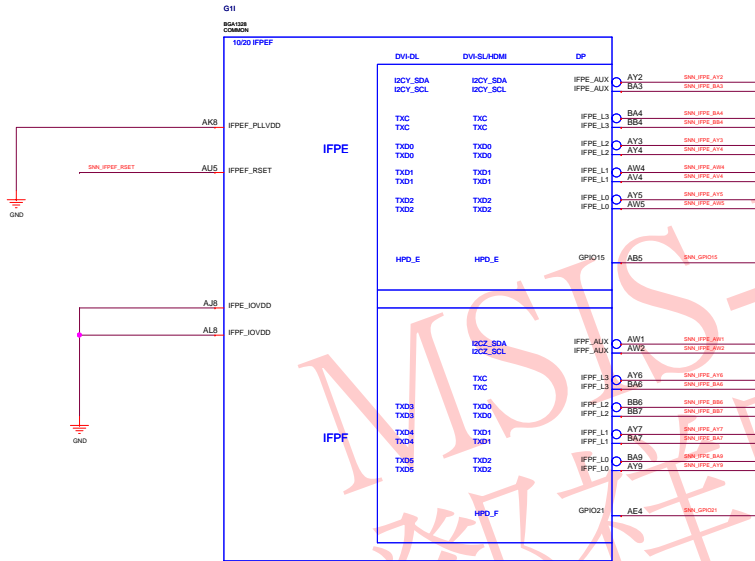


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| NV_PN | 600-11062-base-100 | | |
| PCB REV | P1062-B00 | PAGE | |
| BOM REV | A | DATE | 06-APR-2010 |

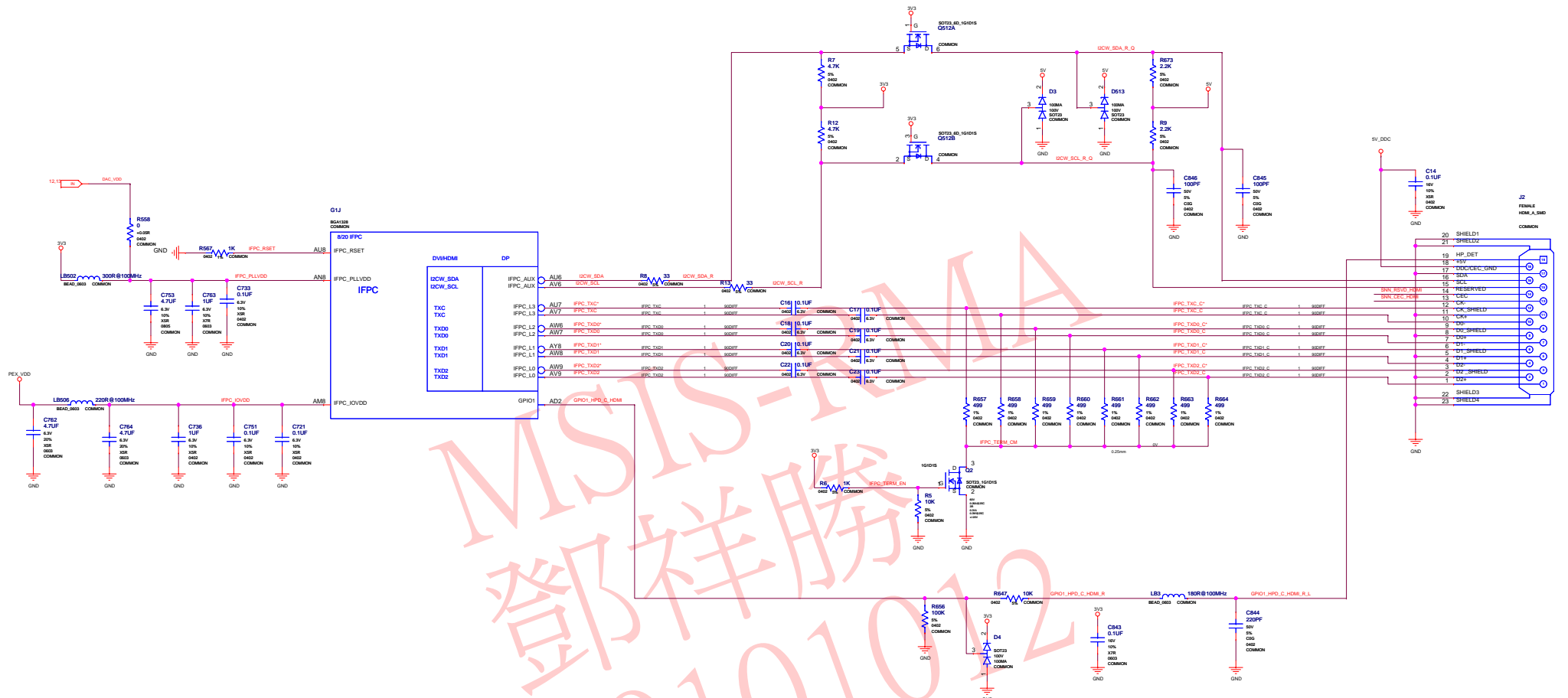
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| | |
|-------------|---|
| ASSEMBLY | P1062 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| PAGE DETAIL | IFP A/B Interface -- DVI Connector, South |

IFP E/F Interface -- Unused




IFP C Interface -- HDMI, North



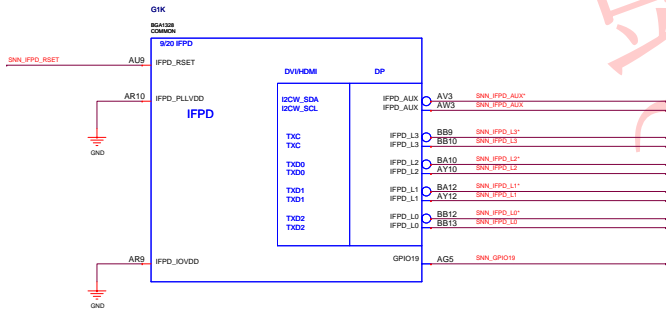
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|-------------|---|
| ASSEMBLY | PT062 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| PAGE DETAIL | IFPC Interface -- HDMI |

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| PCB REV | P1062-B00 | PAGE | | |
| BOM REV | A | DATE | 06-APR-2010 | |




IFP D INTERFACE (UNUSED)



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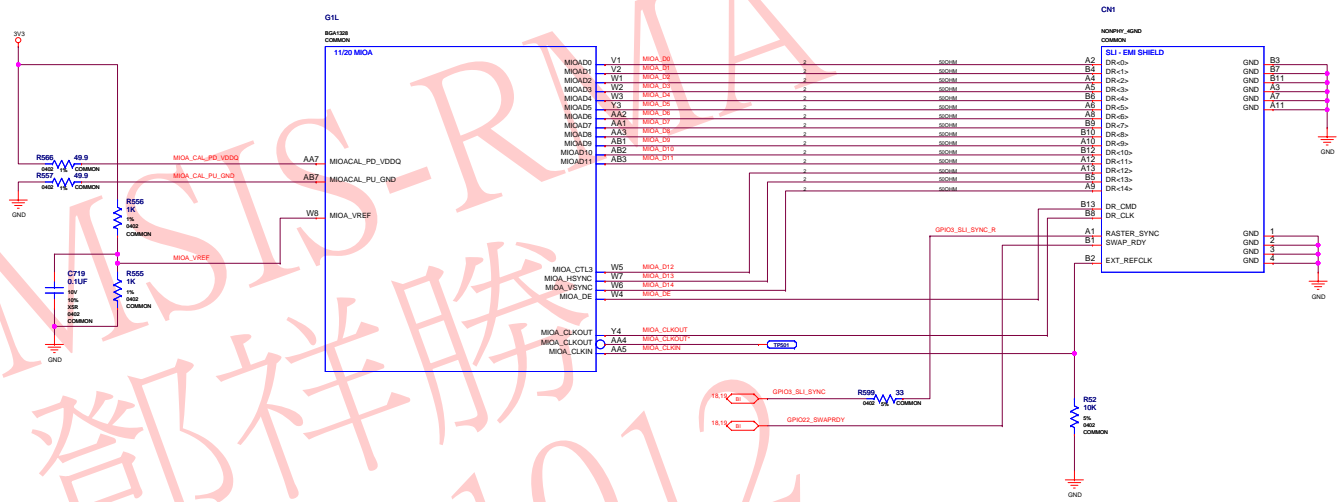
SANTA CLARA, CA 95050, USA



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MIO A Interface, SLI



| NET | NV_CRITICAL_NET | IMPEDANCE | DIFFPAIR |
|------------------|-----------------|-----------|--------------------|
| MIOA_CLKIN | 1 | 50OHM | |
| MIOA_CLKOUT | 1 | 50OHM | |
| MIOA_DE | 2 | 50OHM | |
| GPIO3_SLI_SYNC_R | 2 | 50OHM | |
| GPIO3_SLI_SYNC | 2 | 50OHM | |
| GPIO32_SWAPRDY | 2 | 50OHM | |
| NET | MIN_LINE_WIDTH | VOLTAGE | NV_NET_MAX_CURRENT |
| MIOA_VREF | 22MS | 1.6V | |
| MIOA_CAL_PD_VDDQ | 22MS | 3.3V | |
| MIOA_CAL_PLI_GND | 22MS | 0.0V | |

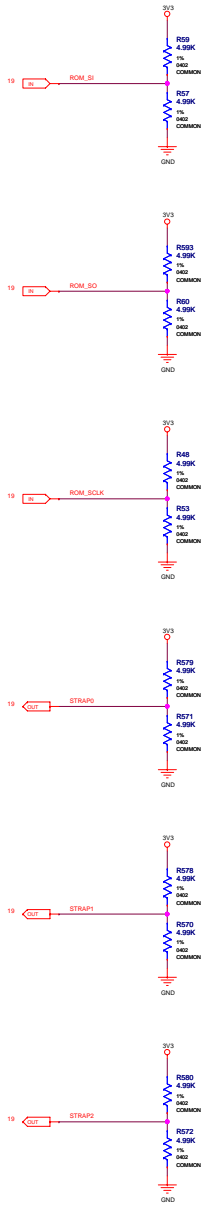
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| ASSEMBLY | P1062 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| PAGE DETAIL | MIO A Interface, SLI |

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| NV_PN | 600-11062-base-100 |
| PCB REV | P1062-3000 |
| BOM REV | A |
| PAGE | 1 |
| DATE | 06-APR-2010 |

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Strap Configuration

GF106-300: 0x0DC0



| | | | | | |
|----------|---|-----|--|--------|-----|
| ROM_S0 | VGA_DEVICE SMB_ALT_ADDR PU_0_B4H_SIZE KCLK_4T | LSb | 1: VGA_DEVICE 0: Disabled(POR) 0: Disabled(POR) 0: Disabled | PU | PD |
| ROM_S1 | RAM_CFG0(S) | MSb | 000010: 100-bit Samsung 000010: 100-bit Hynix 000010: 100-bit Winbond 000010: 100-bit Hynix | 20k PD | 20k |
| ROM_SCLK | PEL_RLL_EN_TERMINO SLOT_CLK_CONFIG SMB_VENDOR PCI_DEVICE_EXT | LSb | 0: Disabled (POR) 1: GPU and MECH COMMON REFCLK 1: MECH ROM is PRESENT 0: PCDEVICE(S) | | 35k |
| STRAP0 | USER_BIT0(S) | | 0x0: DEFAULT | | 0k |
| STRAP1 | SGD_PADCFG_LUT_ADDR(S) | | 0x0: Default High Imped | | 0k |
| STRAP2 | PCI_DEVICE(S) | | 0x0: PCI_DEVICE_000000 | | 0k |

| | |
|-----------------------------------|-------|
| Multi-level Strap Resistor Values | |
| Strap PU/PD Value (kOhm) | |
| 0 | PD 5 |
| 1 | PD 10 |
| 2 | PD 15 |
| 3 | PD 20 |
| 4 | PD 25 |
| 5 | PD 30 |
| 6 | PD 35 |
| 7 | PD 40 |
| 8 | PU 5 |
| 9 | PU 10 |
| 10 | PU 15 |
| 11 | PU 20 |
| 12 | PU 25 |
| 13 | PU 30 |
| 14 | PU 35 |
| 15 | PU 40 |

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NV_PN

600-11062-base-100

PCB REV

A

PAGE

1

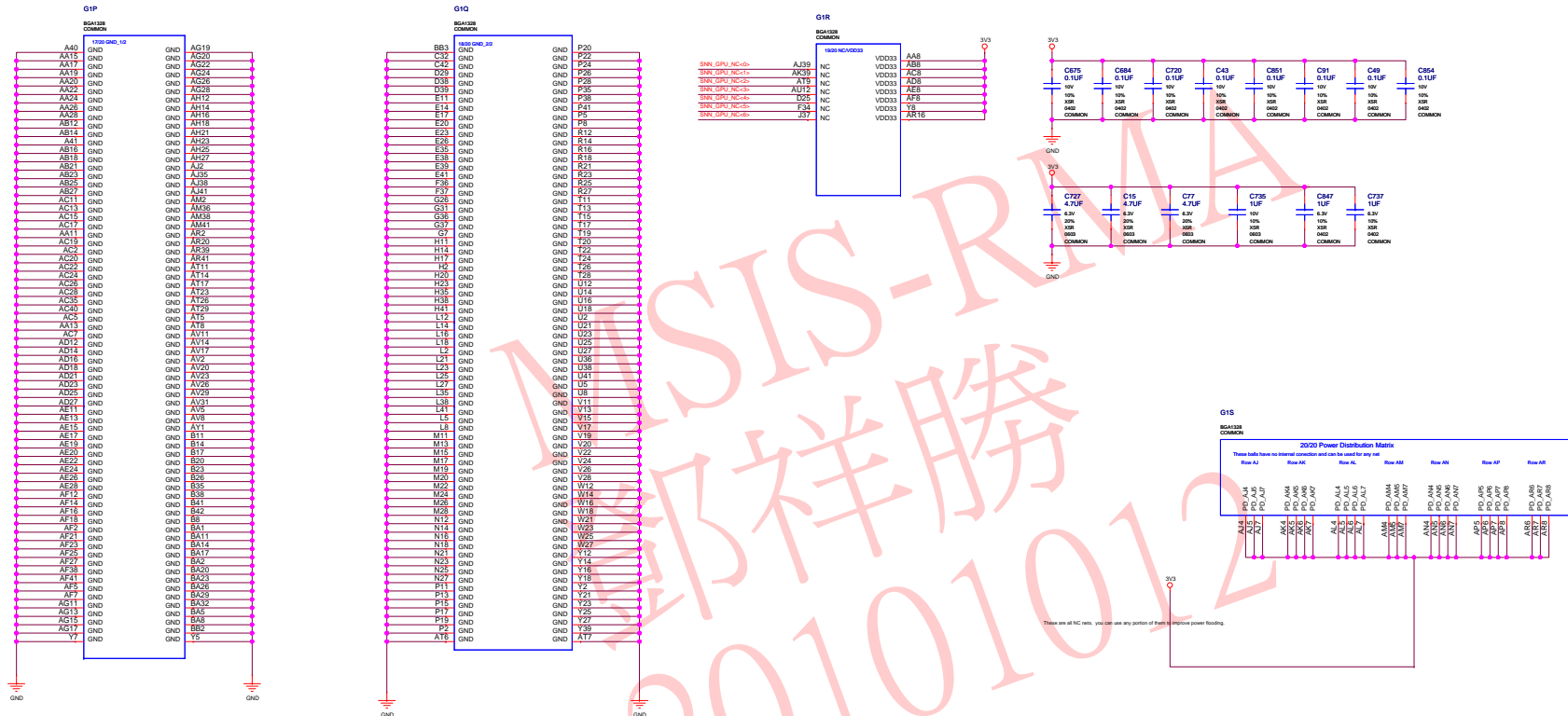
BOM REV

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DATE

06/APR/2010

PWR and GND Signals

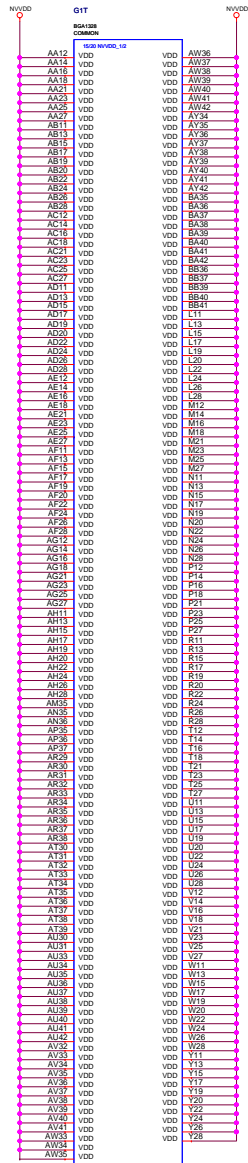


These are all NC nets. you can use any portion of them to improve power flood

| | | | | |
|--|-----------|---------------------------|---|--|
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| NV_PN | | 600-11062-base-100 | | |
| PCB REV | A1062-B00 | PAGE | | |
| BOM REV | A | DATE | 06-APR-2010 | |

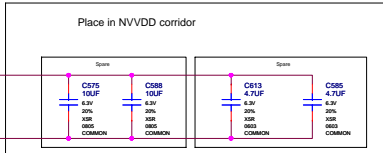
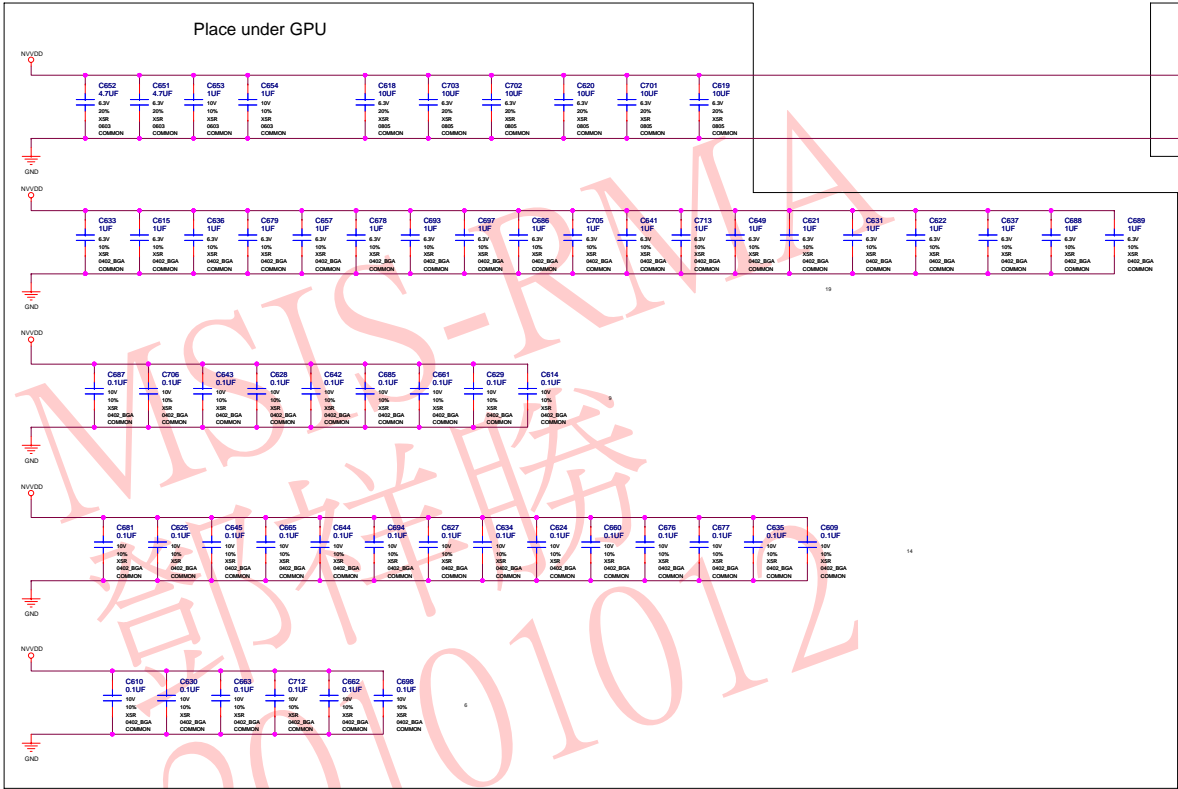
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NVDD Decoupling



0805 0603 0402

Sites placed under GPU: 8x 10uF 2x 4.7uF 18u 1uF 1x 22uF (2x unused) (17x unused)



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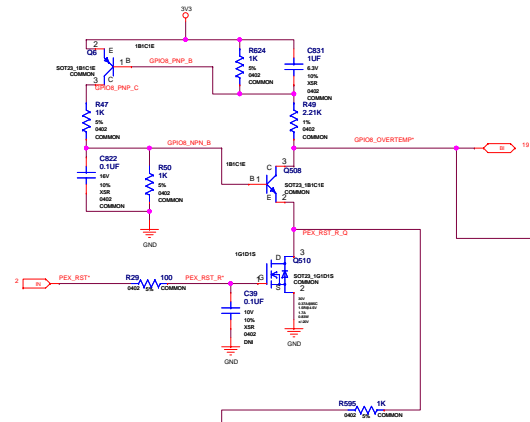
PAGE 10 RETAIL NVDD Decoupling

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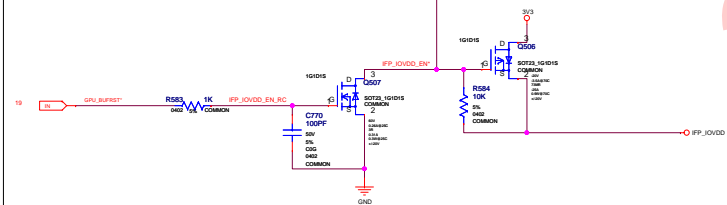
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| PCB REV | P1062-2800 |
| BOM REV | A |
| PAGE | 10 |
| DATE | 06/APR/2010 |

Power Sequencing, Thermal Protection, and IFP_IOVDD Power Supply

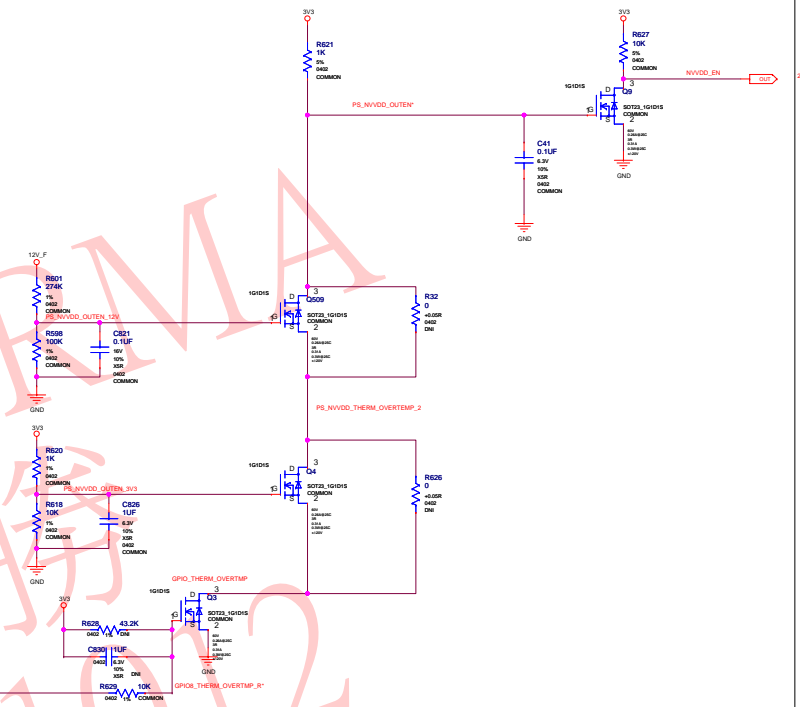
Thermal Protection (latch)



IFP_IOVDD Backdrive Prevention




NVVDD Sequence



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| ASSEMBLY | PT062 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| PAGE DETAIL | Backdrive Protection, Thermal Protection, and Power Sequencing |

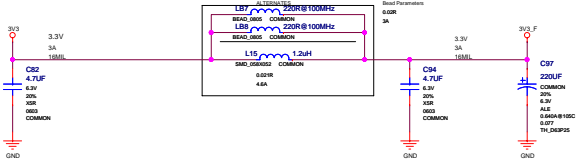
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| PCB REV | P1062-900 | PAGE | |
| BOM REV | A | DATE | 06-APR-2010 |



PS I: 3V3, 12V, and 12V_EXT Power Supply Filter, 5V, DDC VOLTAGE

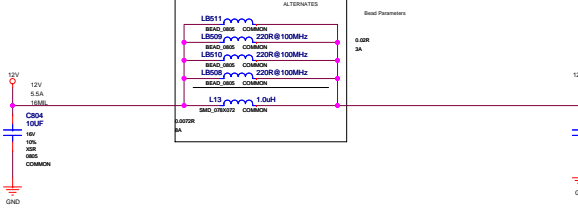
3V3 Power Supply Filter

3V3_F = 3.3V @ 2.4A

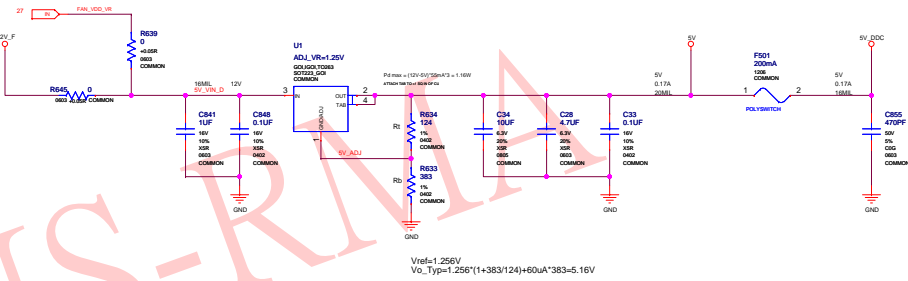


12V Power Supply Filter

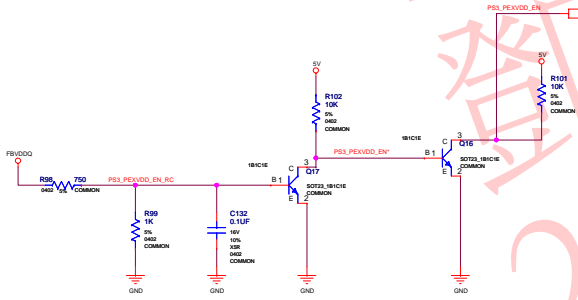
12V_F = 12V @ 5.5A



5V POWER SUPPLY, DDC POWER



PEXVDD ENABLE



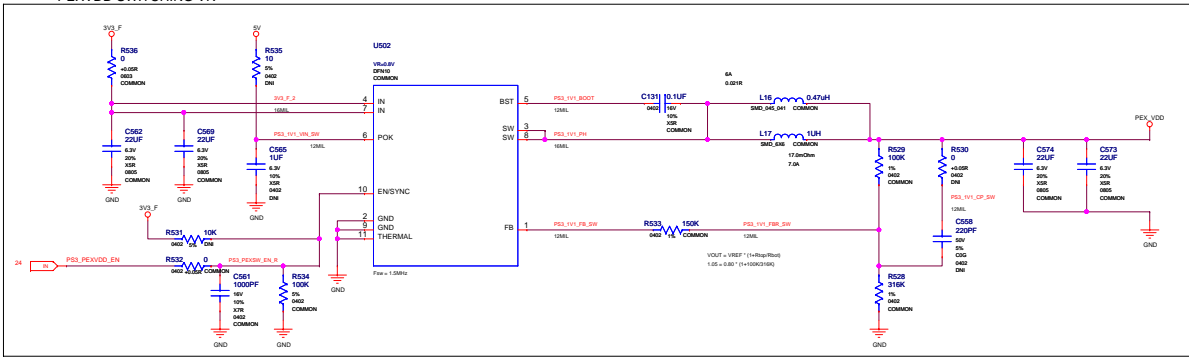
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PEXVDD SWITCHING VR



PEXVDD, 2.1A @ 1.05V

| CONTROLLER | Freq | VREF |
|------------|--------|------|
| AP7010A | 300kHz | 0.6V |
| AP7010B | 300kHz | 0.6V |
| AP7010C | 300kHz | 0.6V |
| AP7010D | 300kHz | 0.6V |
| AP7010E | 300kHz | 0.6V |
| AP7010F | 300kHz | 0.6V |
| AP7010G | 300kHz | 0.6V |
| AP7010H | 300kHz | 0.6V |
| AP7010I | 300kHz | 0.6V |
| AP7010J | 300kHz | 0.6V |
| AP7010K | 300kHz | 0.6V |
| AP7010L | 300kHz | 0.6V |
| AP7010M | 300kHz | 0.6V |
| AP7010N | 300kHz | 0.6V |
| AP7010O | 300kHz | 0.6V |
| AP7010P | 300kHz | 0.6V |
| AP7010Q | 300kHz | 0.6V |
| AP7010R | 300kHz | 0.6V |
| AP7010S | 300kHz | 0.6V |
| AP7010T | 300kHz | 0.6V |
| AP7010U | 300kHz | 0.6V |
| AP7010V | 300kHz | 0.6V |
| AP7010W | 300kHz | 0.6V |
| AP7010X | 300kHz | 0.6V |
| AP7010Y | 300kHz | 0.6V |
| AP7010Z | 300kHz | 0.6V |

Input Ripple current = 3A RMS @ 275kHz with 0.5uH

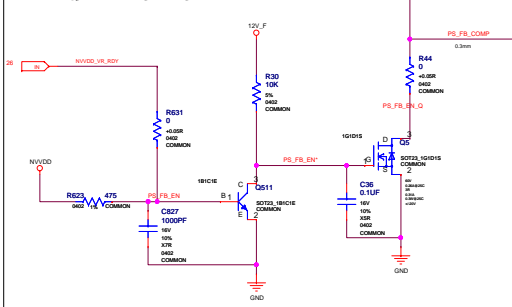
Vout = 1.5V, 9.4A

Ripple output = 1.45V

Ripple output = 0.7A

 $V_{out} = V_{ref} * (1 + R_{top} / R_{bot})$

FBVDDQ/PEXVDD GATING



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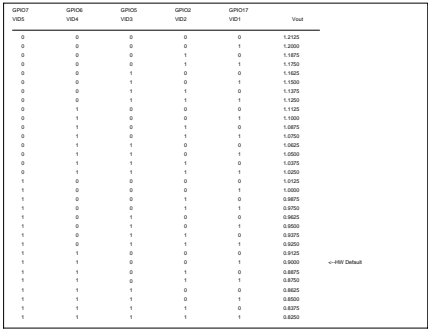
BOX REV A

PAGE 1


DATE 06/APR/2010

ASSEMBLY
PAGE DETAIL
PEX_VDD switcher, FBVDDQ

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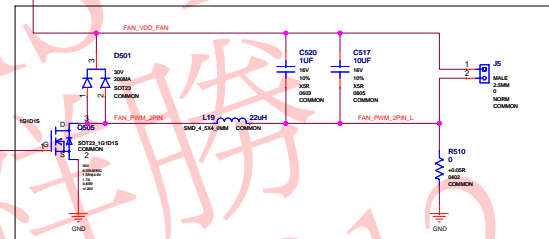
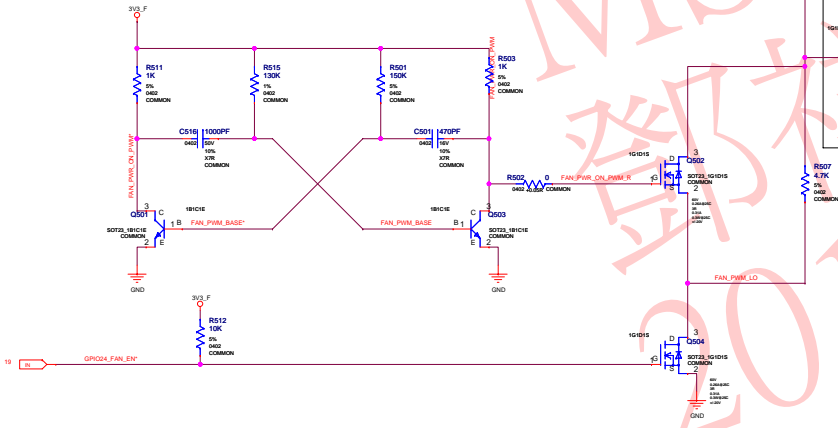
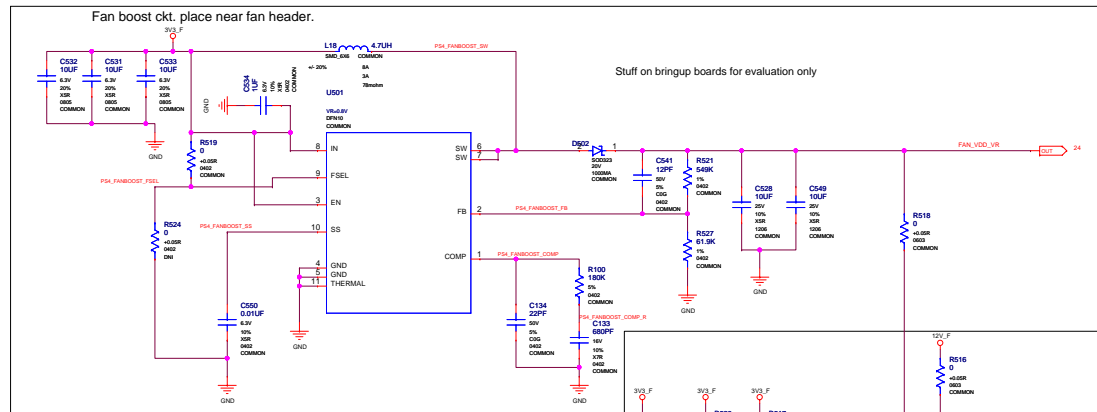


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|-------------|-------------------|
| ASSEMBLY | |
| PAGE DETAIL | NVDD Power Supply |

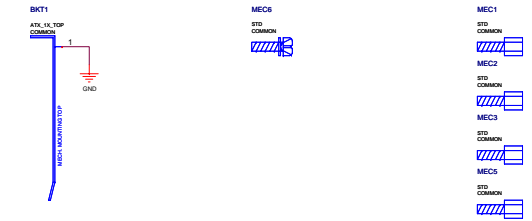
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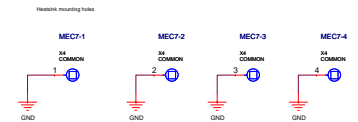
Thermal, Mechanical, and Bracket



Bracket



SPECIAL MECHANIC



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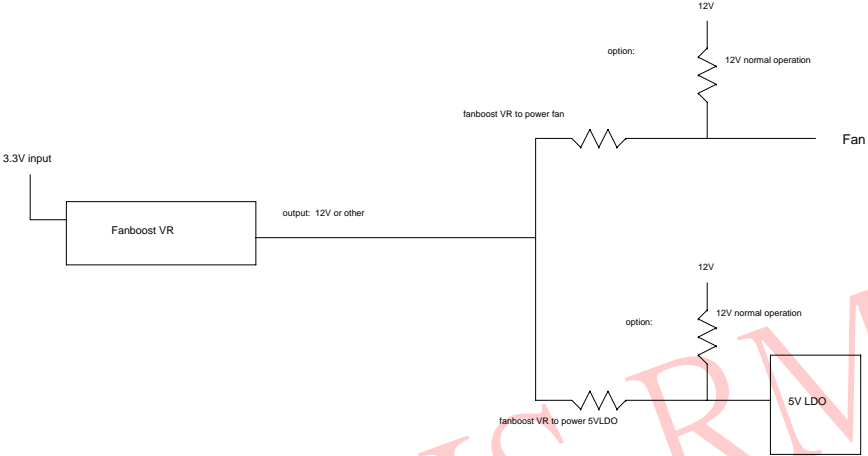
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| ASSEMBLY | P1062 BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL |
| PAGE DETAIL | Fanboost VR, multivibrator, Thermal, and Mechanical |



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