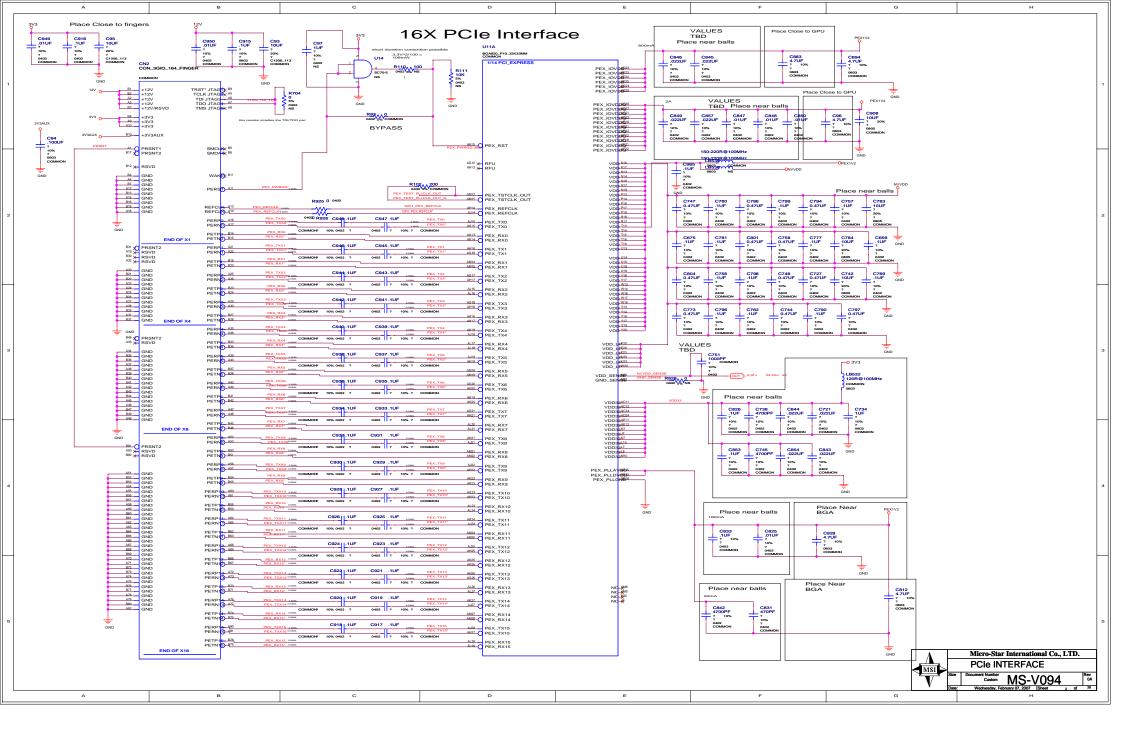
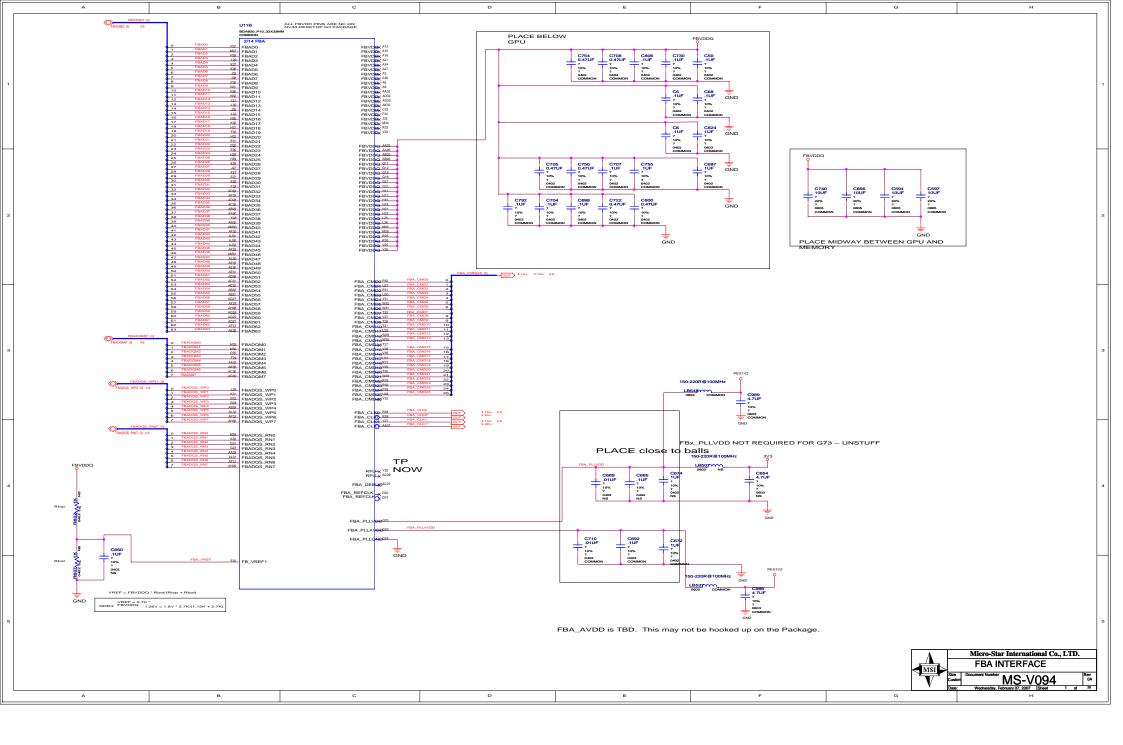
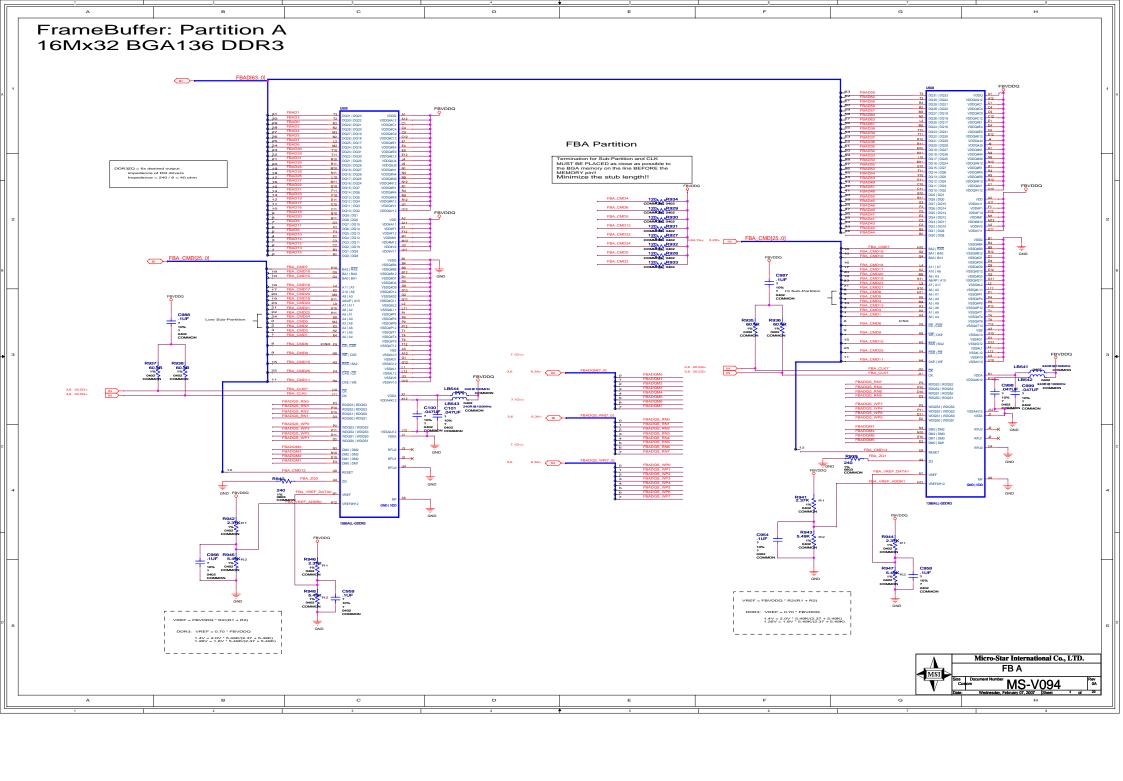
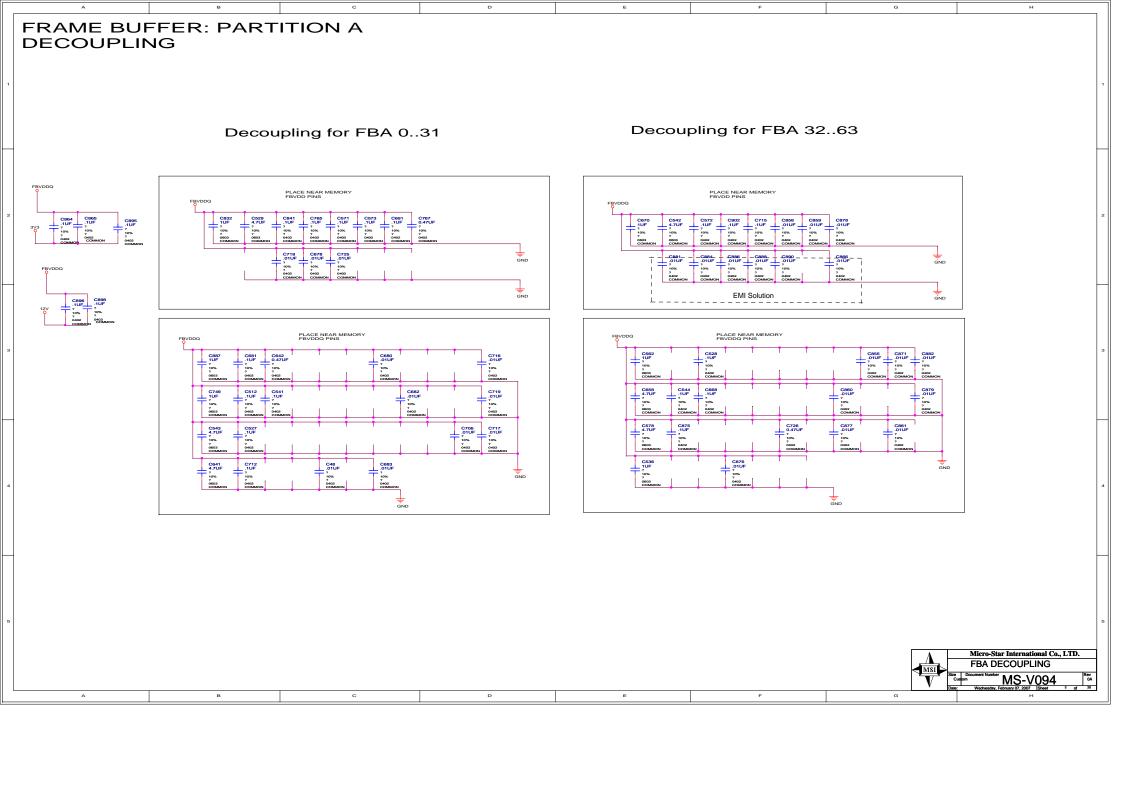
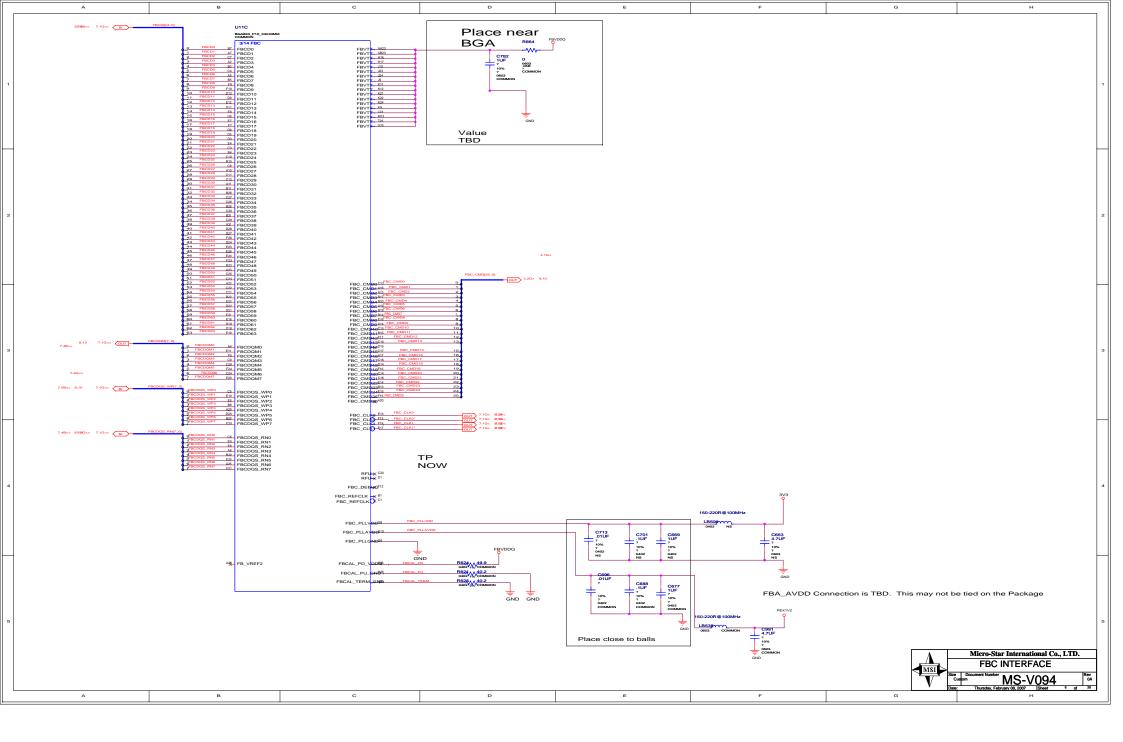
MS-V094-0A-- G86GS, 128/256 Mb DDR3-136pin, VGA, DVI-I, SD/HDTV **SUMMARY: Rev History** 1.Remove VIDEO IN 1. Base on P456-A02 to Modify 2. Modify Page 9, Modify SLI circuit for G86 2. BGA-136 DDRIII 3. Modify Page 14, Remove TMDS LINK C&D circuit 4. Modify Page 15, Remove SPDIF circuit Table of Contents: 5.Modify Page 20 FBVDDQ,PWM IC change to use 7210, Add LINEAR POWER & APW 7080 Page 1: TITLE Page 2: PCIe INTERFACE Page 3: FBA INTERFACE Page 4: FB A Page 5: FBA DECOUPLING Page 6: FBC INTERFACE Page 7: FB C Page 8: FBC DECOUPLING Page 9: MIOA/MIOB/SLI Page 10: DACC Page 11: DACA Page 12: DACB/TVOUT Page 13: TMDS LINK A & B Page 14: TMDS LINK C & D Page 15:XTAL/PLLVDD Page 16: GPIO/ROM/HDCP ROM Page 17: Straps/Mechanical Page 18: Linear Power Page 19: NVVDD,5V,A3V3 Page 20: FBVDDQ

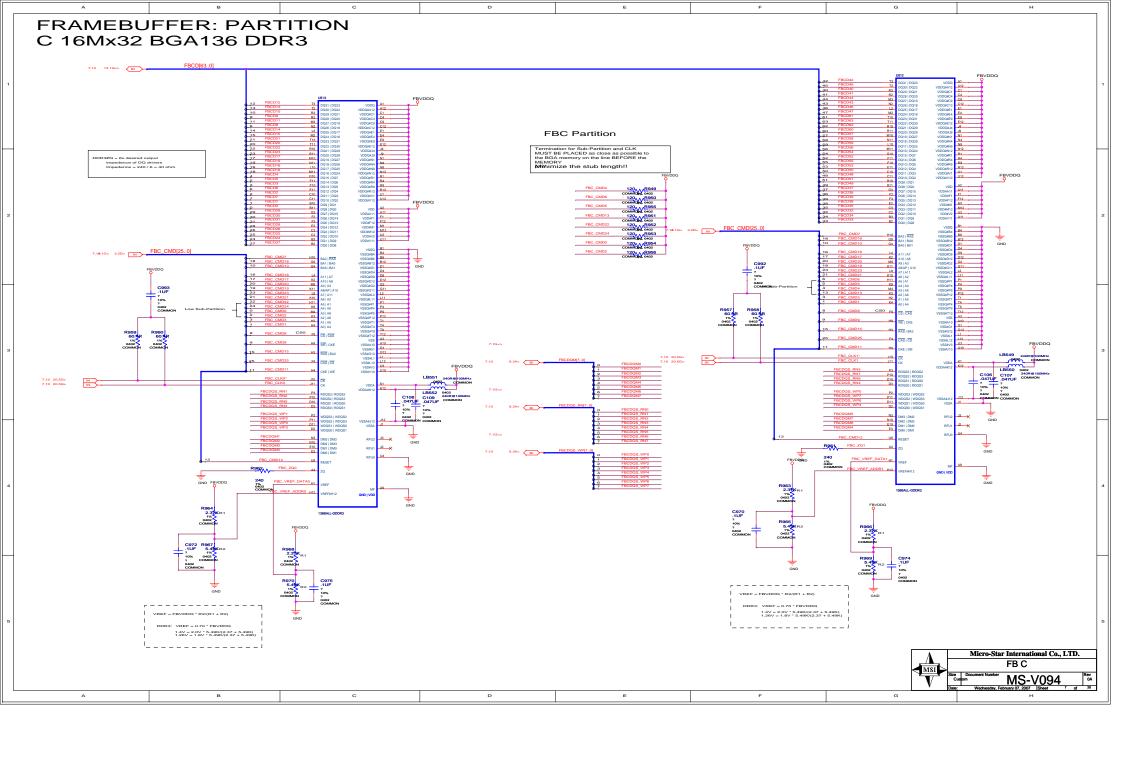


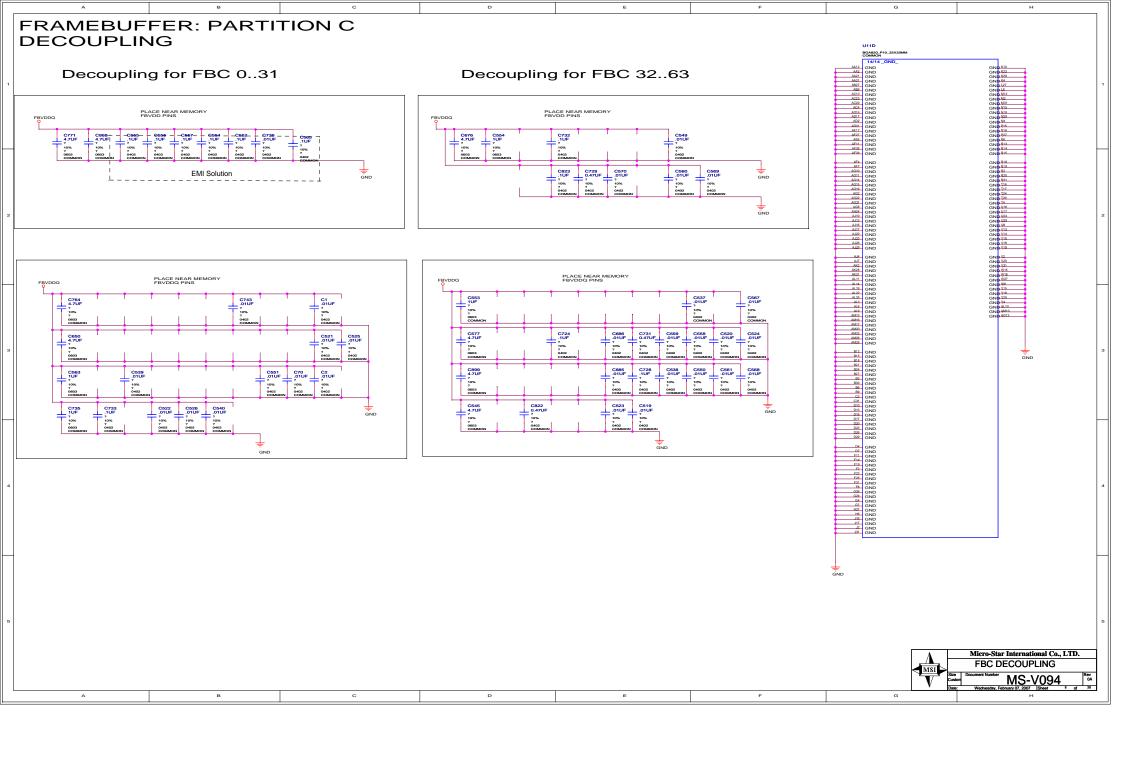


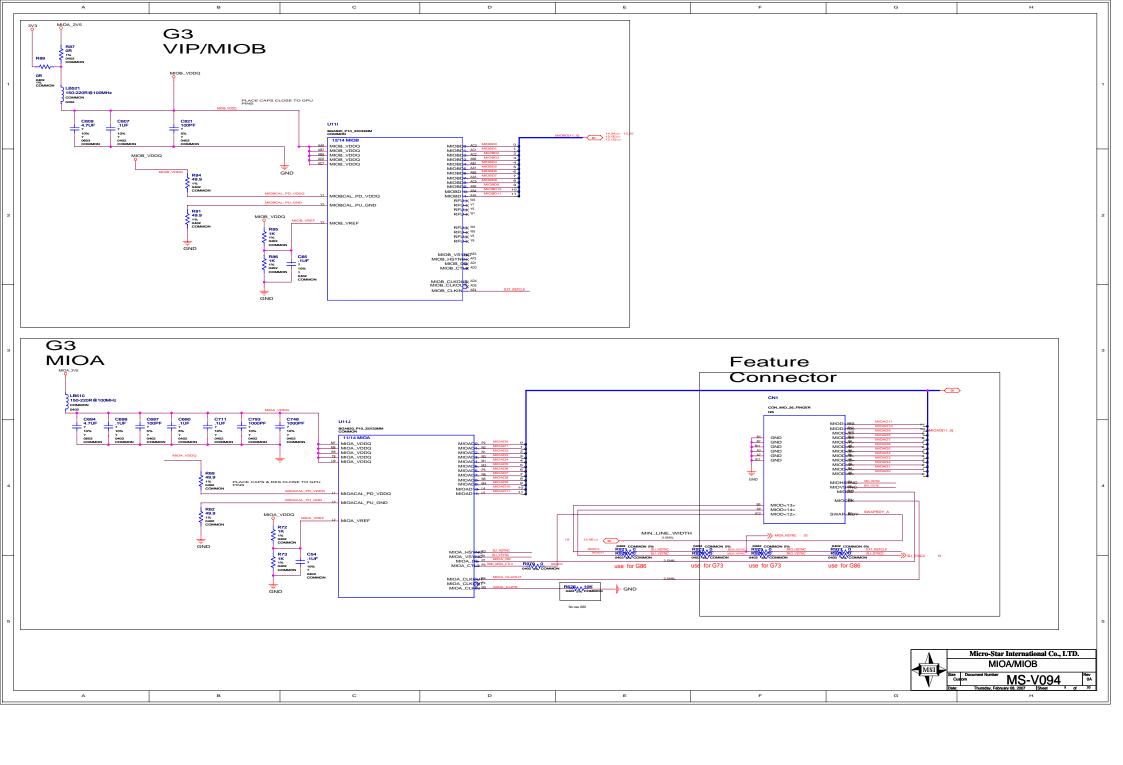


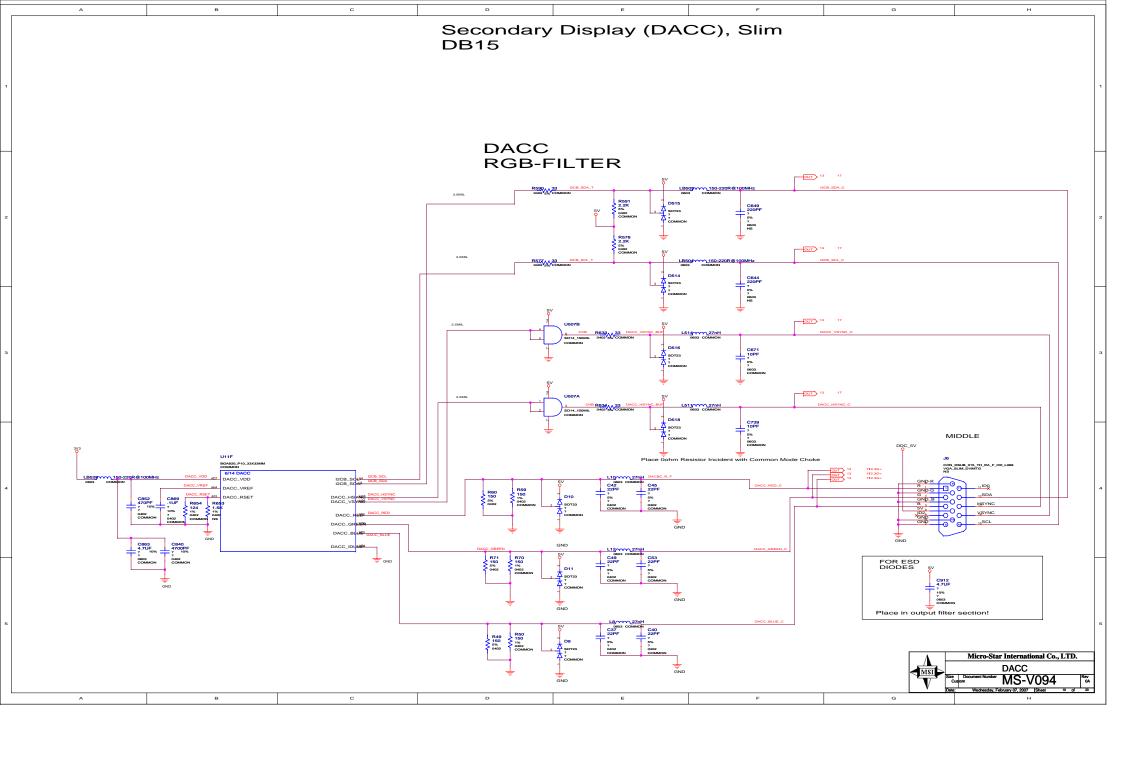


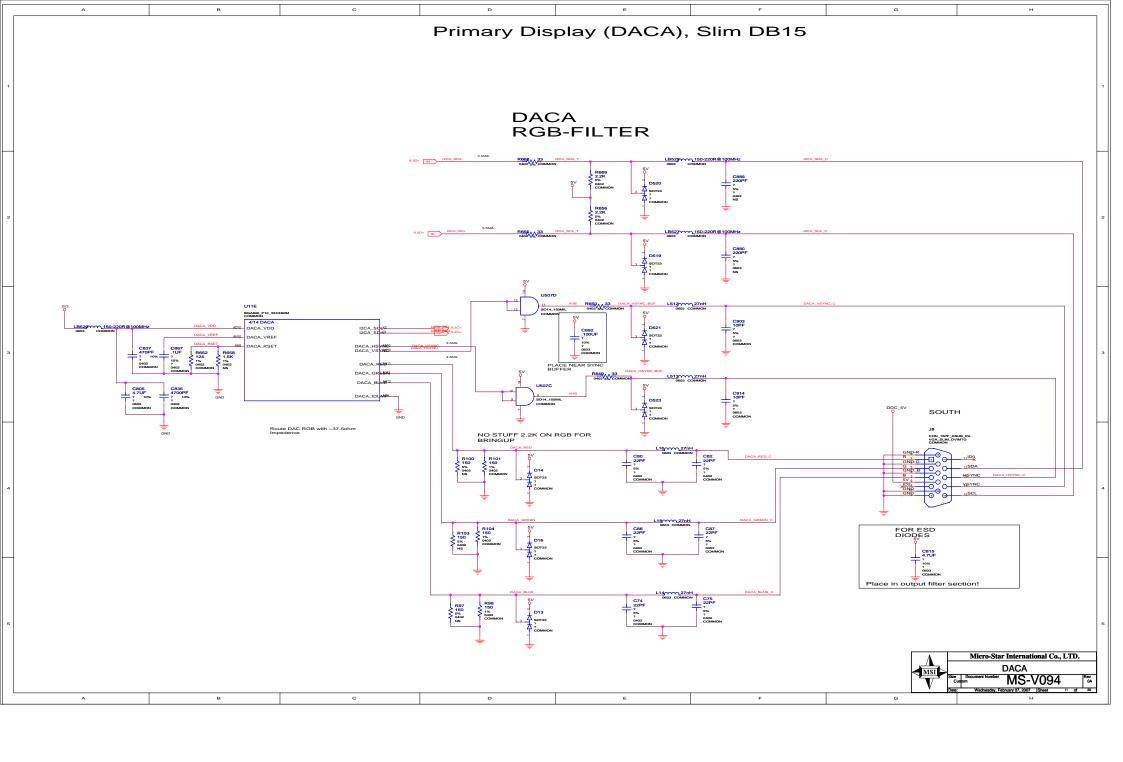


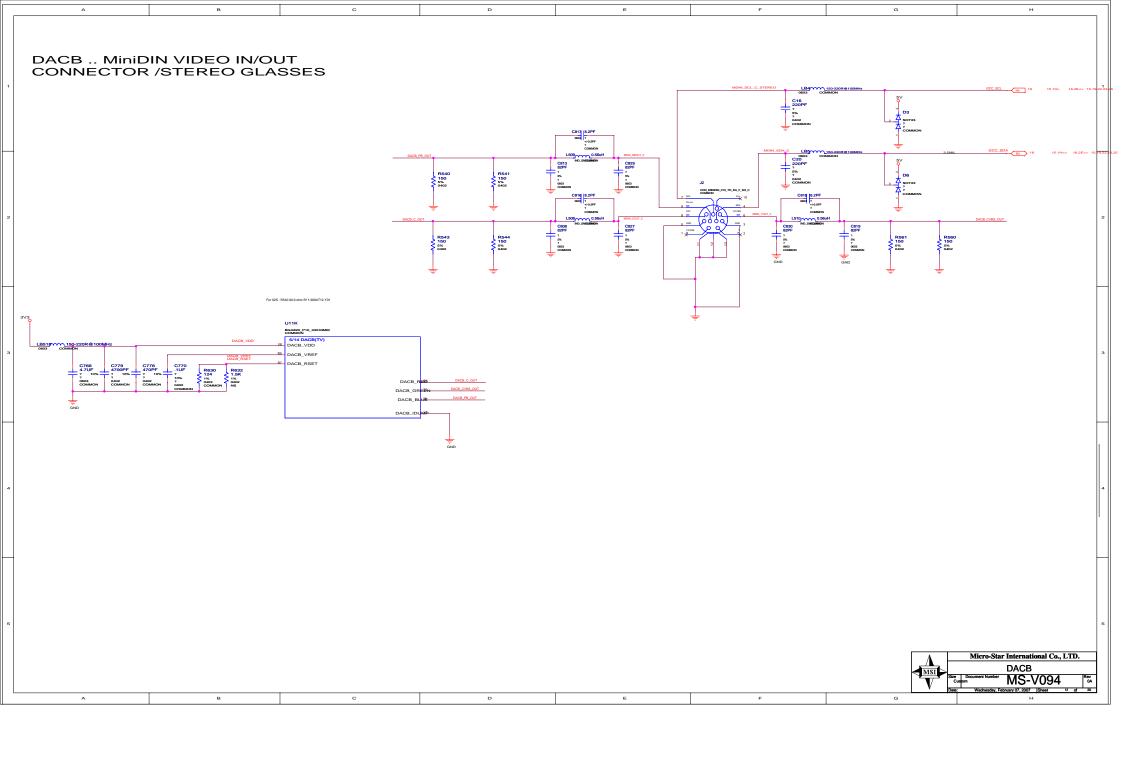


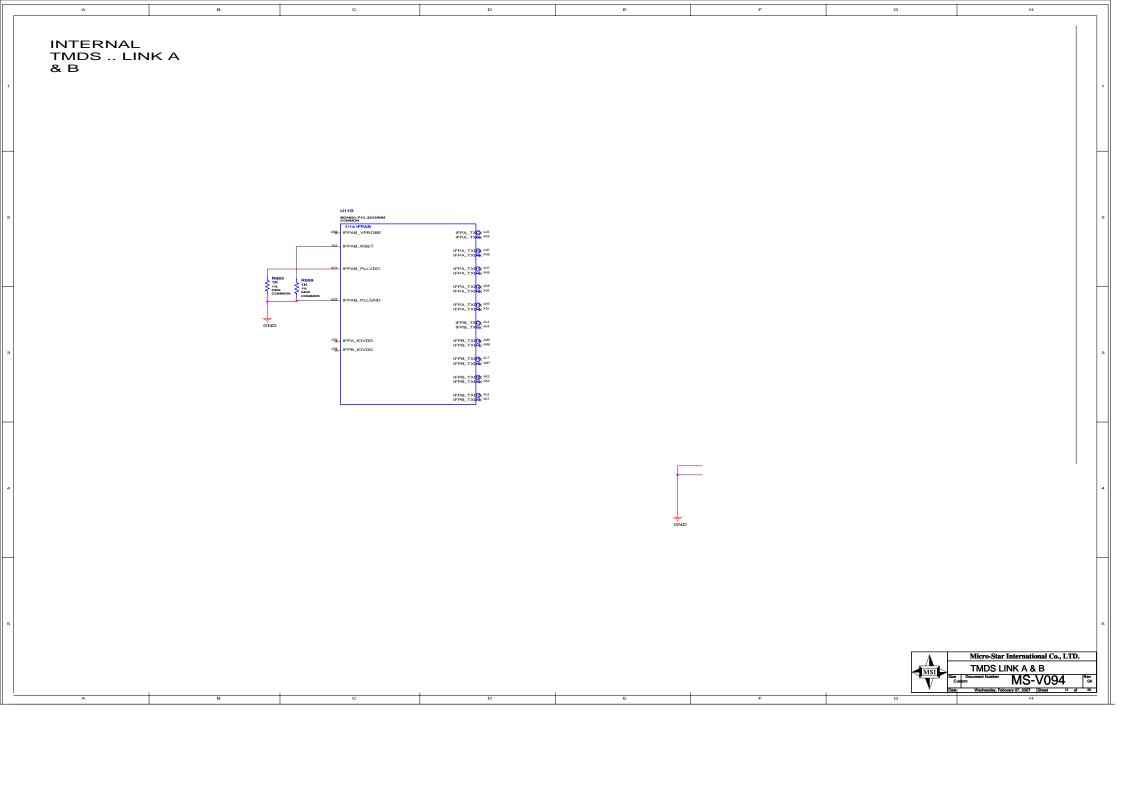


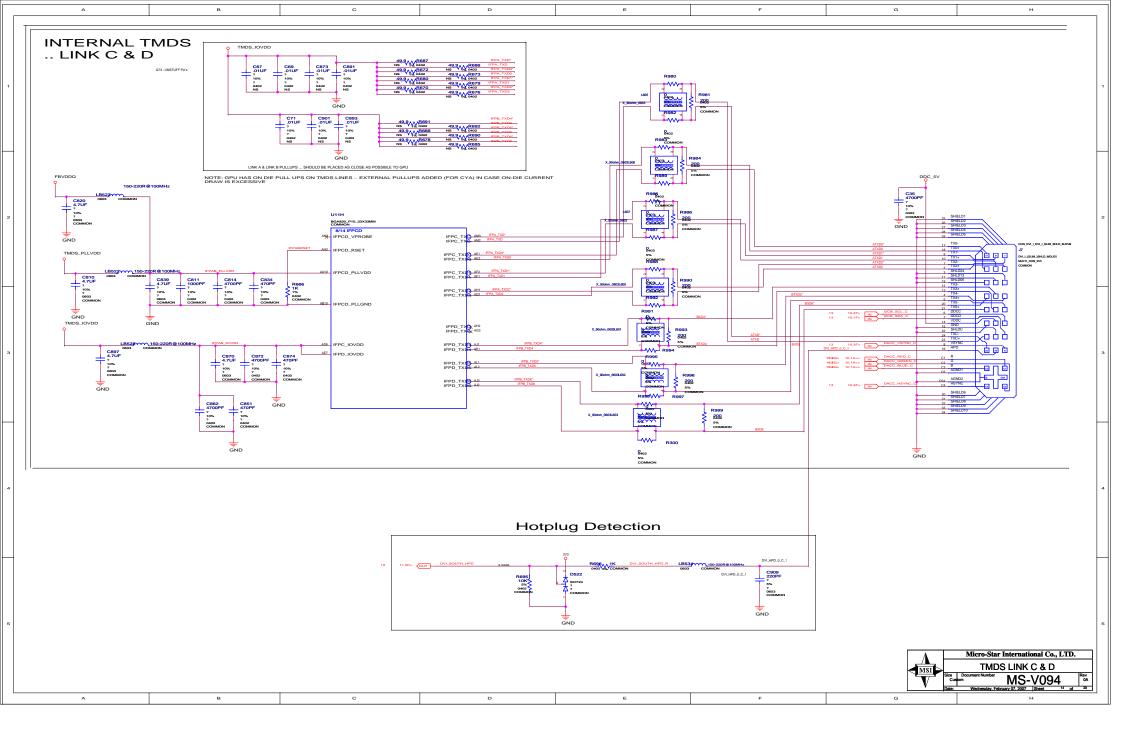


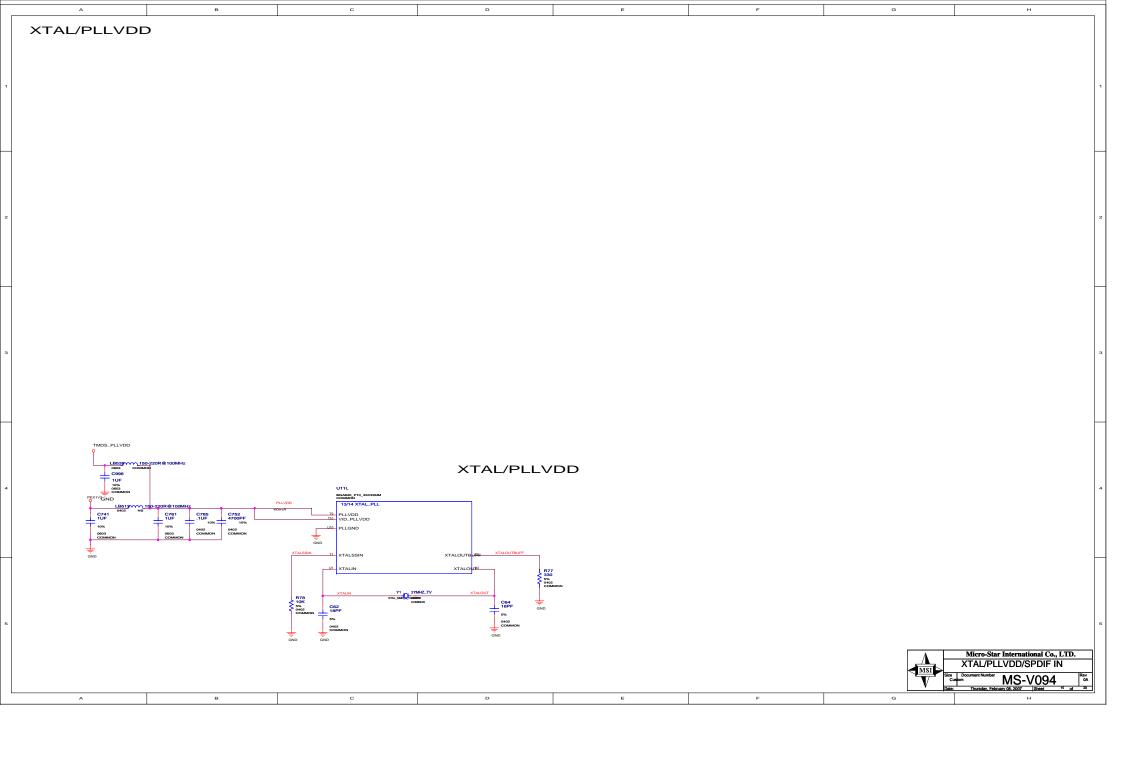


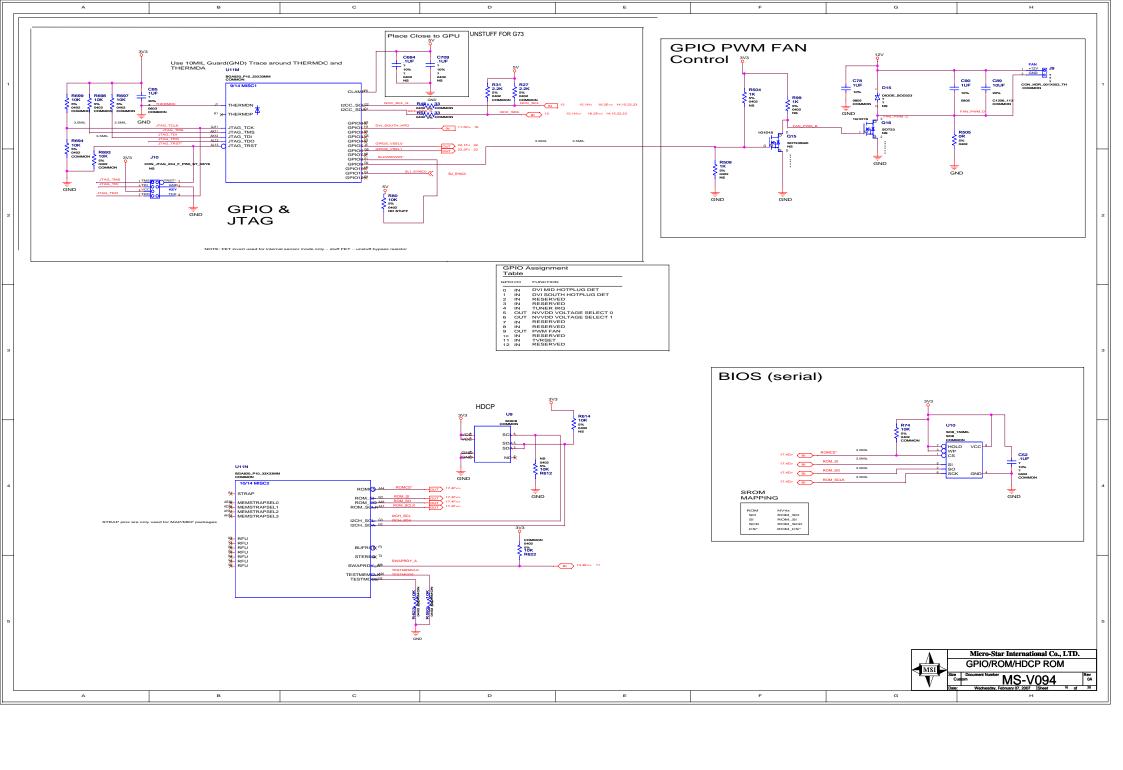


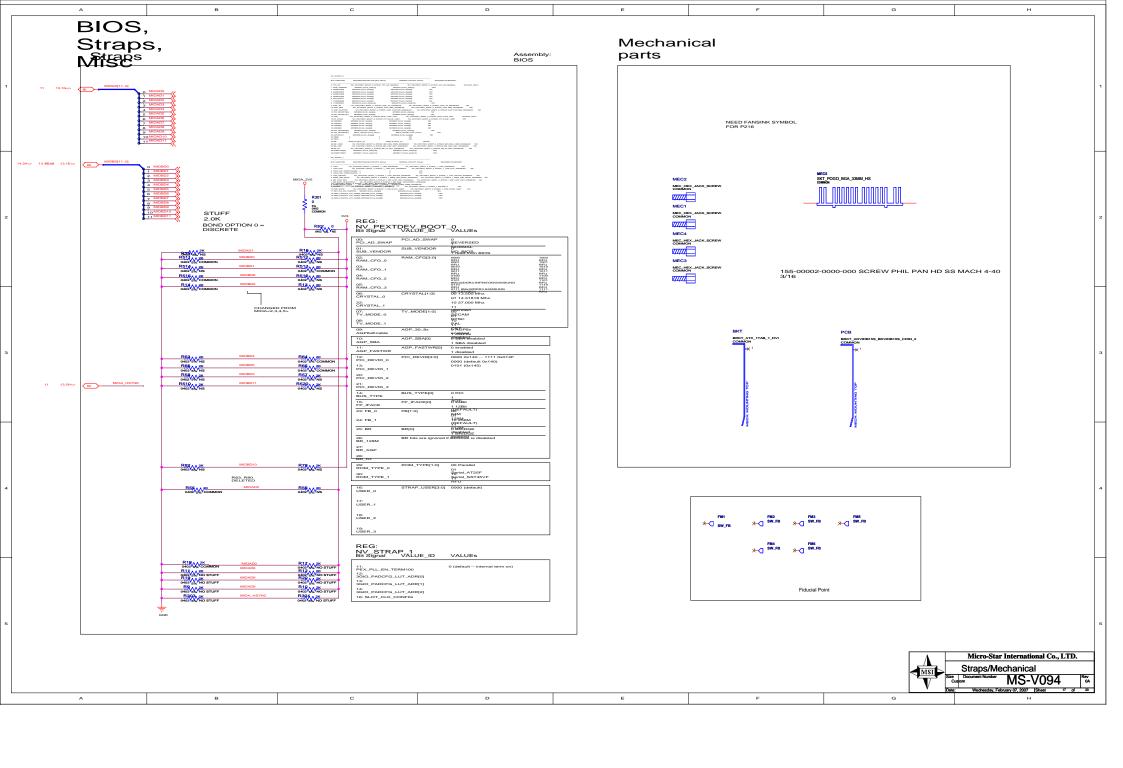


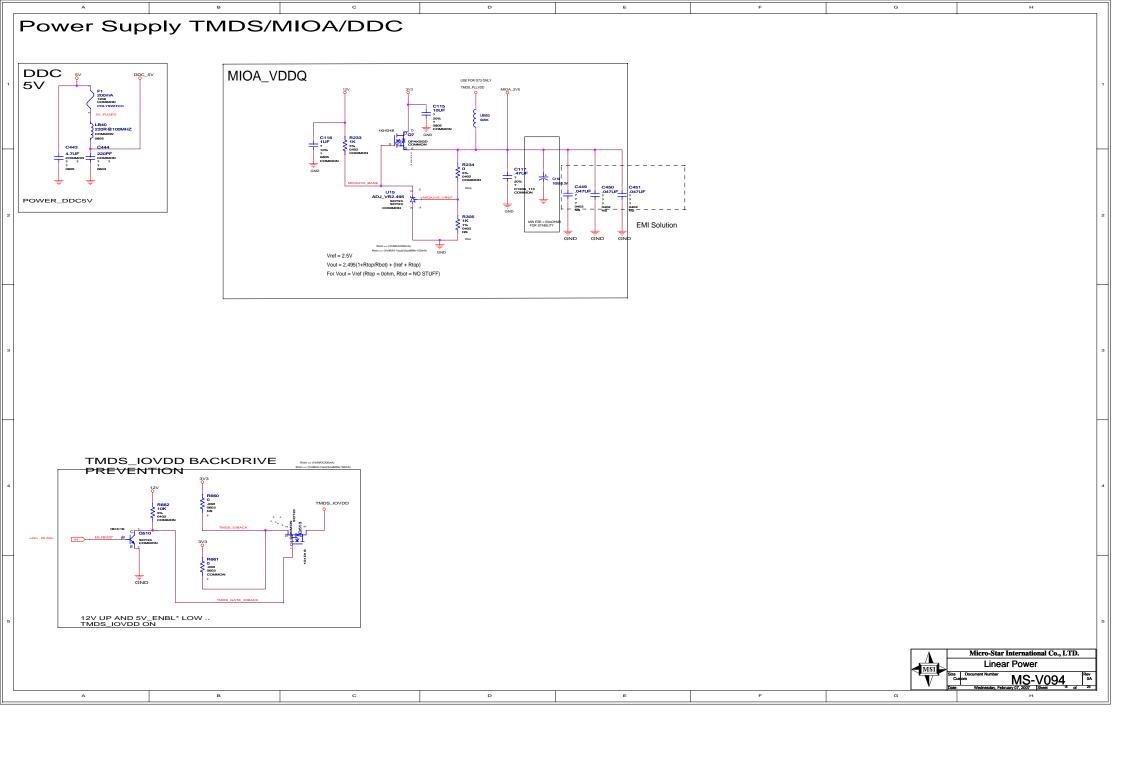












NVVDD ,FBVDD, 5V,A3V3 POWER SUPPLY

NVVDD ,FBVDD, 5V,A3V3 POWER SUPPLY

