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Page	Description
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Del C631,C612,C608,C597

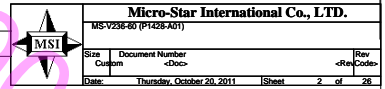
- | | |
|-----|---|
| 15. | Remove DVI-I(Mid) change to D-SUB |
| 16. | Change mini type HDMI connector to A type |
| 21. | U501 pin3 add R130 to GND |
| 21. | Change L7 footprint |
| 14. | Remove DP |

PCB : 61

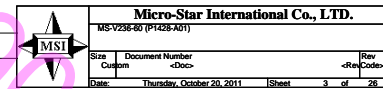
10/12
Page 8 : Add Q801,R801,R802,R803 to quickly fix PCB 60 issue.
Page 26 : Add MEC 18, MEC19 for MOSFET's Heatsink.
10/17
Page11 : Del colay C631,C612,C608,C597
Page 20 : Add C2001

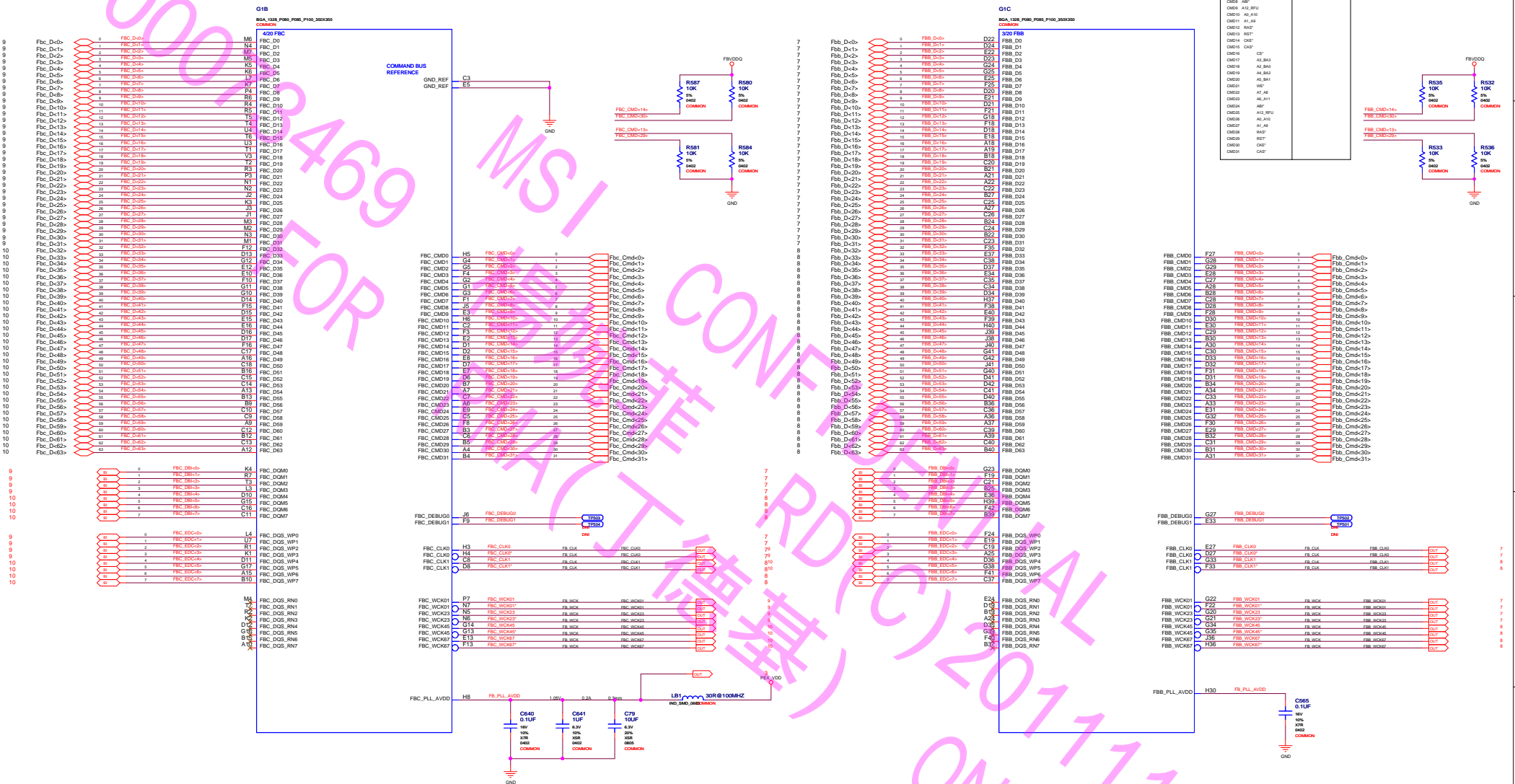
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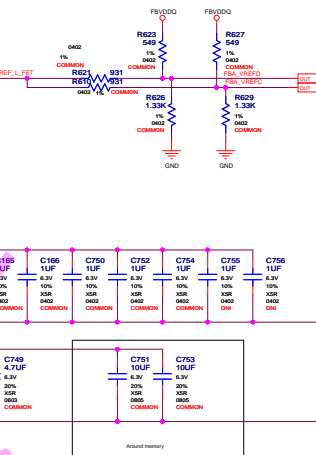
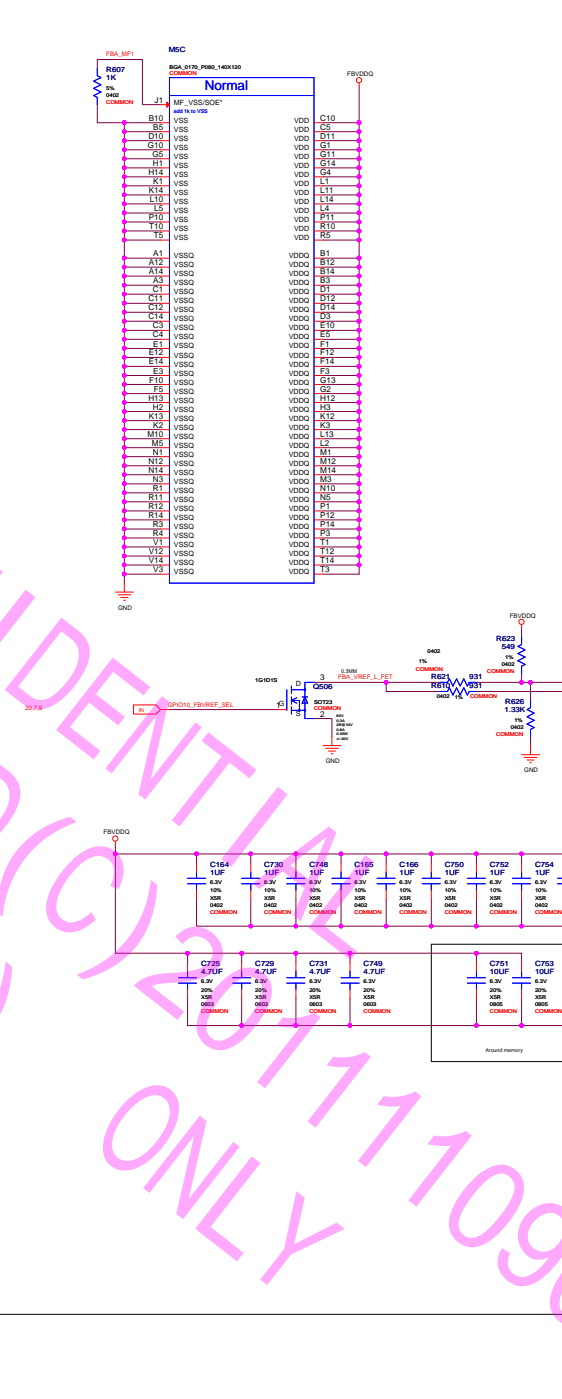
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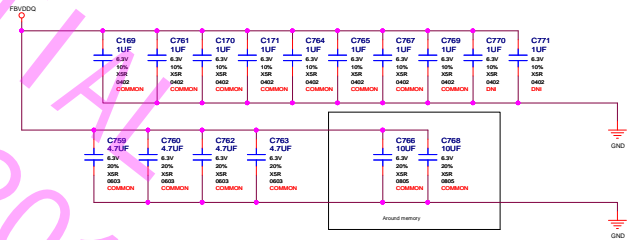
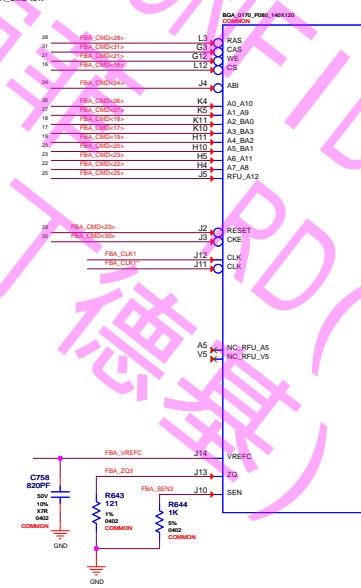


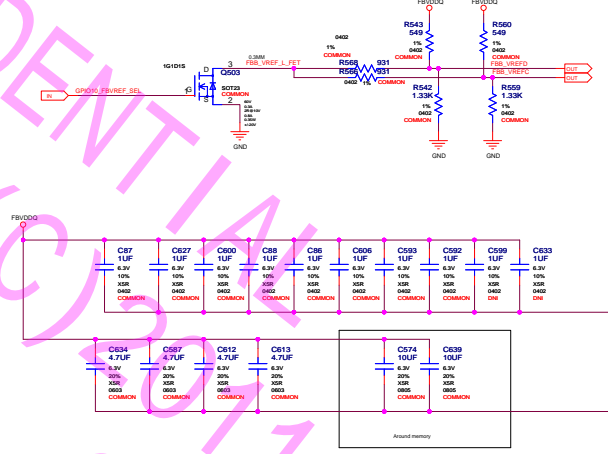
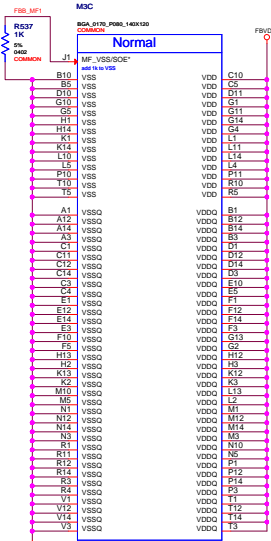
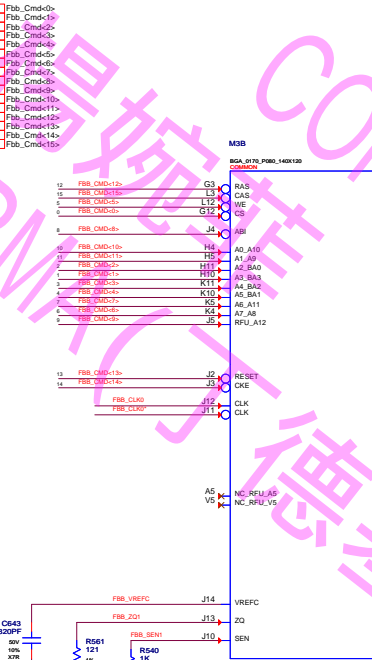
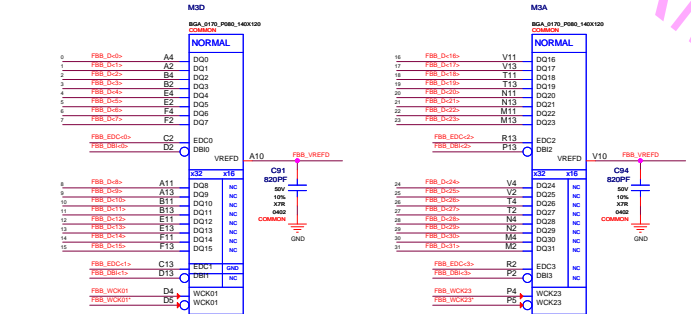
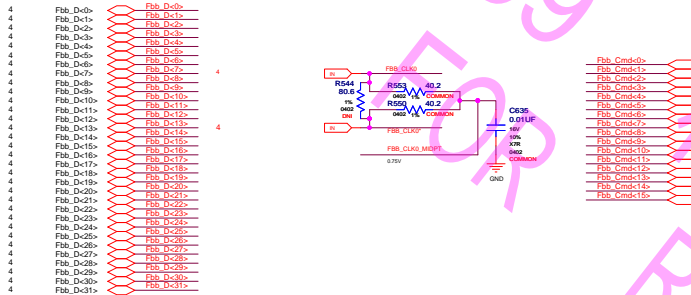
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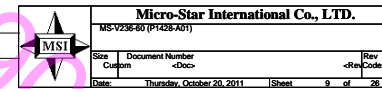


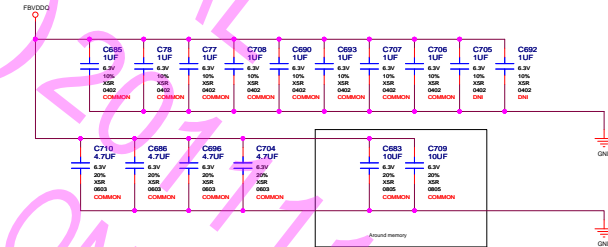
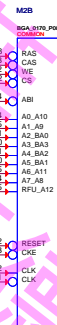
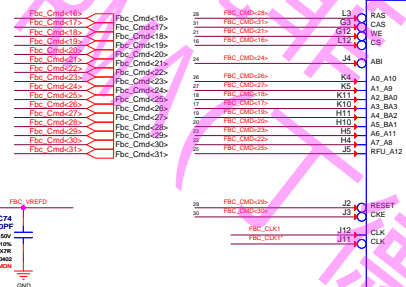
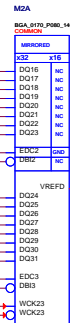
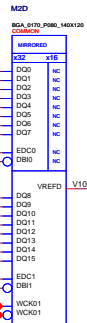
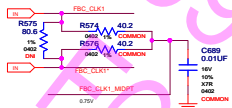





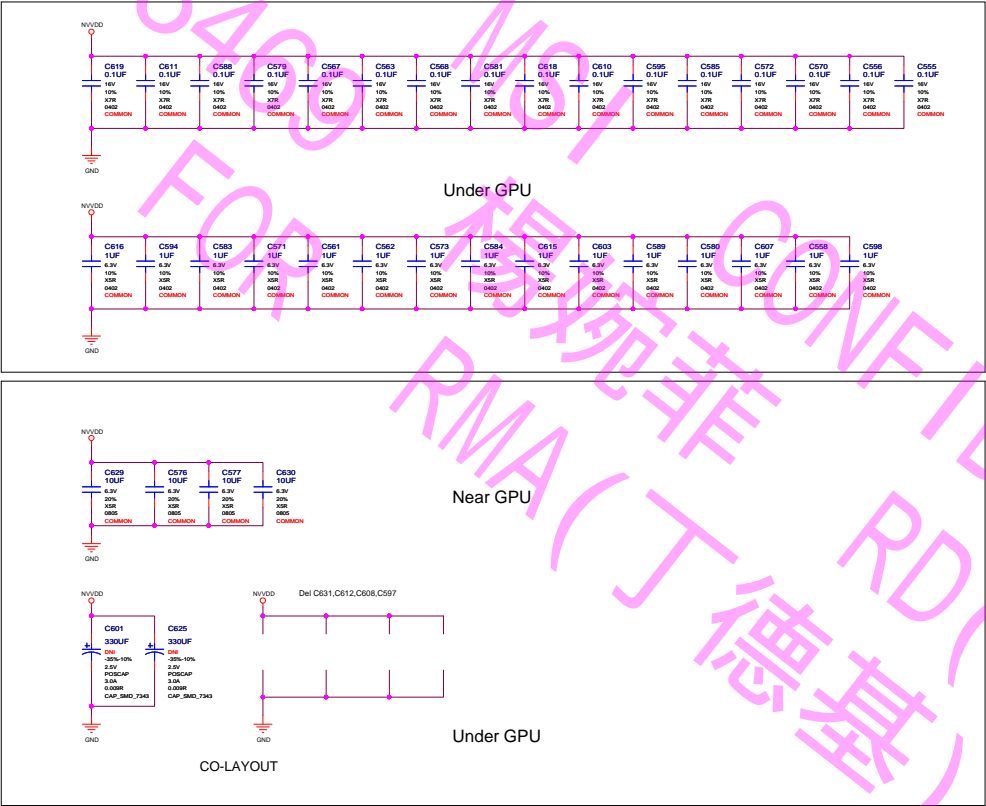
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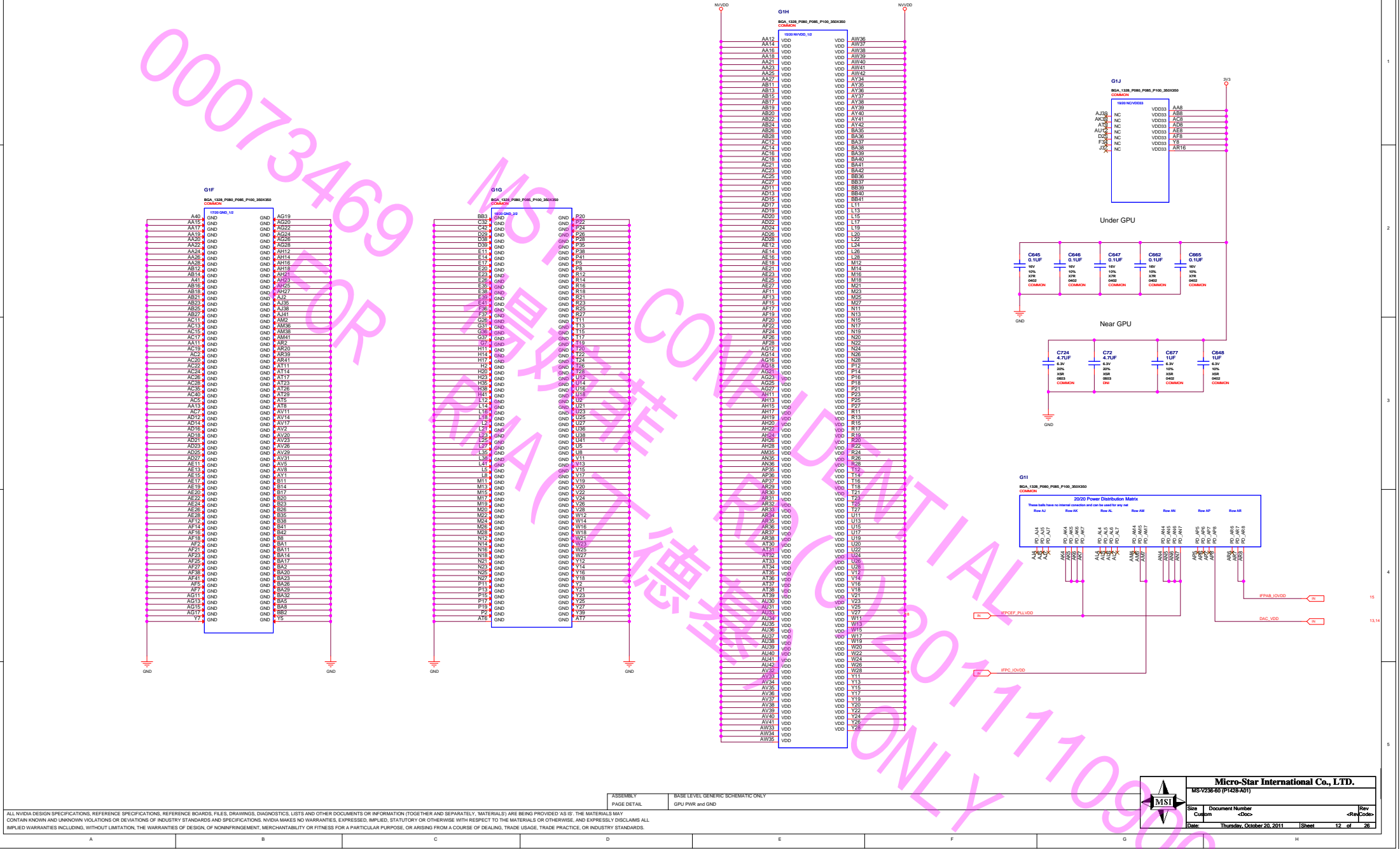
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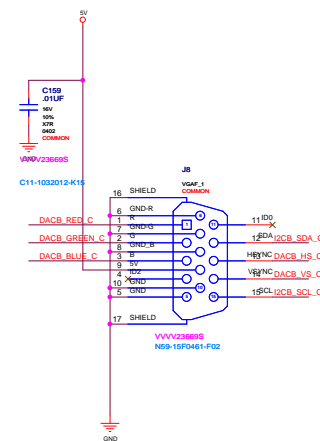
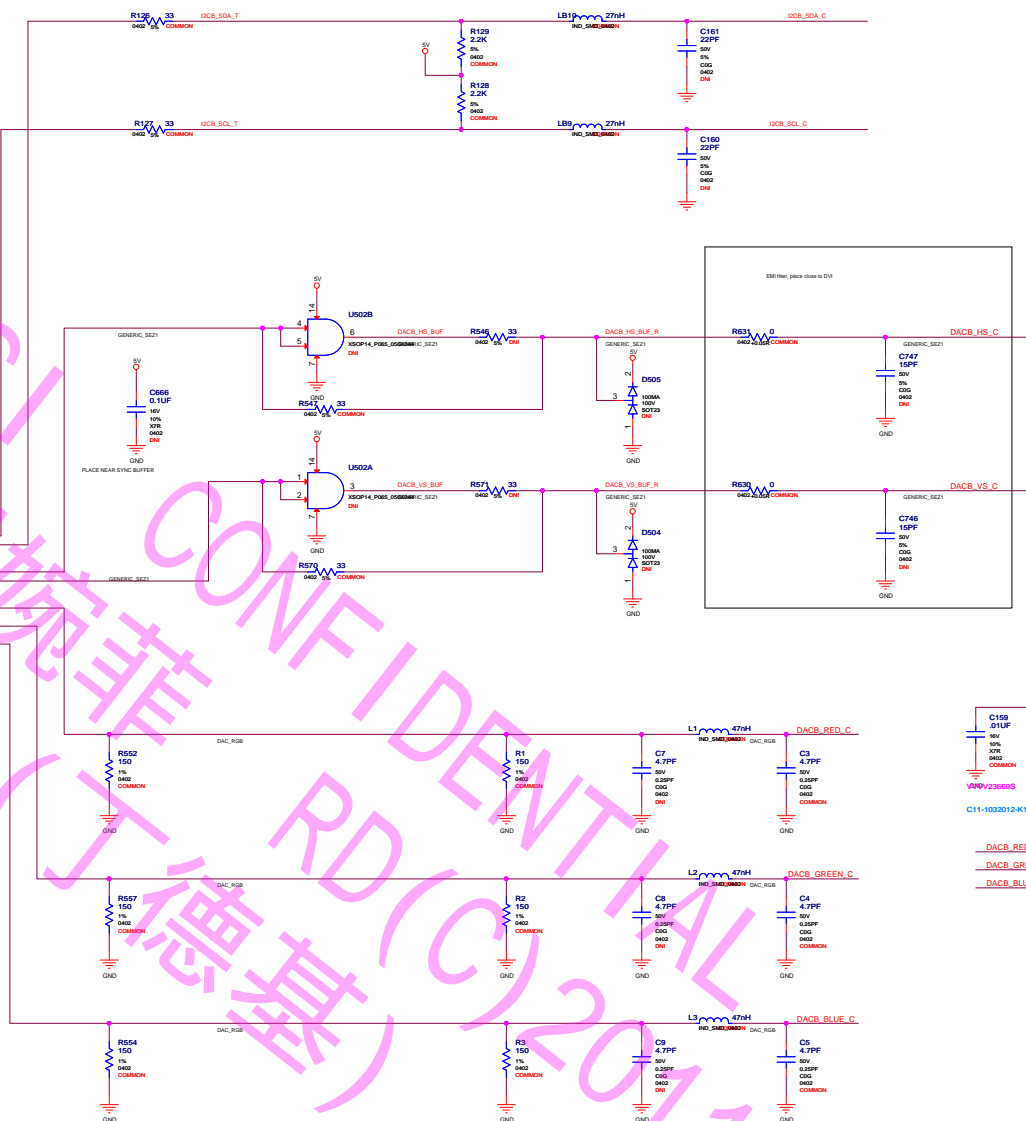



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ASSEMBLY PAGE DETAIL
BASE LEVEL GENERIC SCHEMATIC ONLY
GPU NVVDD Decoupling

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Remove DP


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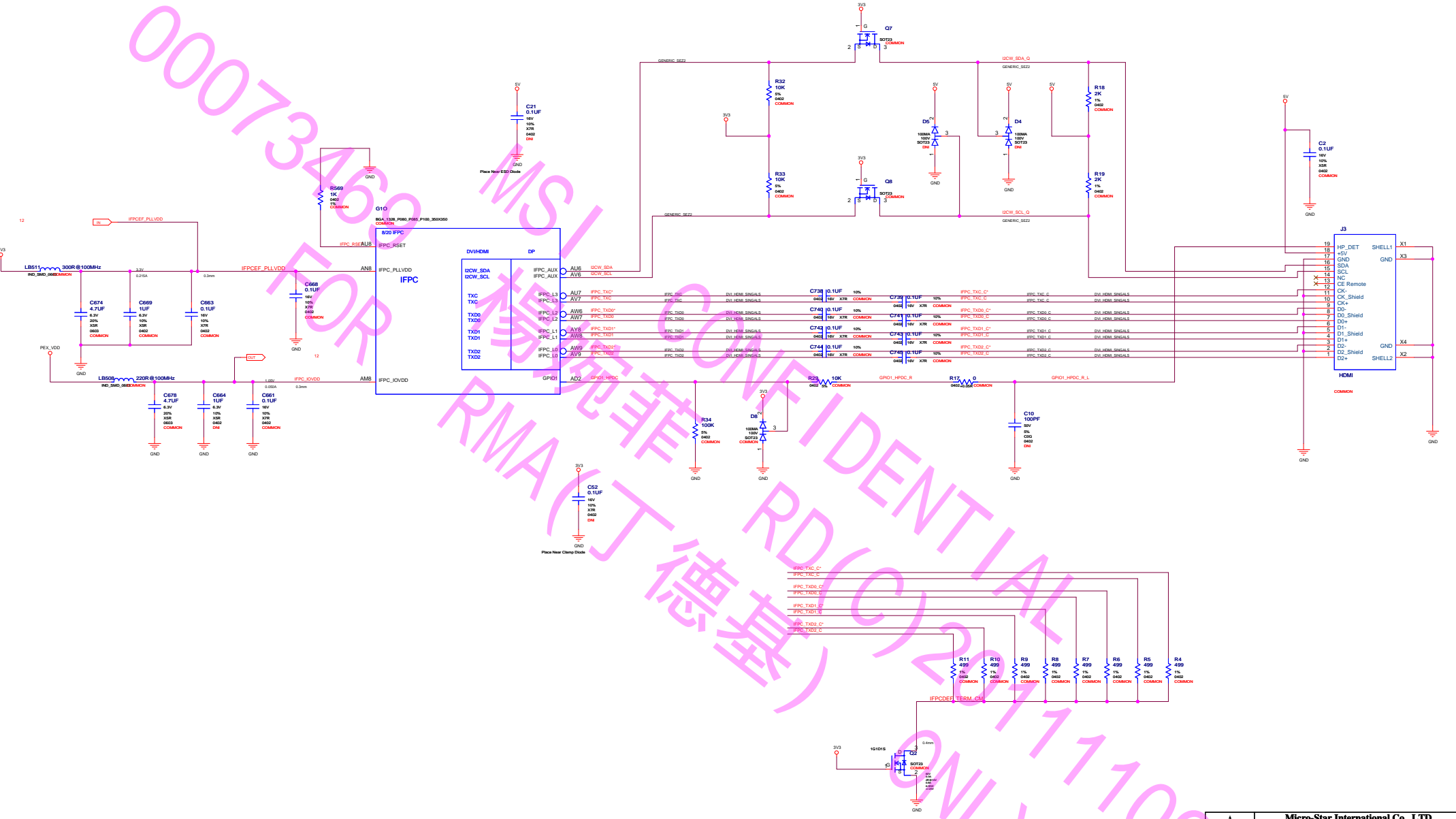
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PAGE DETAIL	IFPE DP(Mid) Option

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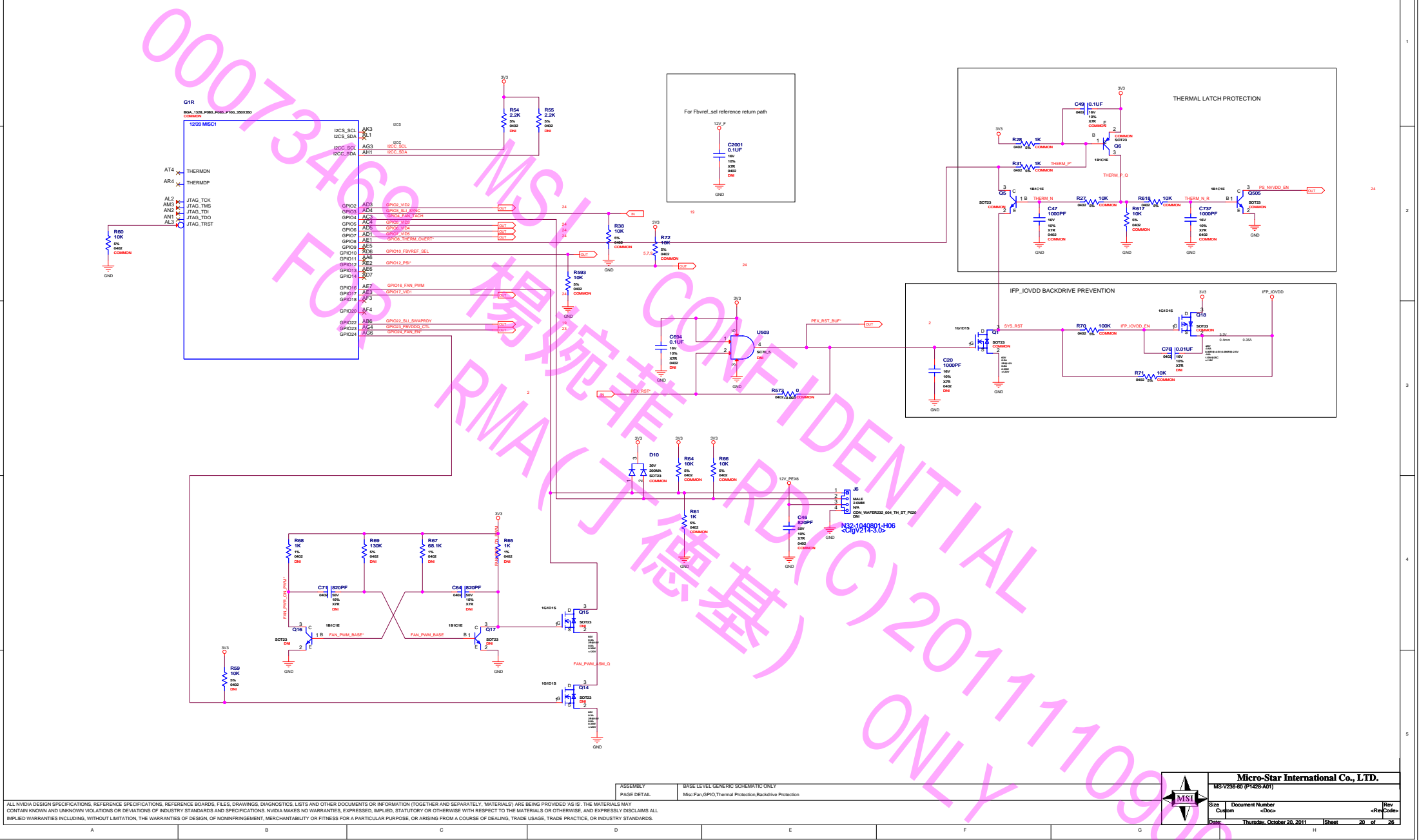
Change mini type HDMI connector to A type



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ASSEMBLY PAGE DETAIL BASE LEVEL GENERIC SCHEMATIC ONLY (IPFC mHDMI(North))

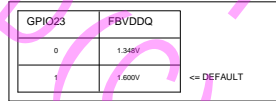
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PAGE DETAIL	Misc:Fan,GPIO,Thermal Protection,Backdrive Protection

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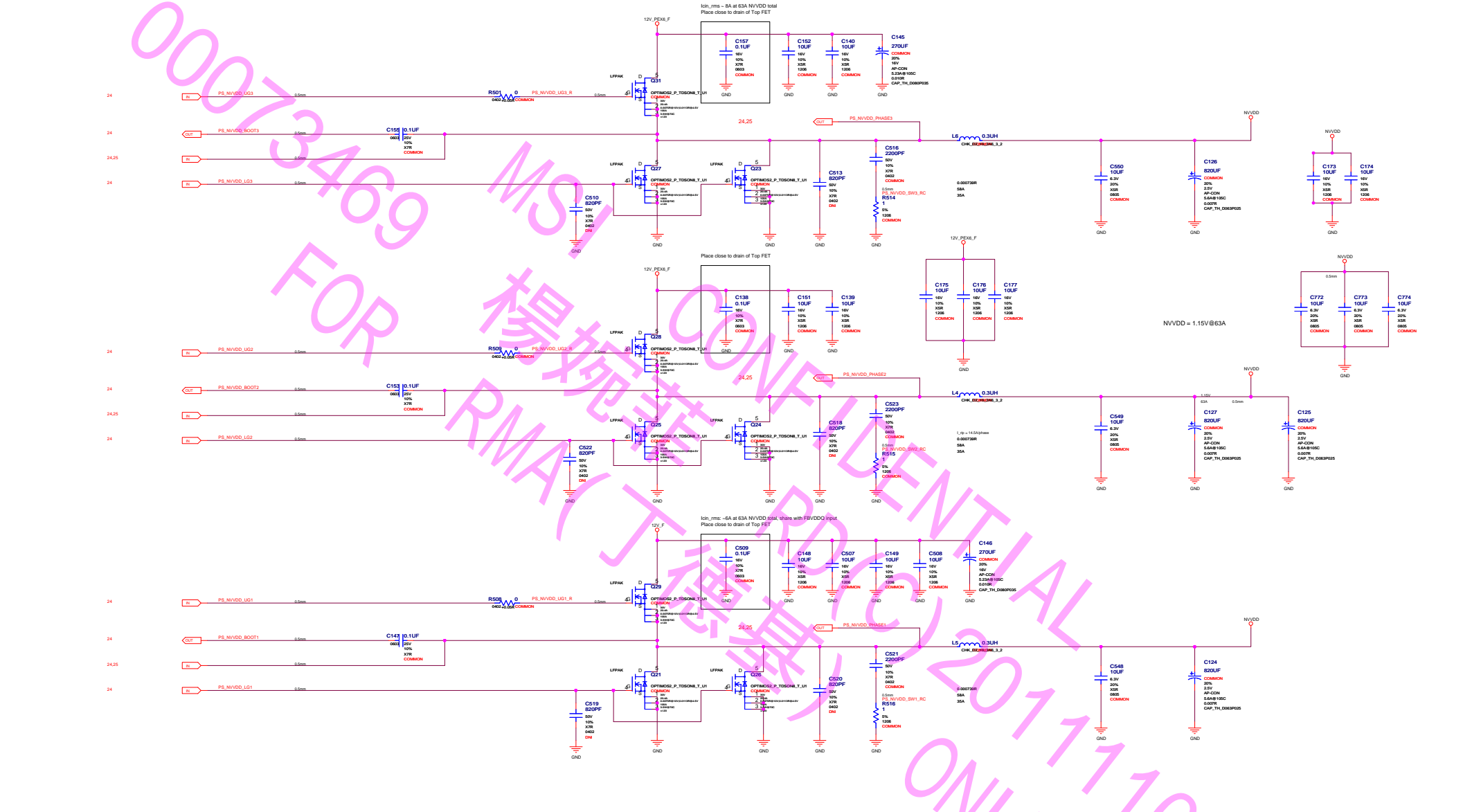

$$V_{out} = 0.6V * (1 + 1.87K / 1.5K) = 1.348V$$

$$V_{out} = 0.6V * (1 + 1.87K / (1.5K // 4.42K)) = 1.600V$$

Page 25:PS:NVVDD Mosfet


1. Low Side MOSFET change to PowerPAK NIKO 0903 vs. 0403

2. Output Choke change to 0.3uH



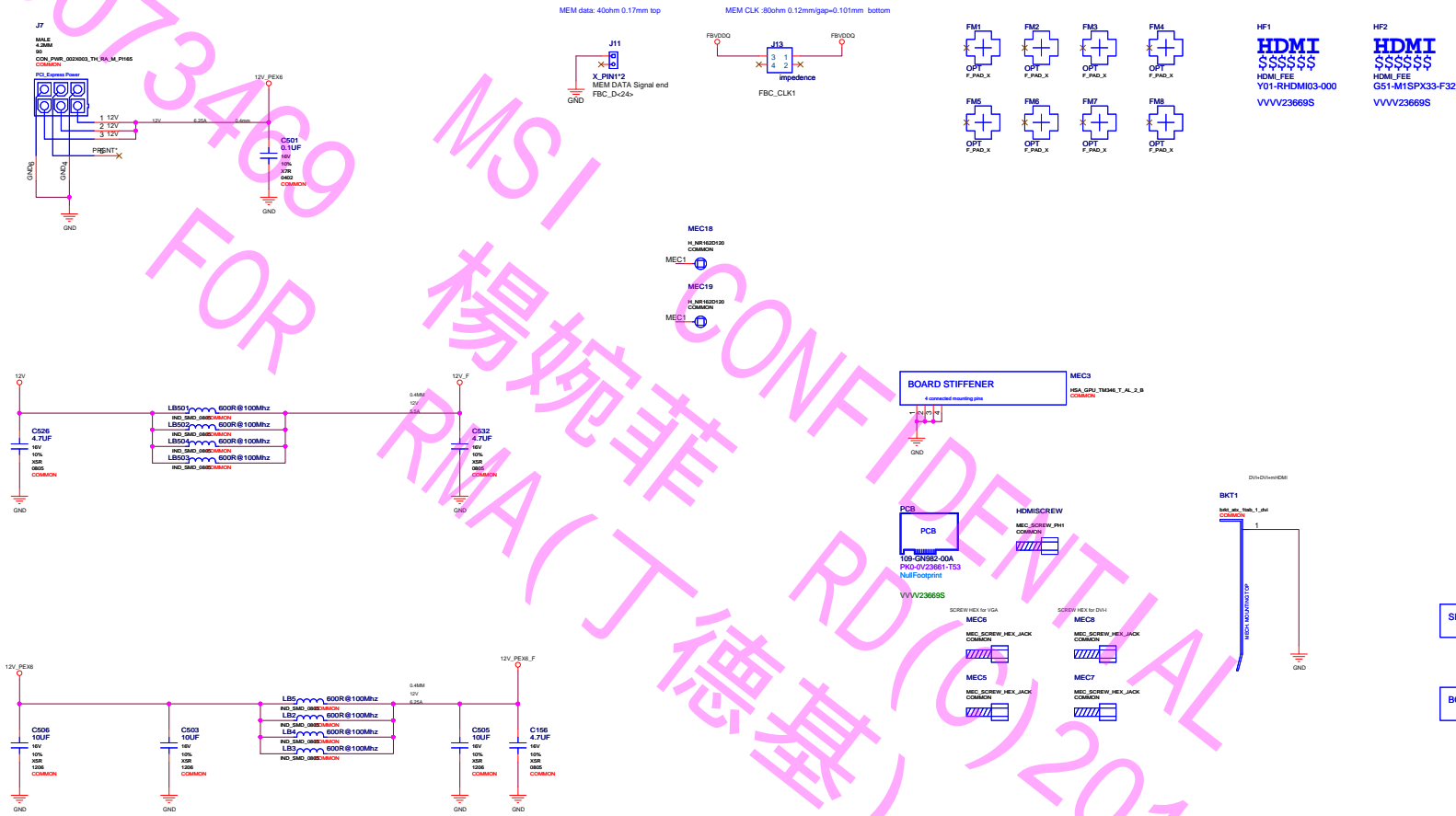
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PS:NVVDD Mosfet



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ASSEMBLY PAGE DETAIL
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Power Inputs, Bracket and Thermal Solution

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