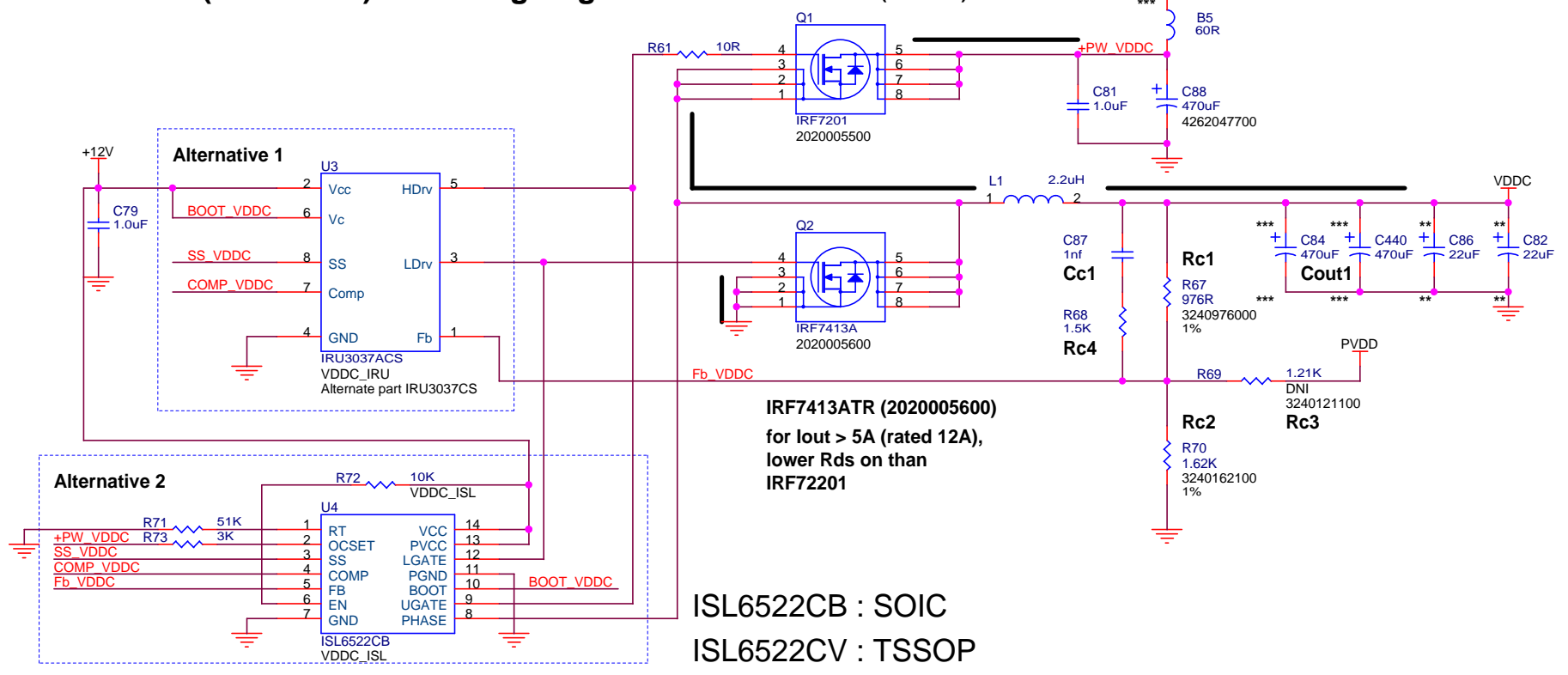


# VDDC(+1.5/1.25V) Switching Regulator

IRF7201TR (2020005500)  
for Iout > 5A (rated 7A)



## Compensation Options for VDDC Regulator

**Alt. Compensation 1**  
This compensation circuit is simplified and will only work with the IRU3037(A) Regulator

**Common**

**Alt. Compensation 2**  
This is required for the ISL6522CB regulator, and provides maximum regulation speed for IRU3037 Regulator

**Note:** Alternative Compensation Circuit 2 will only work if Rc1 is a 1k Ohm Resistor. Alternative Compensation Circuit 1 has no requirements for the divider circuit.

**Cout1** 470uF thru hole capacitor (P/N 4051047700) has 30mR ESR where as 470uF SMT (P/N 4262047700) capacitor has 150mR ESR. For current below 4.5A, 1 thru 470uF is enough.

\*\*\* Indicates number of vias required for the connection

Part	INSTALL	Compensation Circuit	DO NOT INSTALL
IRU3037 IRU3037A	Alternative1	Common, and Either Alt. Compensation 1, or Alt. Compensation 2	Cc4, Rc6 Alternative 2
ISL6522CB	Alternative2	Common and Alt. Compensation 2	Alternative1

**Regulator for VDD\_CORE/VDDC (R200 Core)**  
Vin =5V AGP  
Vout = 1.5V  
Iout = 5A MAX (load consumption)

Table for IRU3037 and IRU3037A with Alt. Compensation Circuit 1

Part		Rc1	Rc2	Rc3
IRU3037	0.9V	887R 1%	DNI	1.40K 1%
	1.2V	110R 1%	DNI	1.21K 1%
	1.25V	1.00K 1%	DNI	DNI
	1.4V	511R 1%	6.49K 1%	DNI
	1.5V	1.33K 1% 511R 1%	6.49K 1% 2.43K 1%	DNI DNI
IRU3037A	0.9V	1.00K 1%	8.06K 1%	DNI
	1.2V	1.00K 1%	2.00K 1%	DNI
	1.25V	976R 1% 1.00K 1%	1.62K 1% 1.78K 1%	DNI DNI
	1.5V	1.00K 1%	1.15k 1%	DNI

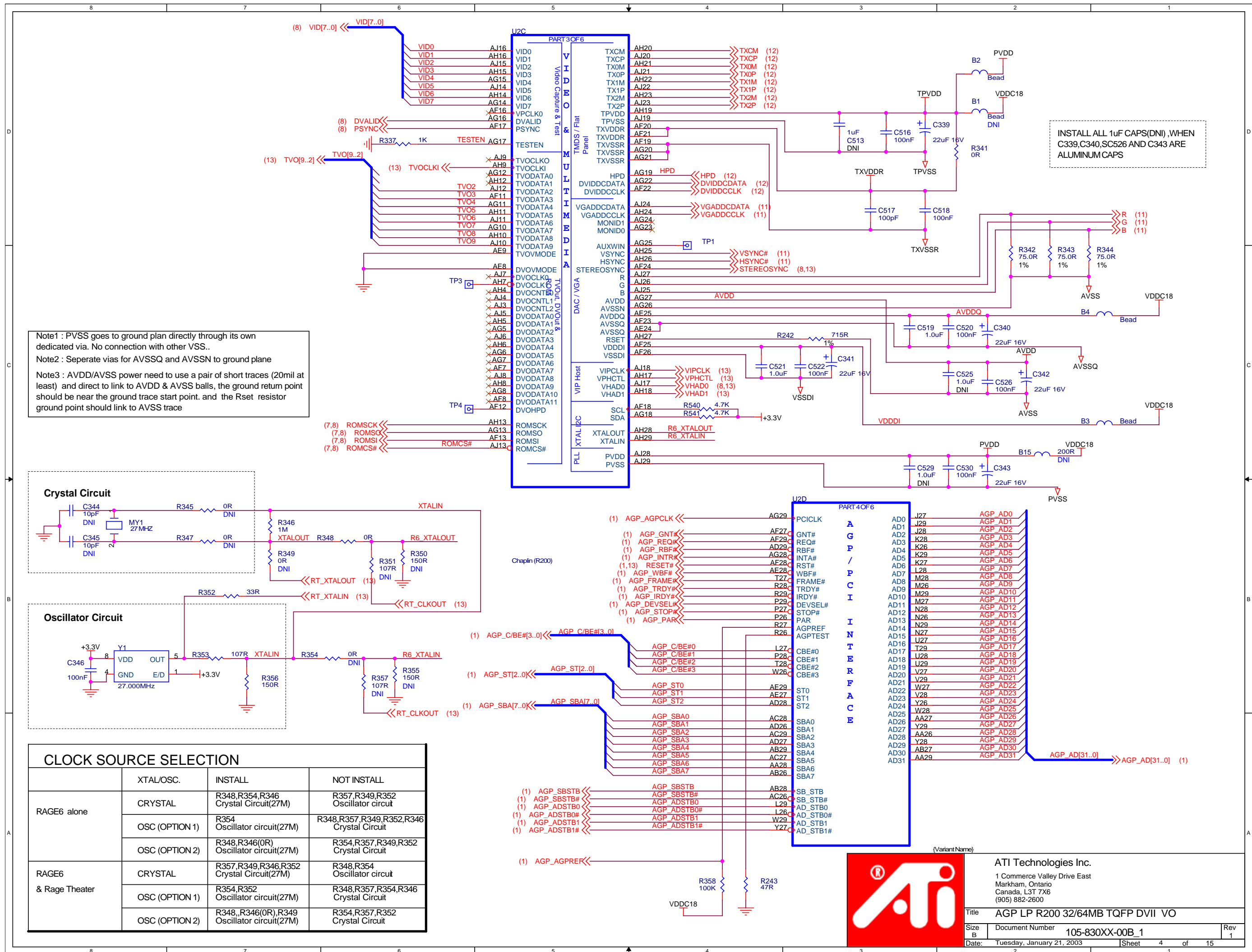
Table for IRU3037, IRU3037A and ISL6522 with Alt. Compensation Circuit 2

Part		Rc1	Rc2	Rc3
IRU3037	0.9V	1.00K 1%	DNI	1.56K 1%
	1.2V	1.00K 1%	DNI	11K 1%
	1.25V	1.00K 1%	DNI	DNI
	1.5V	1.00K 1%	4.99K 1%	DNI
IRU3037A ISL6522C	0.9V	1.00K 1%	8.06K 1%	DNI
	1.2V	1.00K 1%	2.00K 1%	DNI
	1.25V	1.00K 1%	1.78K 1%	DNI
	1.5V	1.00K 1%	1.15k 1%	DNI

ATI Technologies Inc.  
1 Commerce Valley Drive East  
Markham, Ontario  
Canada, L3T 7X6  
(905) 882-2600

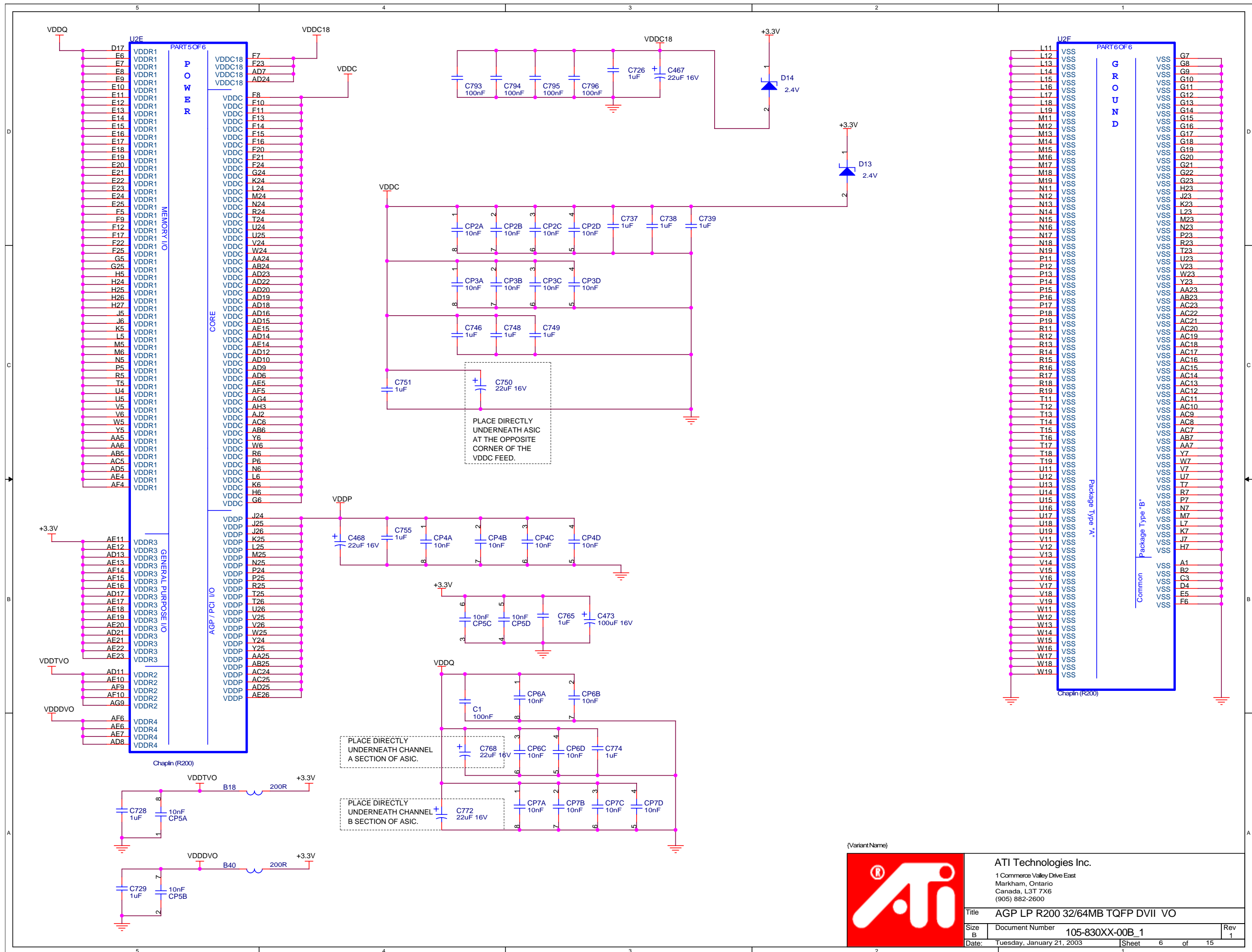
Title AGP LP R200 32/64MB TQFP DVII VO  
Size B Document Number 105-830XX-00B\_1 Rev 1  
Date: Tuesday, January 21, 2003 Sheet 2 of 15











(Variant Name)



ATI Technologies Inc.

1 Commerce Valley Drive East  
Markham, Ontario  
Canada, L3T 7X6  
(905) 882-2600

Title AGP LP R200 32/64MB TQFP DVII VO

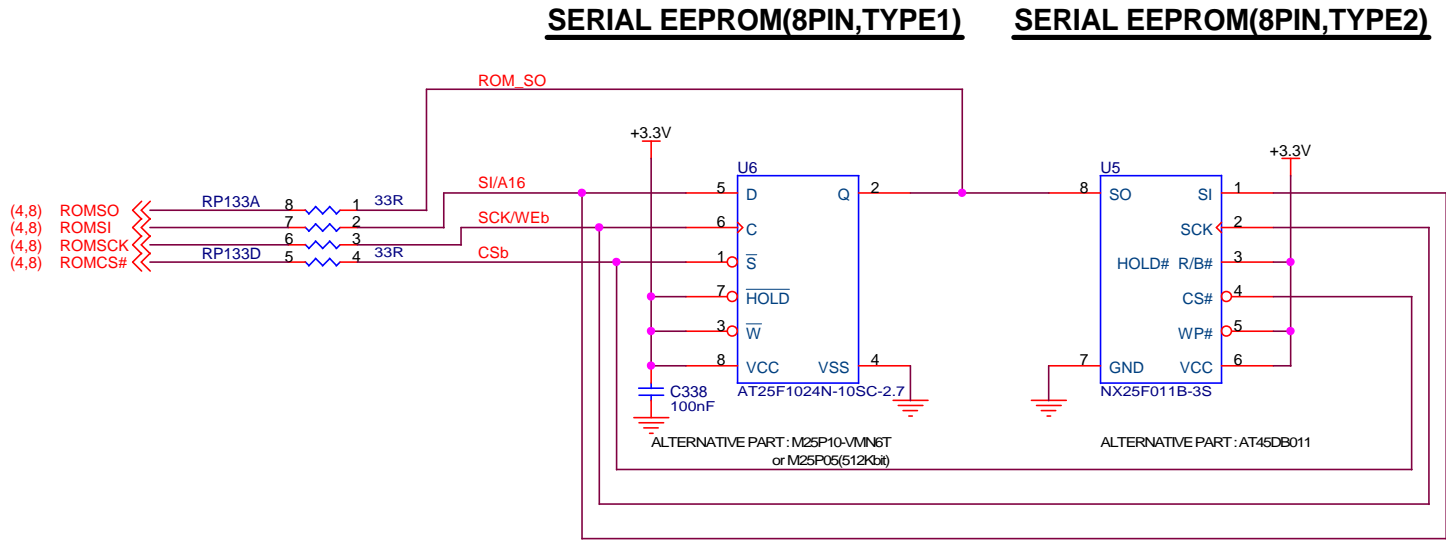
Size B Document Number 105-830XX-00B\_1

Date: Tuesday, January 21, 2003

Sheet 6 of 15

Rev 1

BIOS



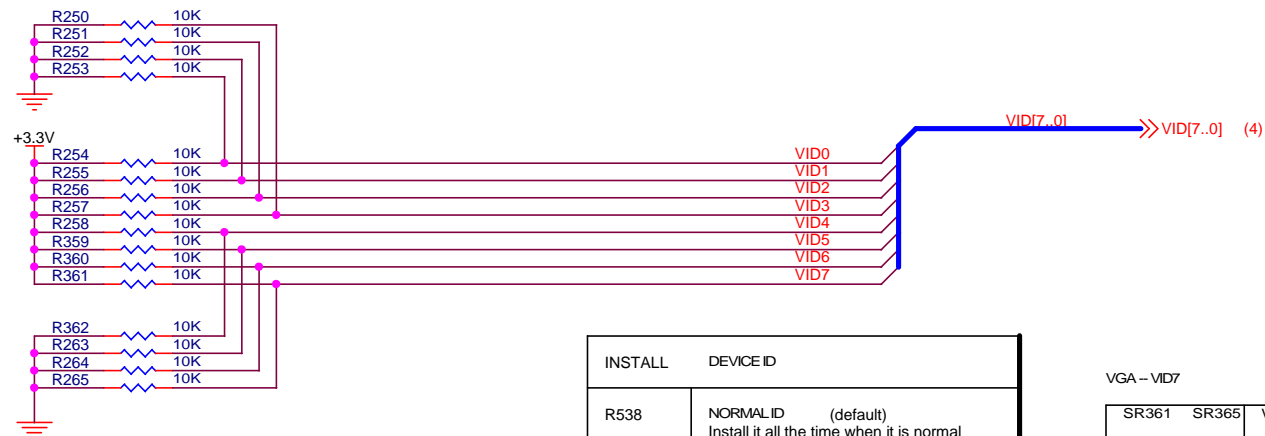
{Variant Name}



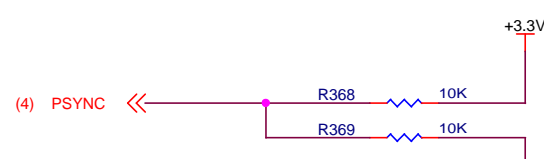
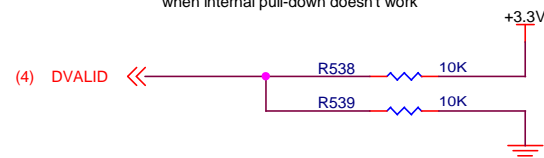
ATI Technologies Inc.  
1 Commerce Valley Drive East  
Markham, Ontario  
Canada, L3T 7X6  
(905) 882-2600

Title AGP LP R200 32/64MB TQFP DVII VO		
Size B	Document Number 105-830XX-00B_1	Rev 1
Date: Tuesday, January 21, 2003	Sheet 7	of 15

# STRAPS



Install SR350-SR353 and SR362-SR365 when internal pull-down doesn't work



INSTALL	DEVICE ID
R538	NORMAL ID (default) Install it all the time when it is normal device ID
R539	WORKSTATION ID

INSTALL	ID_DISABLE
R369	NORMAL OPERATION (default) Install it when internal pull-down doesn't work
R368	CHIP SHUTSDOWN

INSTALL	VIP DEVICE
R370	NO SLAVE VIP (DEFAULT) Install it when internal pull-up doesn't work
	SLAVE VIP VIP device will drive low when VIP is attached.

INSTALL	INTERRUPT
SR370	ENABLE (Default) Install it when internal pull-down doesn't work
SR368	DISABLE

VGA - VID7

SR361	SR365	VGA_DISABLE
DNI	10K	ENABLE (DEFAULT)
10K	DNI	DISABLE

AGPFBSKEW - VID(1:0)

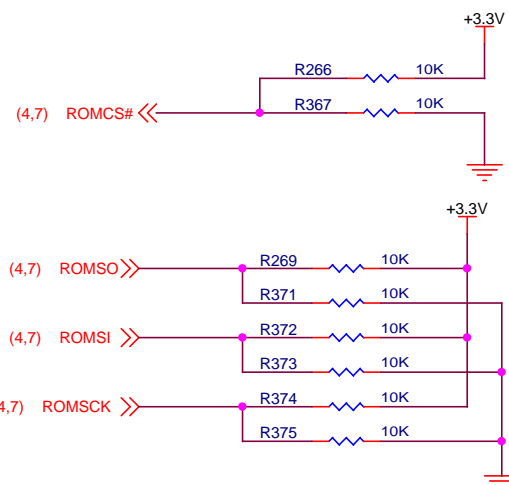
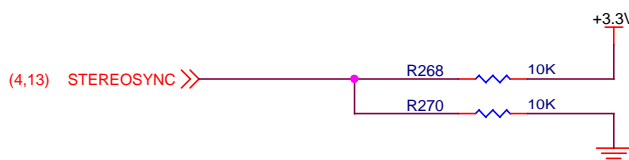
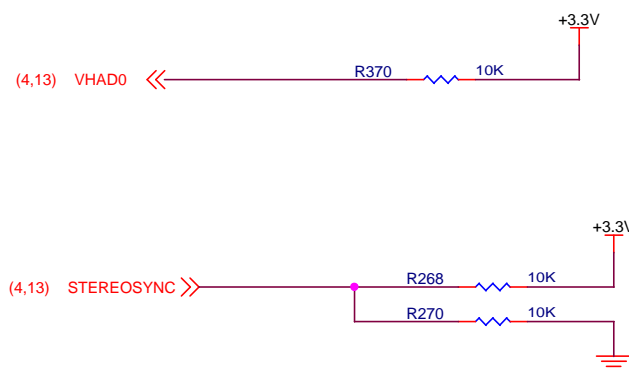
SR355	SR352	SR354	SR353	
DNI	10K	DNI	10K	DEFAULT
X	X	X	X	TBD

X1CLK\_SKEW - VID(3:2)

SR357	SR350	SR356	SR351	
DNI	10K	DNI	10K	DEFAULT
X	X	X	X	TBD

BUSCFG -- VID(6:4)

VID6		VID5		VID4		DESCRIPTION
SR360	SR364	SR359	SR363	SR358	SR362	
DNI	10K	DNI	10K	DNI	10K	AGP 4X, PLL CLK, IDSEL = AD16 (Default)
DNI	10K	DNI	10K	10K	DNI	AGP 4X, PLL CLK, IDSEL = AD17
DNI	10K	10K	DNI	DNI	10K	AGP 1X/2X, PLL CLK, IDSEL = AD16
DNI	10K	10K	DNI	10K	DNI	AGP 1X/2X, PLL CLK, IDSEL = AD17
10K	DNI	DNI	10K	DNI	10K	PCI 66MHz, PLL CLK
10K	DNI	DNI	10K	10K	DNI	PCI 33MHz, 3.3V, REF CLK
10K	DNI	10K	DNI	DNI	10K	AGP 1X, REF CLK, IDSEL = AD16
10K	DNI	10K	DNI	10K	DNI	AGP 1X, REF CLK, IDSEL = AD17

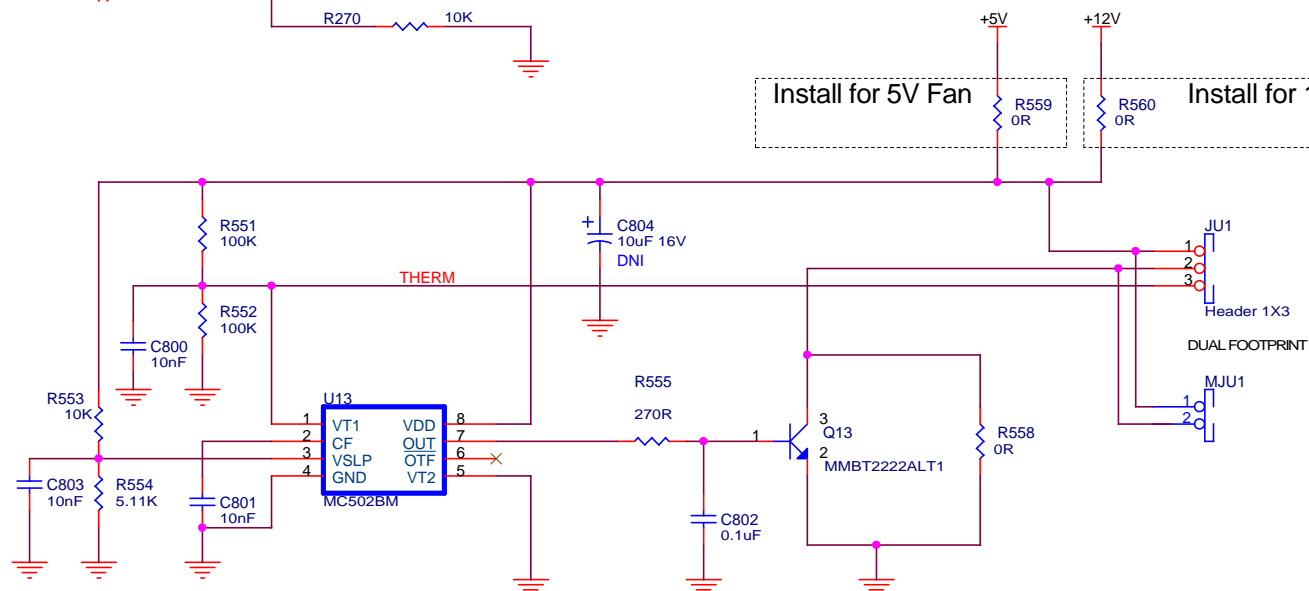


SR367	SR369	SR372	SR374	ROMIDCFG[3:0]
INSTALL	DNI	DNI	DNI	No ROM, CHG ID = 00
INSTALL	DNI	INSTALL	DNI	No ROM, CHG ID = 01
INSTALL	INSTALL	DNI	DNI	No ROM, CHG ID = 10
INSTALL	INSTALL	INSTALL	DNI	No ROM, CHG ID = 11
DNI	DNI	DNI	DNI	Parallel ROM on TVO (default)
DNI	DNI	DNI	INSTALL	Serial AT25F1024, ID's from ROM
DNI	DNI	INSTALL	DNI	Serial AT45DB011, ID's from ROM
DNI	DNI	INSTALL	INSTALL	Serial ST M25P05/ M25P10, ID's from ROM
DNI	INSTALL	DNI	DNI	RESERVED
DNI	INSTALL	INSTALL	DNI	Parallel ROM on DVO

\* Install SR366, SR371, SR373 and SR375 when internal pull-up/down don't work properly.

Install for 5V Fan

Install for 12V Fan



(Variant Name)



ATI Technologies Inc.  
1 Commerce Valley Drive East  
Markham, Ontario  
Canada, L3T 7X6  
(905) 882-2600

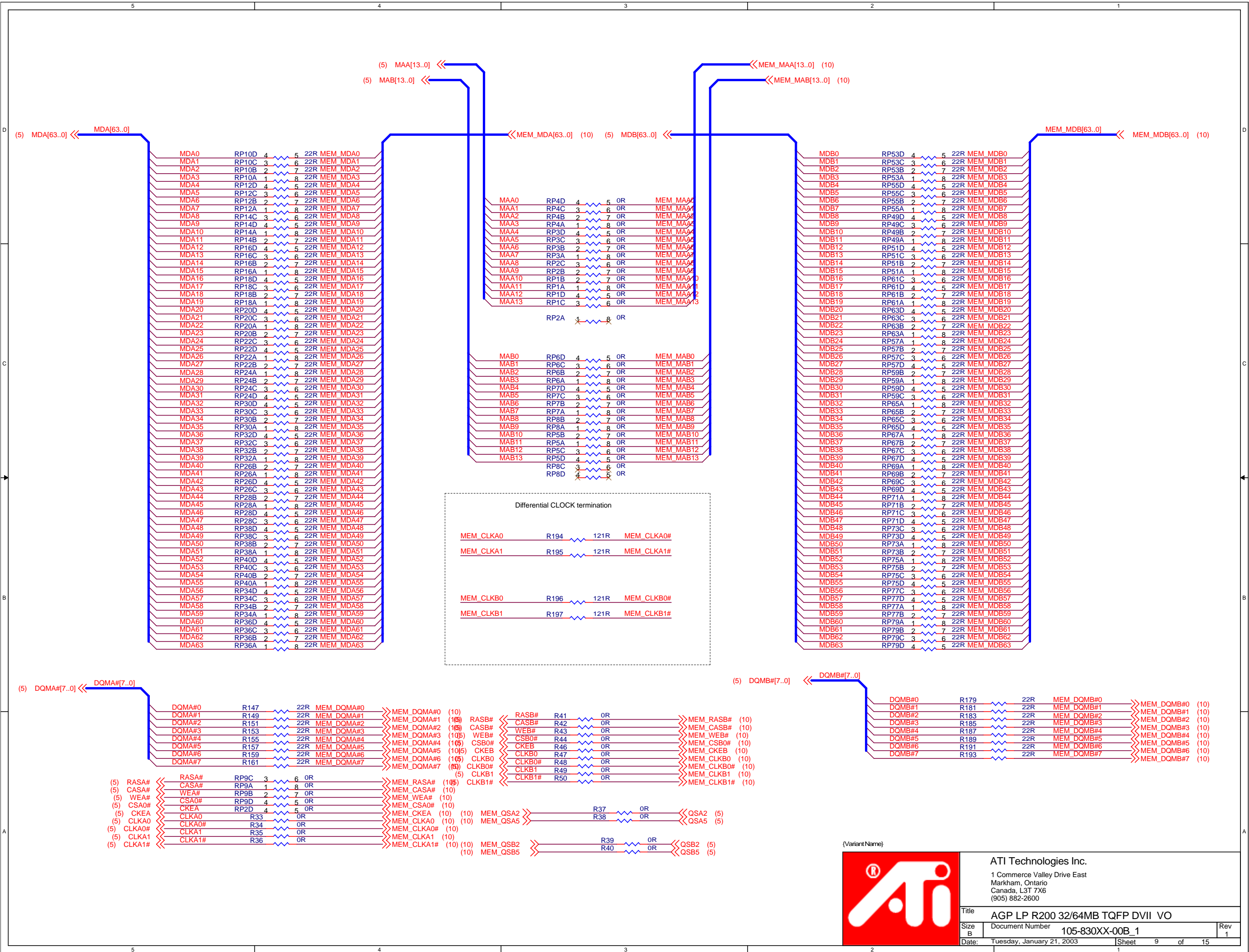
Title AGP LP R200 32/64MB TQFP DVII VO

Size B Document Number 105-830XX-00B\_1

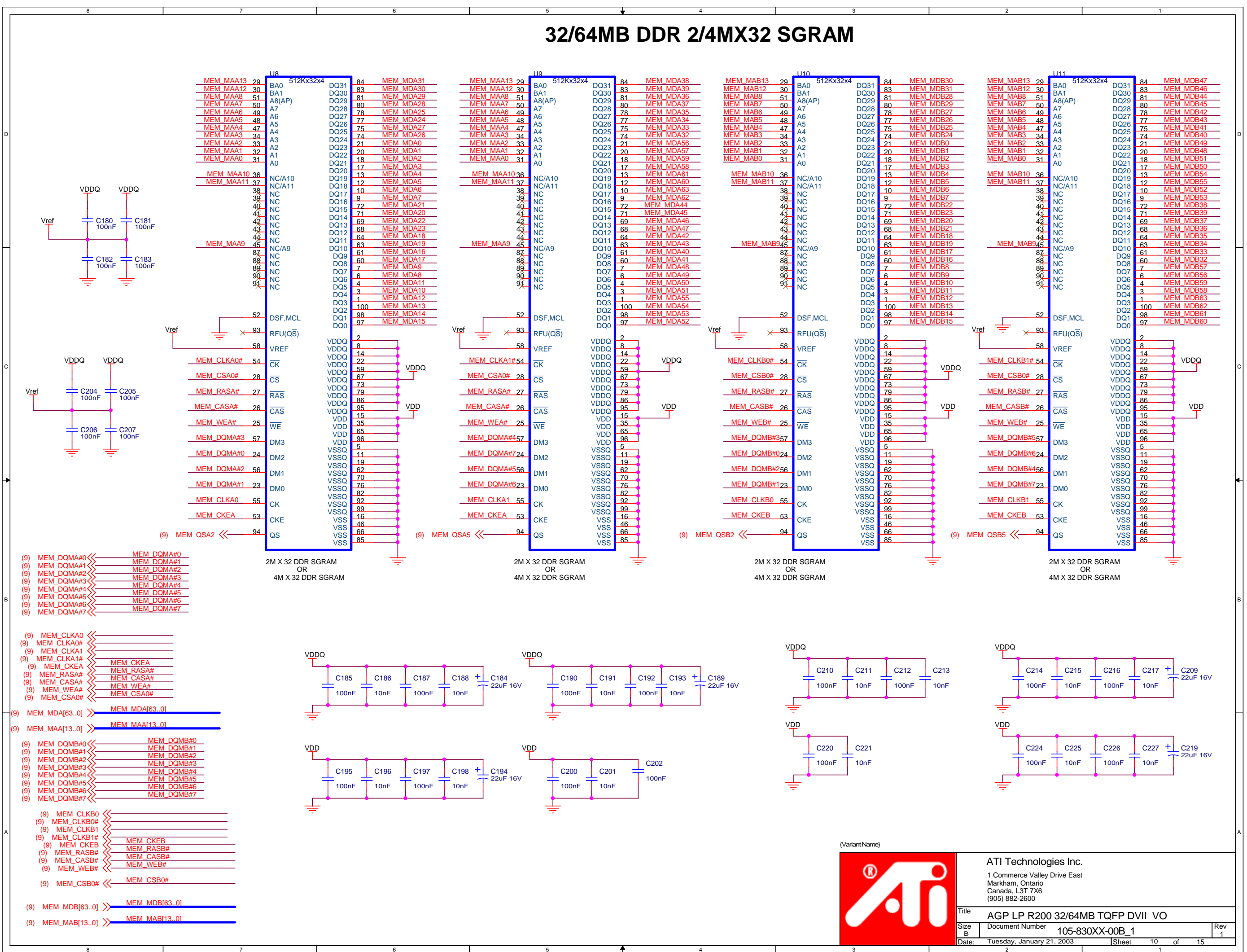
Date: Tuesday, January 21, 2003 Sheet 8 of 15

Rev 1

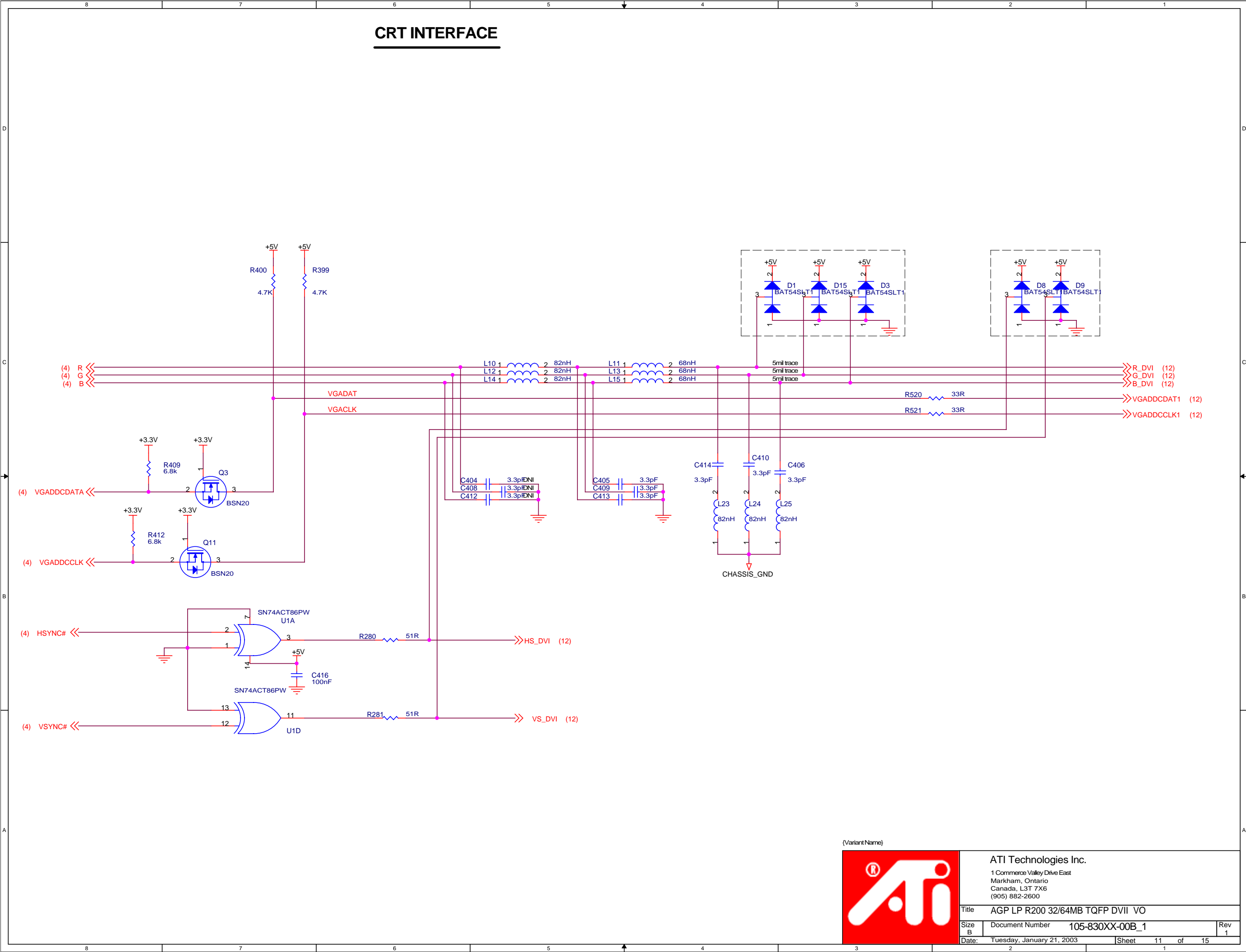




# 32/64MB DDR 2/4MX32 SGRAM



CRT INTERFACE

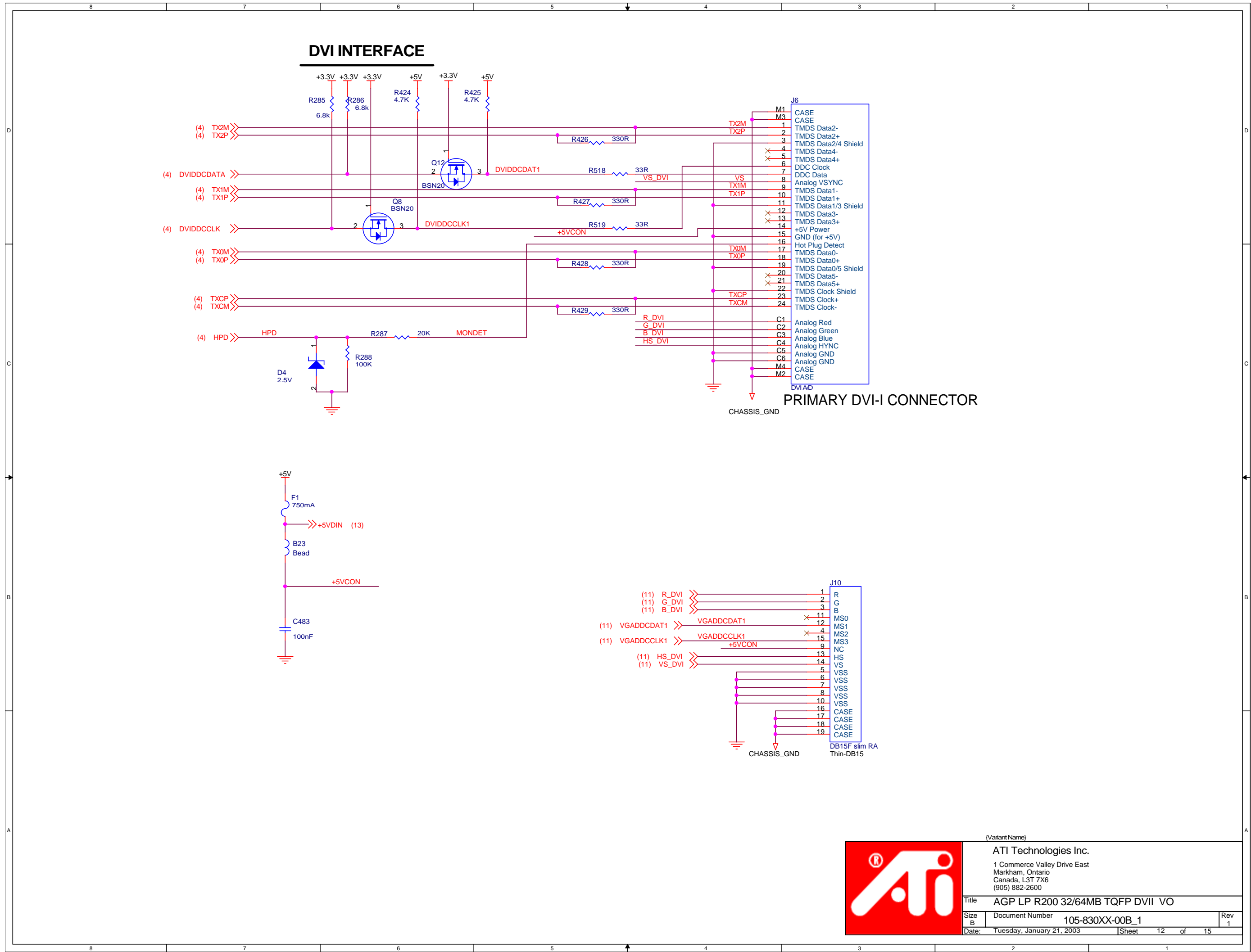


{VariantName}



ATI Technologies Inc.  
1 Commerce Valley Drive East  
Markham, Ontario  
Canada, L3T 7X6  
(905) 882-2600

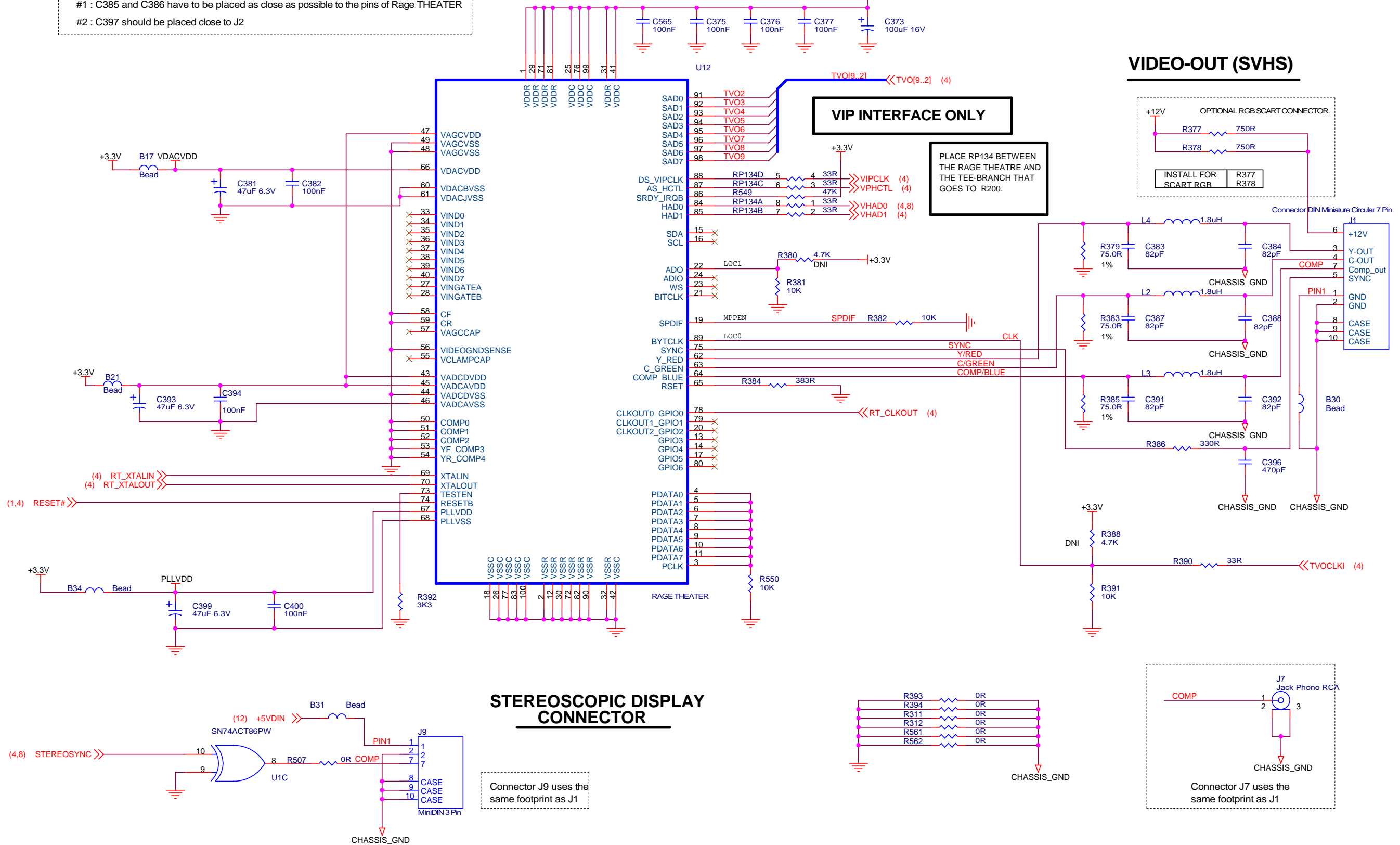
Title AGP LP R200 32/64MB TQFP DVII VO		
Size B	Document Number 105-830XX-00B_1	Rev 1
Date: Tuesday, January 21, 2003	Sheet 11	of 15



# Layout Guide line of THEATER

#1 : C385 and C386 have to be placed as close as possible to the pins of Rage THEATER

#2 : C397 should be placed close to J2

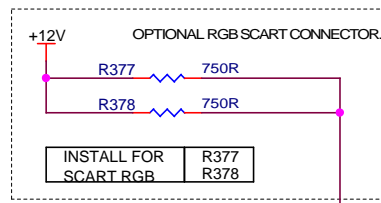


## STEREOSCOPIC DISPLAY CONNECTOR

Connector J9 uses the same footprint as J1

	Install	DNI
VIDEO-OUT(SVHS)	J1, B30, R508	J9, B31, R507
STEREOSCOPIC IMAGE	J9, B31, R507	J1, B30, R508

## VIDEO-OUT (SVHS)



PLACE RP134 BETWEEN THE RAGE THEATRE AND THE TEE-BRANCH THAT GOES TO R200.

(Variant Name)

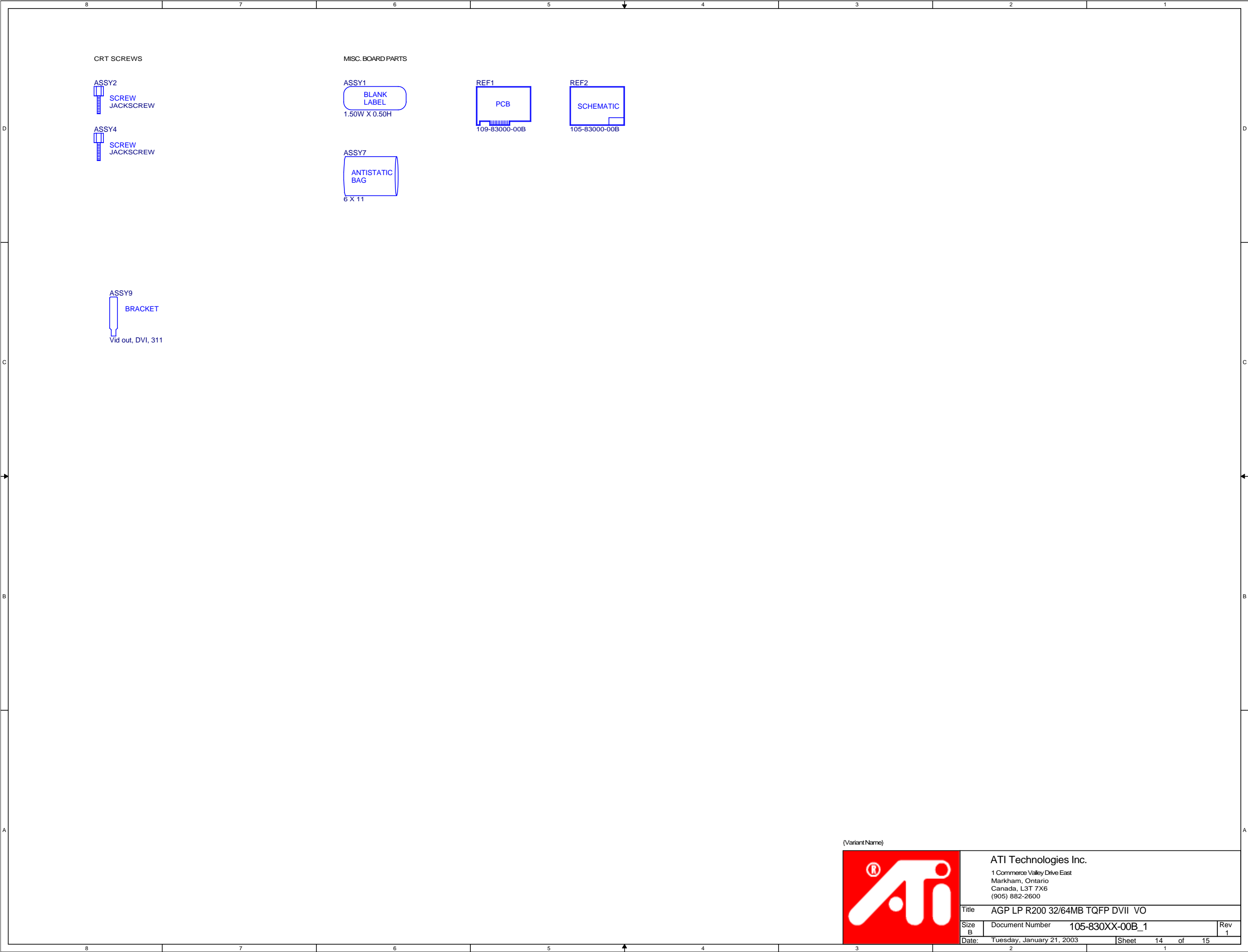


ATI Technologies Inc.


1 Commerce Valley Drive East  
Markham, Ontario  
Canada, L3T 7X6  
(905) 882-2600

Title	AGP LP R200 32/64MB TQFP DVII VO		
Size B	Document Number	105-830XX-00B_1	Rev 1
Date:	Tuesday, January 21, 2003	Sheet 13 of 15	





(Variant Name)



ATI Technologies Inc.  
1 Commerce Valley Drive East  
Markham, Ontario  
Canada, L3T 7X6  
(905) 882-2600

TitleAGP LP R200 32/64MB TQFP DVII VO


SizeB

Document Number105-830XX-00B\_1

Rev1

Date:Tuesday, January 21, 2003

Sheet14 of 15

[variant Name]								8	7	6	5	4	3	2	1	
								Title AGP LP R200 32/64MB TQFP DVII VO				Schematic No. 105-830XX-00B_1		Date: Tuesday, January 21, 2003		
								REVISION HISTORY								
D	Sch Rev	PCB Rev	Date	REVISION DESCRIPTION												D
	0	A	06/25/01	PROTOTYPE I.i.												
	1	B	05/10/02	1. Changes for MPVDD and PVDD regulator to decrease load on +5V 2. Changed the circuitry for speed controlled fan to allow +12V fan option. 3.Added brackets. 4. Changed VDDC and VDDC1.8 regulators												
C																C
B																B
A																A
8								7	6	5	4	3	2	1		