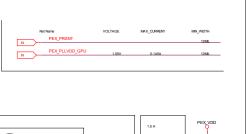
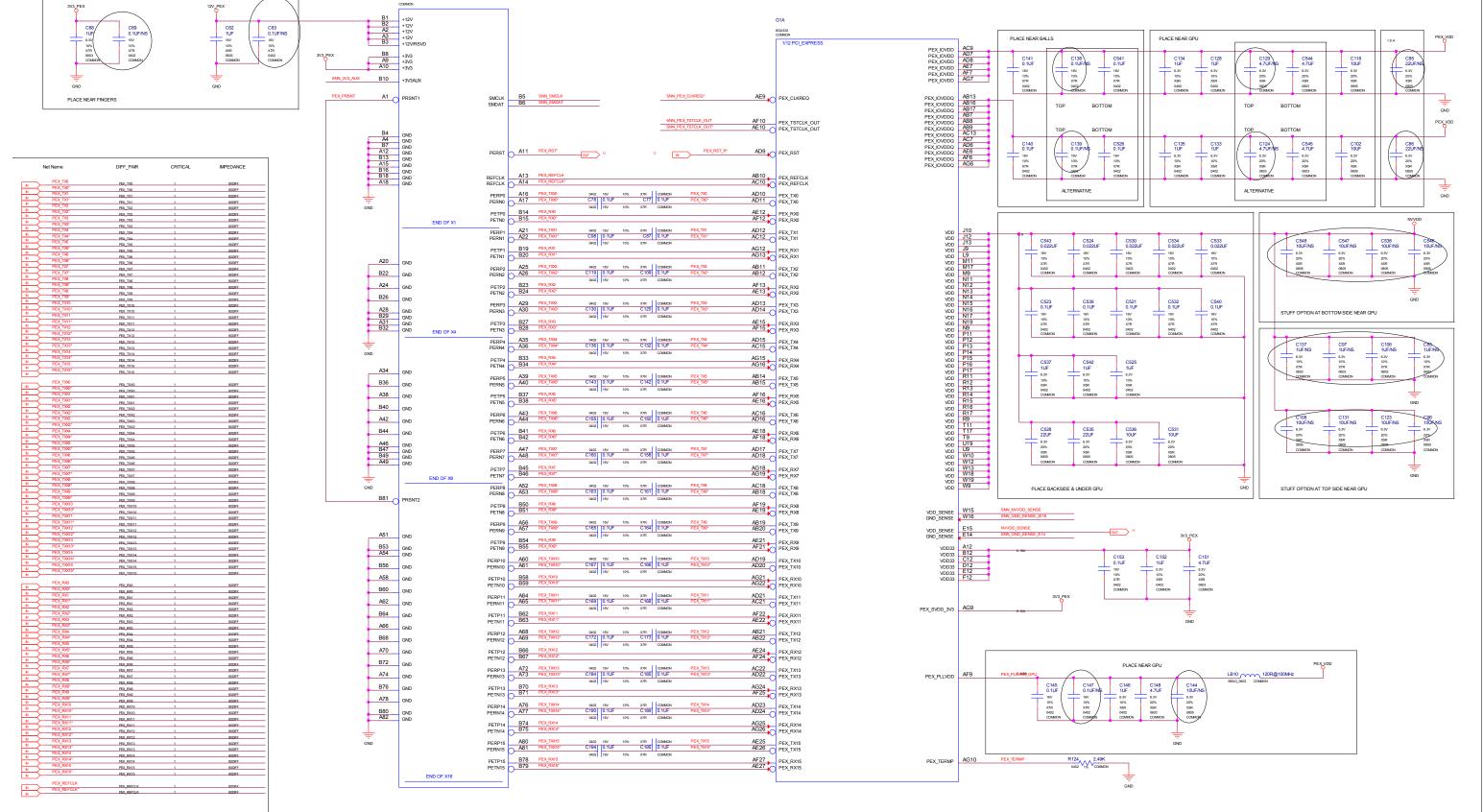
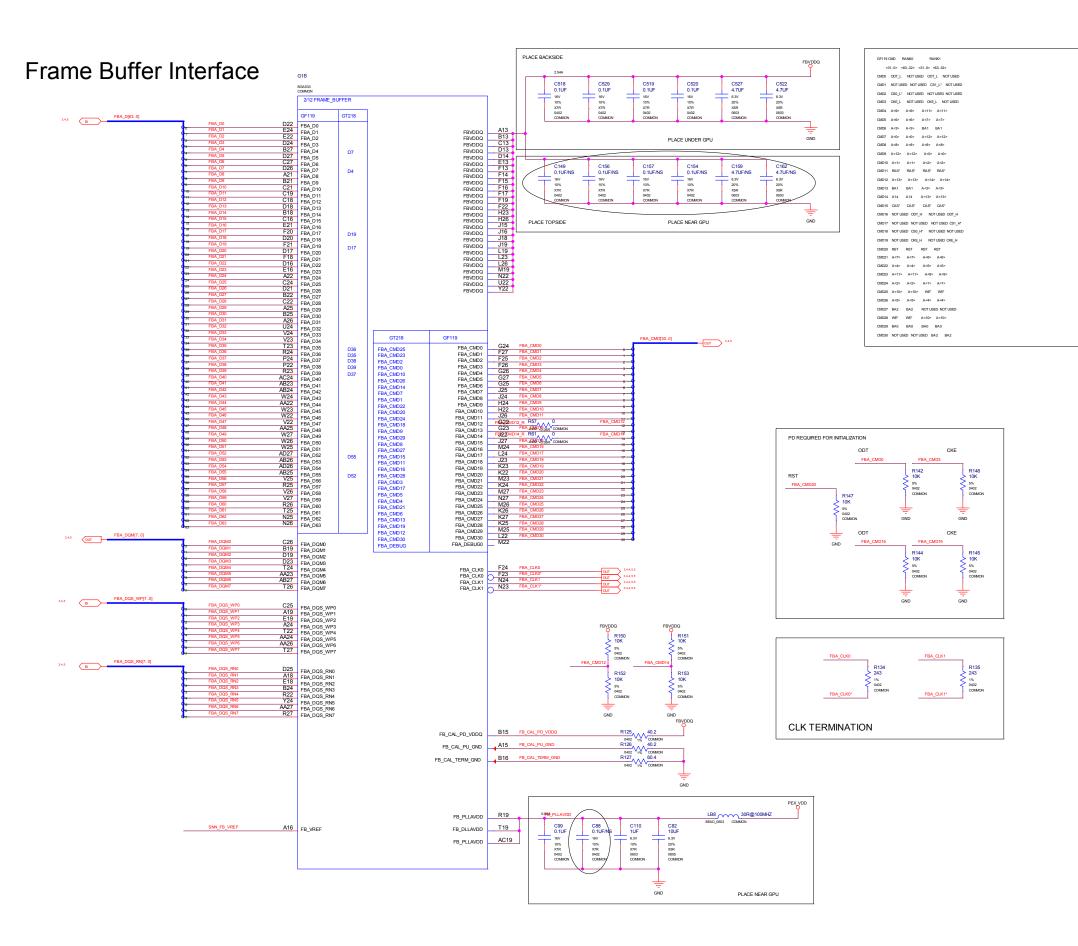
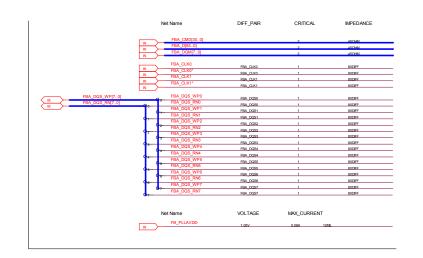
1)Support GT218 (G210)4pcs sDDR3X16 (128Mx16 for B Die,64Mx16 for A Die) 2)Support GF119 4pcs or 8pcs sDDR3X16 (64Mx16 or 128Mx16) 3)Fan confirm

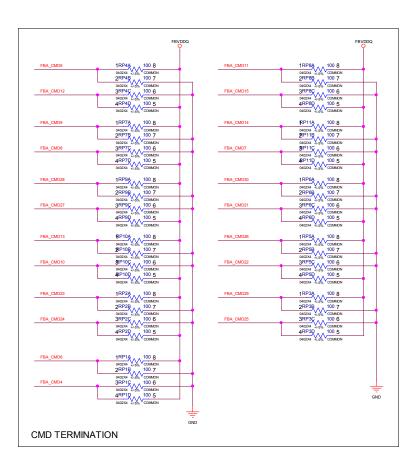
#### PCI Express Interface

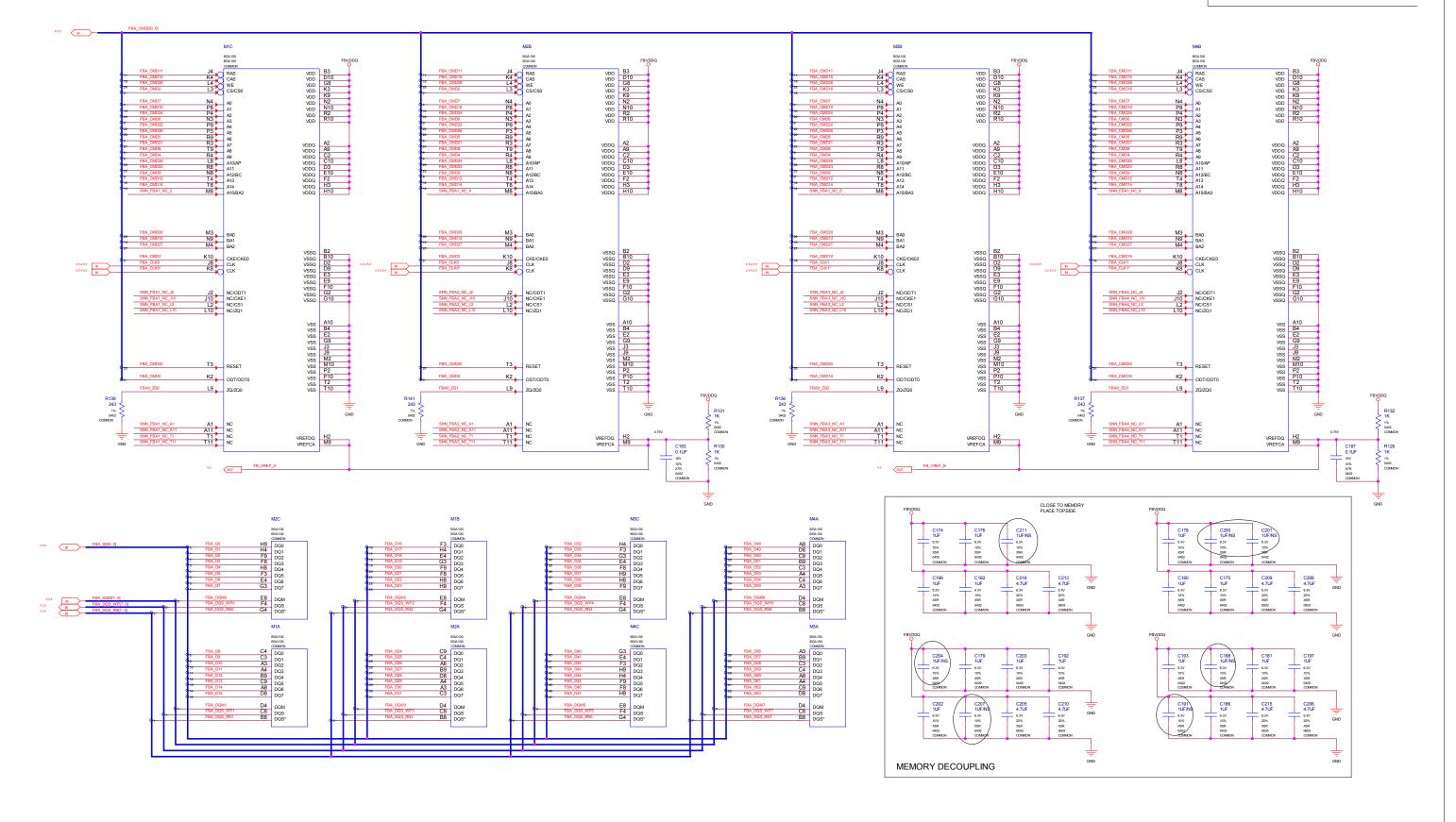




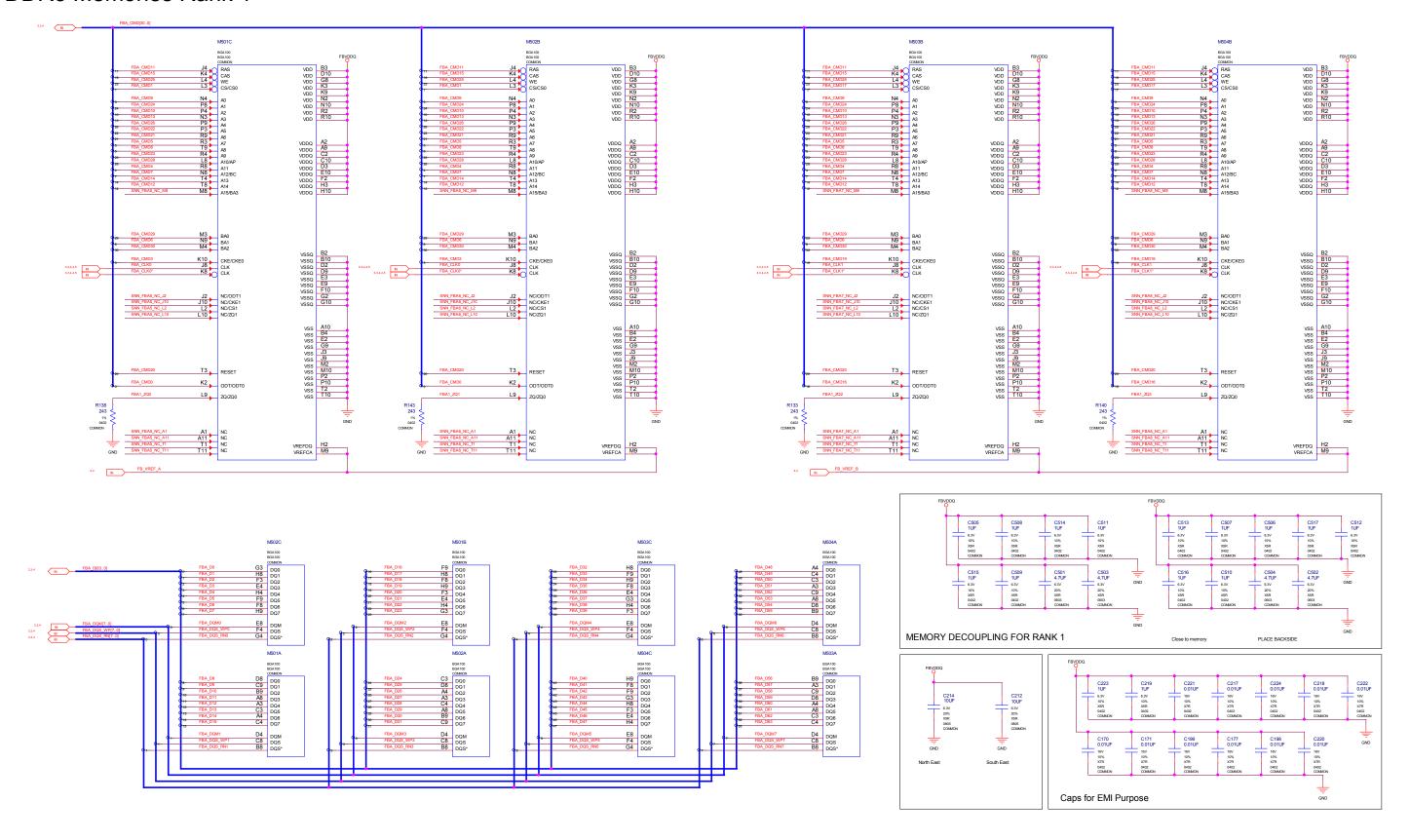




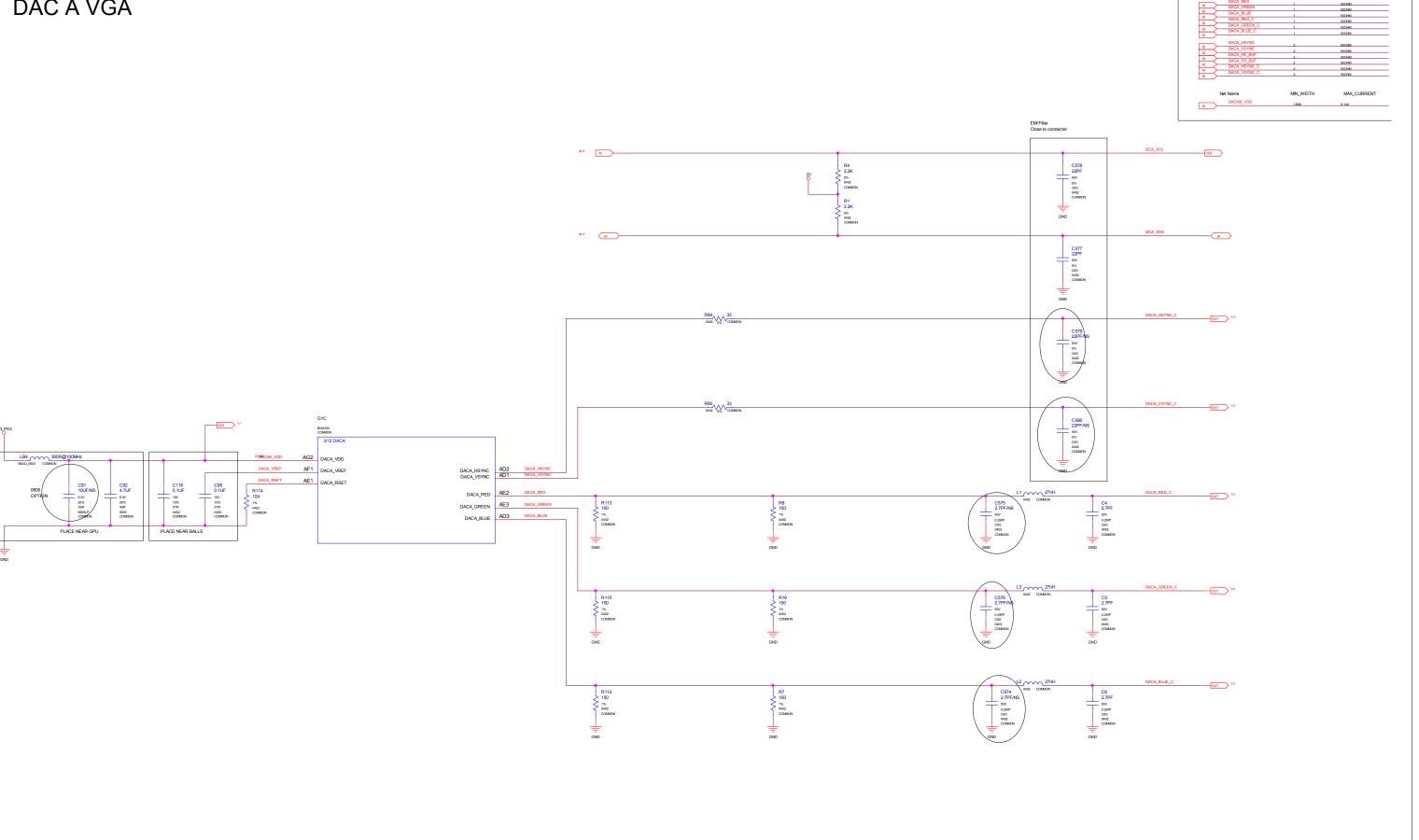


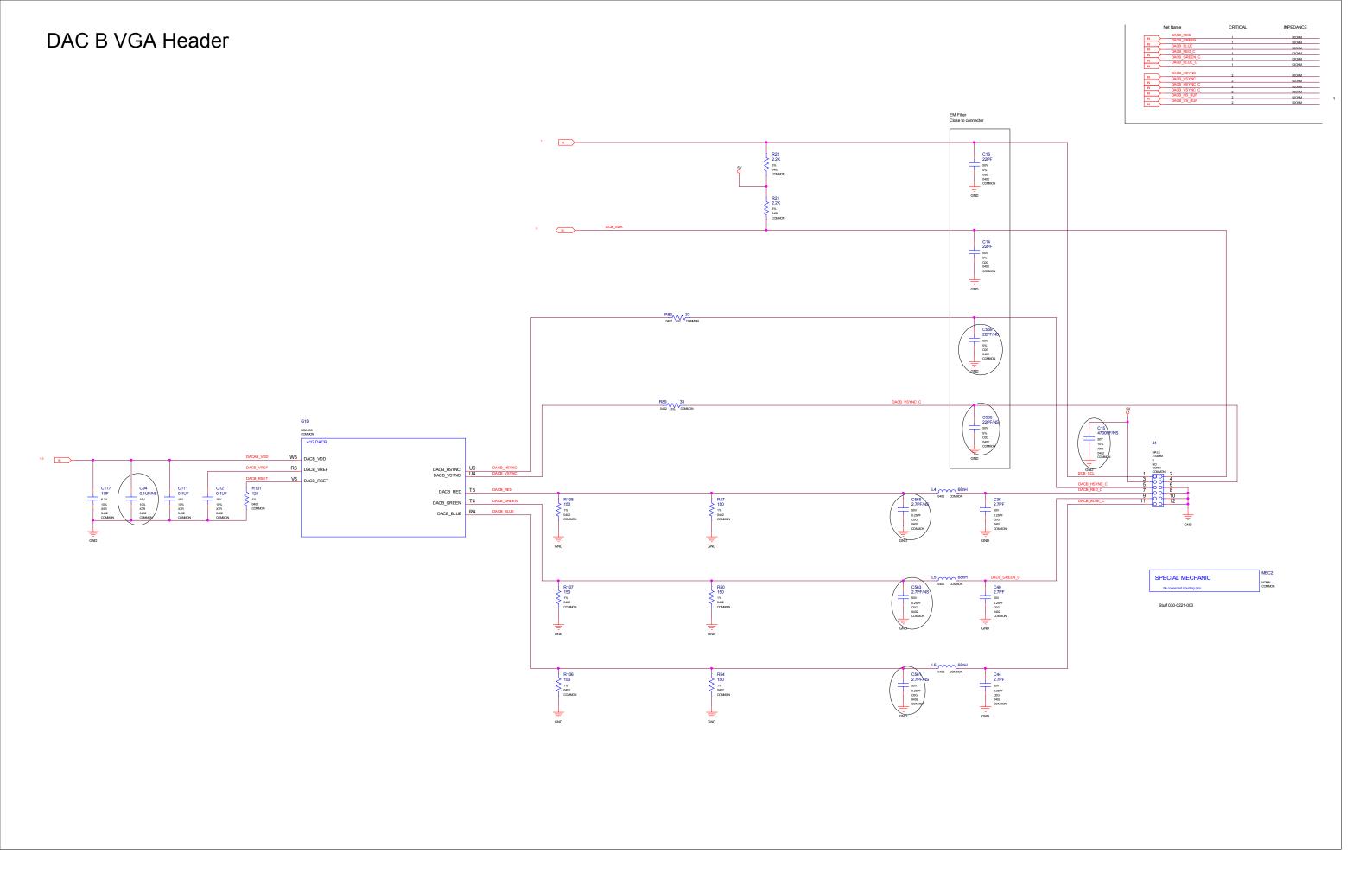


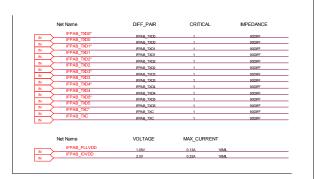
#### DDR3 Memories Rank 1

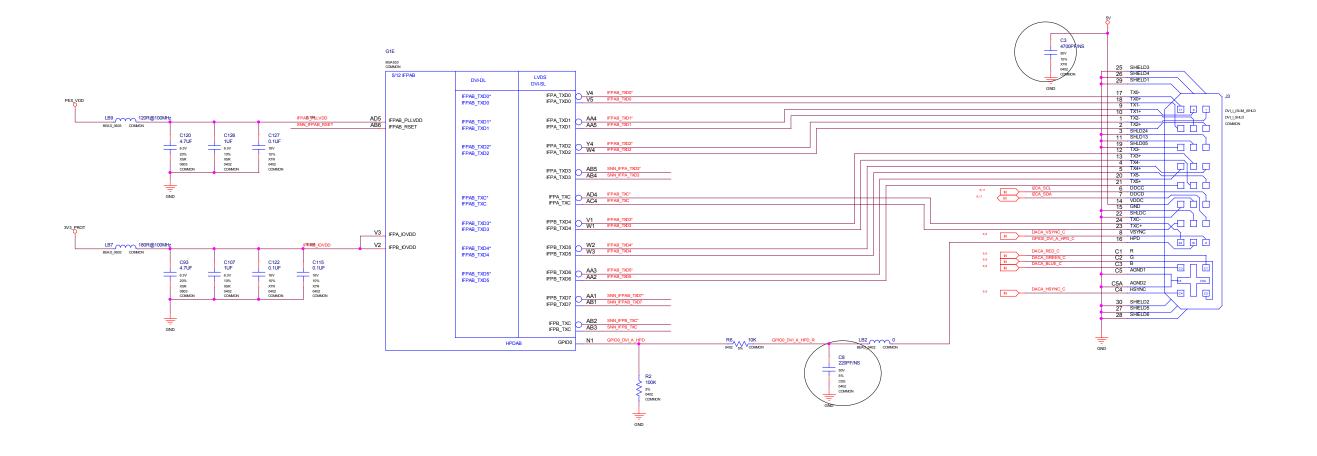


DAC A VGA

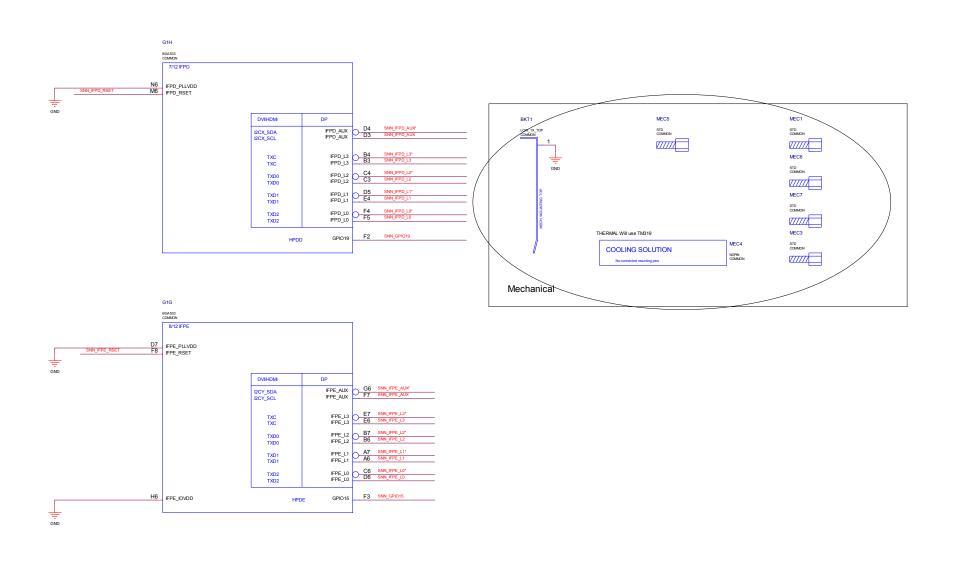


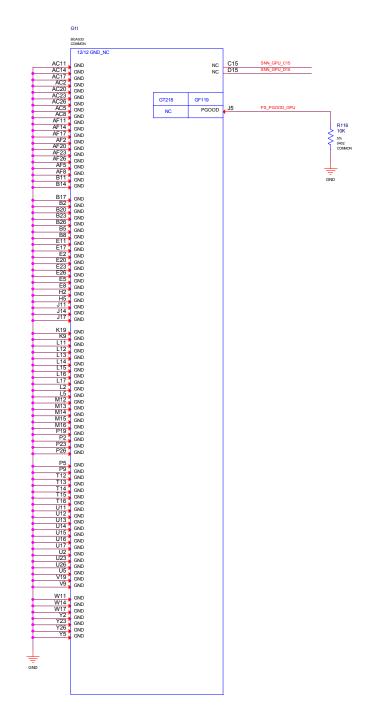






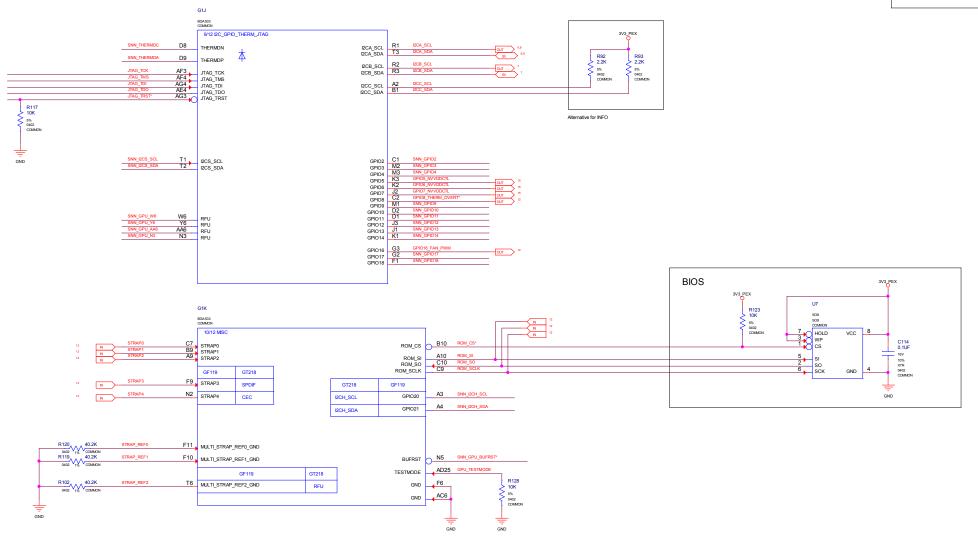
# IFPC HDMI/DP Connector C103 0.1UF 16V 10% X7R 0402 COMMON C 104 0.1UF 16V 10% X7R 0402 COMMO 0.1UF 0.1UF 16V 10% X7R 0402 COMMON TXD1 TXD1 IFPC\_TXC\_C\* IFPC\_TXD0\_C\* IFPC\_TXD0\_C IFPC\_TXD1\_C\* IFPC\_TXD1\_C

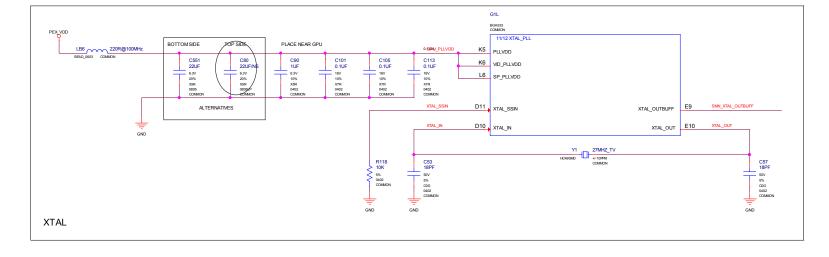




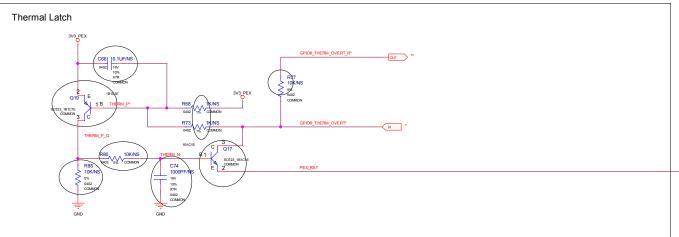
### XTAL, ROM, INFO ROM, JTAG

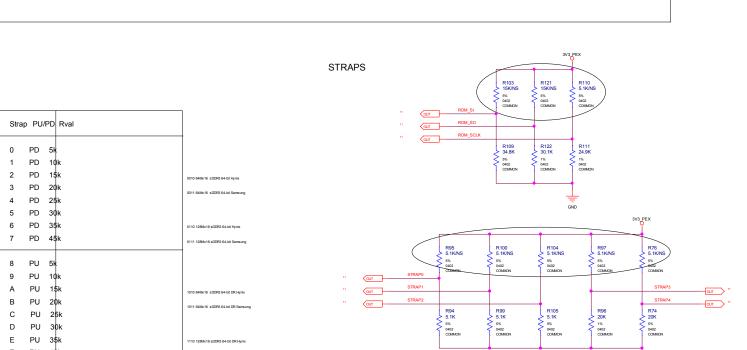






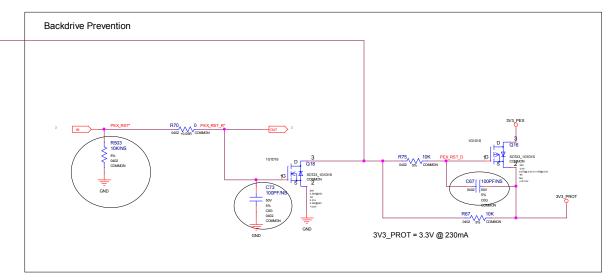
### Thermal Protection, Protected 3V3, Straps, Fan

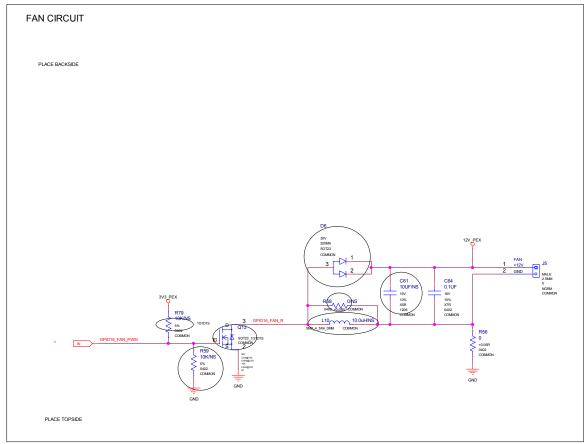




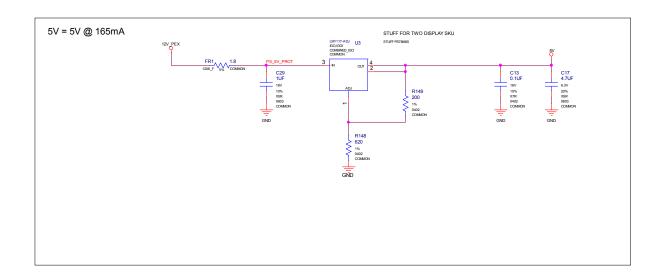
	STRAP PIN MODE TABLE			
	PIN NAME	MULTI-LEVEL bit [3:0]	BINARY PRODUCTION	BINARY BRINGUP
	STRAP0	USER[3:0]	RAMCFG0	3GIO_PADCFG_LUT_ADR0
	STRAP1	3GIO_PADCFG_ADR[3:0]	RAMCFG1	3GIO_PADCFG_LUT_ADR1
GF119	STRAP2 STRAP3	PCI_DEVID[3:0] SOR[3:0] EXPOSED	RAMCFG2 TBD	3GIO_PADCFG_LUT_ADR2 TBD
GF119	STRAP4	RESERVED,RESERVED,PCIE_MAX_SPEED,DP_PLL_VDD33V	TBD	TBD
	ROM_SCLK	PCIDEVID[4], SUB_VENDOR, SLOT_CLK, PEX_PLL_EN_TERM	PCI_DEVID3	3GIO_PADCFG_LUT_ADR3
GF119	ROM_SCLK	PCI_DEVID[4],SUB_VENDOR,PCI_DEVID[5],PEX_PLL_EN_TERM	TBD	TBD
	ROM_SI	RAMCFG[3:0]	PCI_DEVID_EXT	SUB_VENDOR
	ROM_SO	XCLK_417, FB0_BAR_SIZE, SMB_ALT_ADDR, VGA_DEVICE	XCLK_417	XCLK_277
GF119	ROM_SO	FB[1],FB[0],SMB_ALT_ADDR,VGA_DEVICE	TBD	TBD

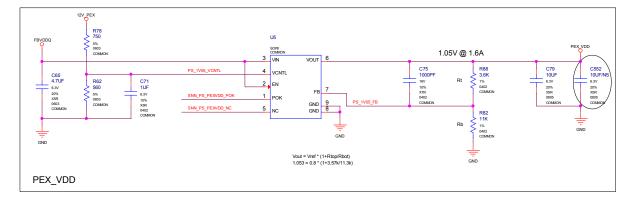






# Power Supply I: 3V3\_FUSE,5V,PEXVDD





## Power Supply II: FBVDDQ

