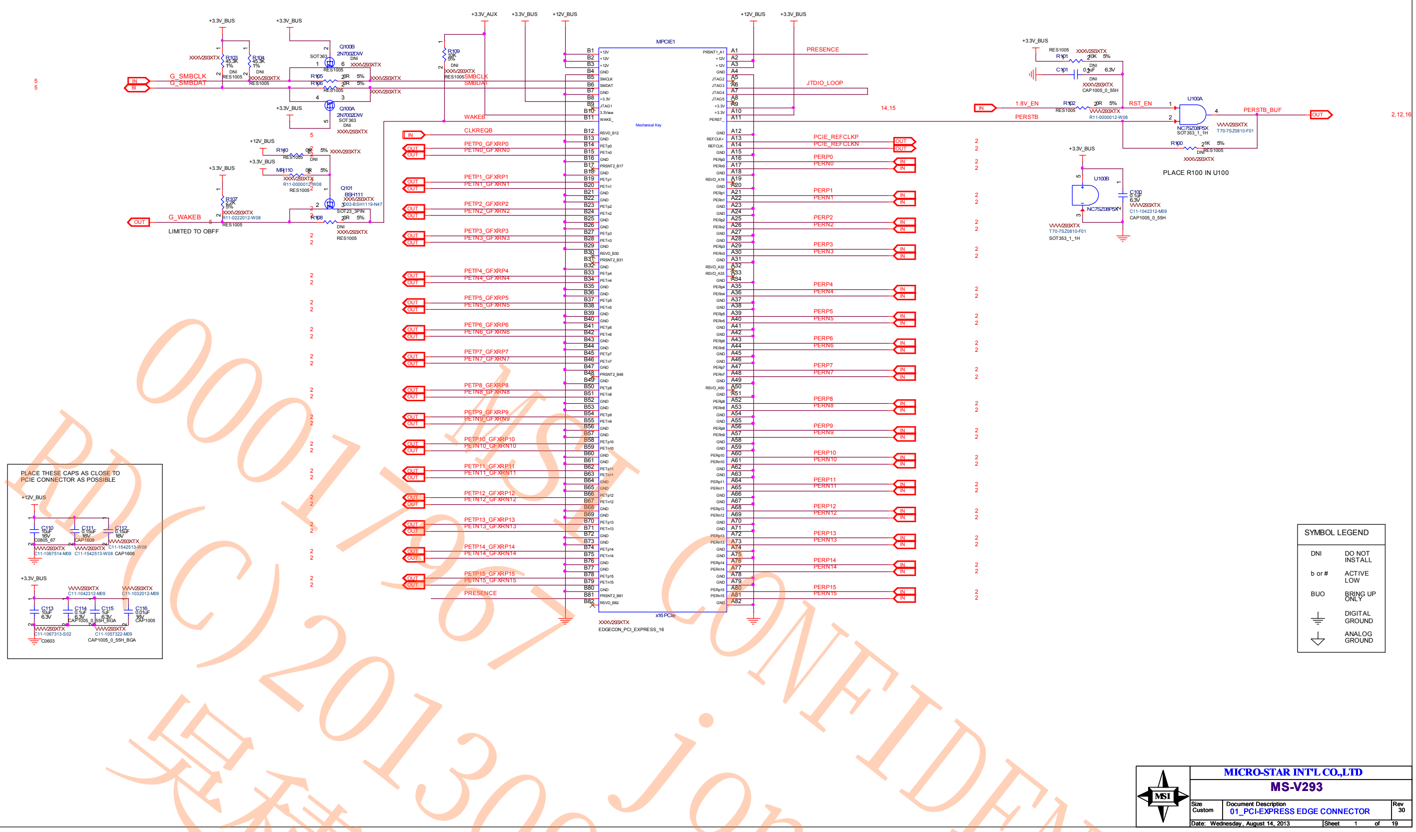
(1) PCI-EXPRESS EDGE CONNECTOR



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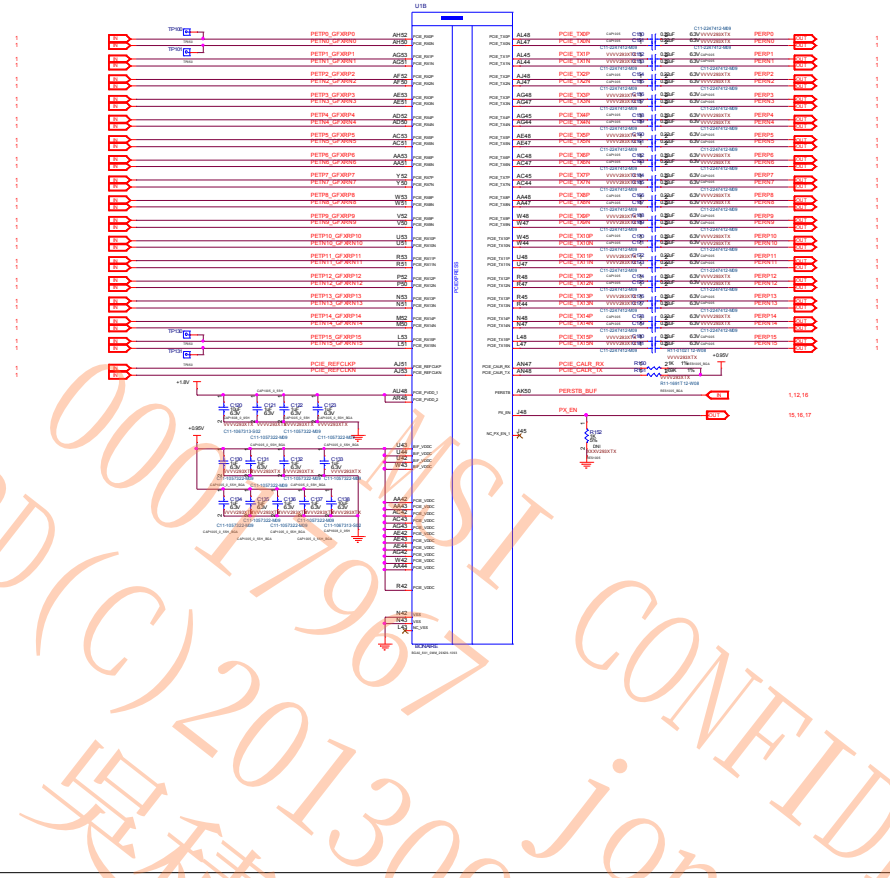
(2) BONAIRE PCIE INTERFACE



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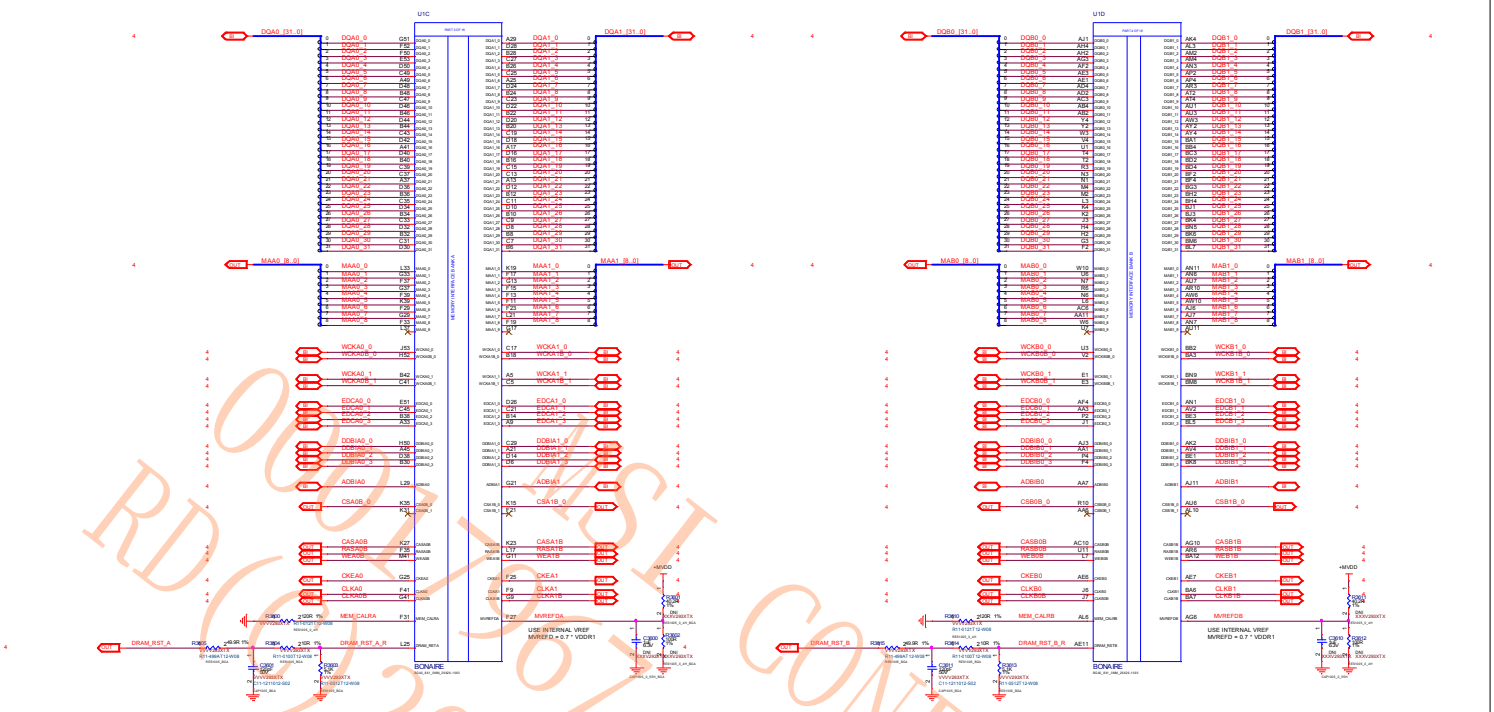
(3) BONAIRE MEMORY INTERFACE



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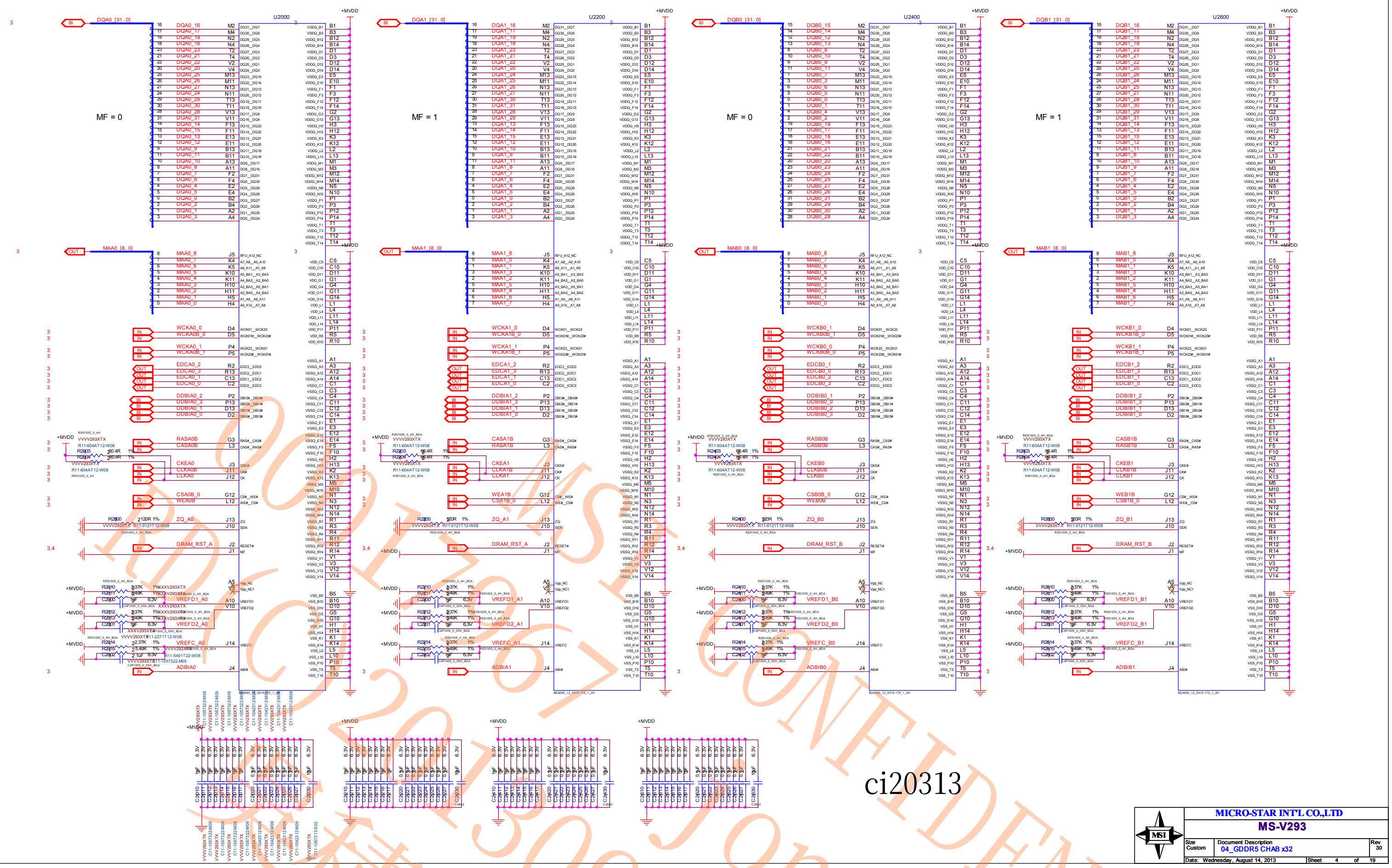
(4) GDDR5 CHAB x32



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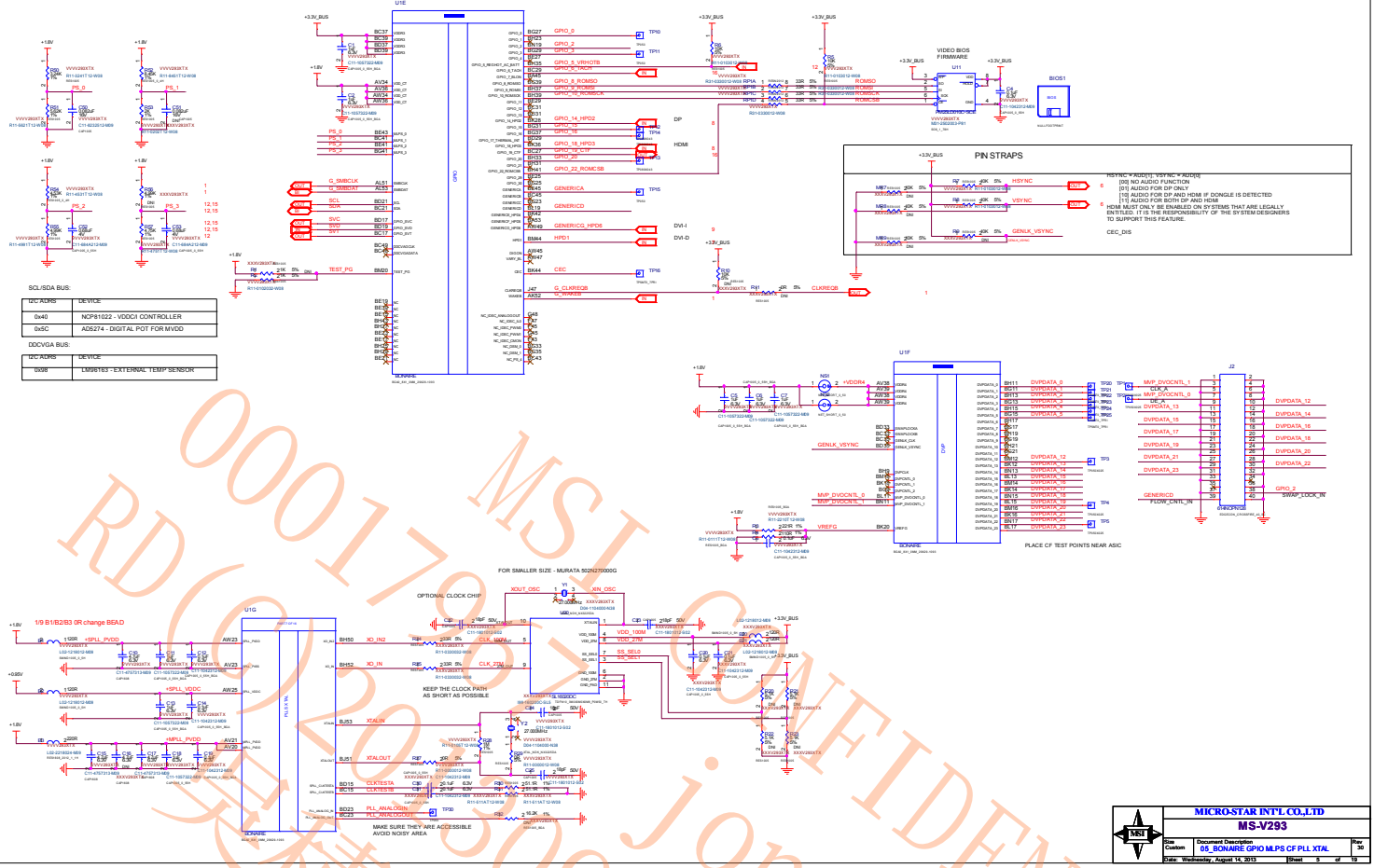
(5) BONAIRE GPIO MLPS CF PLL XTAL



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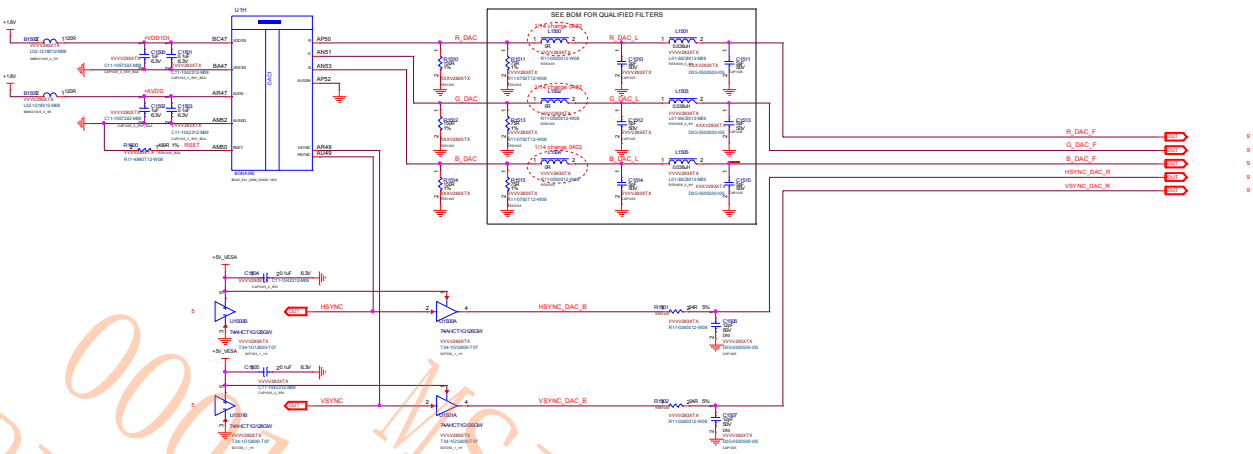
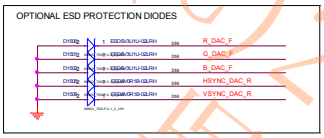
(6) BONAIRE DAC





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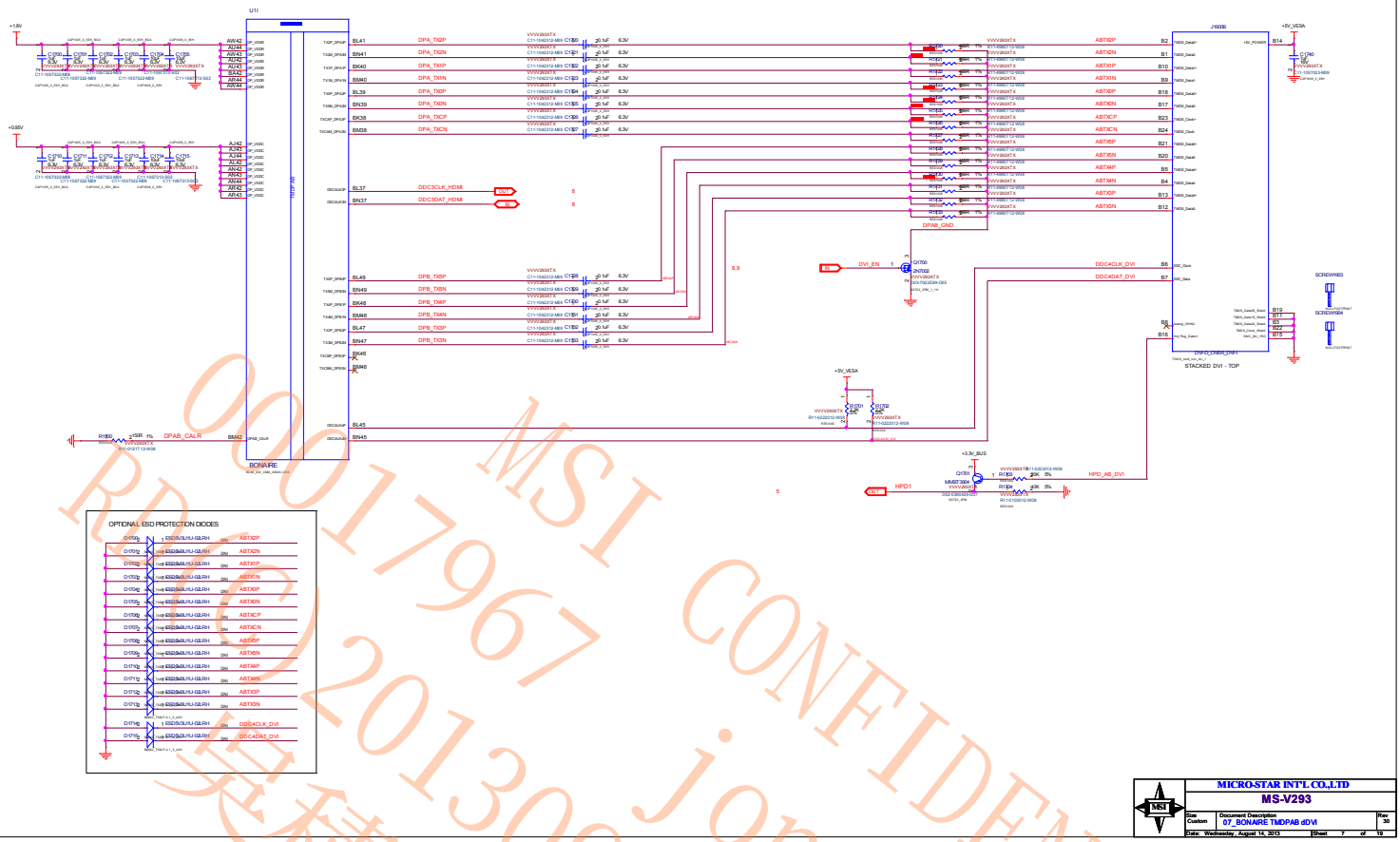
(7) BONAIRE TMDPAB dDVI



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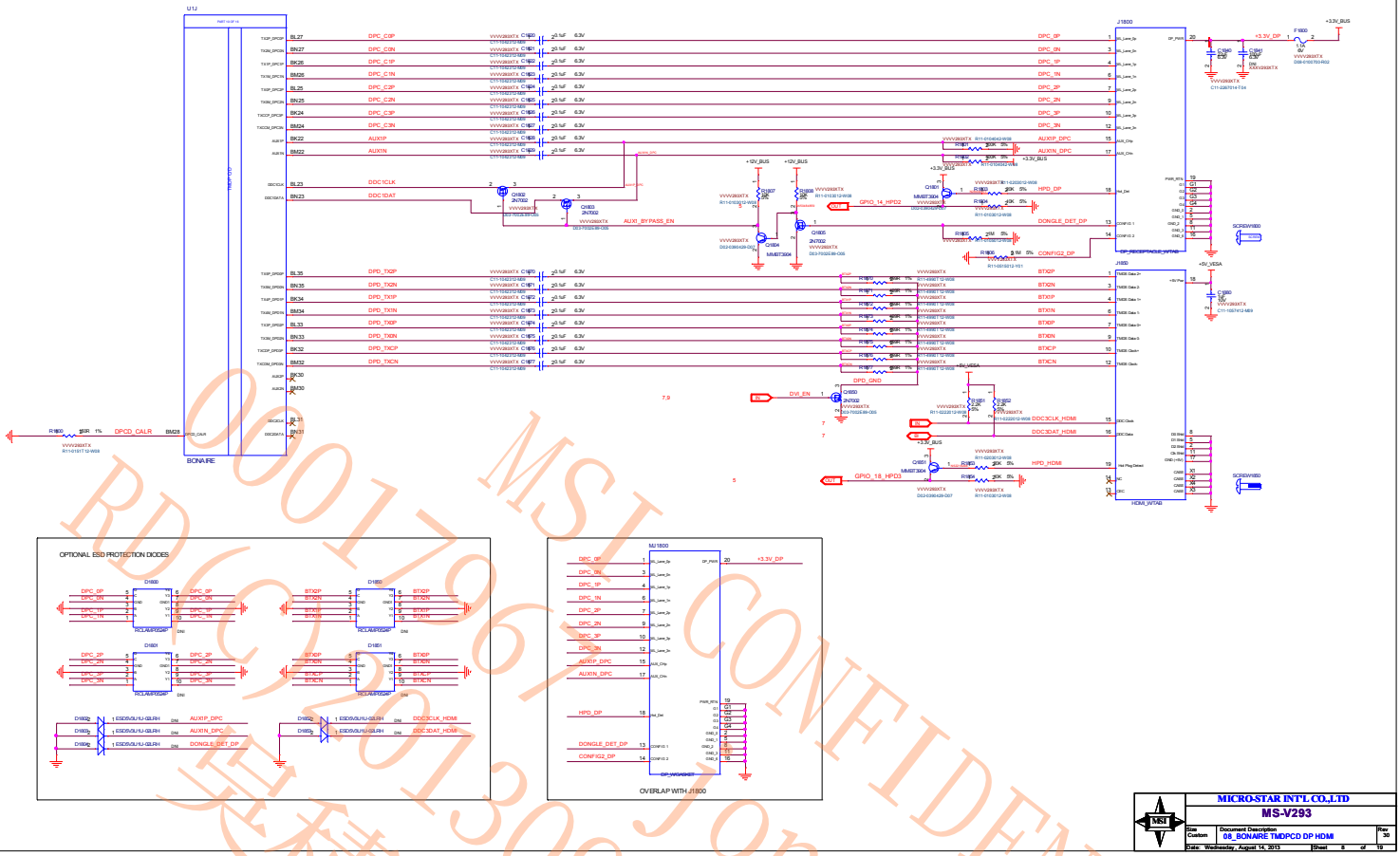
(8) BONAIRE TMDPCD DP HDMI



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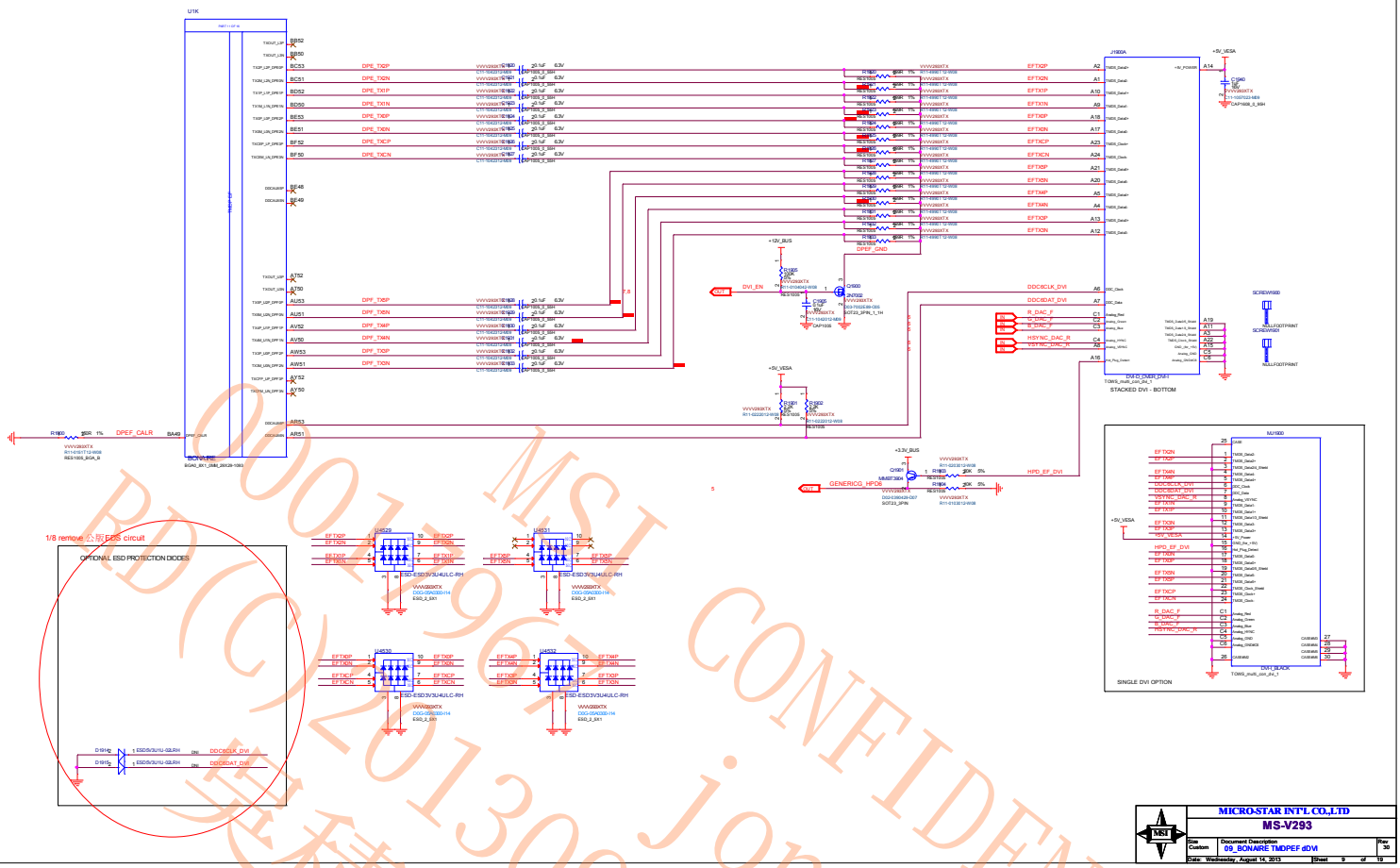
(9) BONAIRE TMDPEF dDVI



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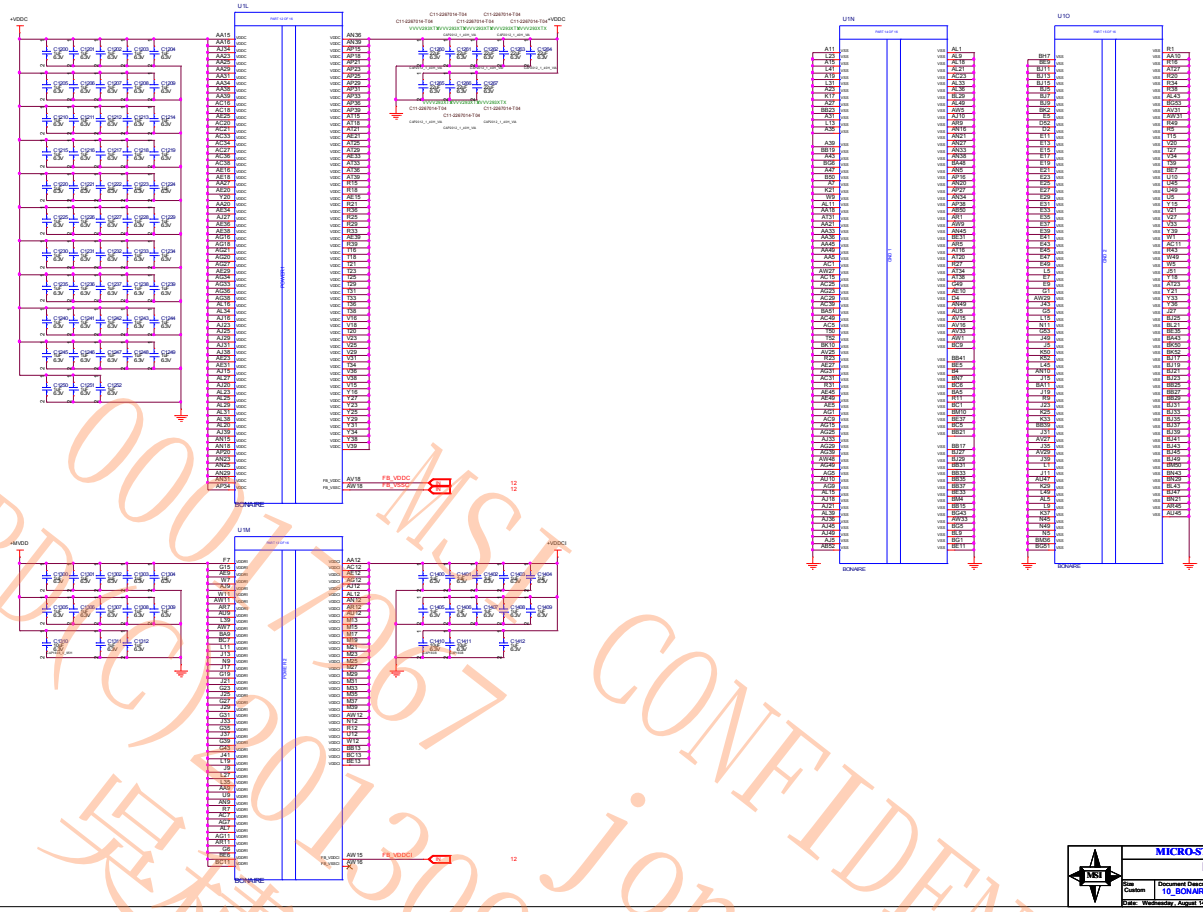
(10) BONAIRE POWER AND GROUND



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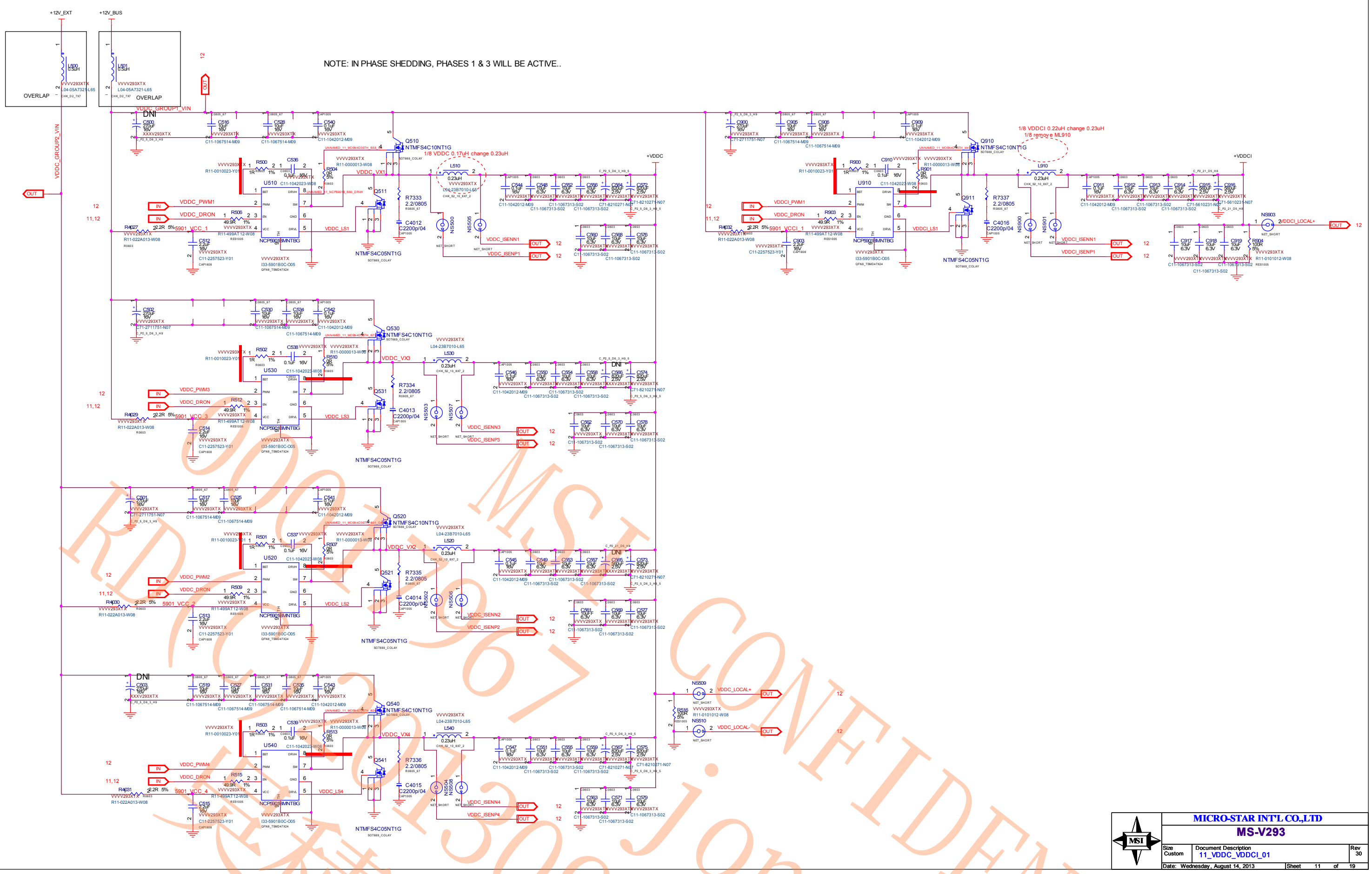
(11) VDDC_VDDCI_01



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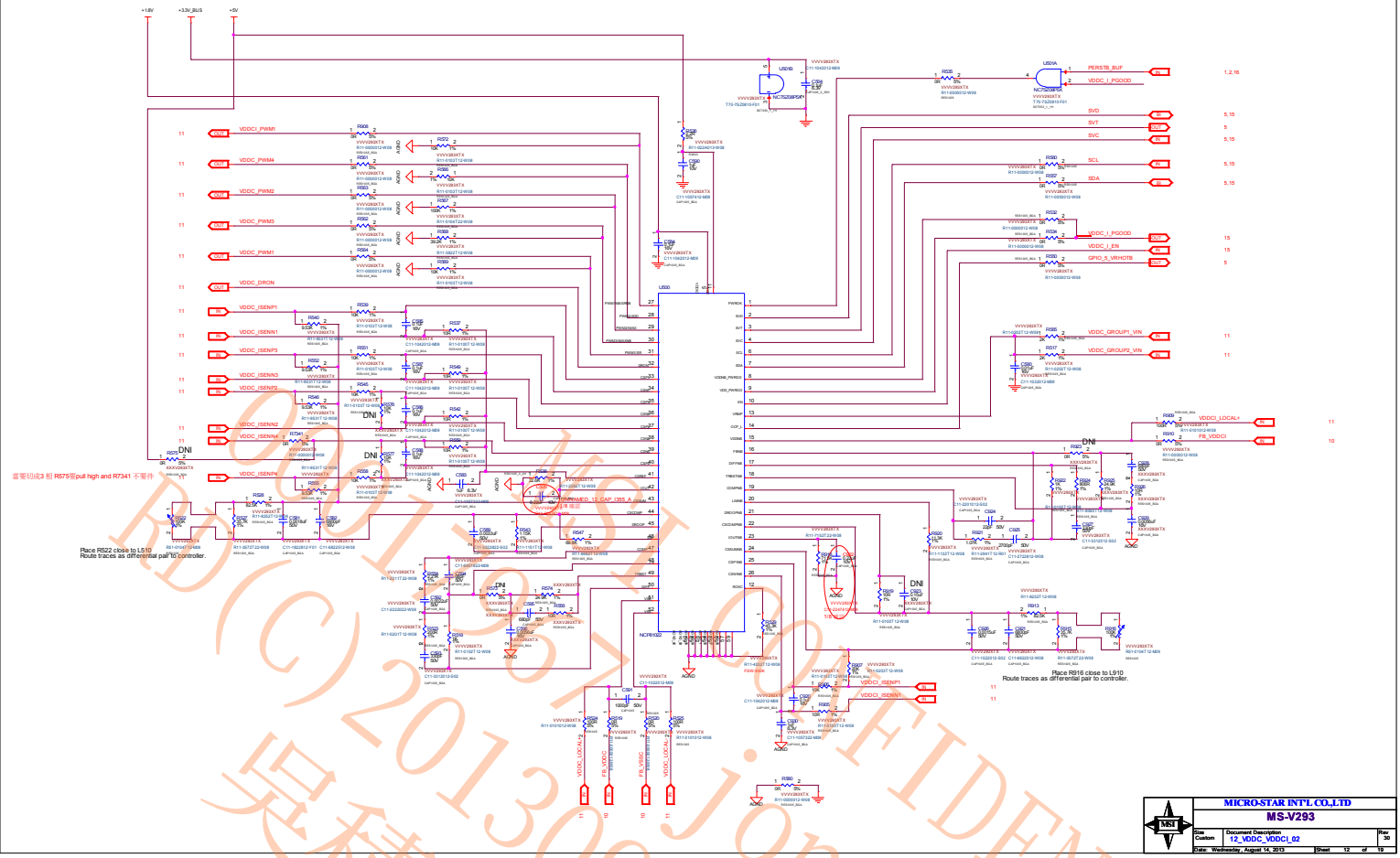
(12) VDDC_VDDCI_02



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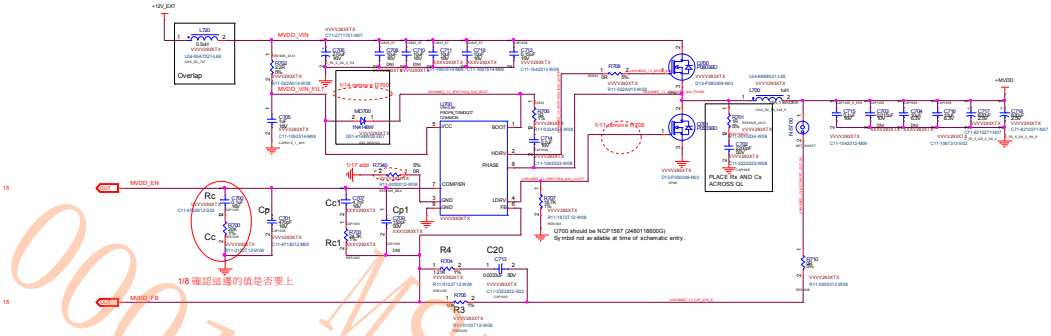
(13) MVDD



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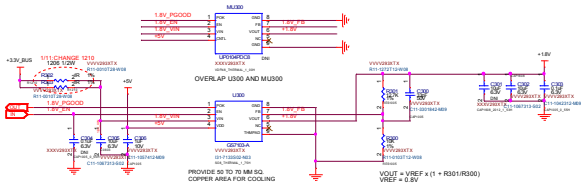
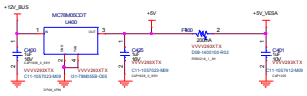
(14) SMALL RAIL REGULATORS

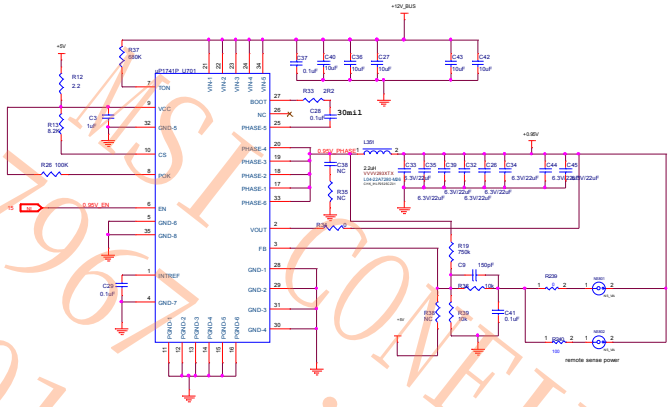
1.8V REGULATOR



5V REGULATOR



0.95V REGULATOR



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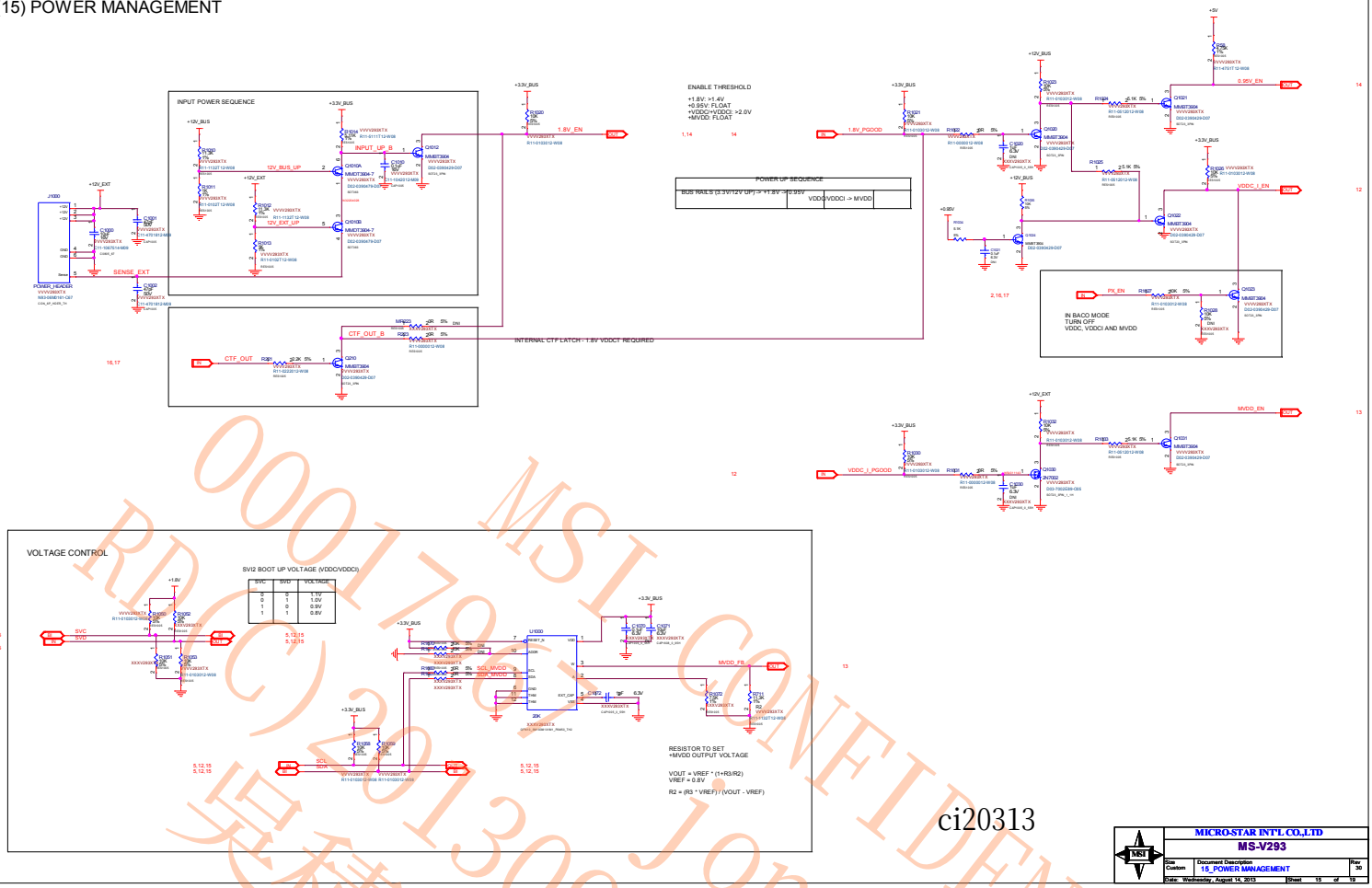
(15) POWER MANAGEMENT



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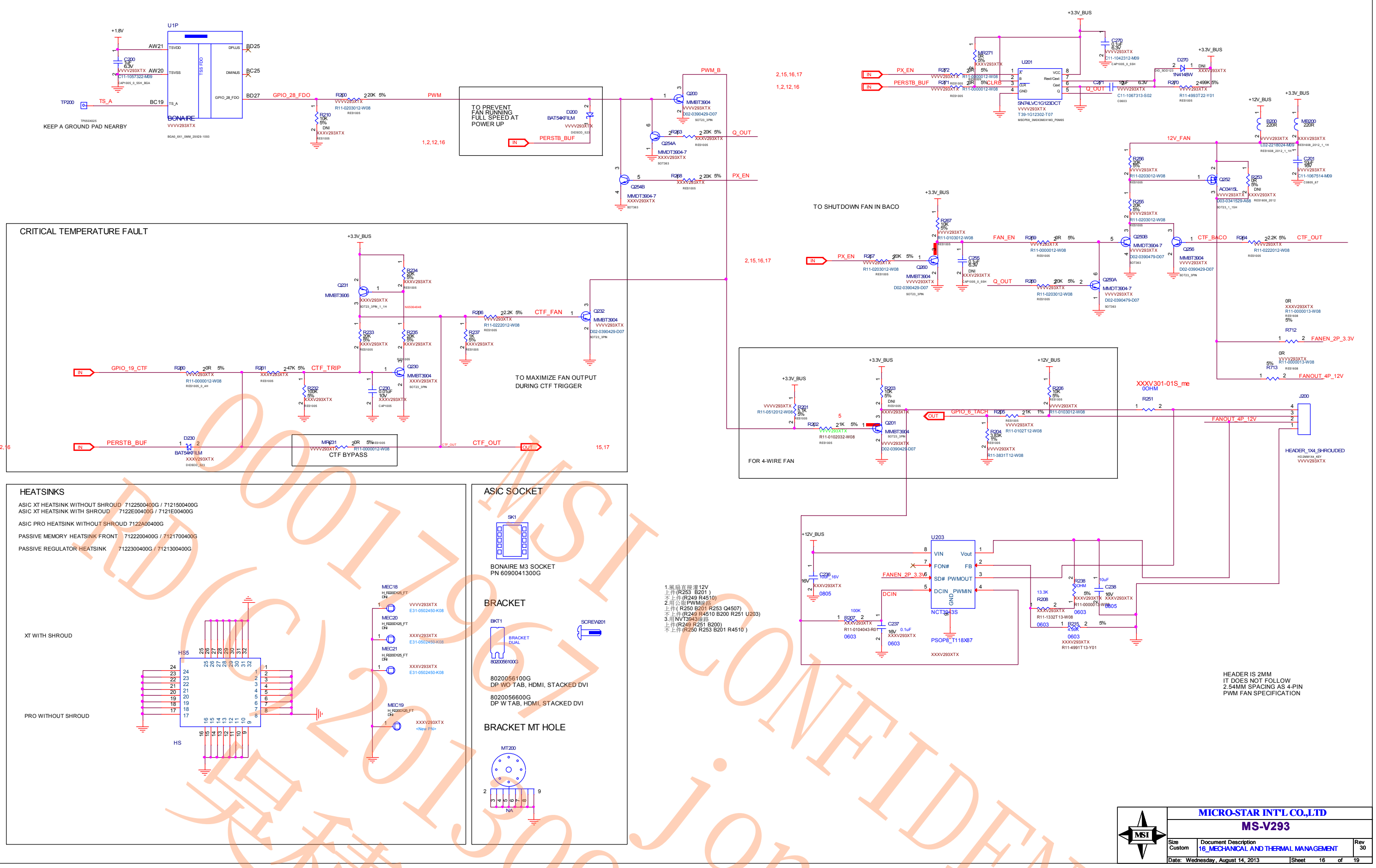
(16) MECHANICAL AND THERMAL MANAGEMENT



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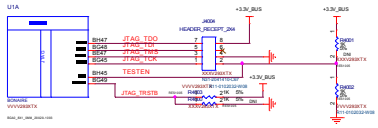
(17) DEBUG CIRCUIT

JTAG



1/10: remove J4001/J4002

LM96163 FOR BACKUP THERMAL CONTROL

1/10: remove U4003 circuit

SWITCHES

1/10: remove SW4002

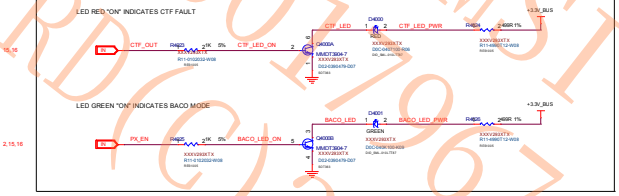
BYPASS CTF

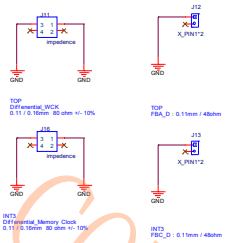
MAXIMIZE FAN

VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS

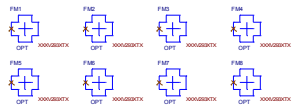
1/10: remove J4010 and J4011

LED LIGHTS





optical cross point



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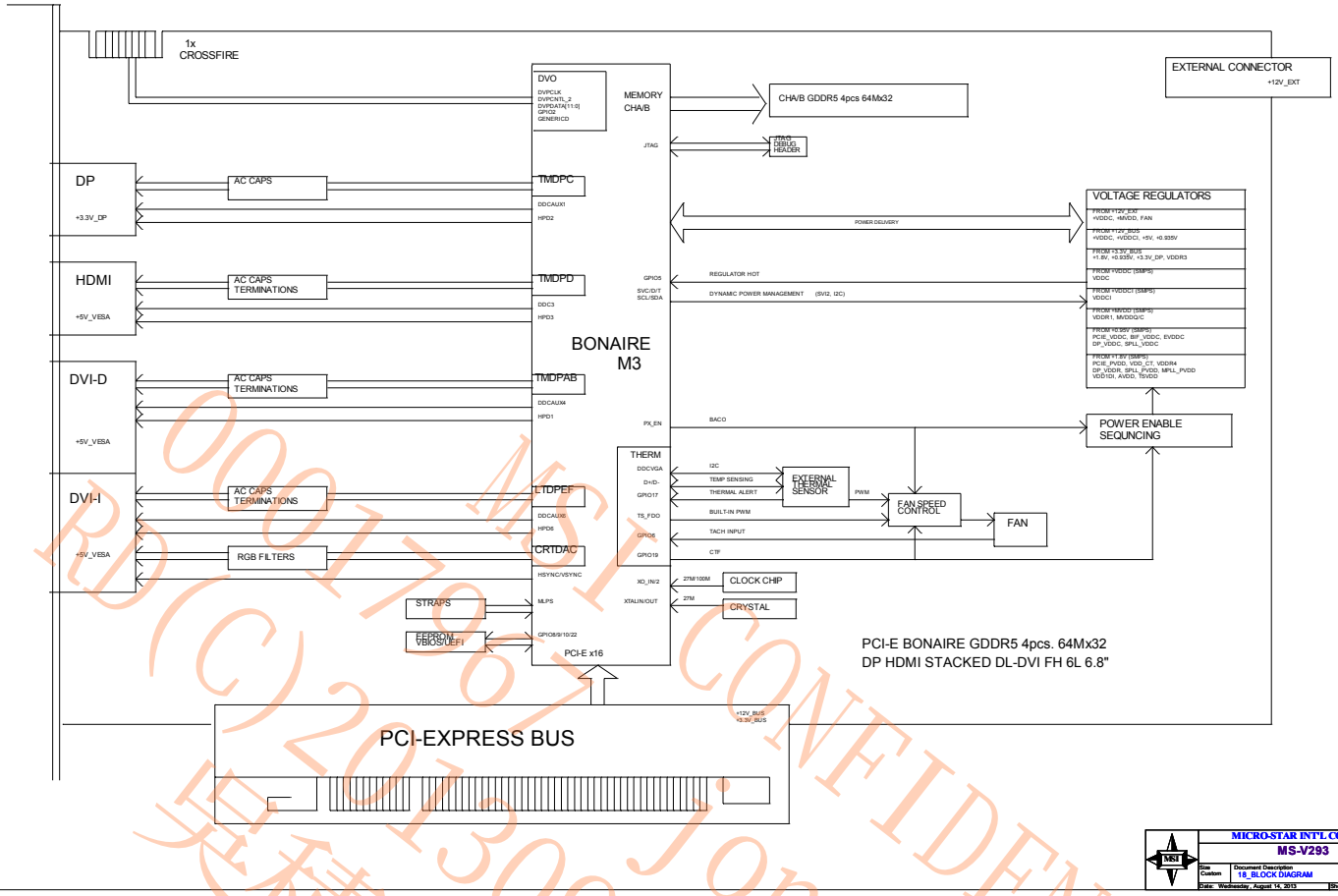

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Bonaire

Stacked DVI-I/DVI-D + HDMI + DP

Base on V293-1.0	P8 : change J1850 pin name P14 : change 0.95V power solution from UPI 1543 to UPi 1741 P15 : add 0.95V power sequence control & add +5V net to 0.95V_EN P16 : change 2pin fan control from AMD solution to Nuvoton NCT3943S solution

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