

PG210 B00

GP107 128b GDDR5, 75W , PCB 5.7"
DVI-D_DL (DP + DP) + HDMI (DP) + DP

TABLE OF CONTENTS

Page	Description
1	Table of Contents
2	Block Diagram
3	PCI Express
4	MEMORY: GPU Partition A/B
5	MEMORY: FBA[31:0]
6	MEMORY: FBA[63:32]
7	MEMORY: FBB[31:0]
8	MEMORY: FBB[63:32]
9	GPU FBVDDQ & 1V8 Decoupling
10	GPU NVVDD & NVVDDS DECOUPLING
11	IFPAB DVI-DL
12	IFPD DP
13	IFPC HDMI
14	IFPEF UNUSED
15	MISC1: Fan, Thermal, JTAG, GPIO
16	MISC2: ROM, XTAL, Straps
17	Power Inputs and Filtering
18	Power Sequence
19	PS: NV3V3 & NV12V
20	PSI: 1V8
21	PSII: 5V & 12V_FAN
22	PS III: PEXVDD
23	PS IV: FBVDDQ
24	PS V: NVVDD OVR2+1
25	PS V: NVVDD Phase 1,2

Page	Description
26	PS V: NVVDD Phase 3
27	MECH: Bracket/Thermal

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ASSEMBLY	<ASSEMBLY DESCRIPTION>
PAGE DETAIL	Table of Contents

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NV_PN

600-1G210-BASE-SCH

PCB REV

PG210-B00

BOM REV


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PAGE

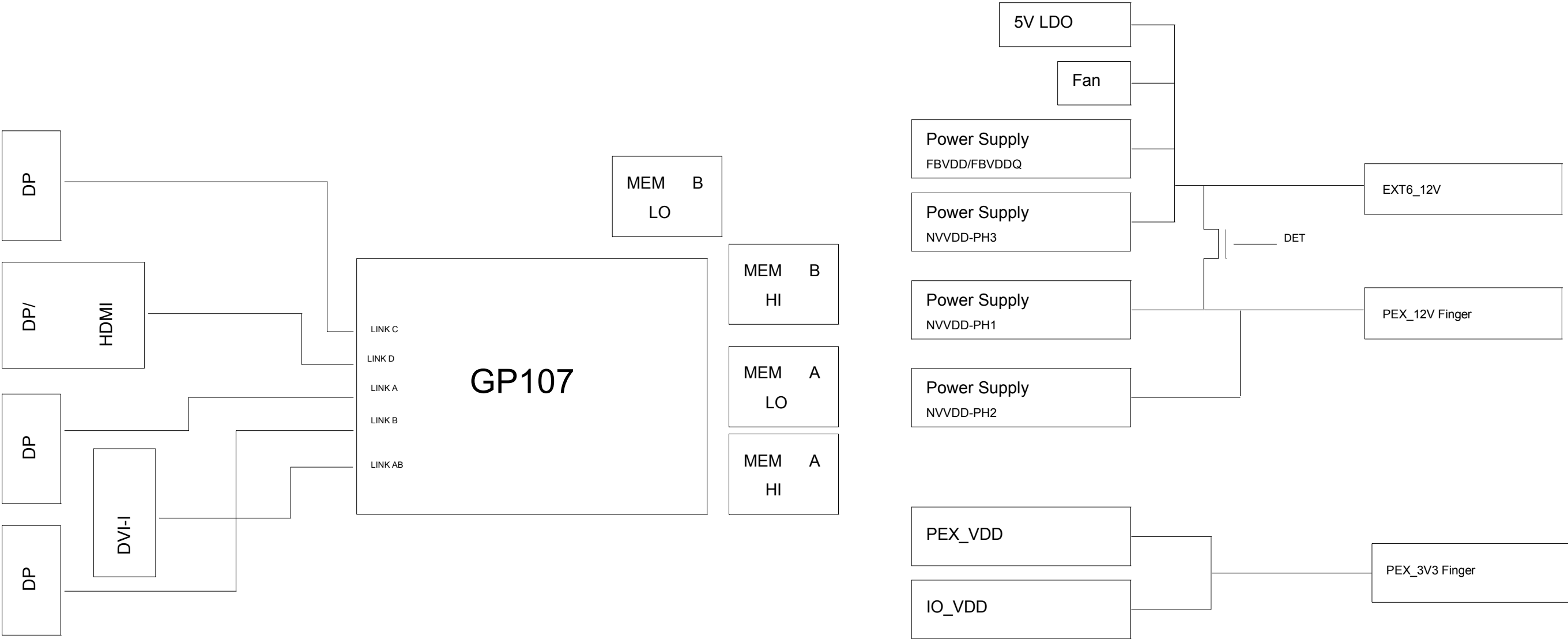
1 OF 27

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Block Diagram




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PCB REV	PG210-B00	PAGE	2 OF 27	
BOM REV	A	DATE	18-DEC-2015	

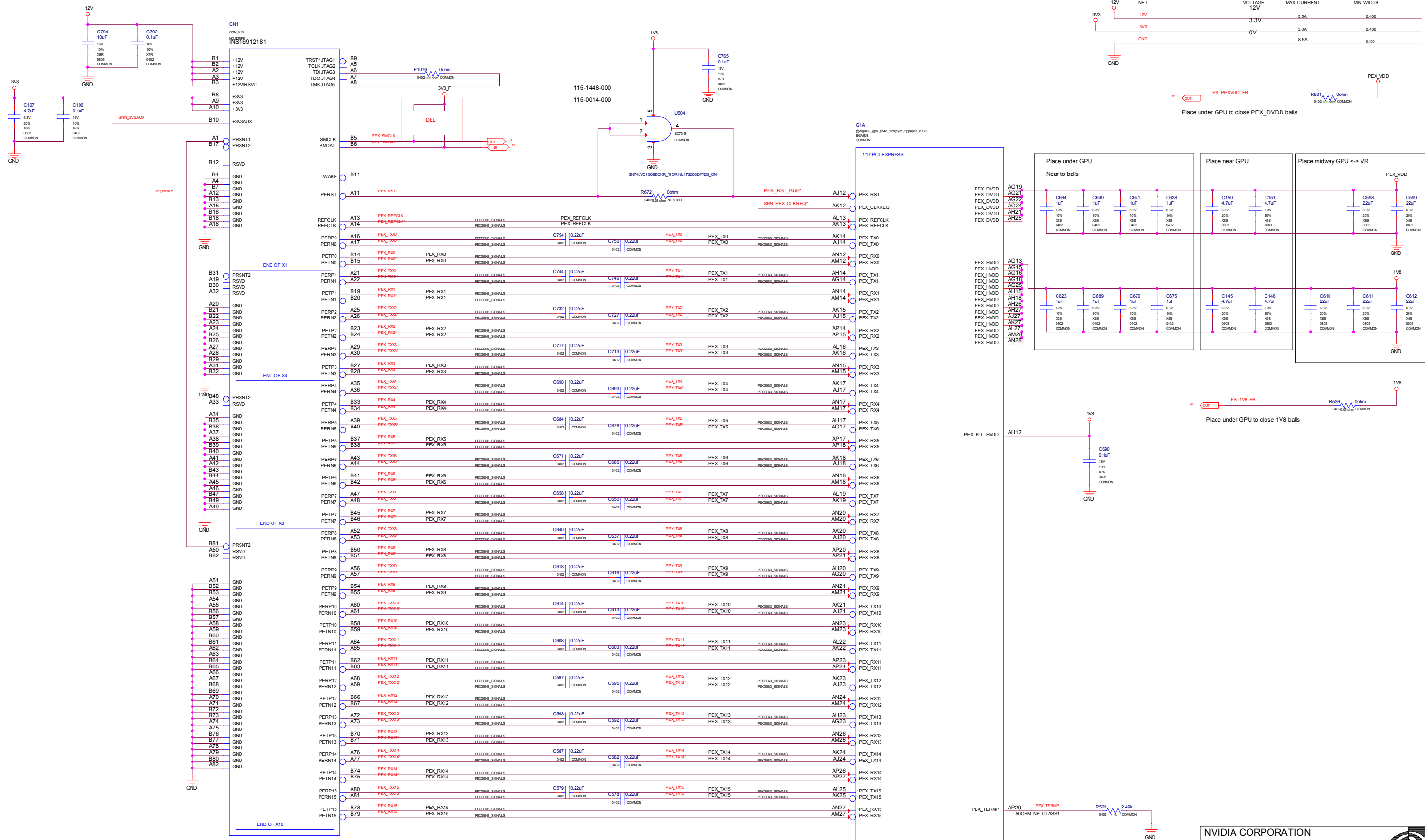
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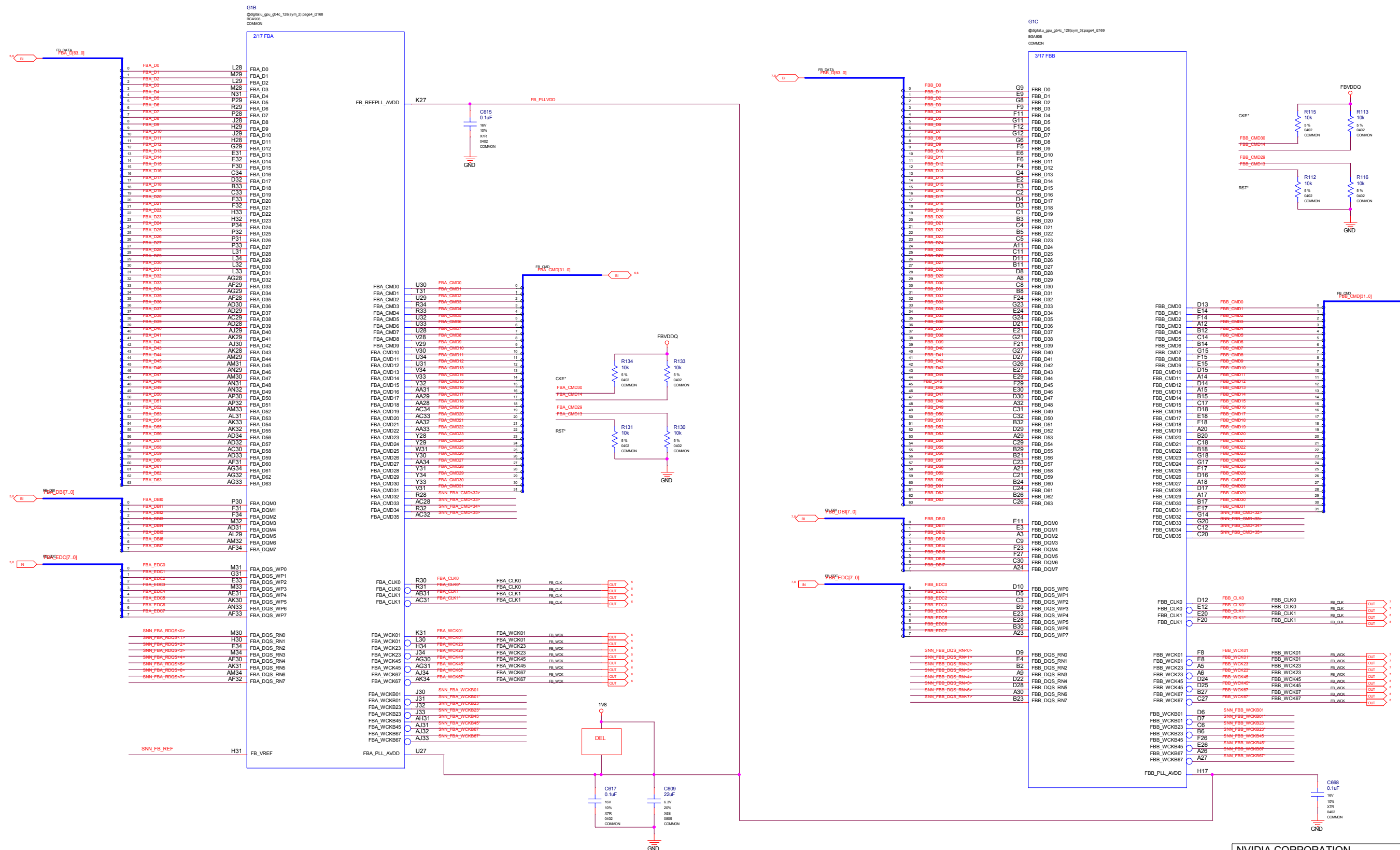
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PAGE DETAIL	PCI Express	PCB REV	PG210-B00	PAGE	3 OF 27
		BOM REV	A	DATE	18-DEC-2015

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MEMORY: GPU Partition A/B



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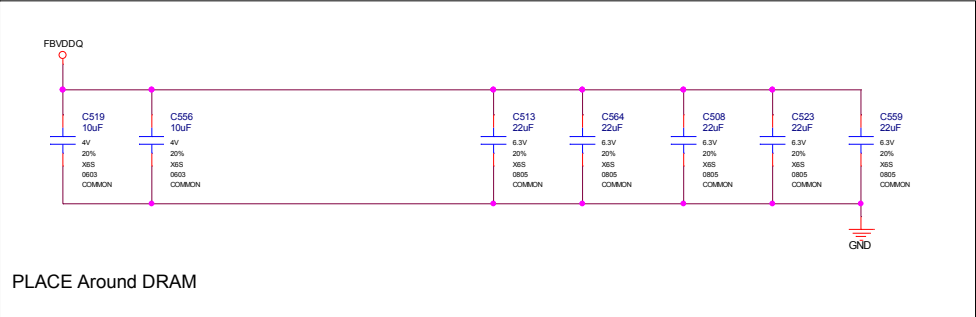
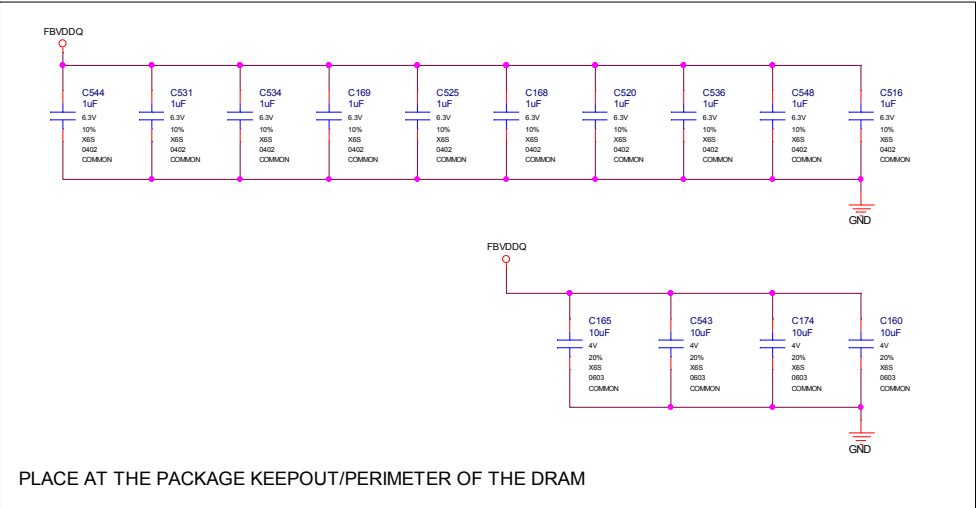
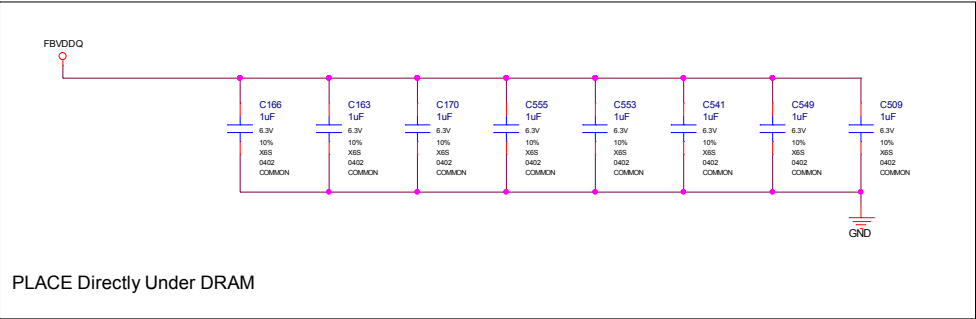
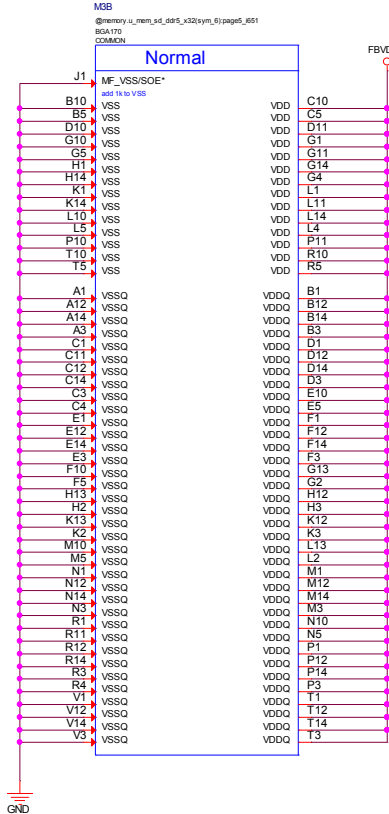
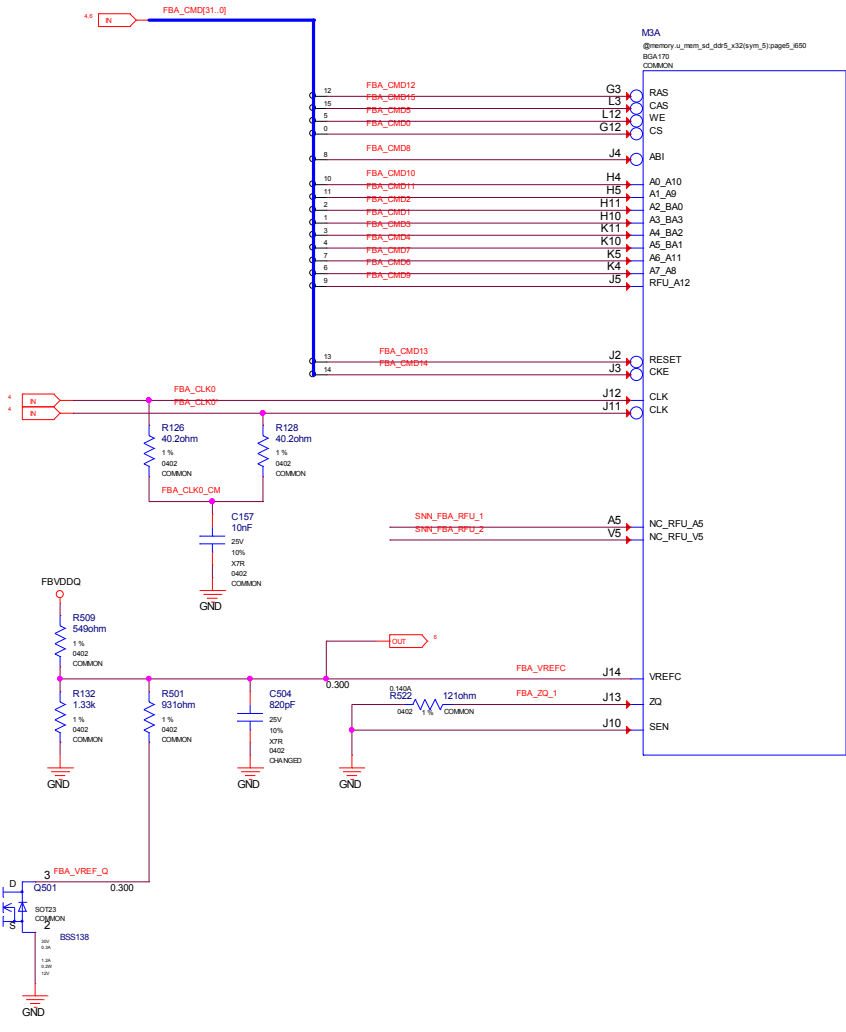
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PCB REV	PG210-B00
BOM REV	A

PAGE	4 OF 27
DATE	18-DEC-2015

MEMORY: FBA Partition 31..0

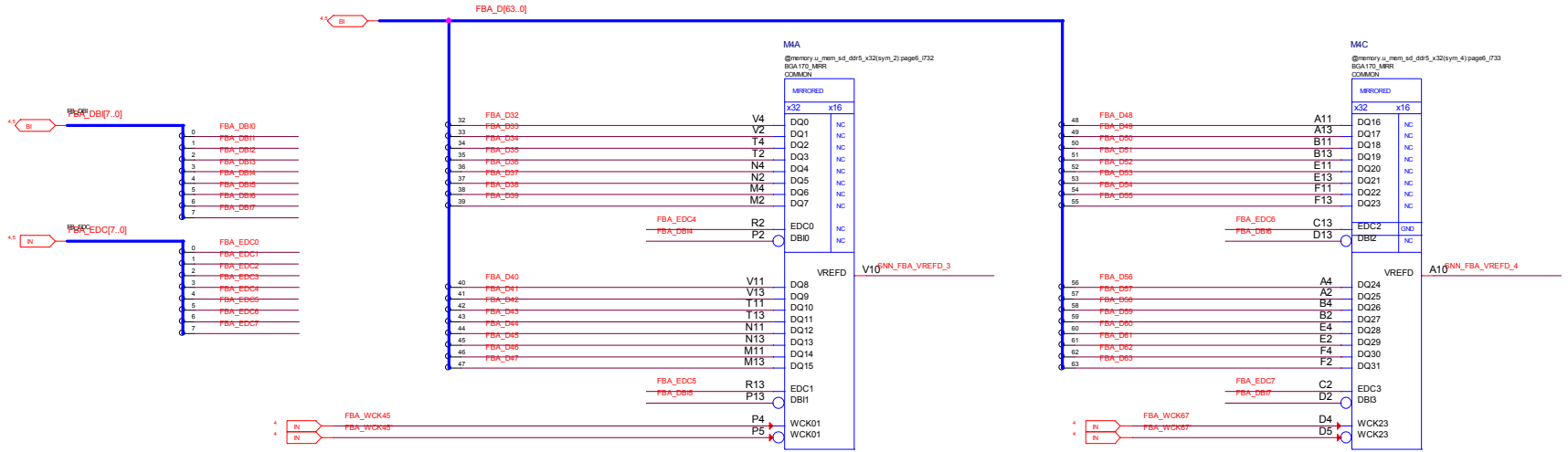
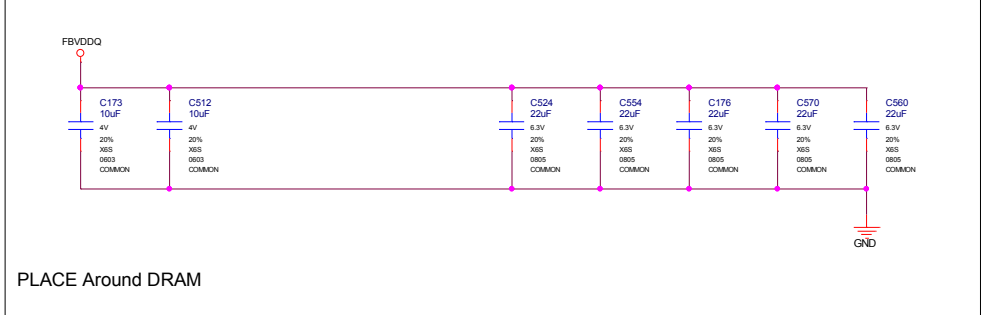
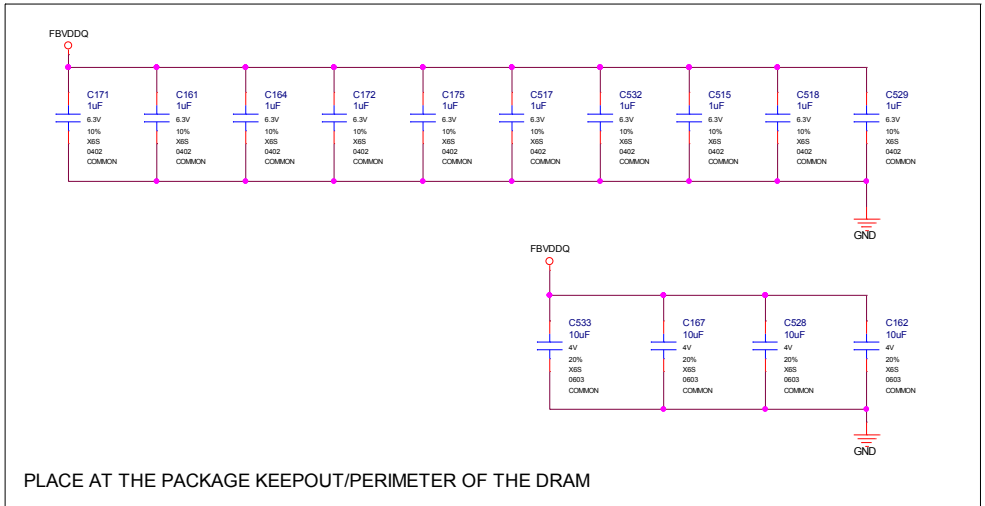
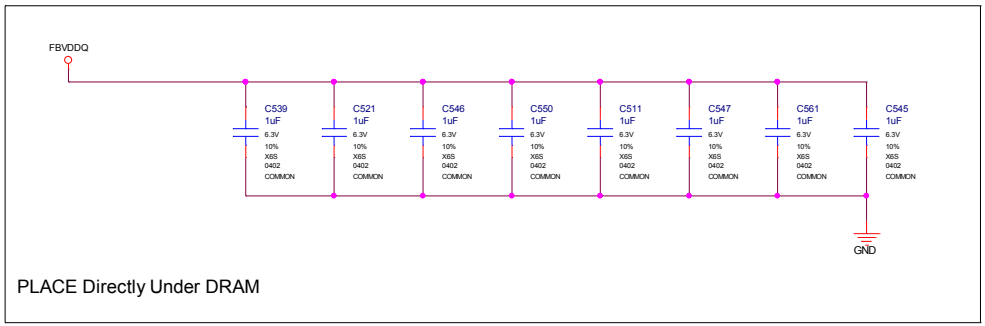
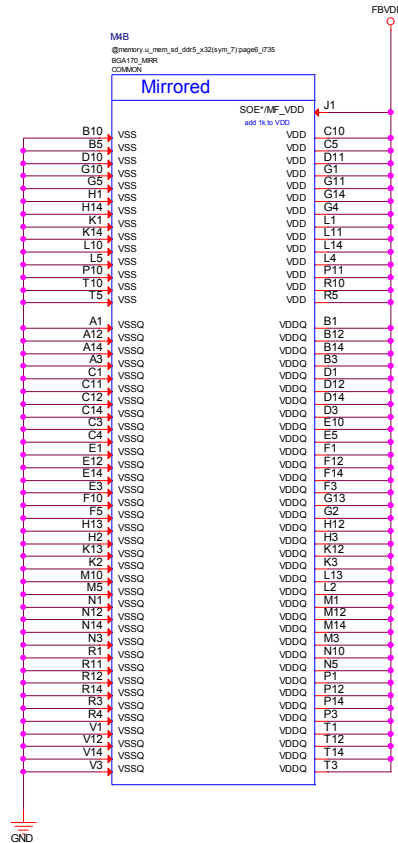
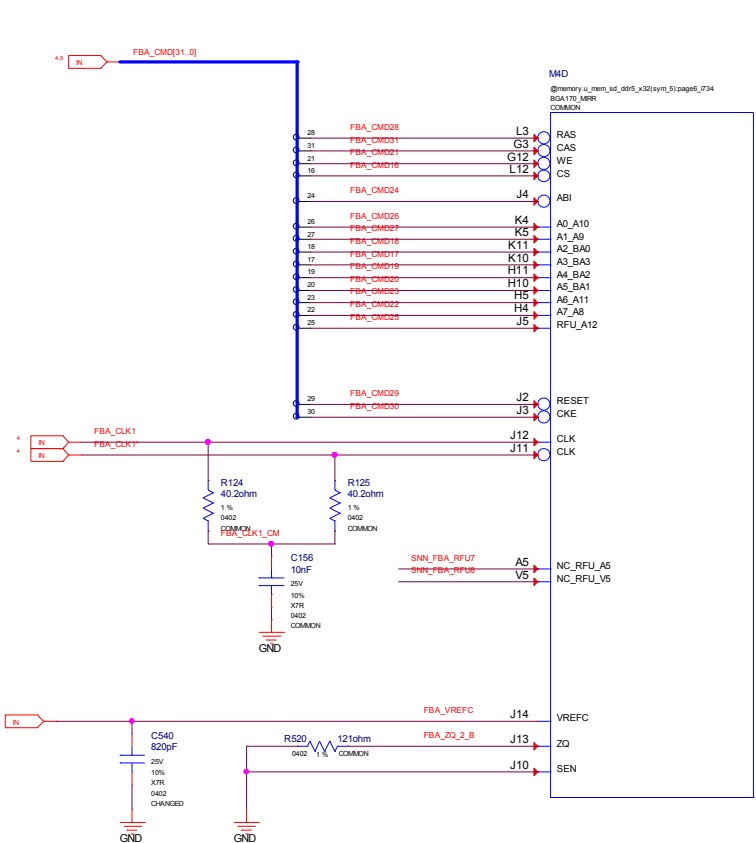
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CMD1	A3_BA3	
CMD2	A2_BA0	
CMD3	A4_BA2	
CMD4	A5_BA1	
CMD5	WE*	
CMD6	A7_A8	
CMD7	AB_A11	
CMD8	AB*	
CMD9	A12_RFU	
CMD10	A0_A10	
CMD11	A1_A9	
CMD12	RAS*	
CMD13	RST*	
CMD14	CKE*	
CMD15	CAS*	
CMD16		CS*
CMD17		A3_BA3
CMD18		A2_BA0
CMD19		A4_BA2
CMD20		A5_BA1
CMD21		WE*
CMD22		A7_A8
CMD23		AB_A11
CMD24		AB*
CMD25		A12_RFU
CMD26		A0_A10
CMD27		A1_A9
CMD28		RAS*
CMD29		RST*
CMD30		CKE*
CMD31		CAS*



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MEMORY: FBA Partition 63..32

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CMD0	CS*	
CMD1	A3_BA3	
CMD2	A2_BA0	
CMD3	A4_BA2	
CMD4	A5_BA1	
CMD5	WE*	
CMD6	A7_A8	
CMD7	AB*	
CMD8	A12_RFU	
CMD9	A0_A10	
CMD10	A1_A9	
CMD11	RAS*	
CMD12	RST*	
CMD13	CKE*	
CMD14	CAS*	
CMD15		CS*
CMD16		A3_BA3
CMD17		A2_BA0
CMD18		A4_BA2
CMD19		A5_BA1
CMD20		WE*
CMD21		A7_A8
CMD22		AB*
CMD23		A12_RFU
CMD24		A0_A10
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CMD26		RAS*
CMD27		RST*
CMD28		CKE*
CMD29		CAS*
CMD30		
CMD31		



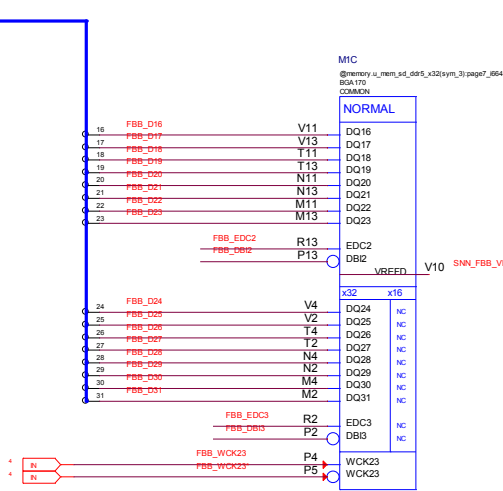
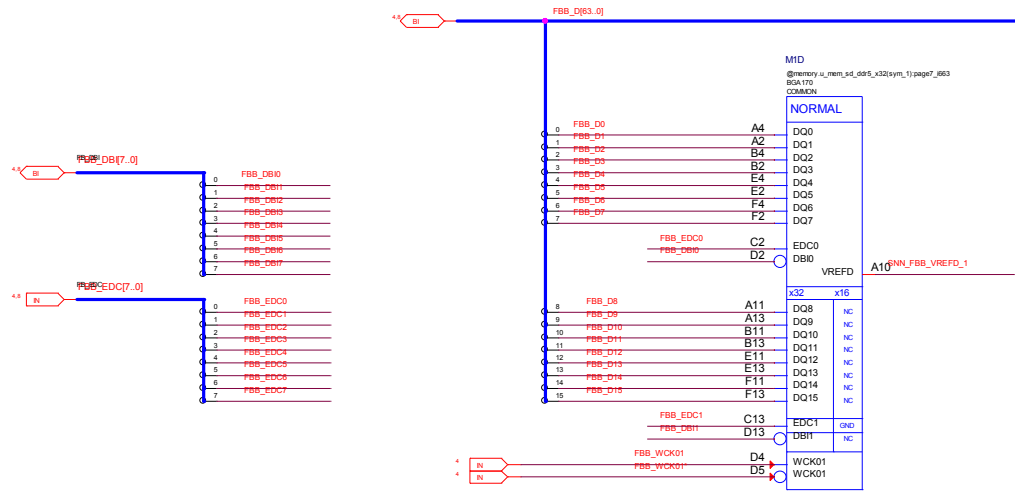
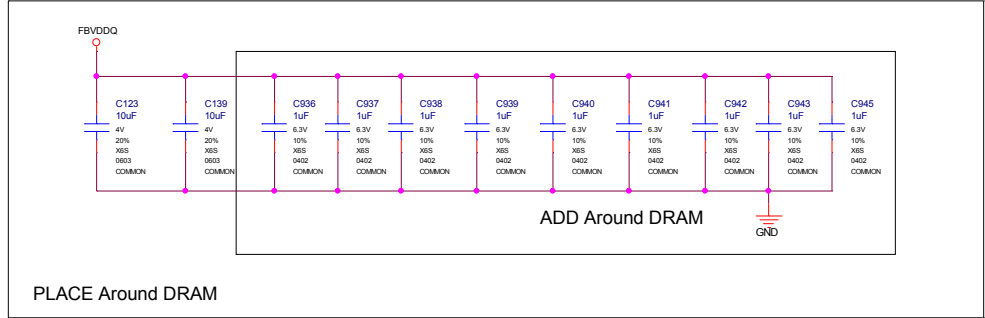
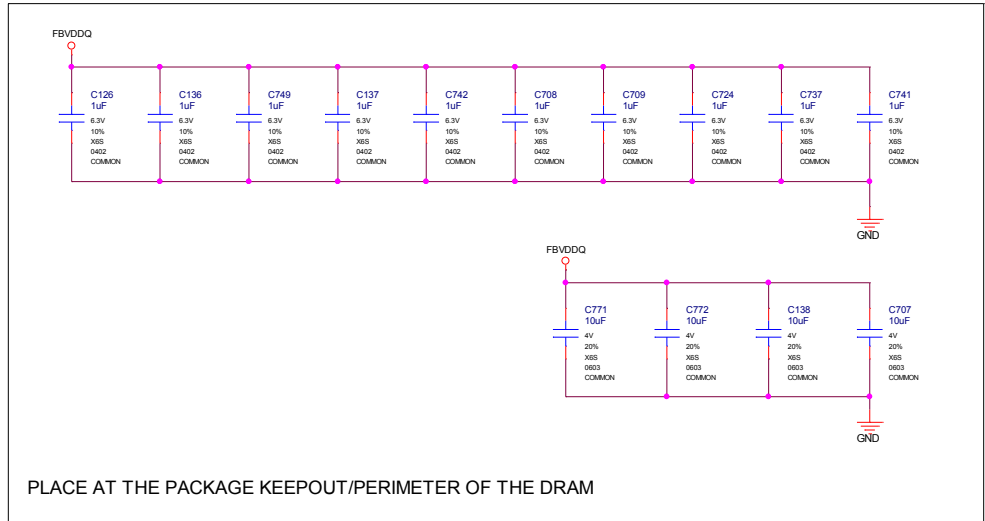
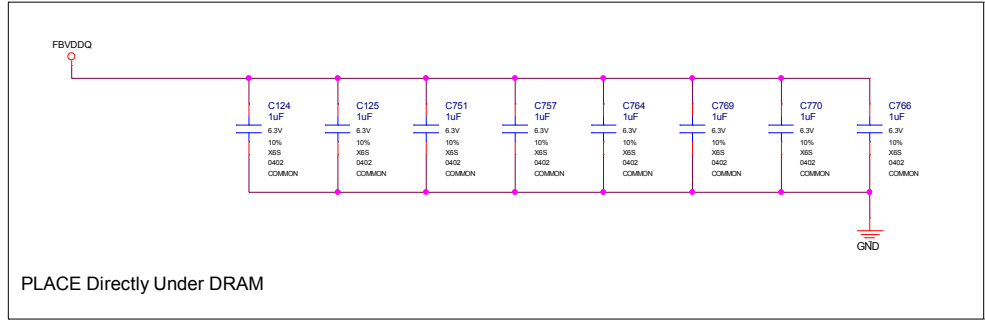
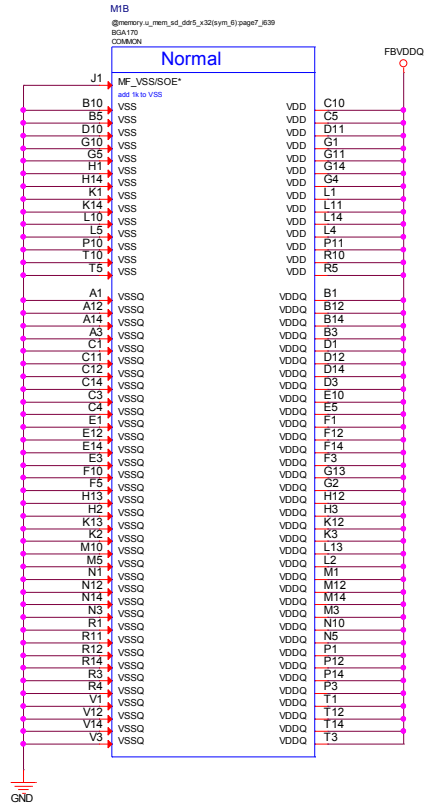
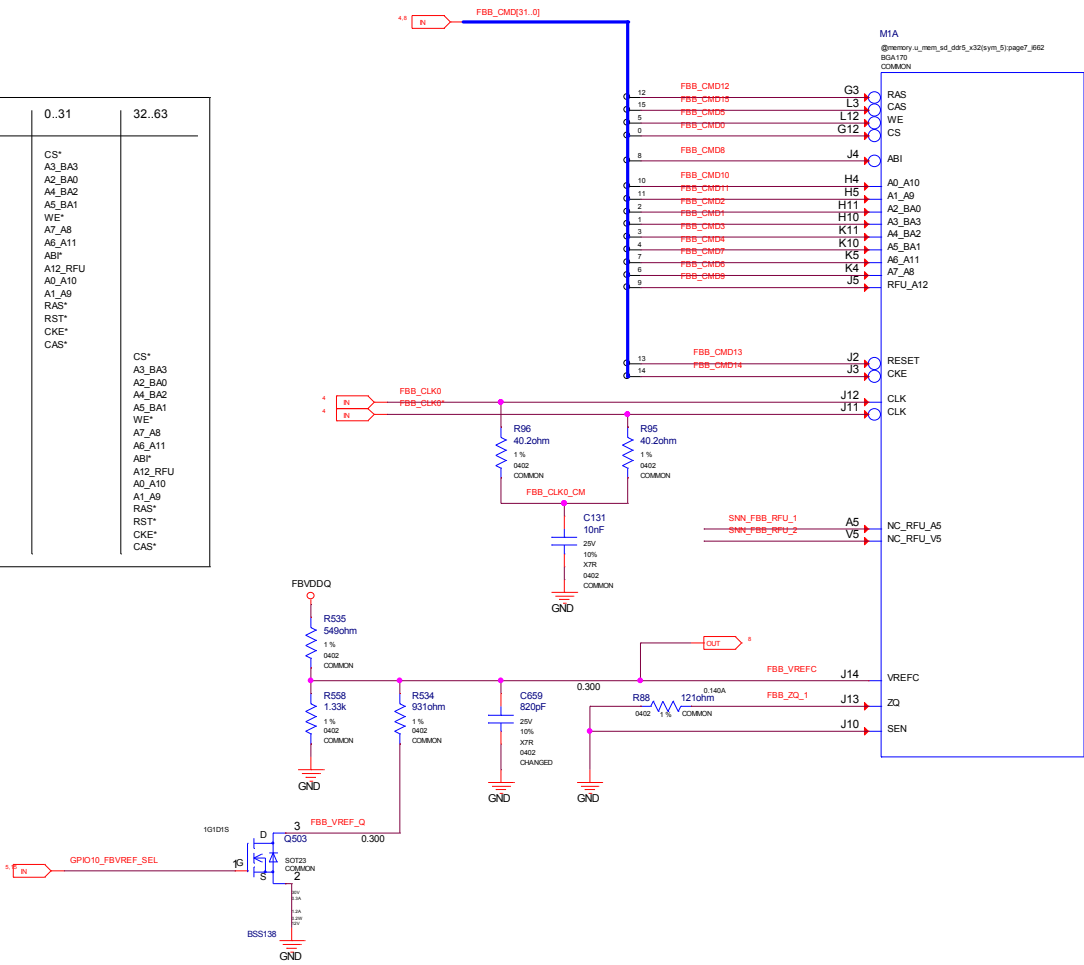
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NV_PN	600-1G210-BASE-SCH		
PCB REV	PG210-800	PAGE	6 OF 27
BOM REV	A	DATE	18-DEC-2015

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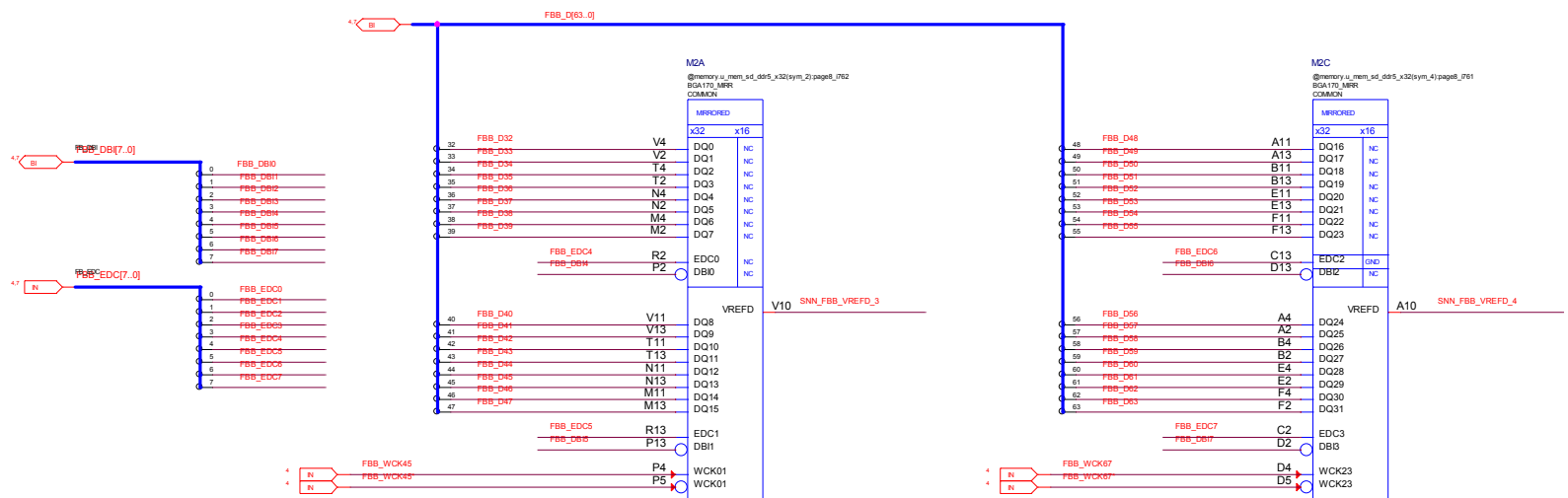
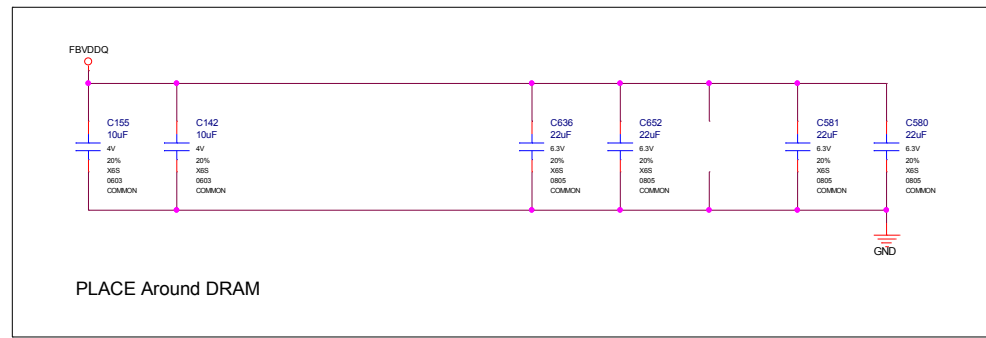
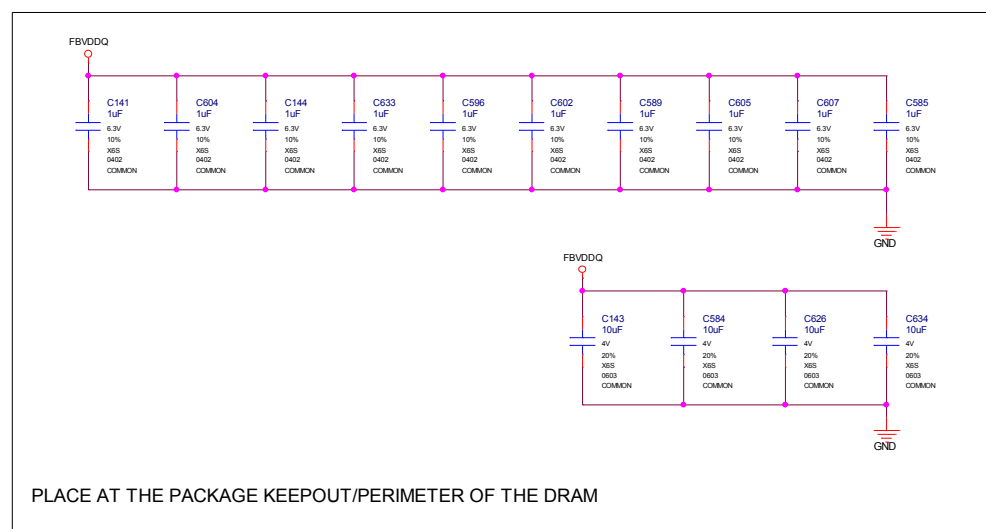
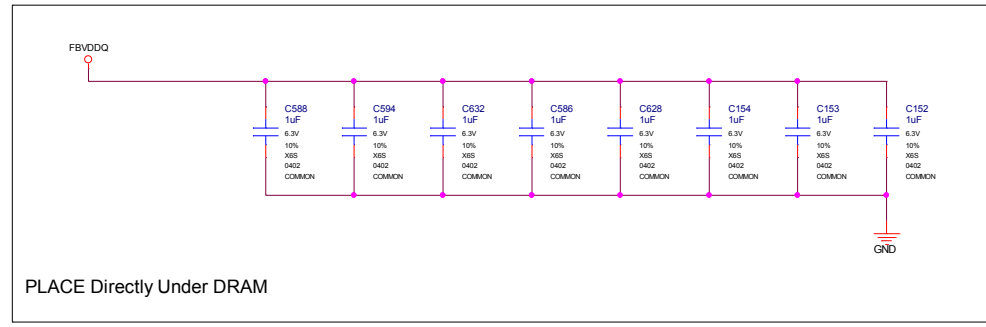
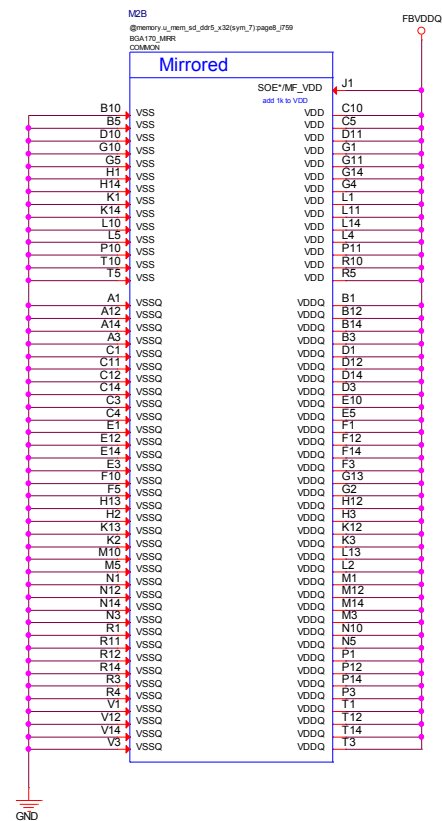
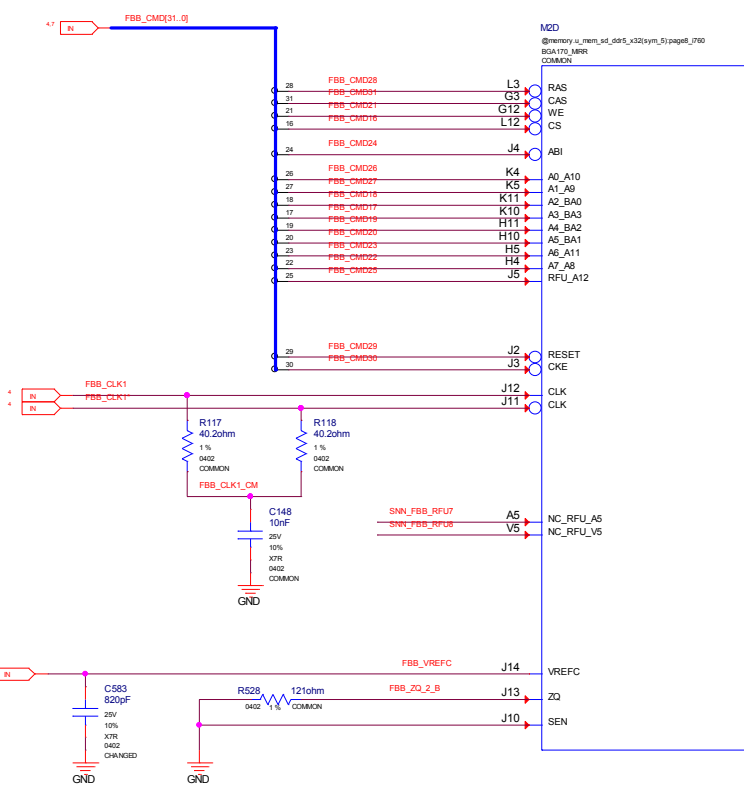
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CMD3	A4_BA2	
CMD4	A5_BA1	
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CMD6	A7_A8	
CMD7	A6_A11	
CMD8	AB*	
CMD9	A12_RFU	
CMD10	A0_A10	
CMD11	A1_A9	
CMD12	RAS*	
CMD13	RST*	
CMD14	CKE*	
CMD15	CAS*	
CMD16		CS*
CMD17	A3_BA3	
CMD18	A2_BA0	
CMD19	A4_BA2	
CMD20	A5_BA1	
CMD21	WE*	
CMD22	A7_A8	
CMD23	A6_A11	
CMD24	AB*	
CMD25	A12_RFU	
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CMD27	A1_A9	
CMD28	RAS*	
CMD29	RST*	
CMD30	CKE*	
CMD31	CAS*	



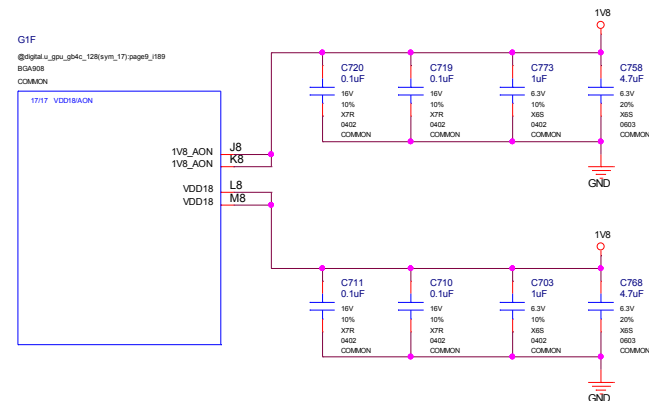
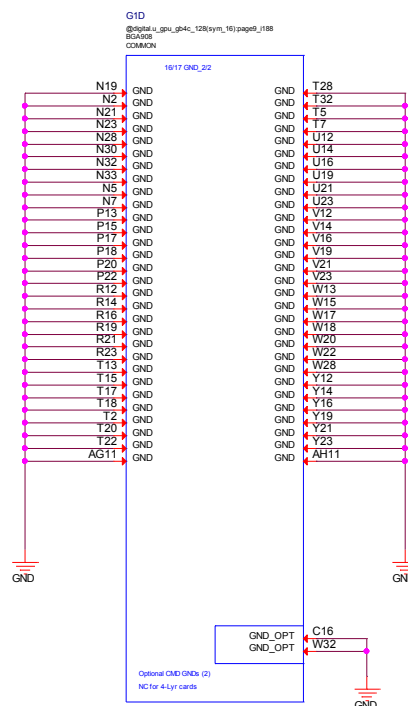
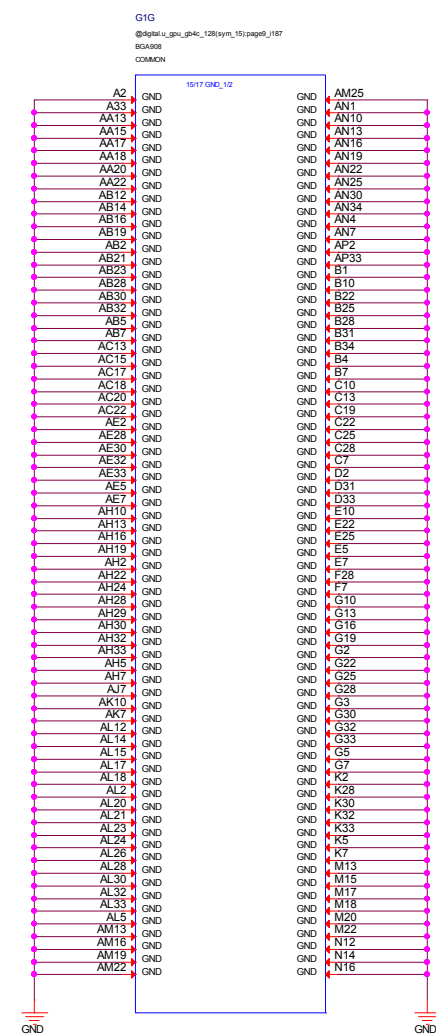
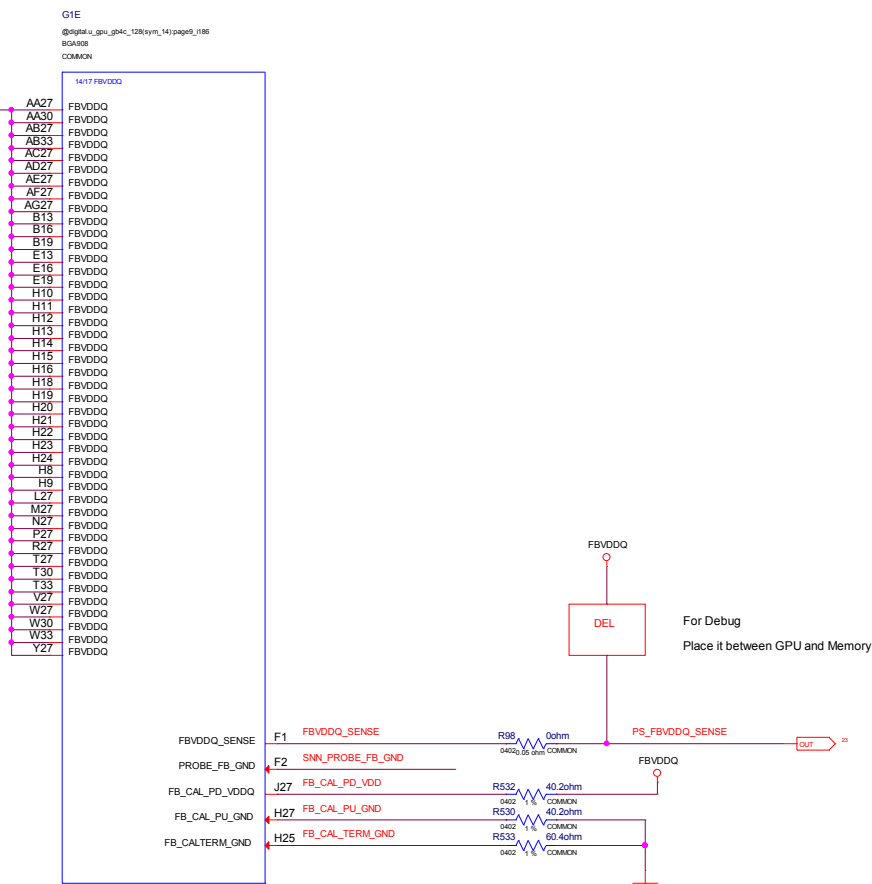
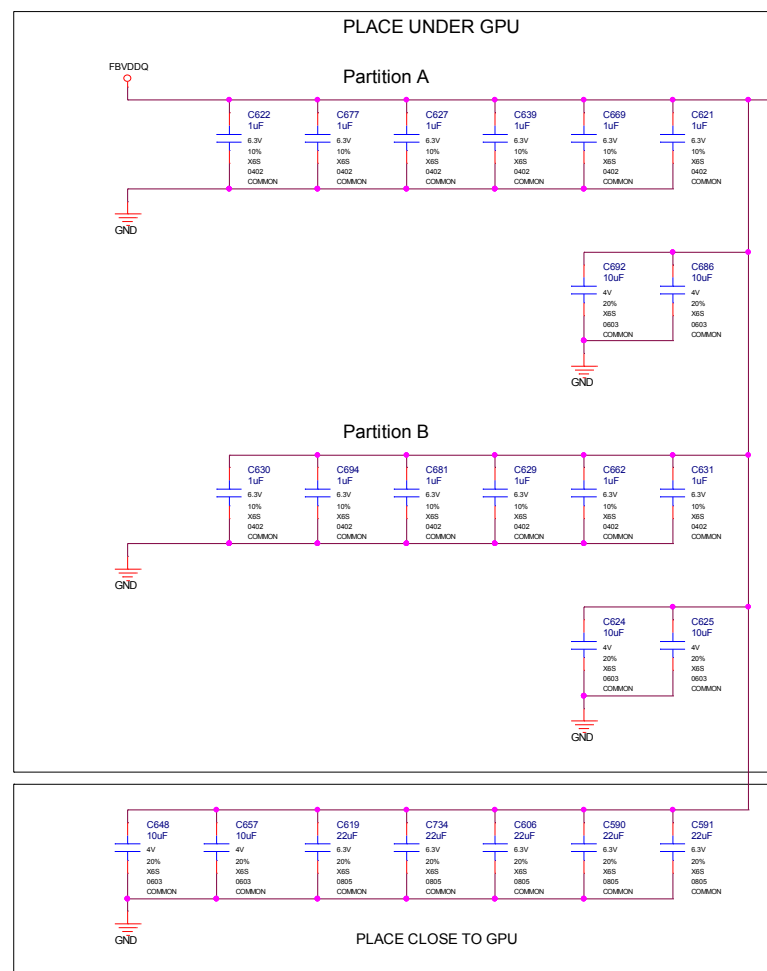
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CMD25		A12_RFU
CMD26		A0_A10
CMD27		A1_A9
CMD28		RAS*
CMD29		RST*
CMD30		CKE*
CMD31		CAS*



GPU FBVDDQ & 1V8 Decoupling



ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	GPU FBVDDQ & 1V8 Decoupling

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PCB REV	PG210-B00
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PAGE	9 OF 27
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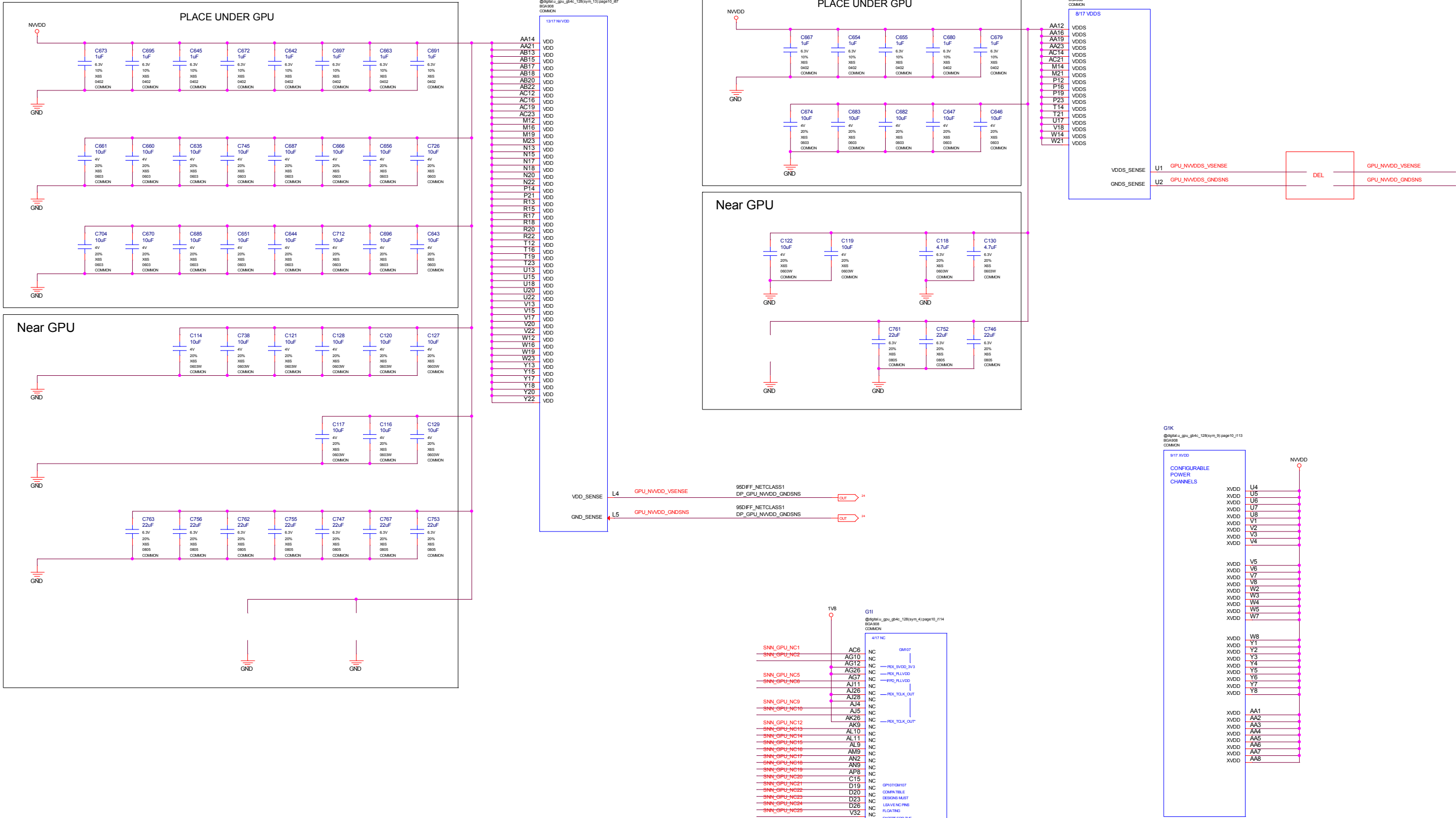
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9 OF 27

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GPU NVVDD & NVVDDS Decoupling



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PAGE DETAIL	GPU NWDD & NWDDS DECOUPLING

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NV_PN	600-1G210-BASE-SCH
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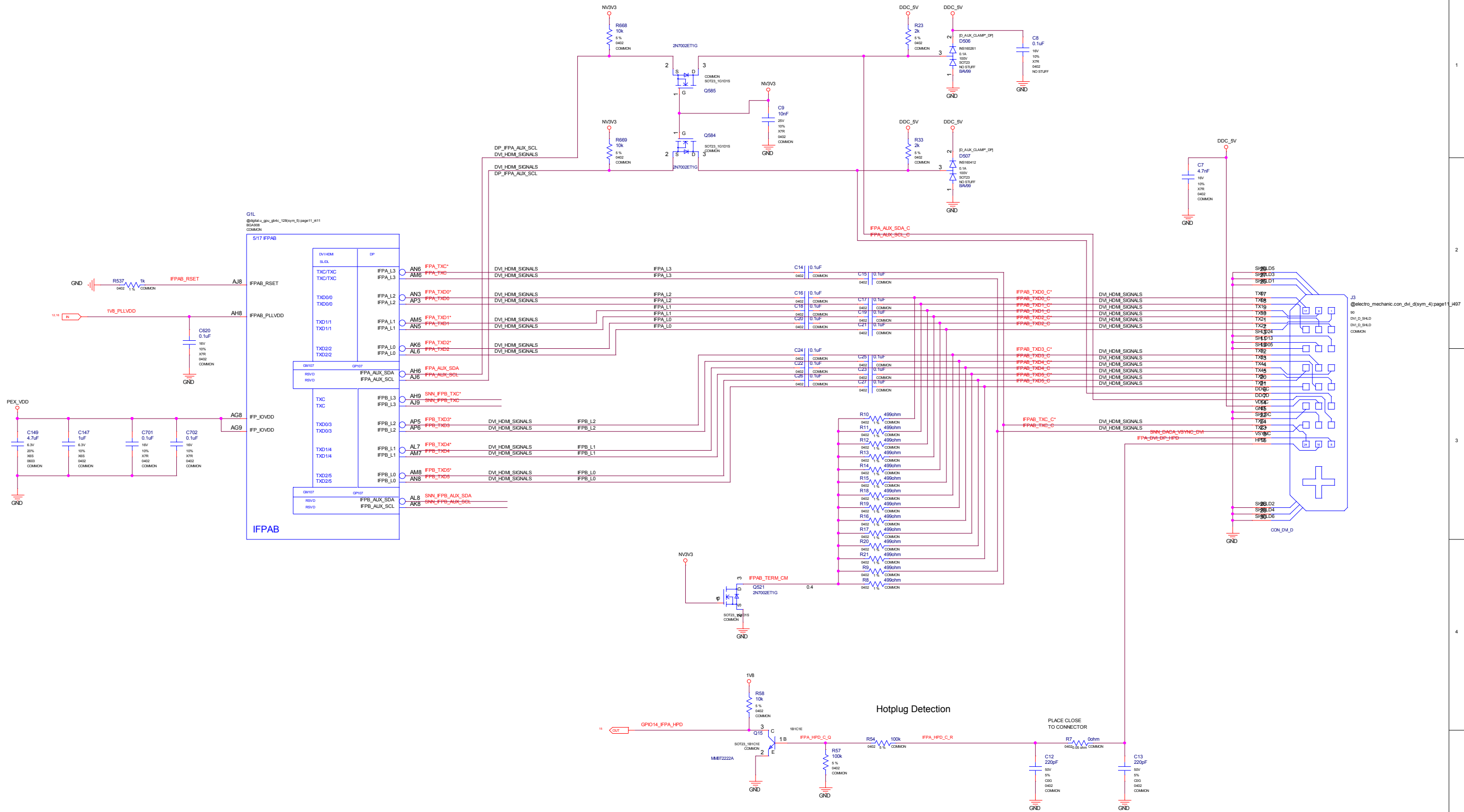
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PAGE 10 OF 27

DATE	18-DEC-2015
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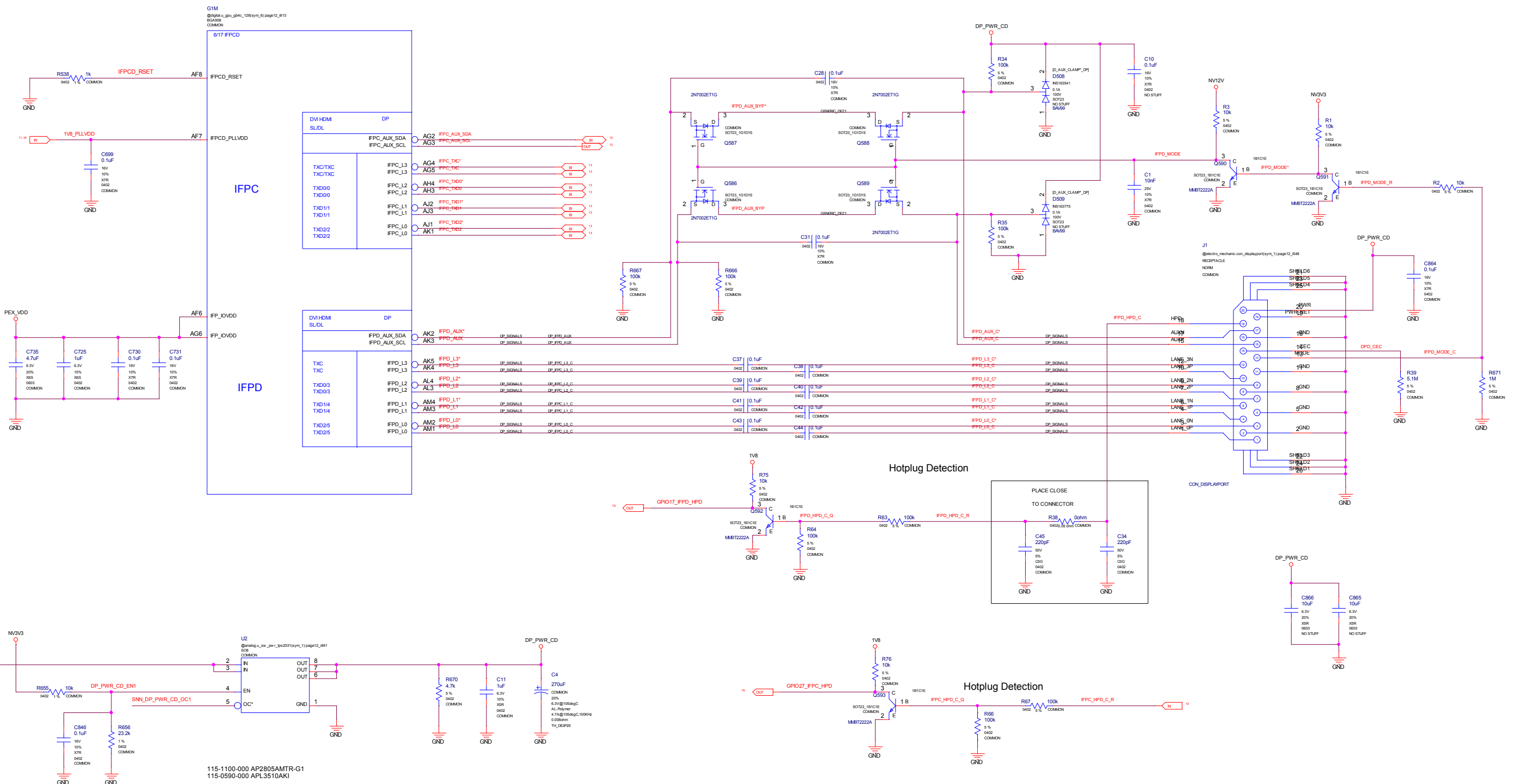
IFPAB DVI-DL



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NVIDIA CORPORATION		2701 SAN TOMAS EXPRESSWAY	
SANTA CLARA, CA 95050, USA			
NV_PN	600-1G210-BASE-SCH	PAGE	11 OF 27
PCB REV	PG210-800	DATE	18-DEC-2015
BOM REV	A		

IFPD DP



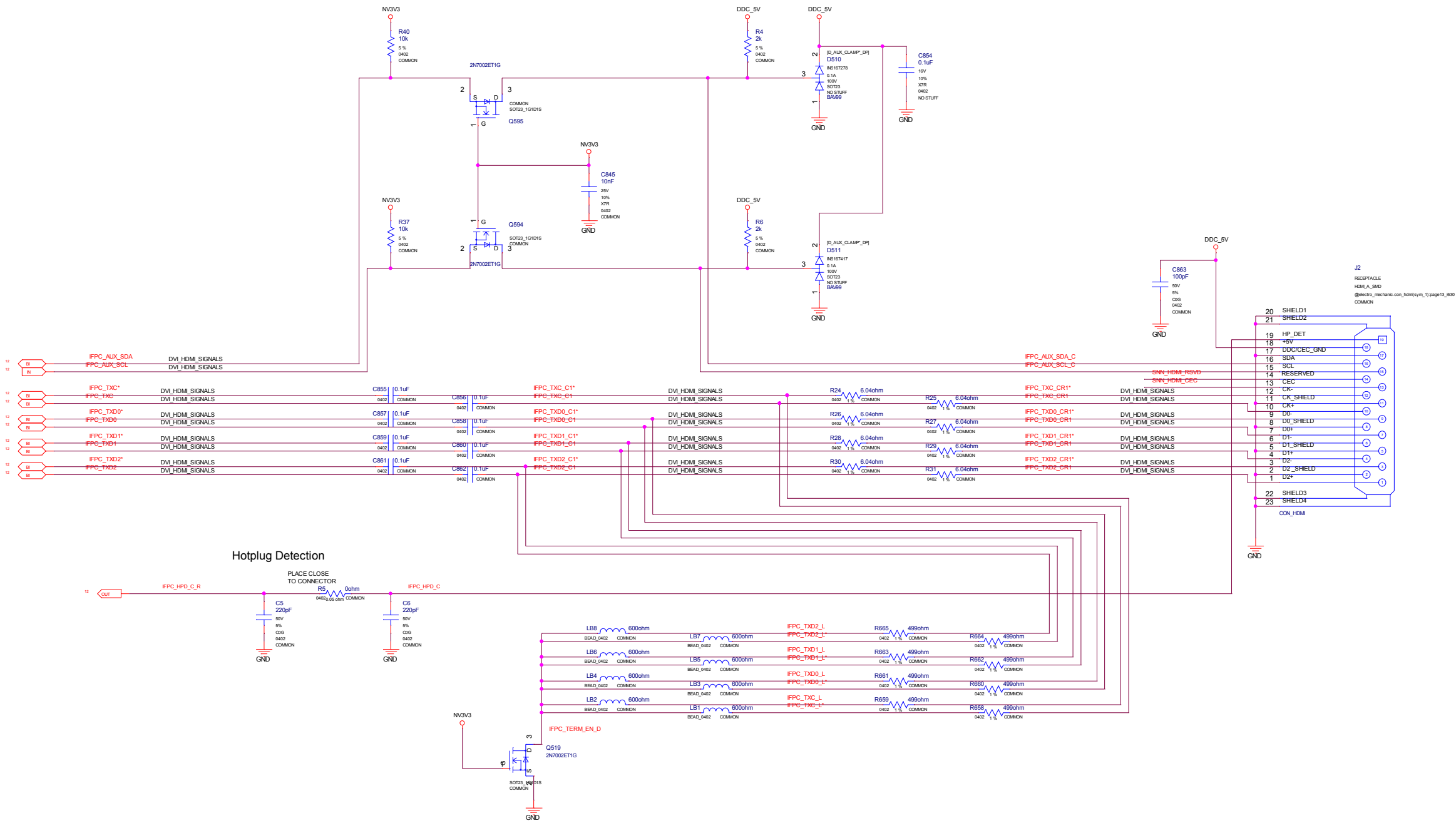
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PAGE DETAIL	IFPD DP

NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA				
NV_PN		600-1G210-BASE-SCH		
PCB REV	P0210-B000	PAGE	12 OF 27	
BOM REV	A	DATE	18-DEC-2015	




IFPC HDMI

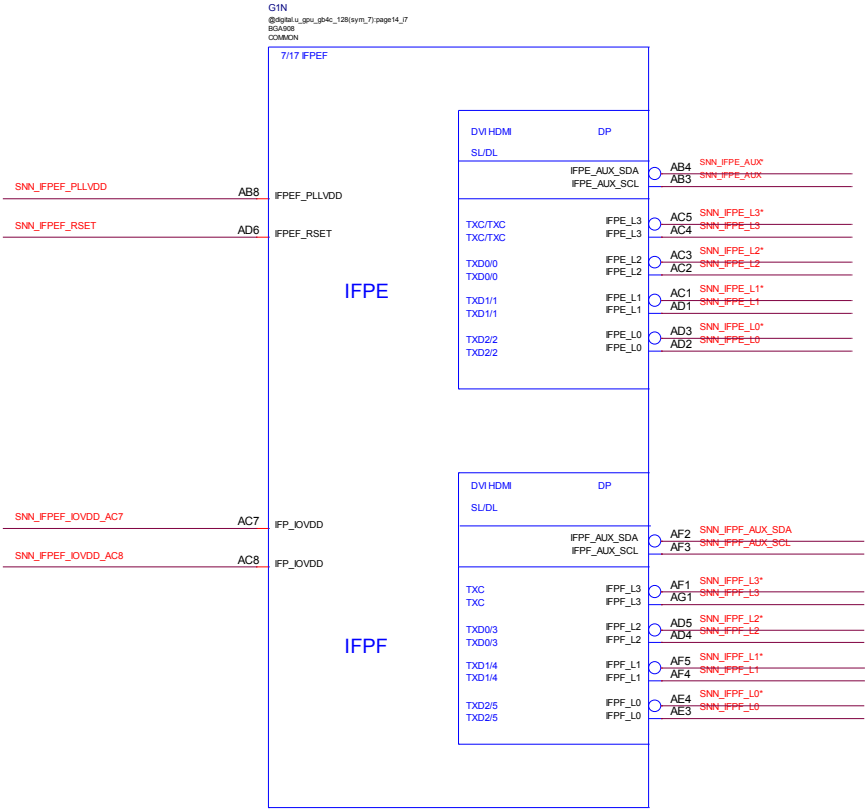


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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	IFPC HDMI

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2701 SAN TOMAS EXPRESSWAY			
SANTA CLARA, CA 95050, USA			
NV_PN	600-1G210-BASE-SCH		
PCB REV	PG210-B00	PAGE	13 OF 27
BOM REV	A	DATE	18-DEC-2015

IFPEF UNUSED




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NVIDIA CORPORATION

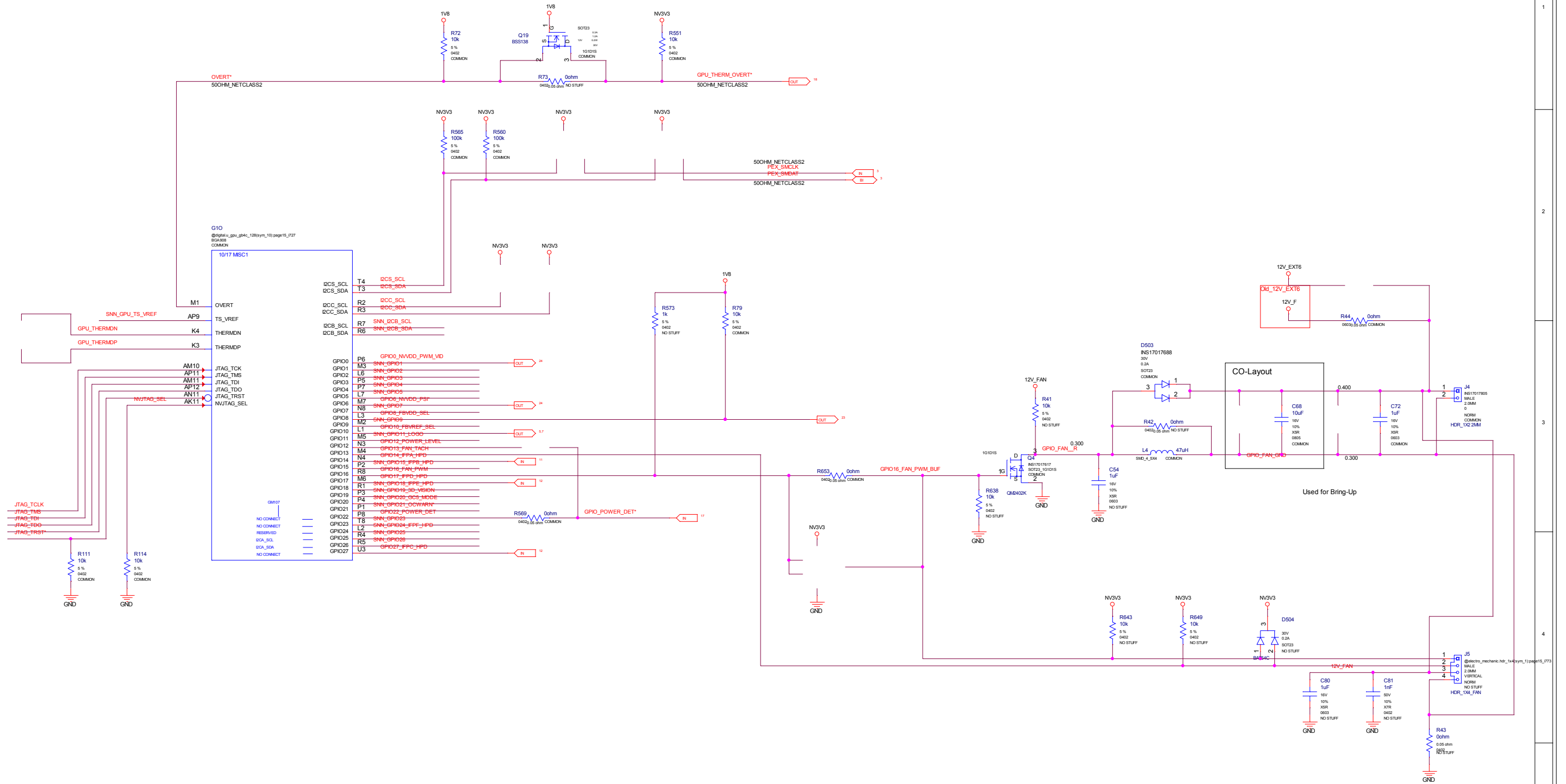
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NV_PN	600-1G210-BASE-SCH		
PCB REV	PG210-B00	PAGE	14 OF 27
BOM REV	A	DATE	18-DEC-2015

MISC1: GPIO, Fan, Thermal, JTAG



ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	MISC1: Fan, Thermal, JTAG, GPIO

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NV_PN	600-1G210-BASE-SCH
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PCB REV	PG210-B00
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BOMREV	A
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PAGE	15 OF 27
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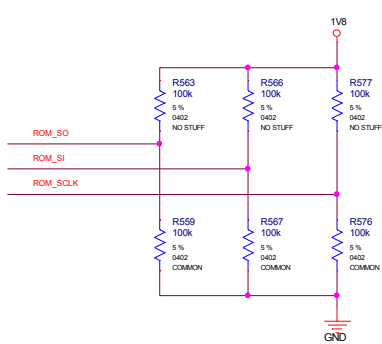
DATE	18-DEC-2015
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MISC2: ROM, XTAL, Straps

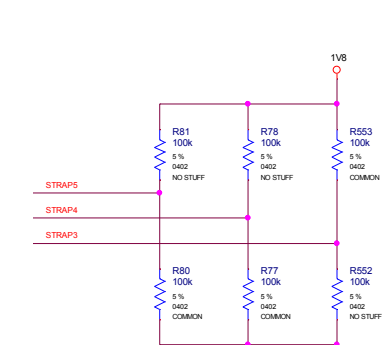
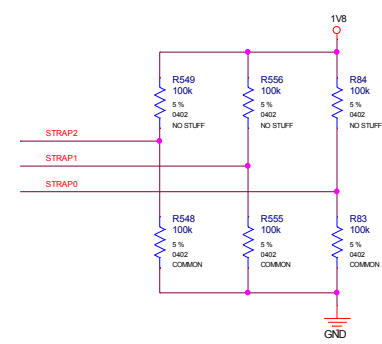
ROM_SO	ROM_SI	ROM_SCLK	SOR_EXPOSED[3:0]	1:ENABLE 0:DISABLE
L	L	L	1111	Default
L	L	H	1110	
L	H	L	1101	
L	H	H	1100	
H	L	L	1011	
H	L	H	1010	
H	H	L	1001	
H	H	H	1000	
L	L	M	0111	
L	M	L	0110	
L	M	H	0101	
L	H	M	0100	
H	L	M	0011	
H	M	L	0010	
H	M	H	0001	
H	H	M	0000	

STRAP2	STRAP1	STRAP0	RAMCFG[4:0]	
L	L	L	0000	Samsung B-Die 8Gb 256Mx32
L	L	H	0001	Micron F-Die 8Gb 256Mx32
L	H	L	0010	Hynix M-Die 8Gb 256Mx32
L	H	H	0011	
H	H	L	0110	Hynix A-Die 4Gb 128Mx32
H	H	H	0111	Samsung E-Die 4Gb 128Mx32
L	L	M	1000	Micron B-Die 4Gb 128Mx32
L	M	L	1001	

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1 DEFAULT
L	L	L	0	0	0	0



H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V

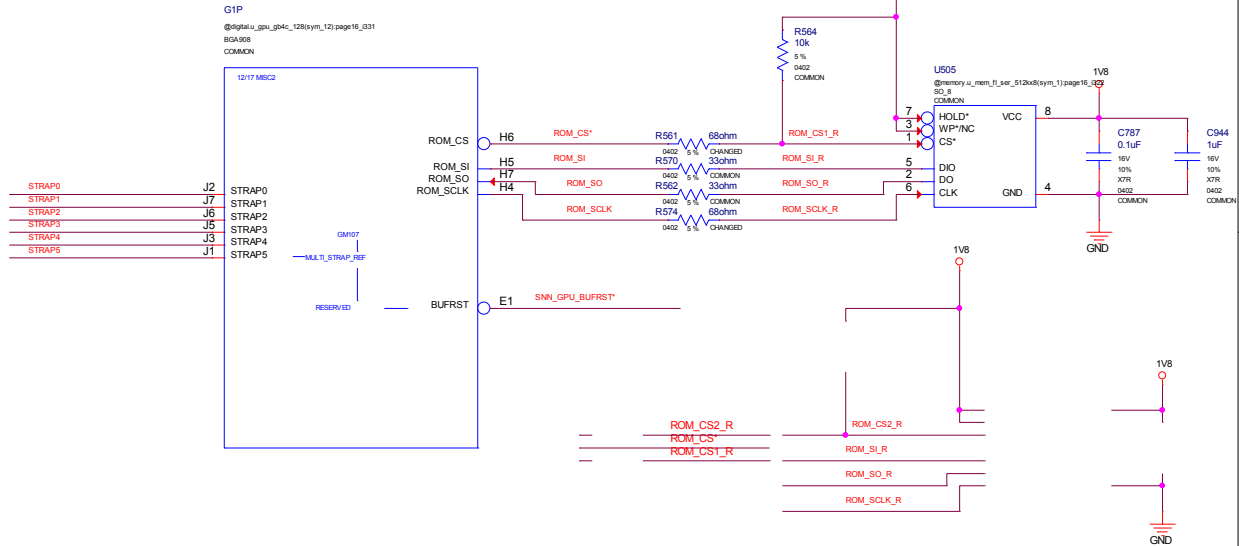


1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE

1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGINAL

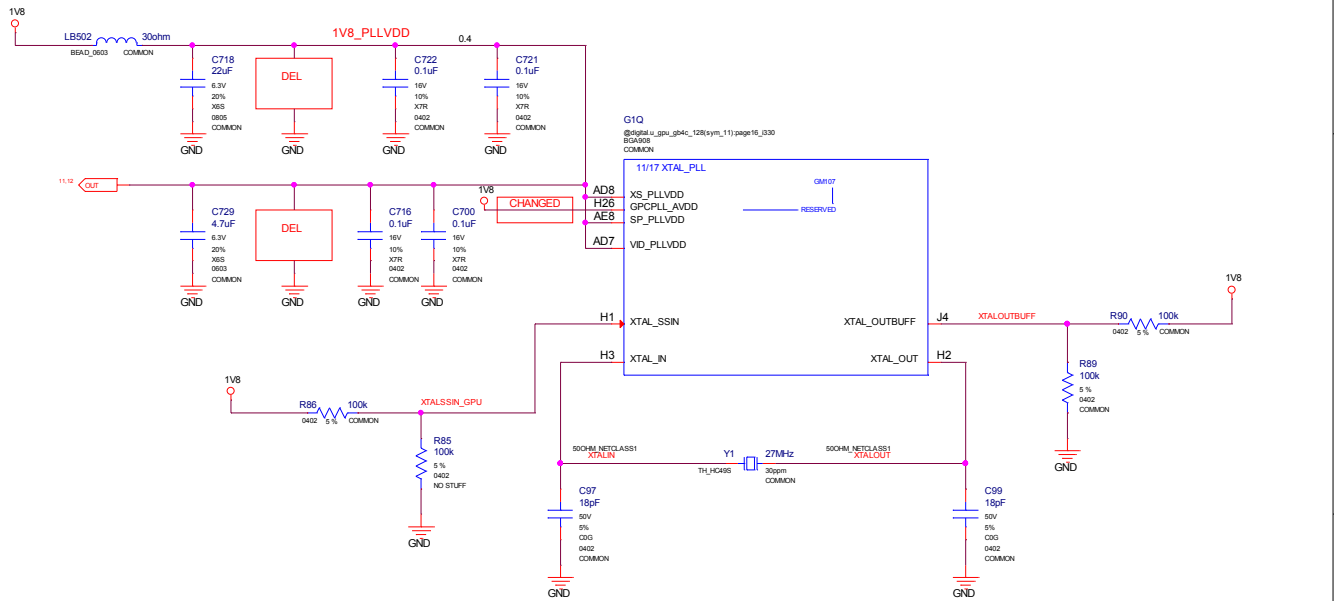
1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER

1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE



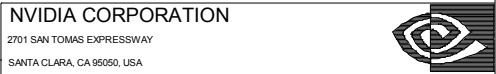
Smart Fan

XTALOUTBUFF	Volatge	SmartFan PWM	GPIO/PWM at the FAN
Low	0V	GPIO Disabled	100%
Middle	0.9V	66% PWM	33% PWM (Typical)
High	1.8V	33% PWM	66% PWM

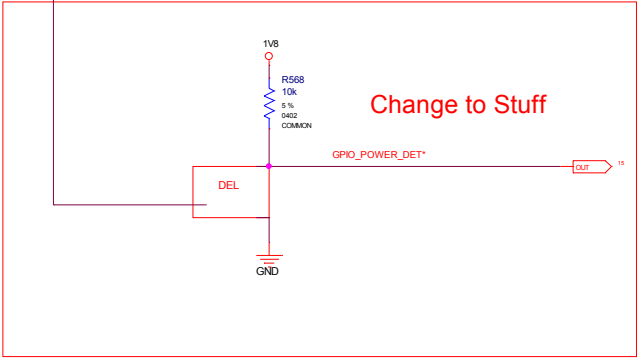
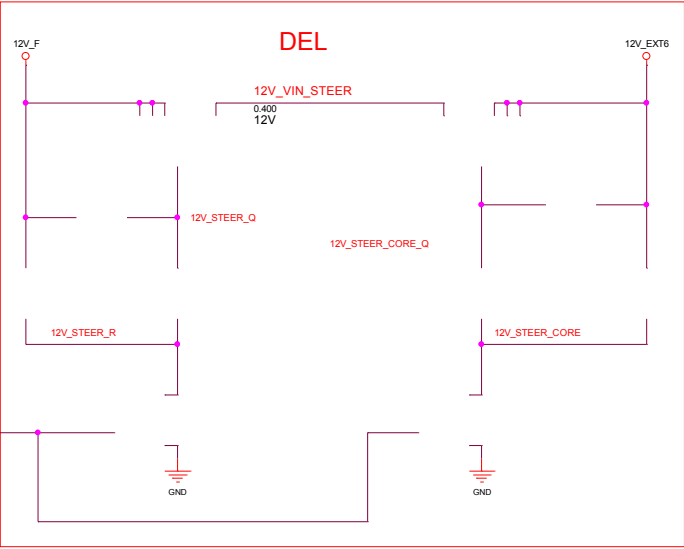
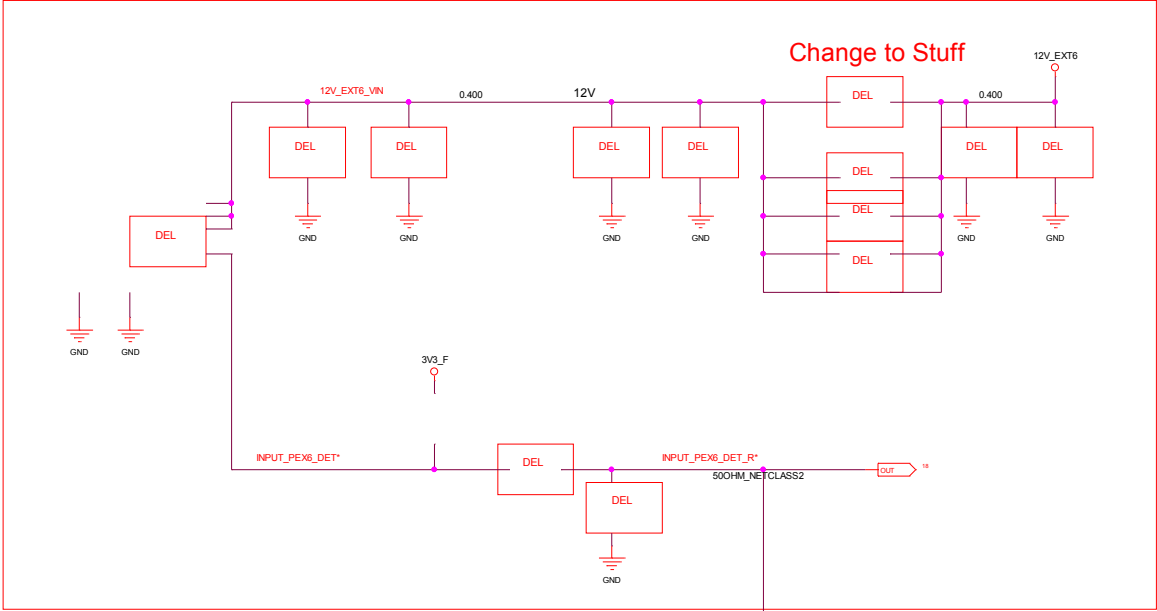
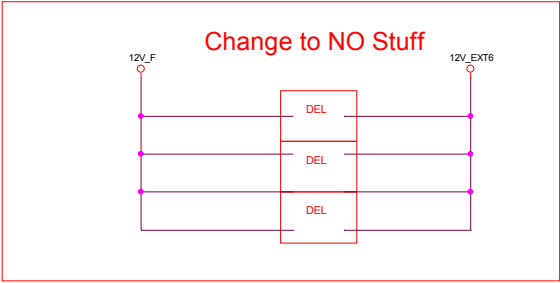
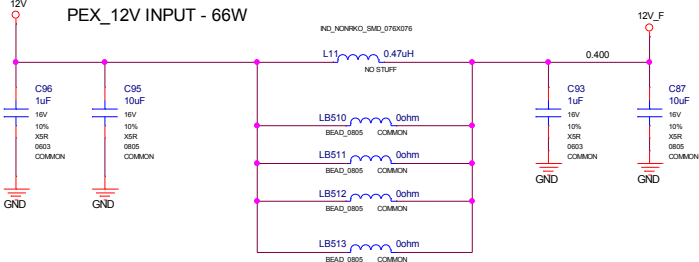
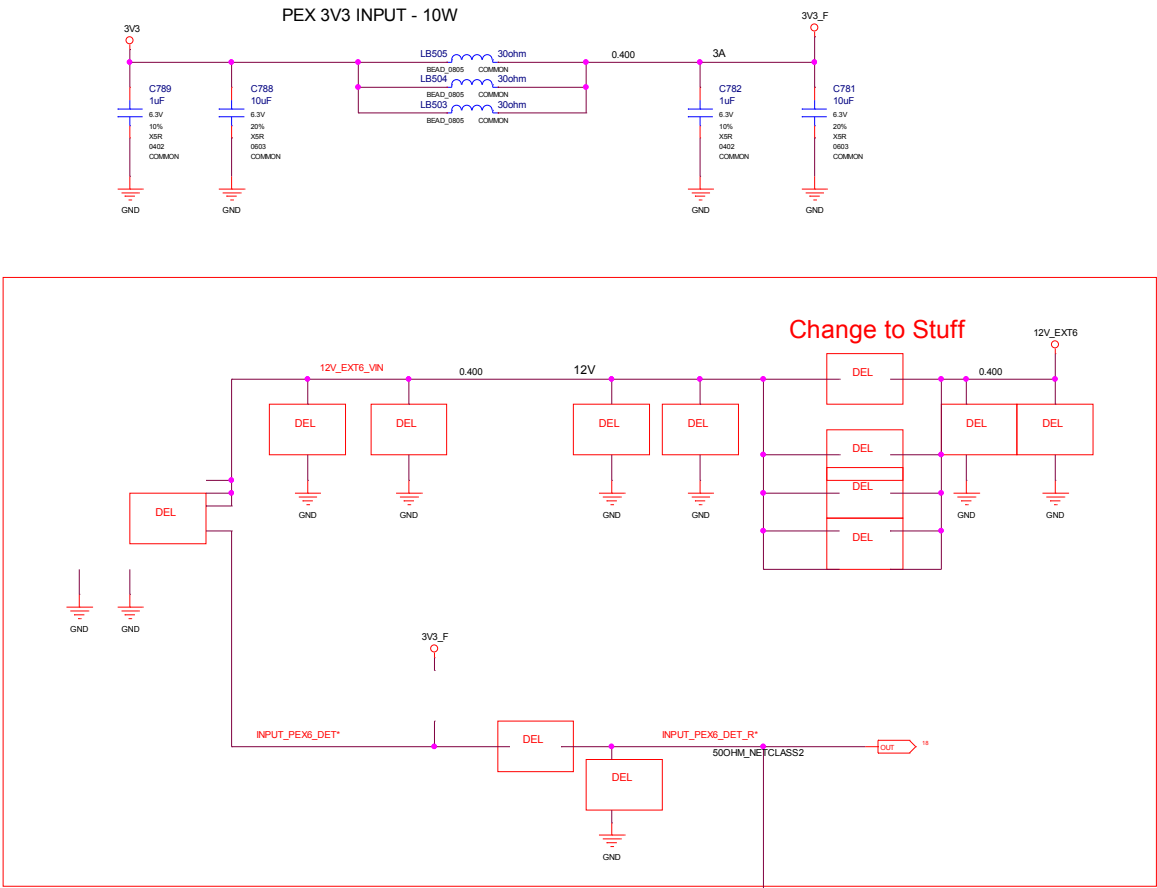


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ASSEMBLY	<ASSEMBLY_DESCRIPTION>		
PAGE DETAIL	MSC2: ROM, XTAL, Straps		
NV_PN	600-1G210-BASE-SCH		
PCB REV	PG210-800	PAGE	16 OF 27
BOM REV	A	DATE	18-DEC-2015



Power Inputs and Filtering

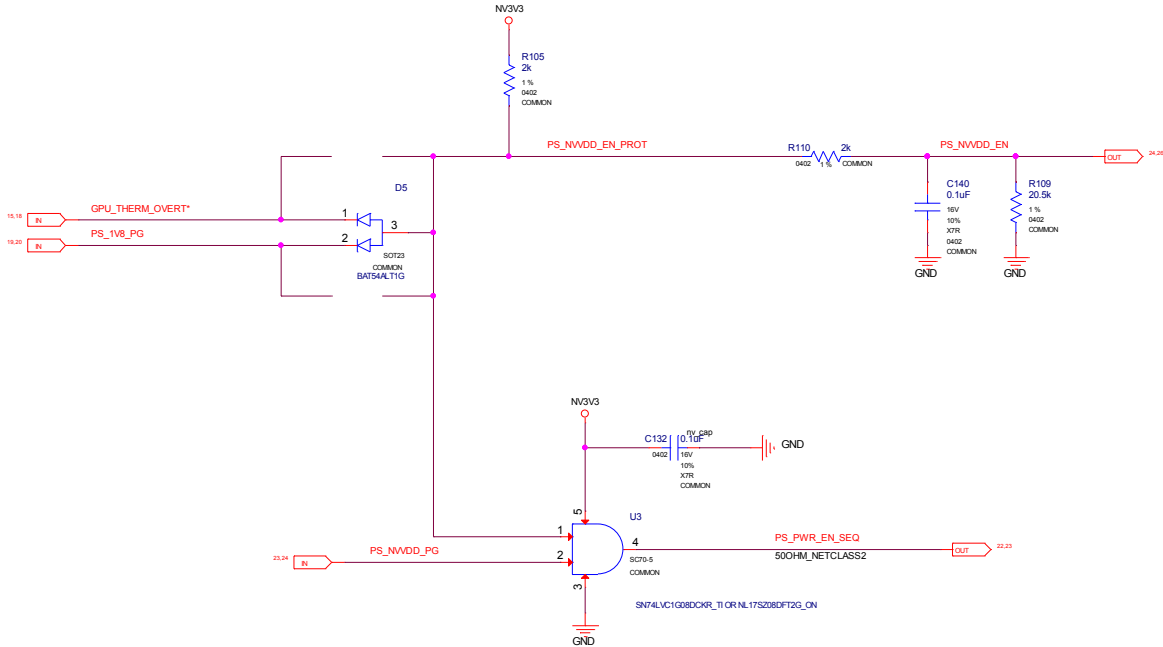
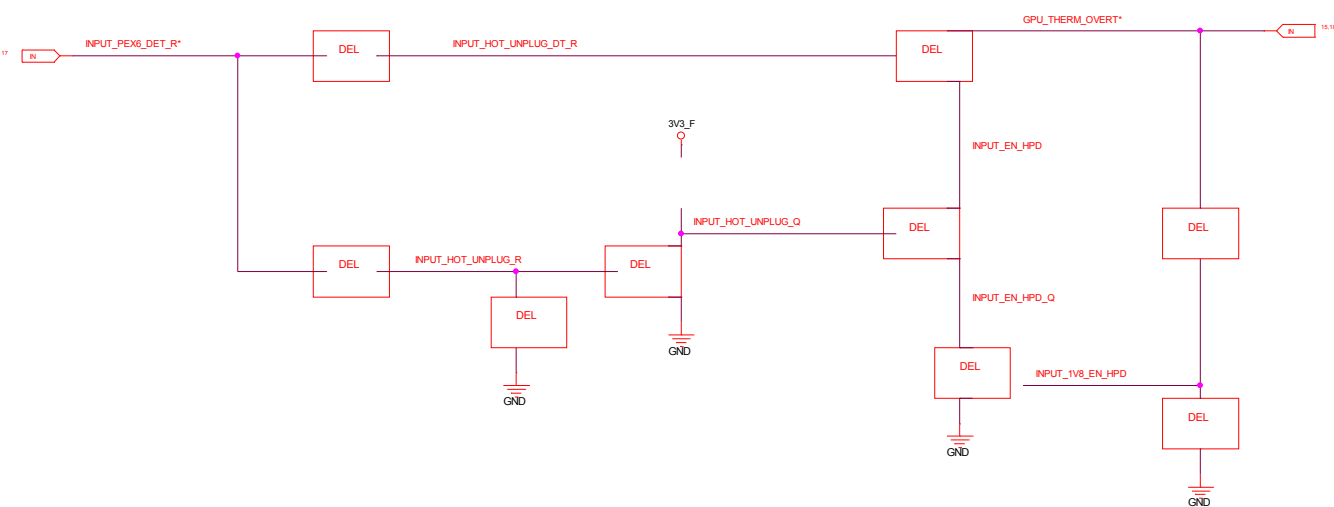
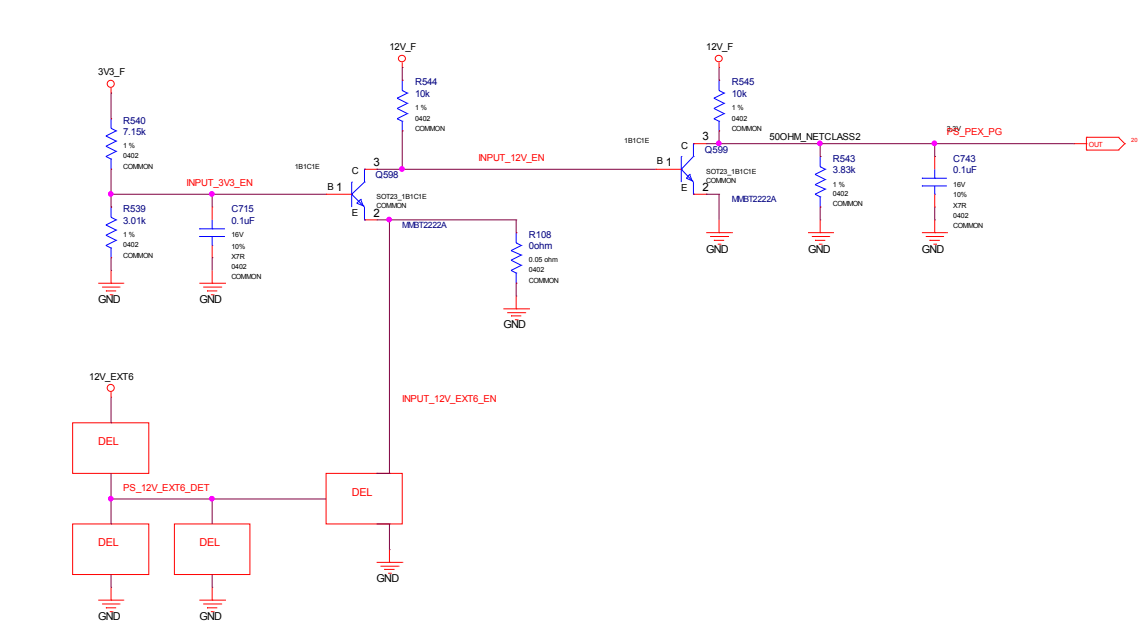


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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	Power Inputs and Filtering


NVIDIA CORPORATION		2701 SAN TOMAS EXPRESSWAY	
SANTA CLARA, CA 95050, USA		PAGE 17 OF 27	
NV_PN	600-1G210-BASE-SCH	DATE	18-DEC-2015
PCB REV	PG210-B00		
BOM REV	A		

Power sequence

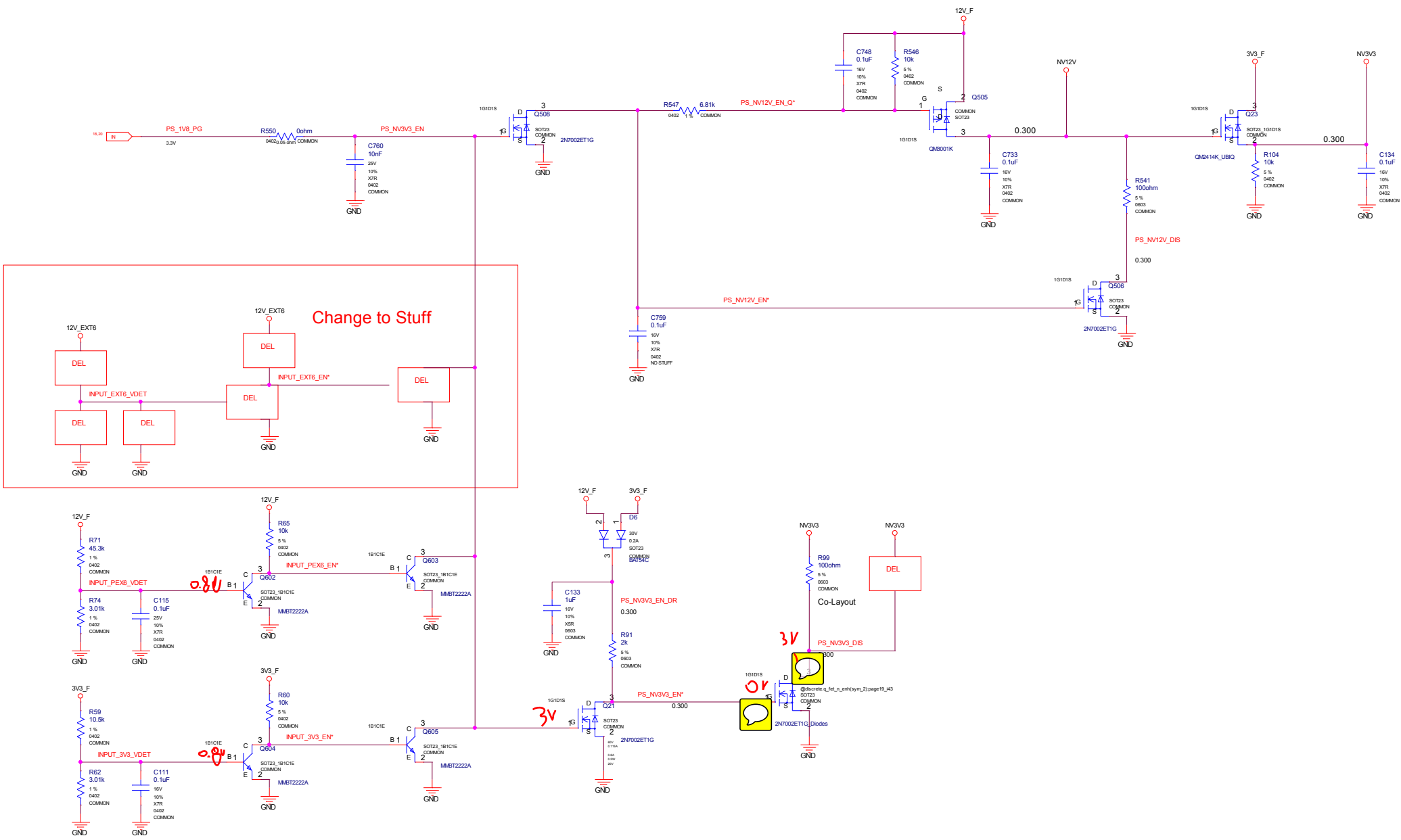


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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	Power Sequence


NVIDIA CORPORATION			
2701 SAN TOMAS EXPRESSWAY			
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NV_PN	600-1G210-BASE-SCH		
PCB REV	PG210-B00	PAGE	18 OF 27
BOM REV	A	DATE	18-DEC-2015

PS: NV3V3 & NV12V

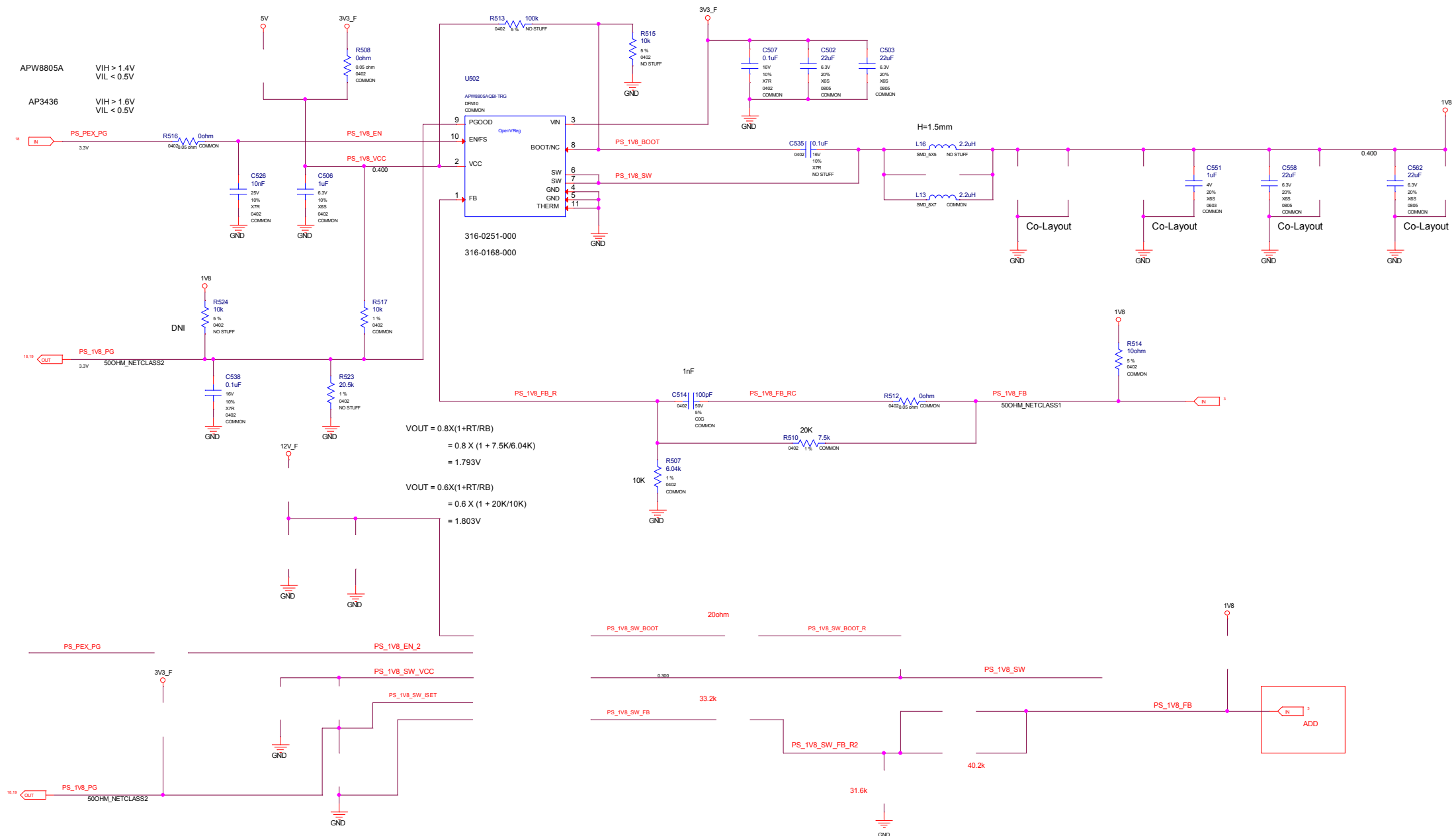


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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: NV3V3 & NV12V

NVIDIA CORPORATION			
2701 SAN TOMAS EXPRESSWAY			
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NV_PN	600-1G210-BASE-SCH		
PCB REV	PG210-B00	PAGE	19 OF 27
BOM REV	A	DATE	18-DEC-2015

PS I: 1V8

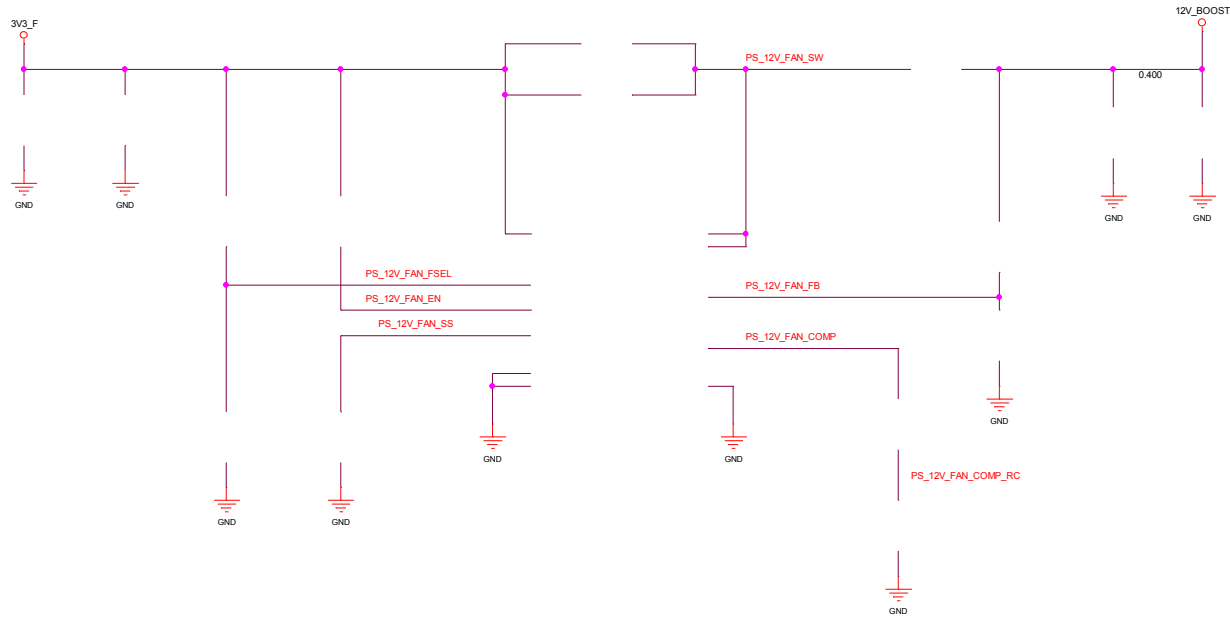
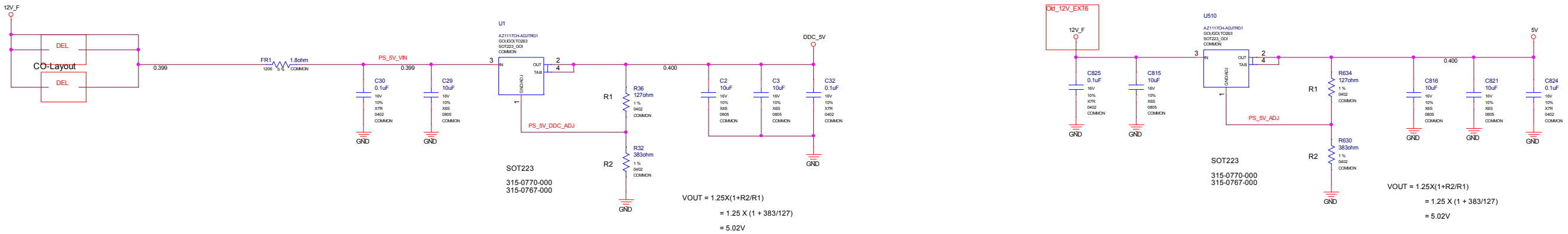


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ASSEMBLY	<ASSEMBLY_DESCRIPTION>	SANTA CLARA, CA 95050, USA			
PAGE DETAIL	PS: 1VS	NV_PN	600-1G210-BASE-SCH		
		PCB REV	PG210-B00	PAGE	20 OF 27
		BOM REV	A	DATE	18-DEC-2015



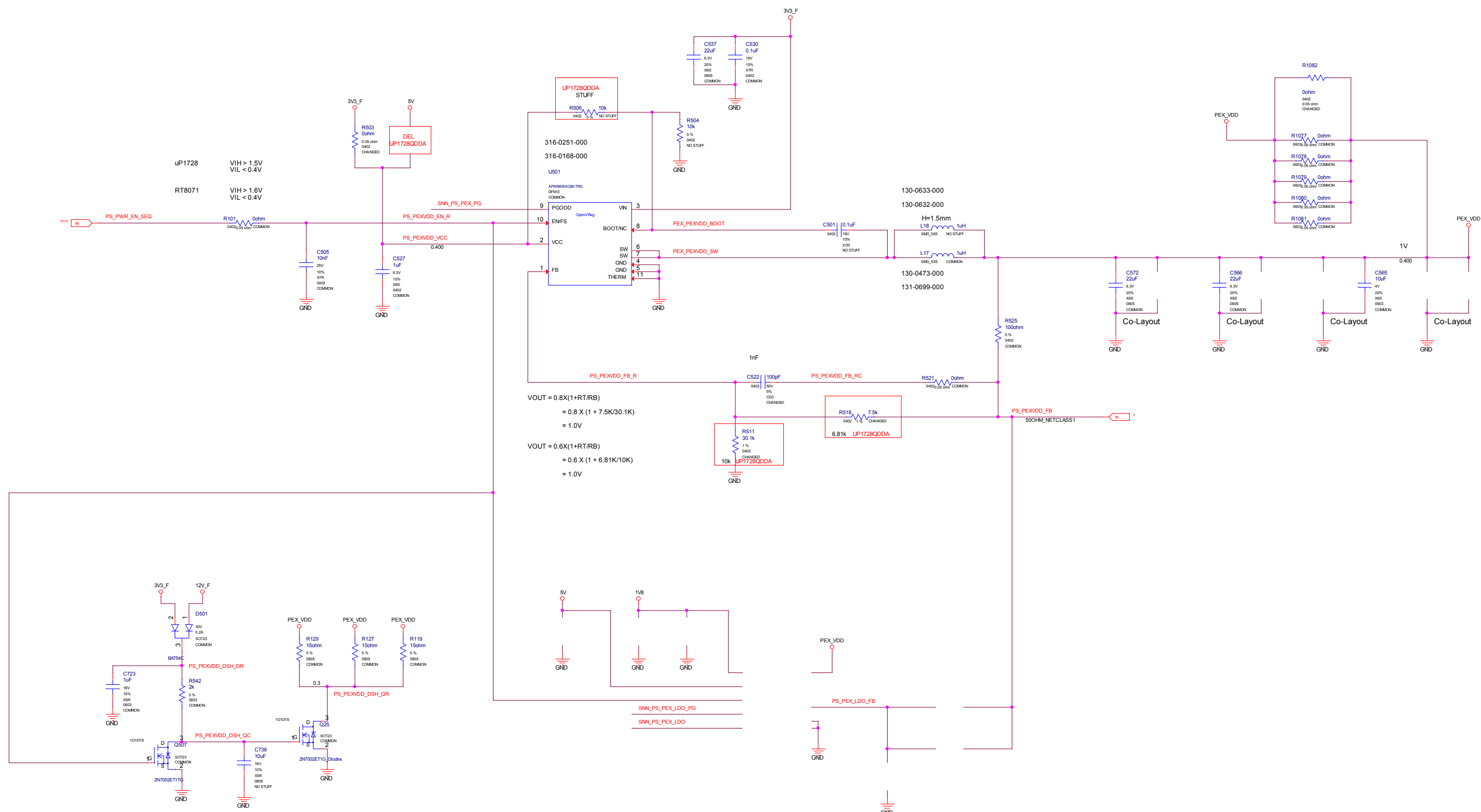
PSII:5V & 12V_FAN



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>	SANTA CLARA, CA 95050, USA			
PAGE DETAIL	PSII: 5V & 12V_FAN	NV_PN	600-1G210-BASE-SCH		
		PCB REV	PG210-B00	PAGE	21 OF 27
		BOM REV	A	DATE	18-DEC-2015

PS III: PEXVDD

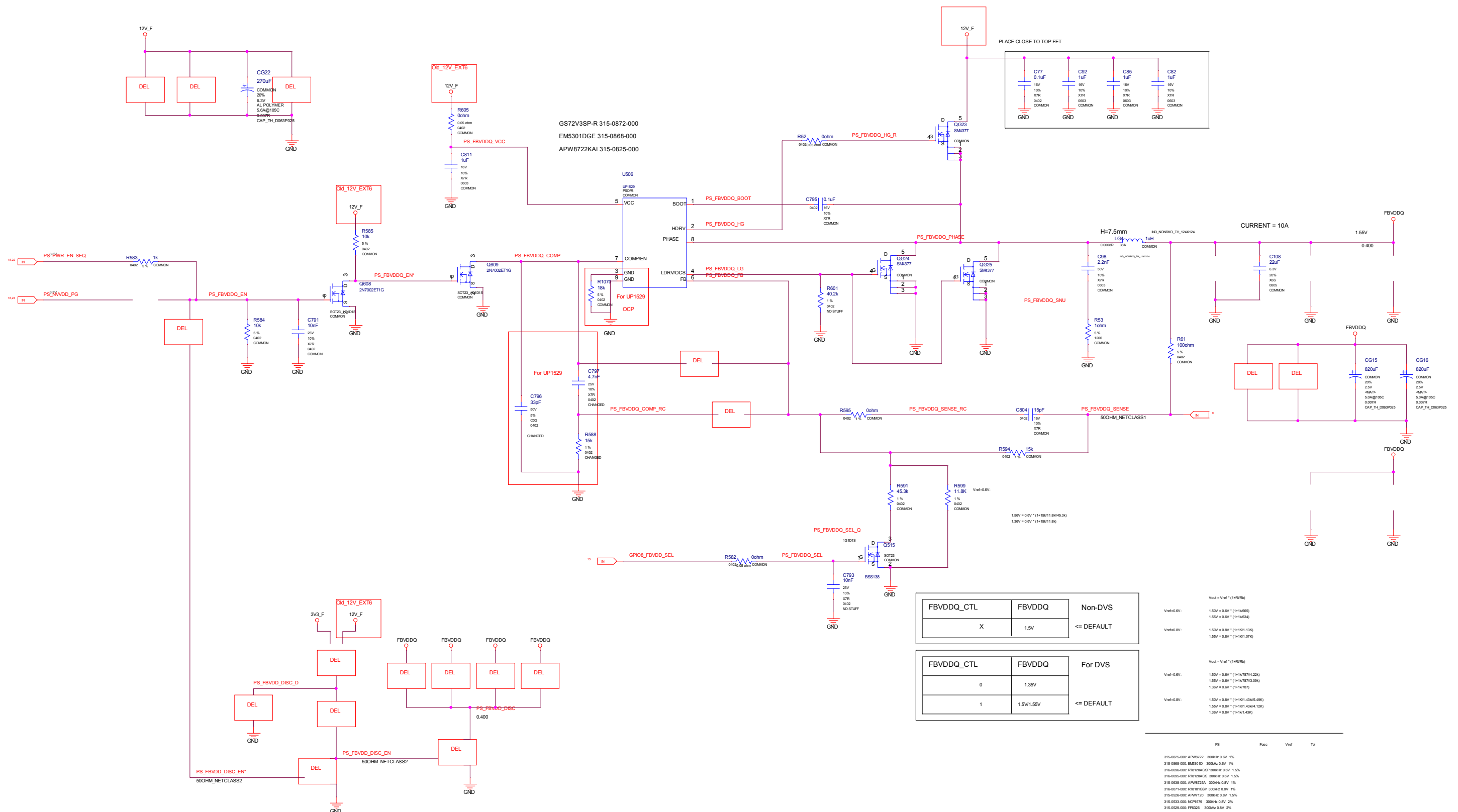


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ASSEMBLY	<ASSEMBLY_DESCRIPTION>	SANTA CLARA, CA 95050, USA	
PAGE DETAIL	PS III: PEXVDD	NV_PN	600-1G210-BASE-SCH
		PCB REV	PG210-B00
		BOM REV	A
		PAGE	22 OF 27
		DATE	18-DEC-2015



PS IV: FBVDDQ




FBVDDQ_CTL	FBVDDQ	Non-DVS
X	1.5V	<= DEFAULT

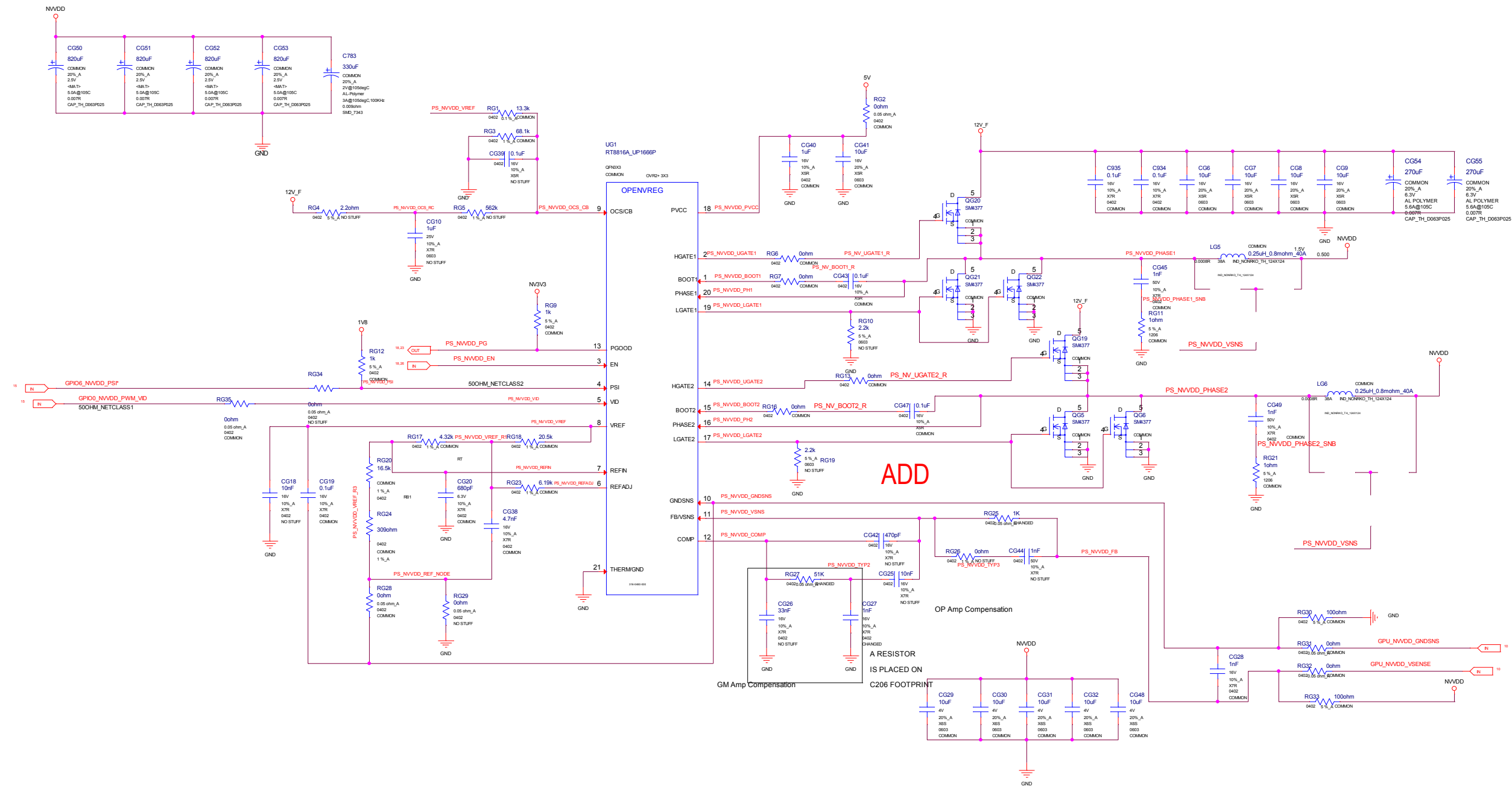
FBVDDQ_CTL	FBVDDQ	For DVS
0	1.35V	
1	1.5V/1.55V	<= DEFAULT

	$V_{out} = V_{in} e^{\frac{1}{R_1 C_1} t}$ (1498R8)
$V_{in} = 0.5V$:	$1.50V = 0.5V \cdot (1 + 14.6655)$ $1.50V = 0.5V \cdot (1 + 14.634)$
$V_{in} = 0.7V$:	$1.50V = 0.7V \cdot (1 + 16.1139)$ $1.50V = 0.7V \cdot (1 + 16.1279)$
	$V_{out} = V_{in} e^{\frac{1}{R_1 C_1} t}$ (1498R8)
$V_{in} = 0.5V$:	$1.50V = 0.5V \cdot (1 + 14.7873(0.226))$ $1.50V = 0.5V \cdot (1 + 14.7873(0.226))$ $1.36V = 0.5V \cdot (1 + 14.7873)$
$V_{in} = 0.7V$:	$1.50V = 0.7V \cdot (1 + 16.1431(0.4936))$ $1.50V = 0.7V \cdot (1 + 16.1431(0.4936))$ $1.36V = 0.7V \cdot (1 + 16.1431)$

	PS	Fosc	Vref	Tot
315-0825-000	APW8722	300kH _z 0.8V	1%	
315-0868-000	EM6301D	300kH _z 0.6V	1%	
315-0306-000	R7812A0GSP	300kH _z 0.8V	1.5%	
315-0095-000	R7812A0GS	300kH _z 0.6V	1.5%	
315-0638-000	R7812A0G	300kH _z 0.8V	1%	
315-0071-000	R7810GSP	300kH _z 0.8V	1%	
315-0526-000	APW7120	300kH _z 0.8V	1.5%	
315-0533-000	NCPI570	300kH _z 0.8V	2%	
315-0529-000	FR6326	300kH _z 0.8V	2%	

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NV_PN	600-1G210-BASE-SCH		
PCB REV	P0210-B00	PAGE	23 OF 27
MOD REV	A	DATE	18-DEC-2015

PS V: NVVDD OVR2+1



ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS V: NWDD OVR2+1

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NV_PN	600-1G210-BASE-SCH		
PCB REV	P/G210-B00	PAGE	24 OF 27
BOM REV	A	DATE	18-DEC-2015

PS V: NVVDD Phase 1,2

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ASSEMBLY	<ASSEMBLY_DESCRIPTION>		
PAGE DETAIL	PS V: NVVDD Phase 1,2		
NV_PN	600-1G210-BASE-SCH		
PCB REV	PG210-B00	PAGE	25 OF 27
BOM REV	A	DATE	18-DEC-2015



PS V: NVVDD Phase 3

1

2

3

4

5

1

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5

ASSEMBLY

<ASSEMBLY_DESCRIPTION>

PAGE DETAIL

PS V: NVVDD Phase 3

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600-1G210-BASE-SCH

PCB REV

PG210-B00

BOM REV

A

PAGE

26 OF 27

DATE

18-DEC-2015

A

B

C

D

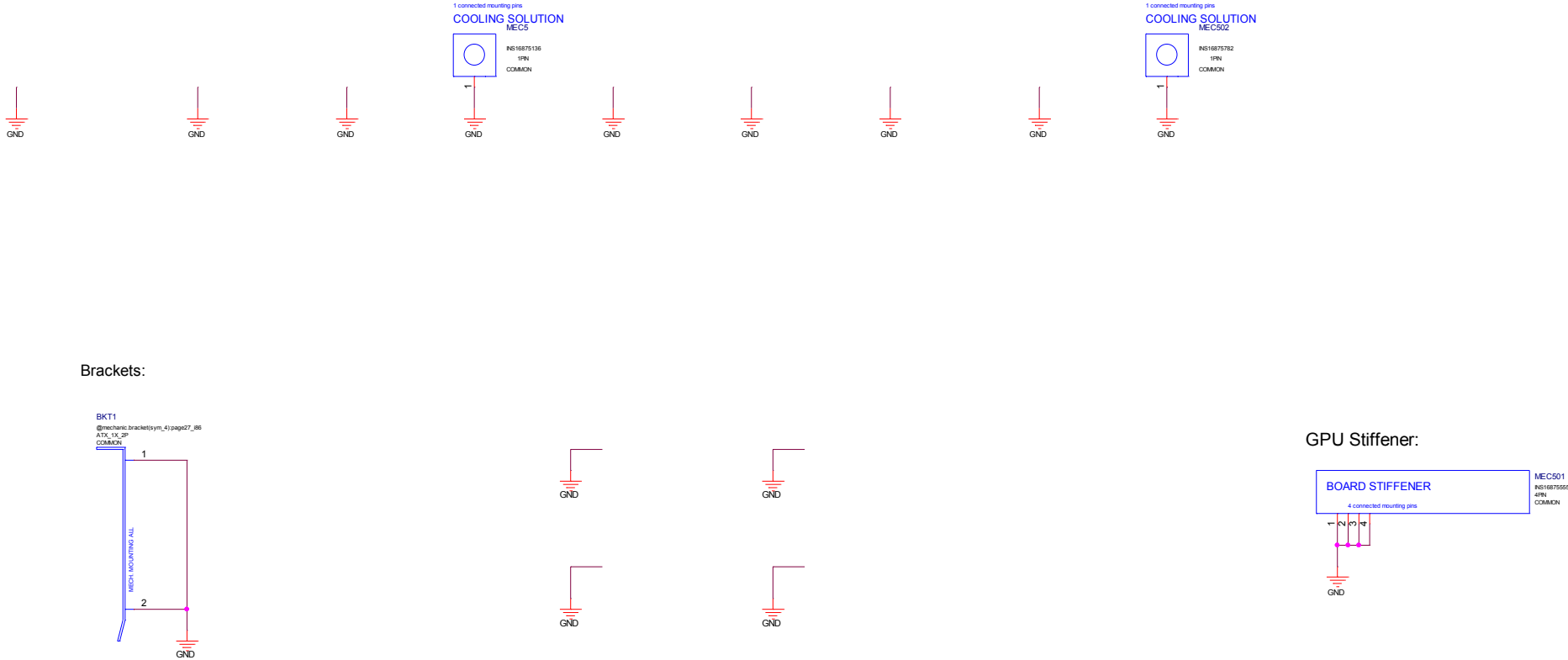
E

F

G

H

Mechanical: Bracket/Thermal Solution



Brackets:

GPU Stiffener:

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ASSEMBLY	<ASSEMBLY_DESCRIPTION>		
PAGE DETAIL	MECH: Bracket/Thermal		
NV_PN	600-1G210-BASE-SCH		
PCB REV	PG210-B00	PAGE	27 OF 27
BOM REV	A	DATE	18-DEC-2015

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