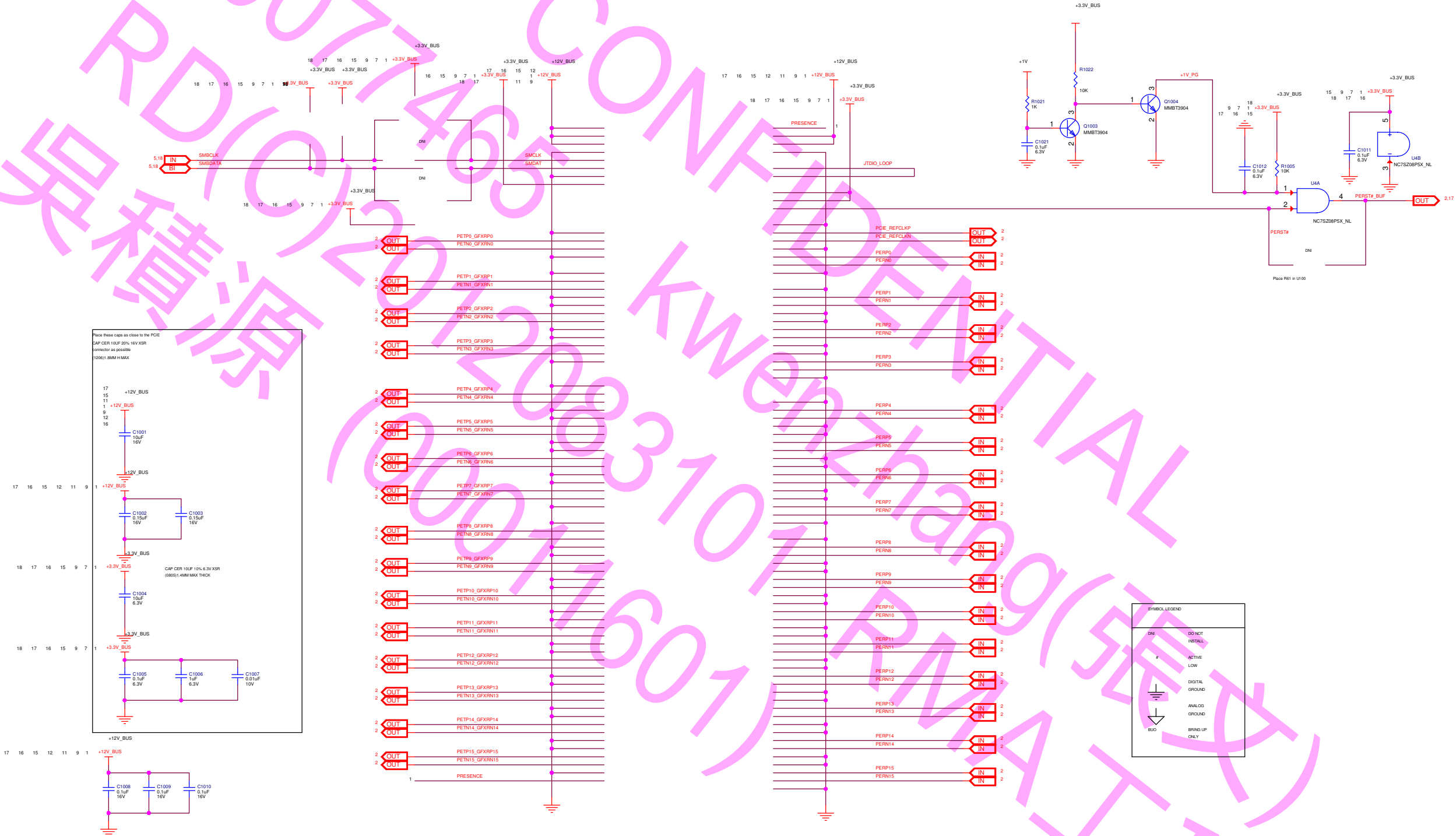




PCI-EXPRESS EDGE CONNECTOR



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SHEET:		TITLE:	
DATE: PCMEdge 08/15/2011	REV: 17		
SHEET NUMBER: 1 OF 21			
DOCUMENT NUMBER: 102-C44101-00			
NOTES: NOTE			

Cape_Verde PCIe Interface

NOTE: Some of the PCIE testpoints will be available through vias on traces.



PCIE_CALR_TX 1.69k pull up for Verde
1.27k pull down for Turks

PCIE_CALR_RX 1k pull up for Verde
2k pull up for Turks

AMD - GRAPHICS
1 COMMERCE VALLEY
MARKHAM, ONTARIO, L3T 7X6

SHEET:

DATE: ~~Capitol Dec 05 13 15 22 2011~~

SHEET NUMBER: 2 OF 21

DOCUMENT NUMBER: 102-C44101-00

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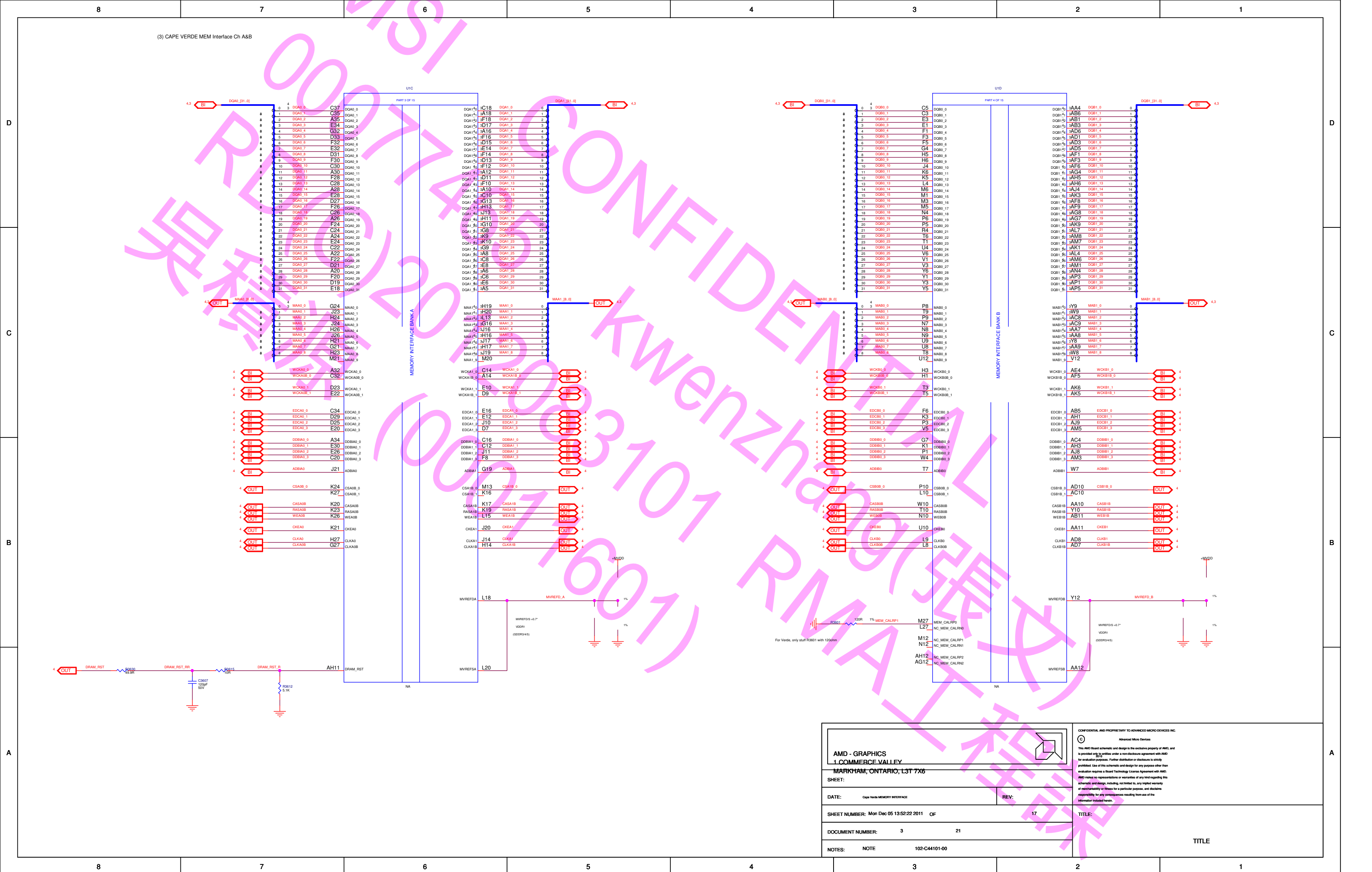
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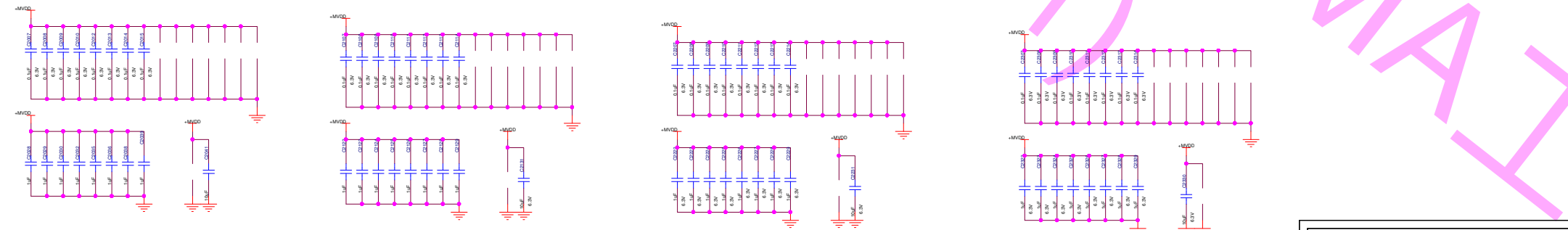
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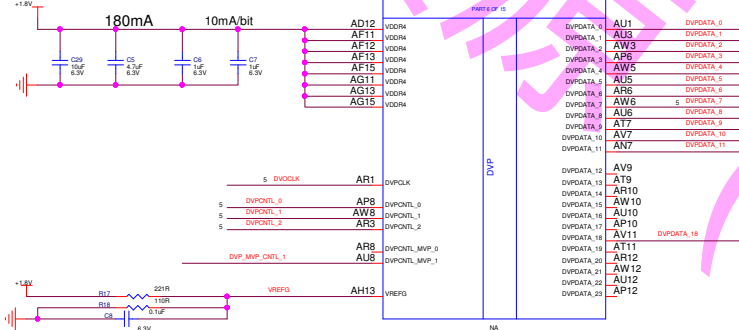
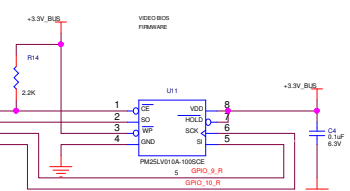
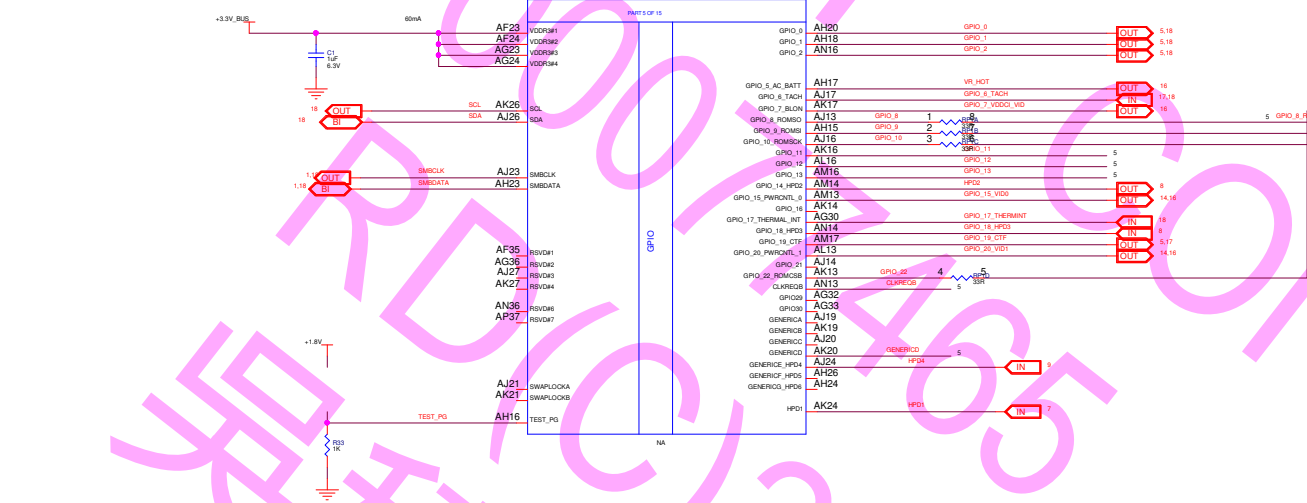
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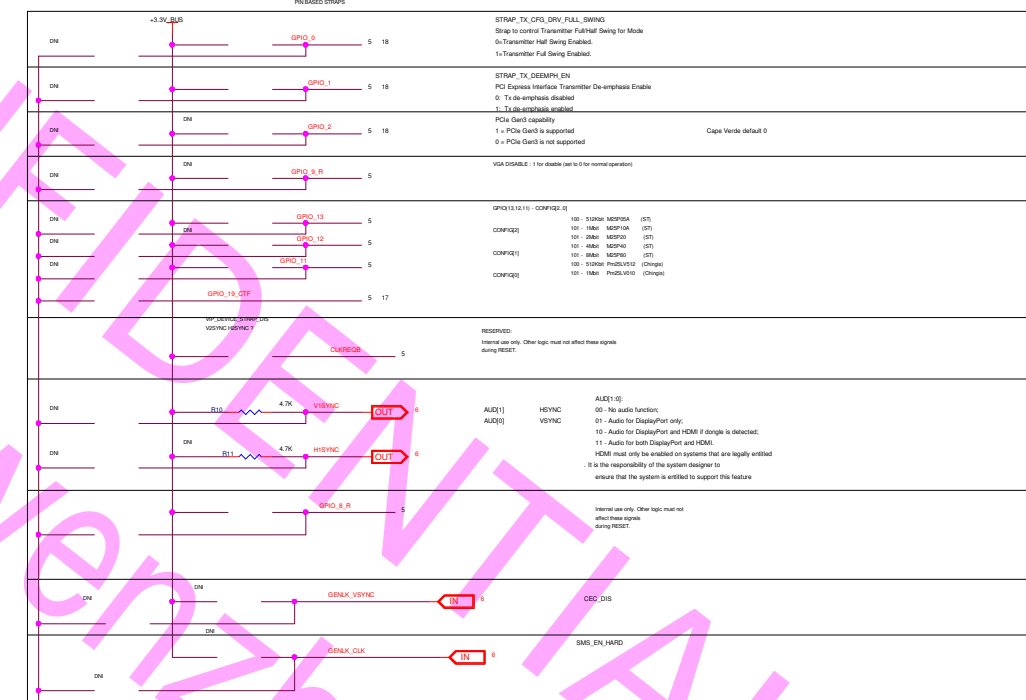
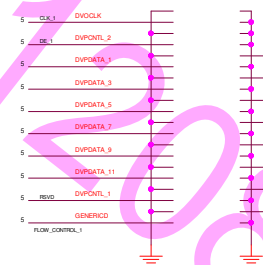
CAPE VERDE GPIOs Strap CF XTAL OSC

PN 228007900G for 1Mbit (PM2SLV010A-100SCe)



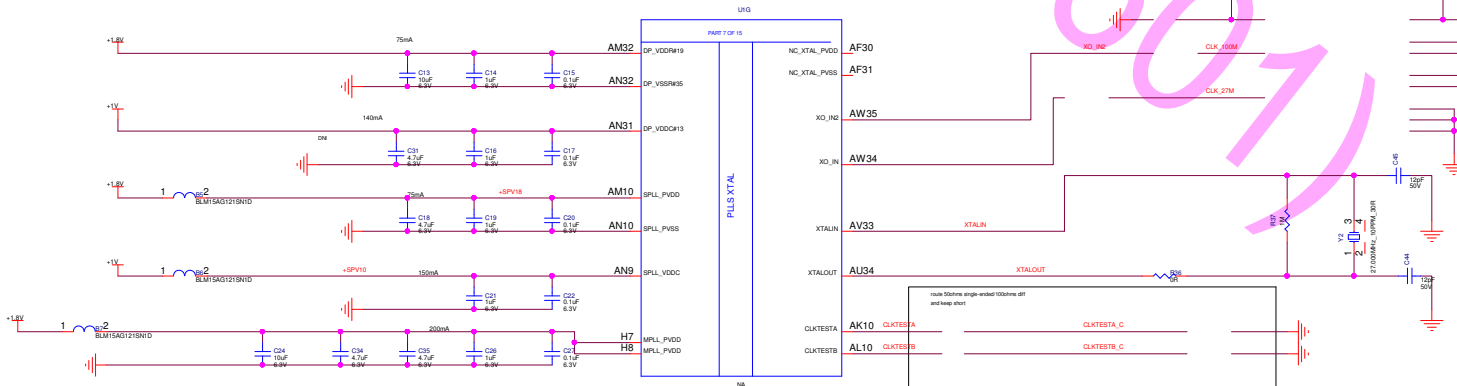
CrossFire Card-Edge

Lower Cable Card-Edge



Place the crossfire testpoints near the ASIC and not the connector

Please pay attention to the grounding strategies for these filter capacitors to maintain a close loop for current.



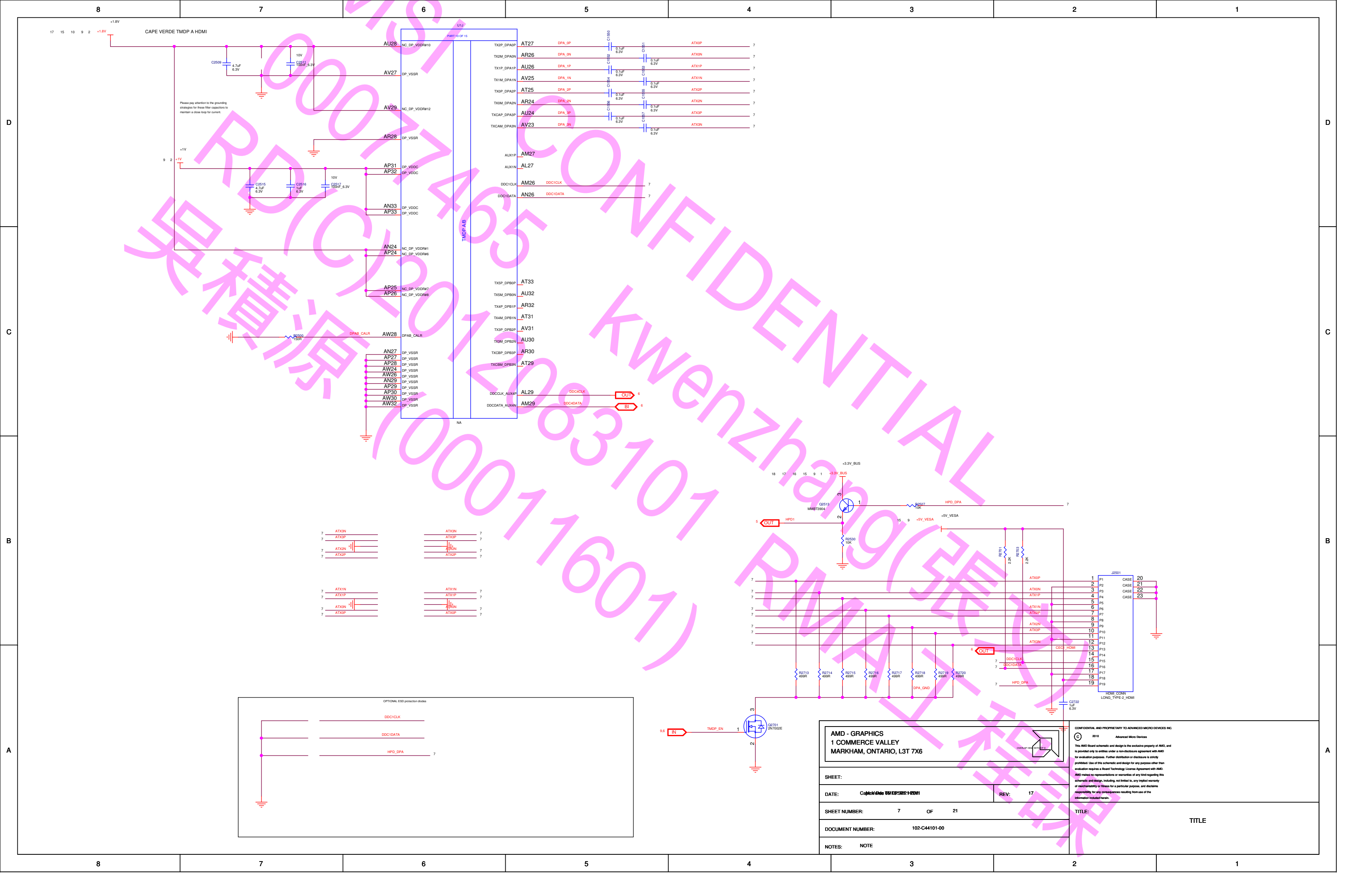
AMD - GRAPHICS
1 COMMERCE VALLEY
MARKHAM, ONTARIO, L3T 7X6

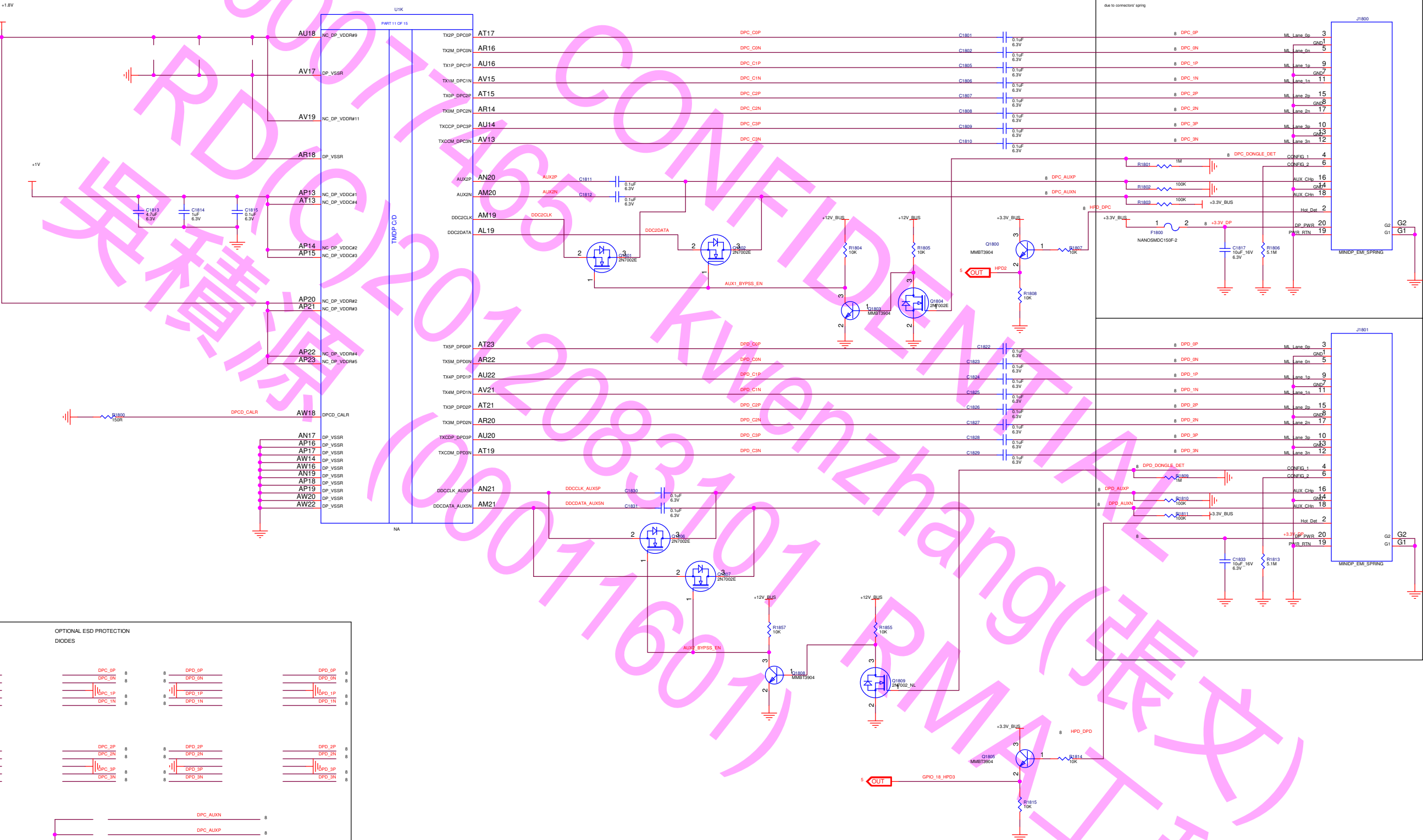
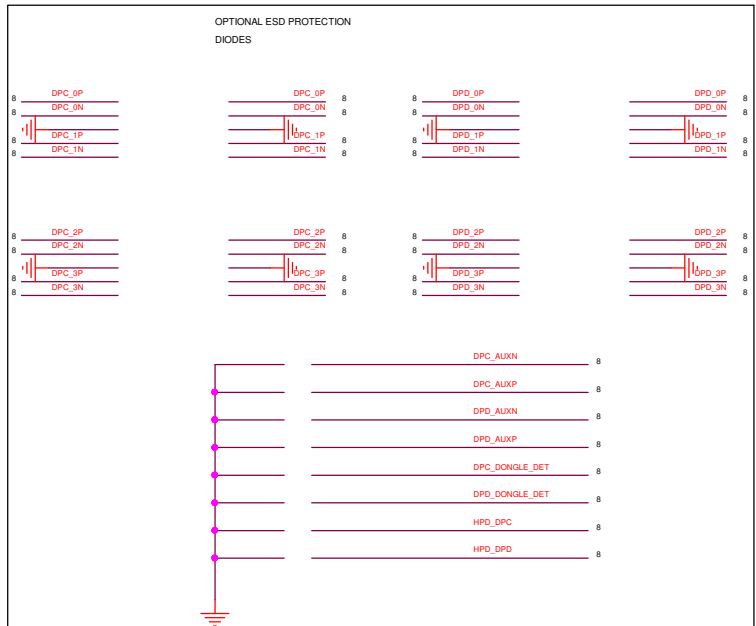
SHEET: Cap Verde GPIO STRAP CF PLL
DATE: Mon Dec 05 13:52:19 2011
SHEET NUMBER: 5 OF 21
DOCUMENT NUMBER: 102-C4101-00
NOTES: NONE




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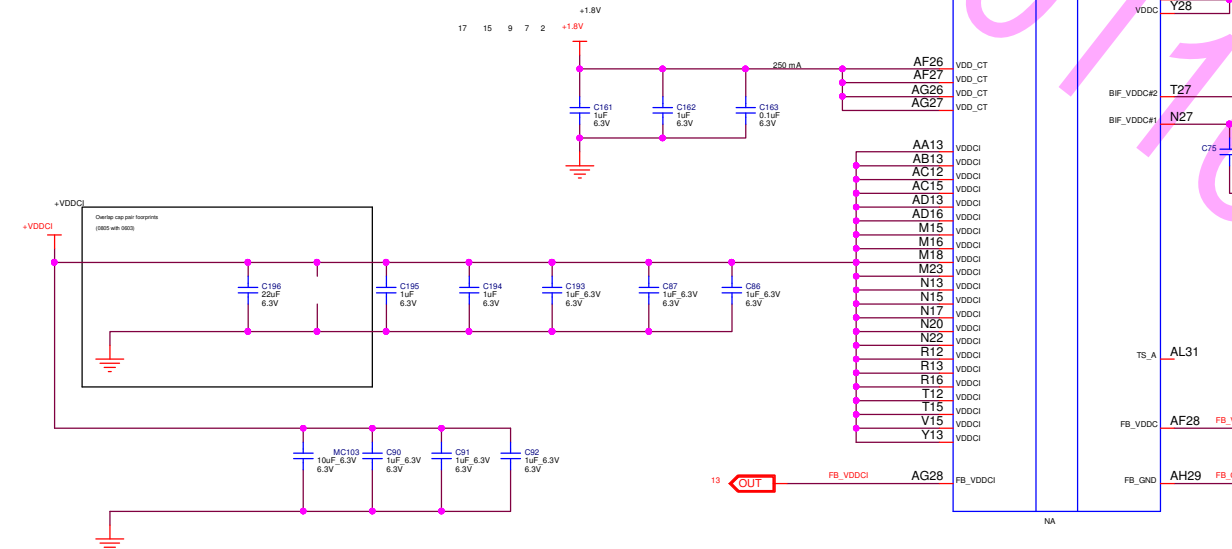
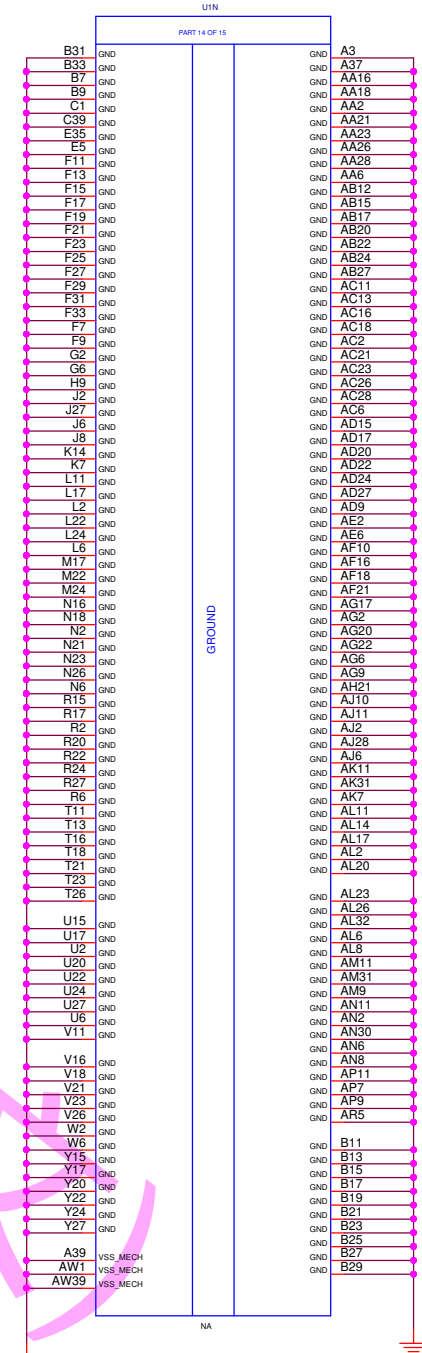
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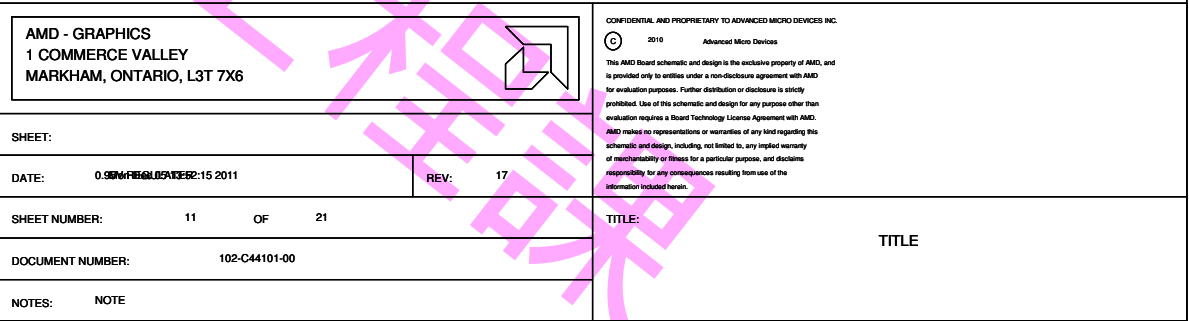


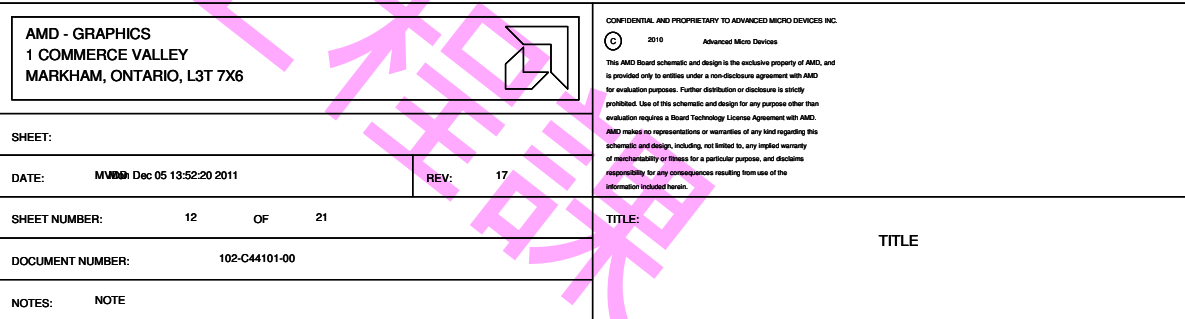
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AMD - GRAPHICS 1 COMMERCE VALLEY MARKHAM, ONTARIO, L3T 7X6		TITLE:	
SHEET:		REV:	
DATE: Cape Verde TMDP C & D mDP		17	
SHEET NUMBER: Mon Dec 05 13:52:22 2011 OF		17	
DOCUMENT NUMBER: 8 21		TITLE:	
NOTES: NOTE 102-C44101-00		TITLE:	

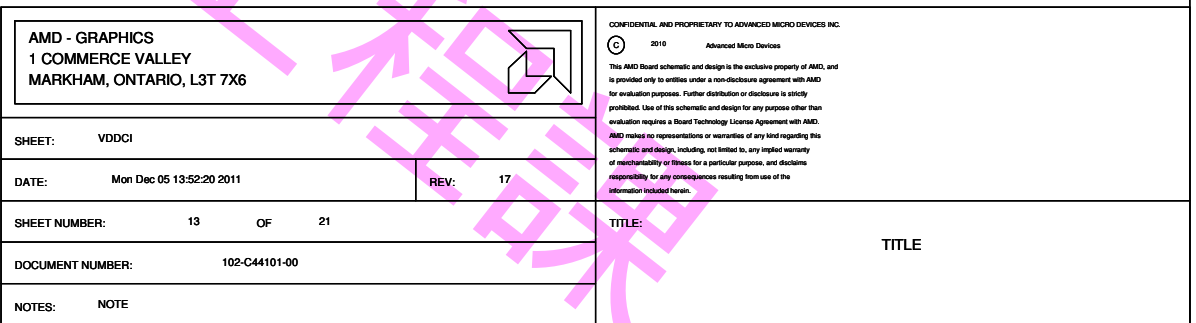
CAPE VERDE Power & GND

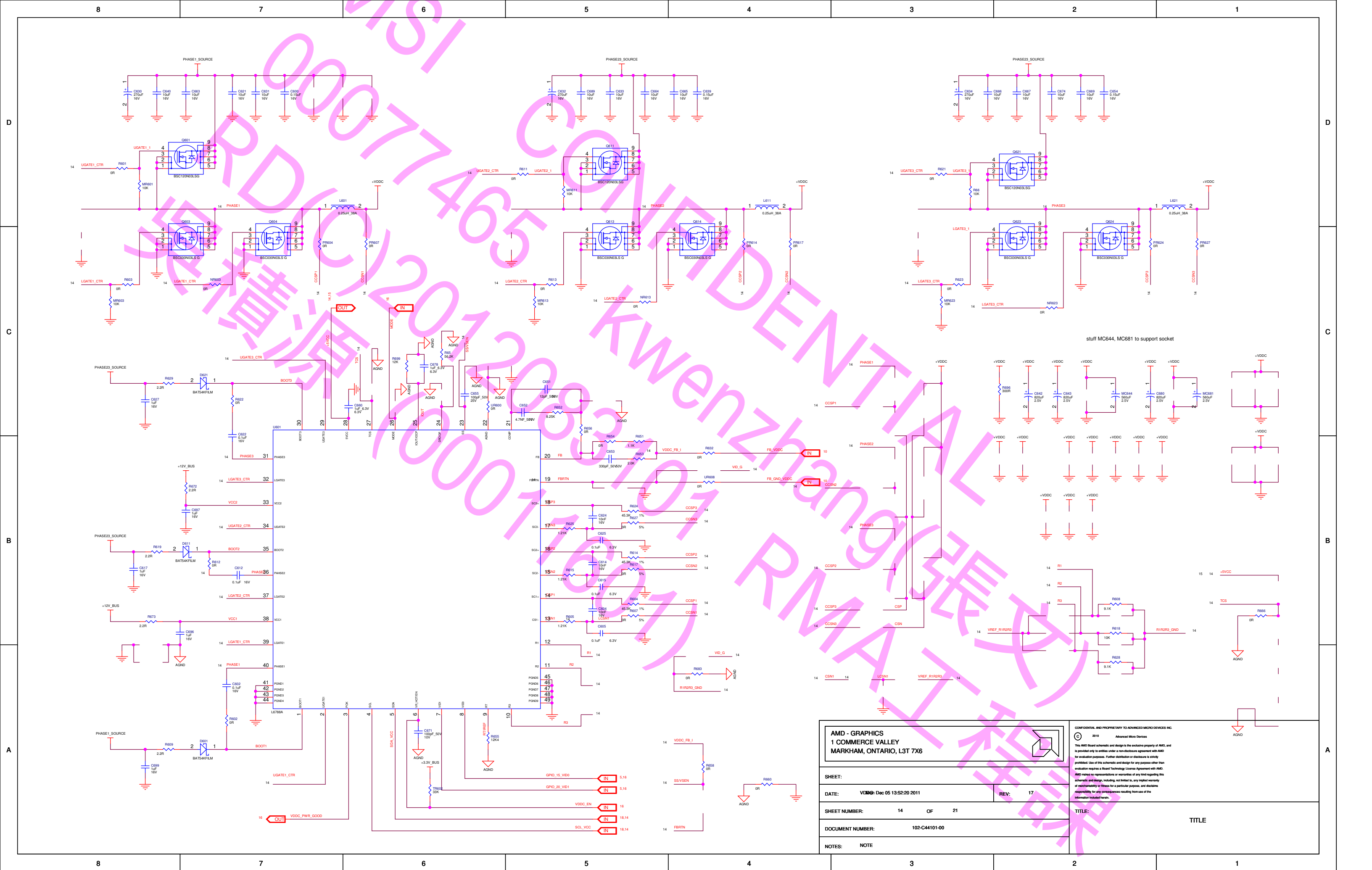


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SHEET NUMBER: 10 OF 21					
DOCUMENT NUMBER: 102-C44101-00					
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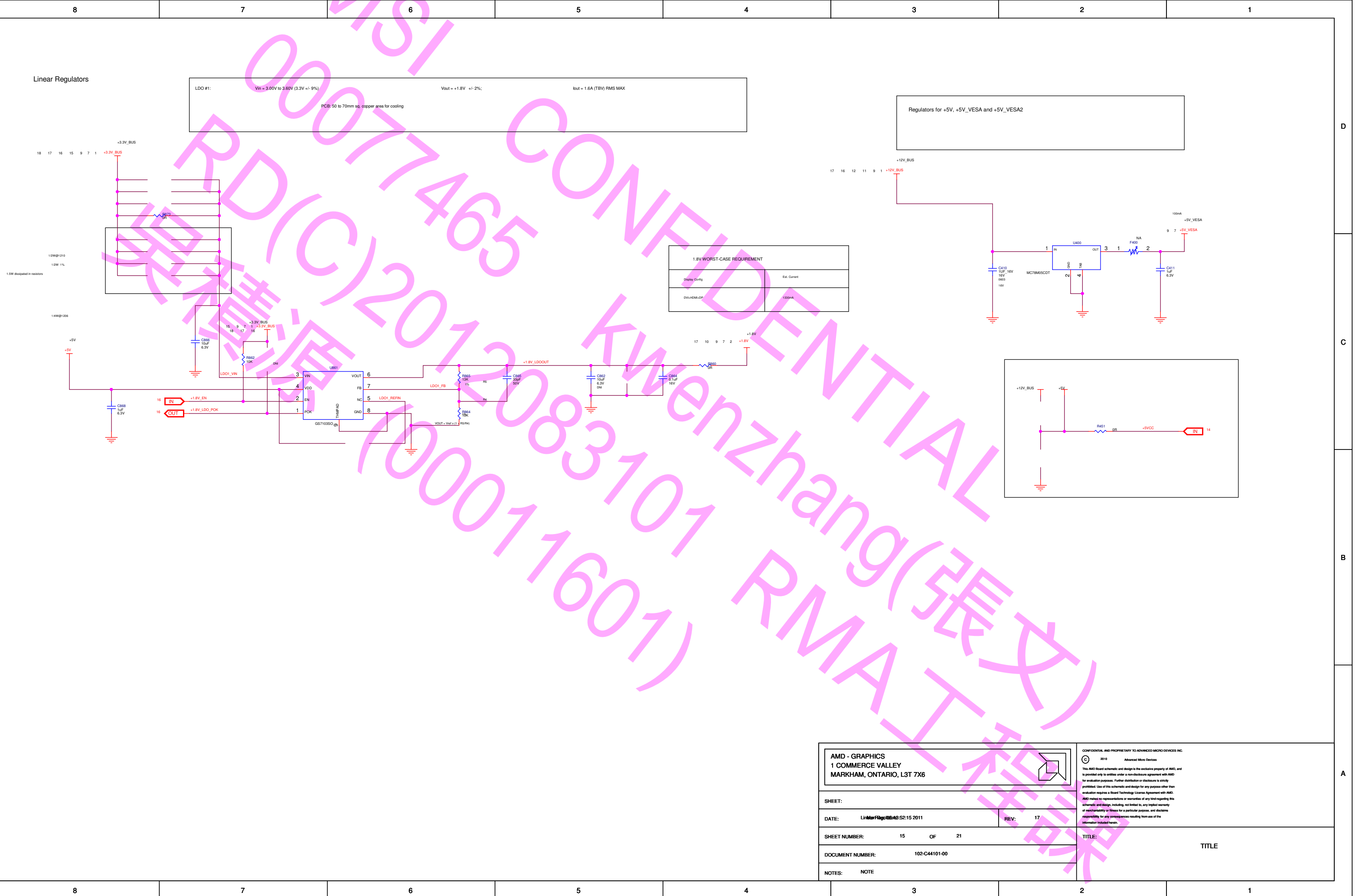




AMD - GRAPHICS 1 COMMERCE VALLEY MARKHAM, ONTARIO, L3T 7X6	
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SHEET NUMBER: 14	OF 21
DOCUMENT NUMBER: 102-C44101-00	TITLE:
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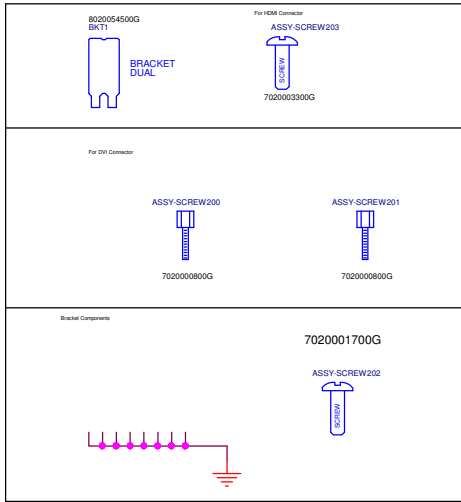
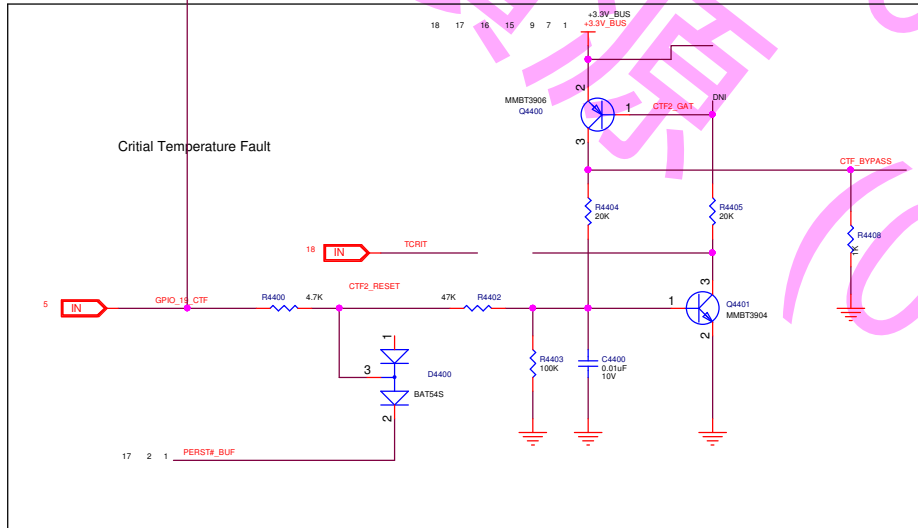


Mechanical and Thermal Management

Socket	6090034500G	
Bracket	8020054500G	DS mDP mDP HDMI (DVI)
Fansink	7120001100G	For 100W board

1 Critical Temperature is reached this will force the fan to run at full speed while power is supplied from CPU & not of the board.
This is an open collector output. Active device has pull-down to ground.

Critical Temperature Fault



Cape Verde socket 6090034500G

For 2 pin fan

+12V_BUS

+12V_BUS

+12V_BUS

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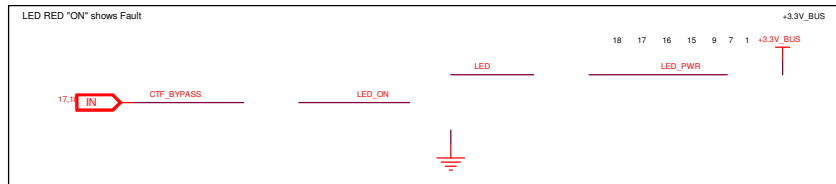
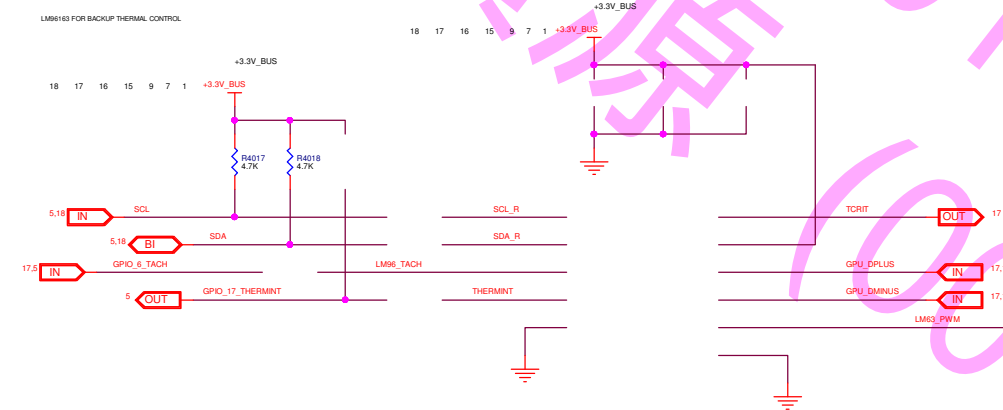
+12V_BUS

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DATE: Mech/Thermal Management	REV:
SHEET NUMBER: Mon Dec 05 13:52:17 2011	OF 17
DOCUMENT NUMBER: 17	21
NOTES: NOTE	102-C44101-00

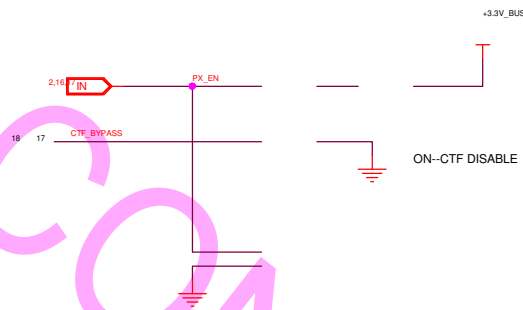
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TITLE

The diagram illustrates the JTAG interface for the U1A component. The U1A component is labeled 'U1A' and 'PART 1 OF 15'. The JTAG pins are connected to the U1A pins as follows: TESTEN to AD28, TMS to AL24, TDI to AK23, TDO to AM23, and TRSTB to AM28. The U1A component is also connected to a +3.3V_BUS. The JTAG pins are also connected to a +3.3V_BUS. The diagram includes a note: 'JTAG_TRSTB engineering board pull high production board pull down'.



(easily accessible and not blocked by the heatsink).



SWITCH CONNECTIONS TO PINSTRAPS	
	<p>GPIO0 - TX_PWRSE_EN (Transmitter Power Savings Enable)</p> <p>0: 50% Tx output swing for mobile mode</p> <p>1: full Tx output swing (Default setting for Desktop)</p>
	<p>GPIO1 - TX_DEEMPH_EN (Transmitter De-emphasis Enable)</p> <p>0: Tx de-emphasis disabled for mobile mode</p> <p>1: Tx de-emphasis enabled (Default setting for Desktop)</p>
	<p>GPIO2 - BIF_GEN_EN</p> <p>0: Default (Disabled)</p> <p>1: Enabled</p>

DATE: ~~Debby~~ 05/15/2011 13:52:18 2011

DOCUMENT NUMBER: 102-C44101-00

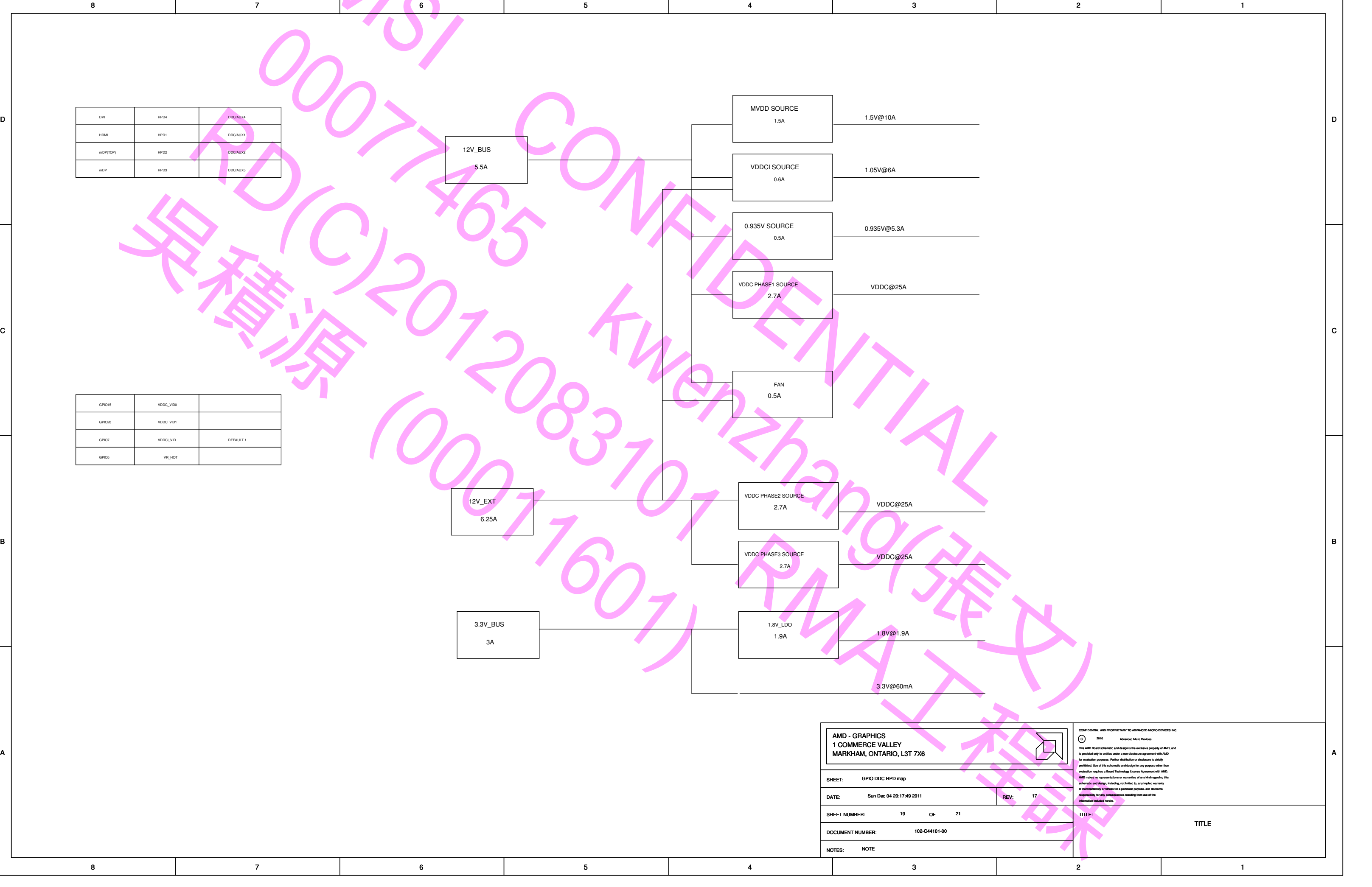
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SHEET: GPIO DDC HPD map		REV: 17		TITLE: TITLE	
DATE: Sun Dec 04 20:17:49 2011					
SHEET NUMBER: 19 OF 21					
DOCUMENT NUMBER: 102-C44101-00					
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