P1071: GF108, 1024MB x16 DDR3, DVI+HDMI+VGA

BRD	VARIANT	NVPN	ASSEMBLY
B	BASE	600-11071-BASE-SCH	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
1	SKU0001	600-11071-0001-100	GF108-300-A1 625/1250/900MHz 1024MB/128bit DDR3, DVI-DL+HDMI+VGA
2	SKU0002	600-11071-0002-100	GF108-200-A1 650/1300/900MHz 1024MB/128bit DDR3, DVI-DL+HDMI+VGA
3	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
4	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
5	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
6	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
7	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
8	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
9	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
10	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
11	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
12	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
13	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
14	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>
15	<UNDEFINED>	<UNDEFINED>	<UNDEFINED>

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Overview

NVIDIA CORPORATION
2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA

NV_PN	600-11071-BASE-SCH		
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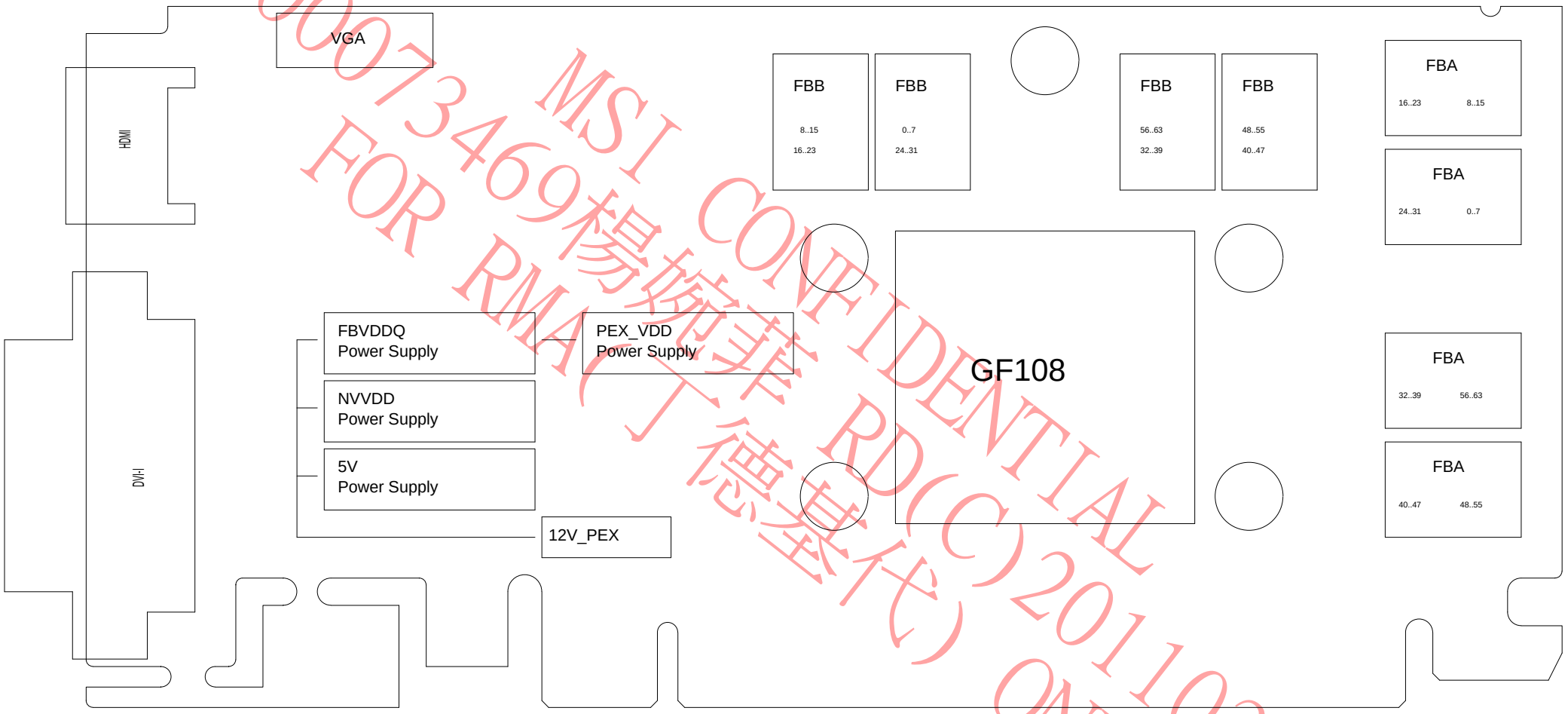
Block Diagram



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Board Block Diagram

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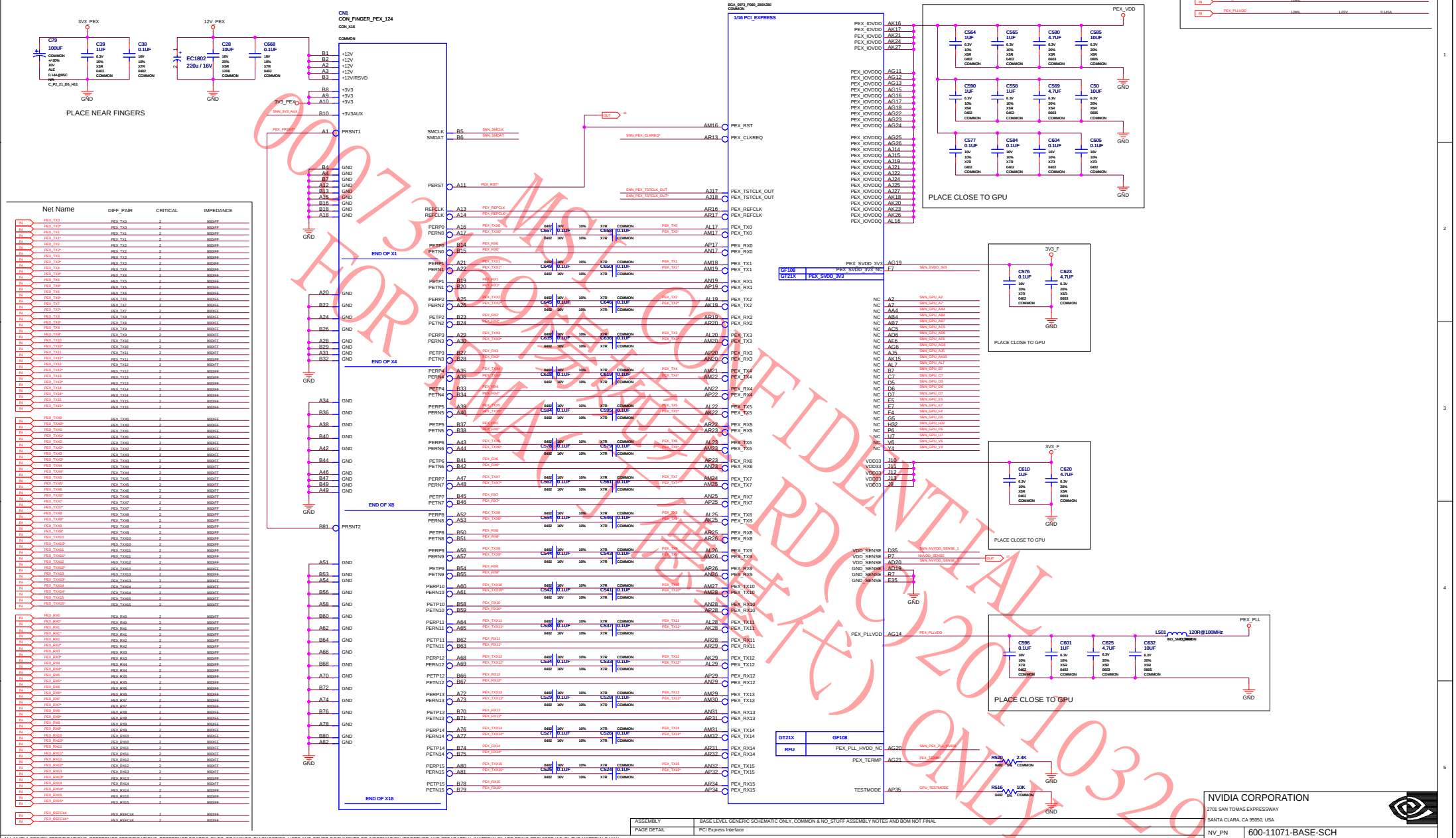
PCI Express Interface



Net Name | DIFF_PAIR | CRITICAL | IMPEDANCE

Net Name	DIFF_PAIR	CRITICAL	IMPEDANCE
PEX_TX0	PEX_TX0	2	90OHM
PEX_TX0*	PEX_TX0	2	90OHM
PEX_TX1	PEX_TX1	2	90OHM
PEX_TX1*	PEX_TX1	2	90OHM
PEX_TX2	PEX_TX2	2	90OHM
PEX_TX2*	PEX_TX2	2	90OHM
PEX_TX3	PEX_TX3	2	90OHM
PEX_TX3*	PEX_TX3	2	90OHM
PEX_TX4	PEX_TX4	2	90OHM
PEX_TX4*	PEX_TX4	2	90OHM
PEX_TX5	PEX_TX5	2	90OHM
PEX_TX5*	PEX_TX5	2	90OHM
PEX_TX6	PEX_TX6	2	90OHM
PEX_TX6*	PEX_TX6	2	90OHM
PEX_TX7	PEX_TX7	2	90OHM
PEX_TX7*	PEX_TX7	2	90OHM
PEX_TX8	PEX_TX8	2	90OHM
PEX_TX8*	PEX_TX8	2	90OHM
PEX_TX9	PEX_TX9	2	90OHM
PEX_TX9*	PEX_TX9	2	90OHM
PEX_TX10	PEX_TX10	2	90OHM
PEX_TX10*	PEX_TX10	2	90OHM
PEX_TX11	PEX_TX11	2	90OHM
PEX_TX11*	PEX_TX11	2	90OHM
PEX_TX12	PEX_TX12	2	90OHM
PEX_TX12*	PEX_TX12	2	90OHM
PEX_TX13	PEX_TX13	2	90OHM
PEX_TX13*	PEX_TX13	2	90OHM
PEX_TX14	PEX_TX14	2	90OHM
PEX_TX14*	PEX_TX14	2	90OHM
PEX_TX15	PEX_TX15	2	90OHM
PEX_TX15*	PEX_TX15	2	90OHM
PEX_TX0	PEX_TX0	2	90OHM
PEX_TX0*	PEX_TX0	2	90OHM
PEX_TX1	PEX_TX1	2	90OHM
PEX_TX1*	PEX_TX1	2	90OHM
PEX_TX2	PEX_TX2	2	90OHM
PEX_TX2*	PEX_TX2	2	90OHM
PEX_TX3	PEX_TX3	2	90OHM
PEX_TX3*	PEX_TX3	2	90OHM
PEX_TX4	PEX_TX4	2	90OHM
PEX_TX4*	PEX_TX4	2	90OHM
PEX_TX5	PEX_TX5	2	90OHM
PEX_TX5*	PEX_TX5	2	90OHM
PEX_TX6	PEX_TX6	2	90OHM
PEX_TX6*	PEX_TX6	2	90OHM
PEX_TX7	PEX_TX7	2	90OHM
PEX_TX7*	PEX_TX7	2	90OHM
PEX_TX8	PEX_TX8	2	90OHM
PEX_TX8*	PEX_TX8	2	90OHM
PEX_TX9	PEX_TX9	2	90OHM
PEX_TX9*	PEX_TX9	2	90OHM
PEX_TX10	PEX_TX10	2	90OHM
PEX_TX10*	PEX_TX10	2	90OHM
PEX_TX11	PEX_TX11	2	90OHM
PEX_TX11*	PEX_TX11	2	90OHM
PEX_TX12	PEX_TX12	2	90OHM
PEX_TX12*	PEX_TX12	2	90OHM
PEX_TX13	PEX_TX13	2	90OHM
PEX_TX13*	PEX_TX13	2	90OHM
PEX_TX14	PEX_TX14	2	90OHM
PEX_TX14*	PEX_TX14	2	90OHM
PEX_TX15	PEX_TX15	2	90OHM
PEX_TX15*	PEX_TX15	2	90OHM
PEX_RX0	PEX_RX0	2	90OHM
PEX_RX0*	PEX_RX0	2	90OHM
PEX_RX1	PEX_RX1	2	90OHM
PEX_RX1*	PEX_RX1	2	90OHM
PEX_RX2	PEX_RX2	2	90OHM
PEX_RX2*	PEX_RX2	2	90OHM
PEX_RX3	PEX_RX3	2	90OHM
PEX_RX3*	PEX_RX3	2	90OHM
PEX_RX4	PEX_RX4	2	90OHM
PEX_RX4*	PEX_RX4	2	90OHM
PEX_RX5	PEX_RX5	2	90OHM
PEX_RX5*	PEX_RX5	2	90OHM
PEX_RX6	PEX_RX6	2	90OHM
PEX_RX6*	PEX_RX6	2	90OHM
PEX_RX7	PEX_RX7	2	90OHM
PEX_RX7*	PEX_RX7	2	90OHM
PEX_RX8	PEX_RX8	2	90OHM
PEX_RX8*	PEX_RX8	2	90OHM
PEX_RX9	PEX_RX9	2	90OHM
PEX_RX9*	PEX_RX9	2	90OHM
PEX_RX10	PEX_RX10	2	90OHM
PEX_RX10*	PEX_RX10	2	90OHM
PEX_RX11	PEX_RX11	2	90OHM
PEX_RX11*	PEX_RX11	2	90OHM
PEX_RX12	PEX_RX12	2	90OHM
PEX_RX12*	PEX_RX12	2	90OHM
PEX_RX13	PEX_RX13	2	90OHM
PEX_RX13*	PEX_RX13	2	90OHM
PEX_RX14	PEX_RX14	2	90OHM
PEX_RX14*	PEX_RX14	2	90OHM
PEX_RX15	PEX_RX15	2	90OHM
PEX_RX15*	PEX_RX15	2	90OHM
PEX_REFCLK	PEX_REFCLK	2	90OHM
PEX_REFCLK*	PEX_REFCLK	2	90OHM

Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
PEX_PRSNT*	12MIL		
PEX_TERMP	12MIL		
GPU_TESTMODE	12MIL		
PEX_PLLVDD	12MIL	1.05V	0.145A

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PCI Express Interface

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NV_PN	600-11071-BASE-SCH		
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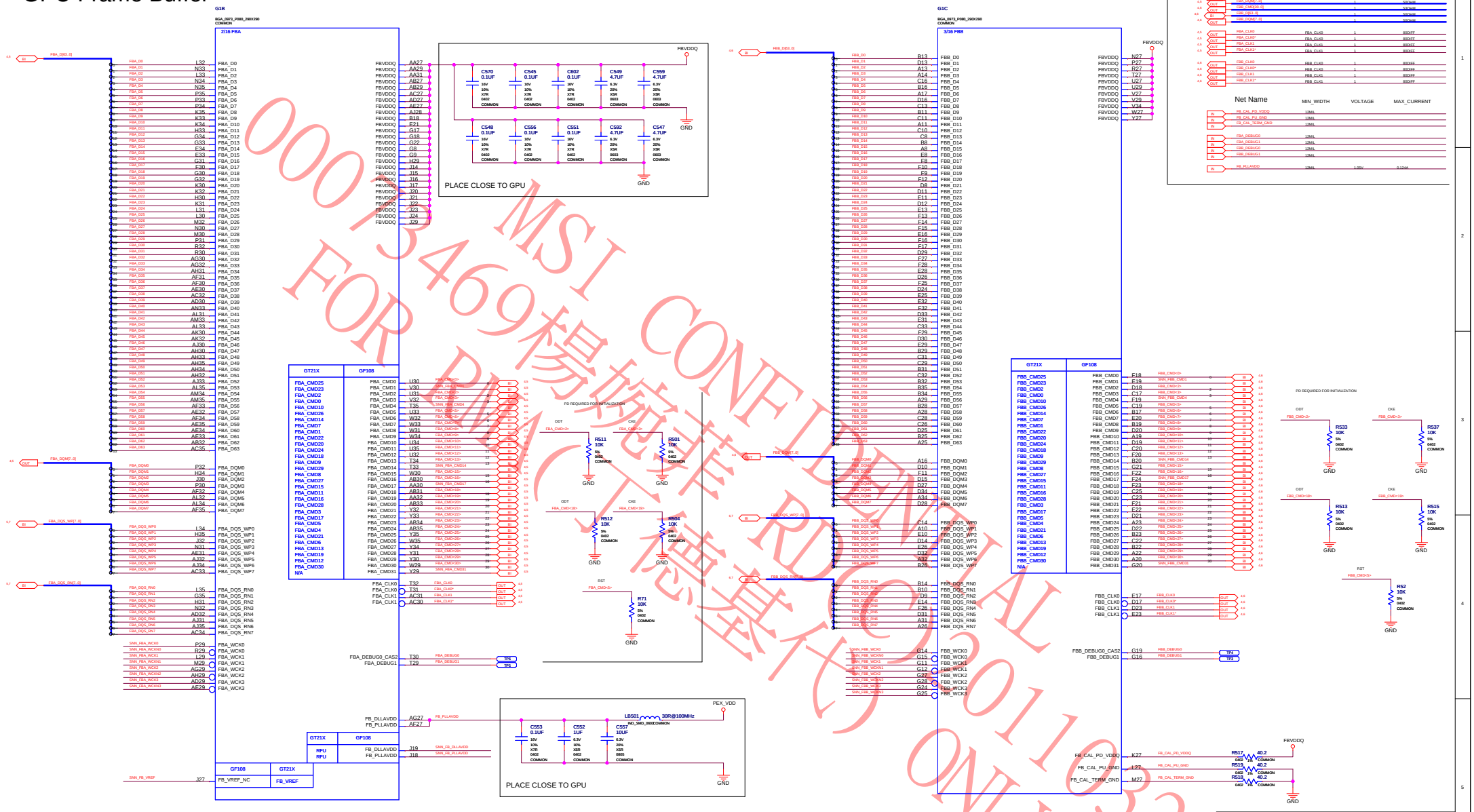
GPU Frame Buffer



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	GPU Frame Buffer

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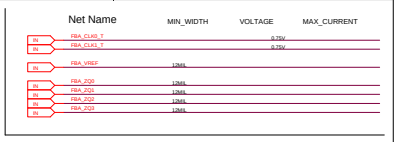
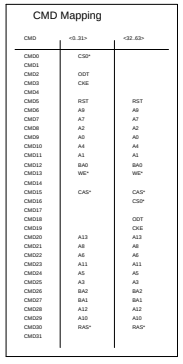
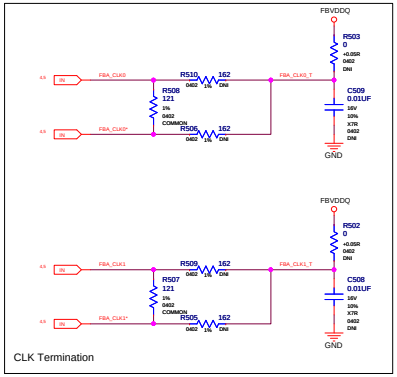
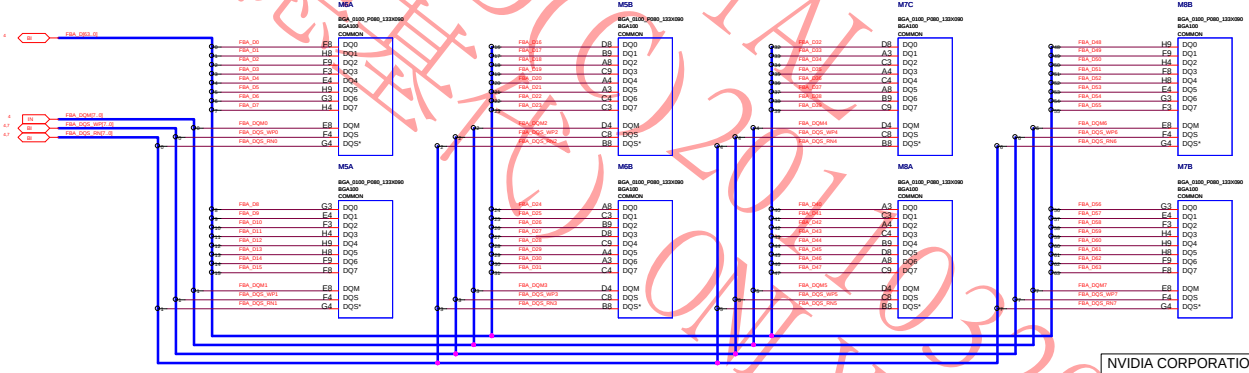
FBA Partition









ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	FBA Partition

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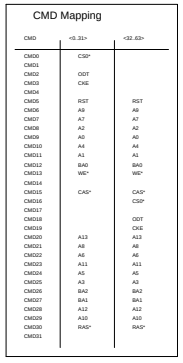
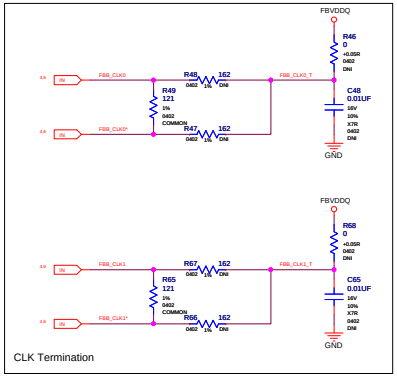
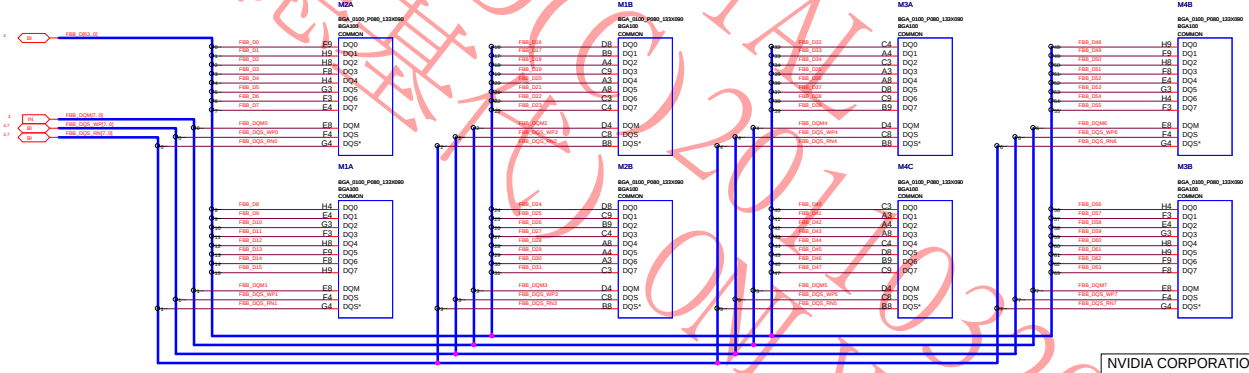
FBB Partition

Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
FBB_CLK0_T		0.75V	
FBB_CLK1_T		0.75V	
FBB_VREF0	12MIL		
FBB_VREF1	12MIL		
FBB_ZQ0	12MIL		
FBB_ZQ1	12MIL		
FBB_ZQ2	12MIL		
FBB_ZQ3	12MIL		







ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	FBB Partition

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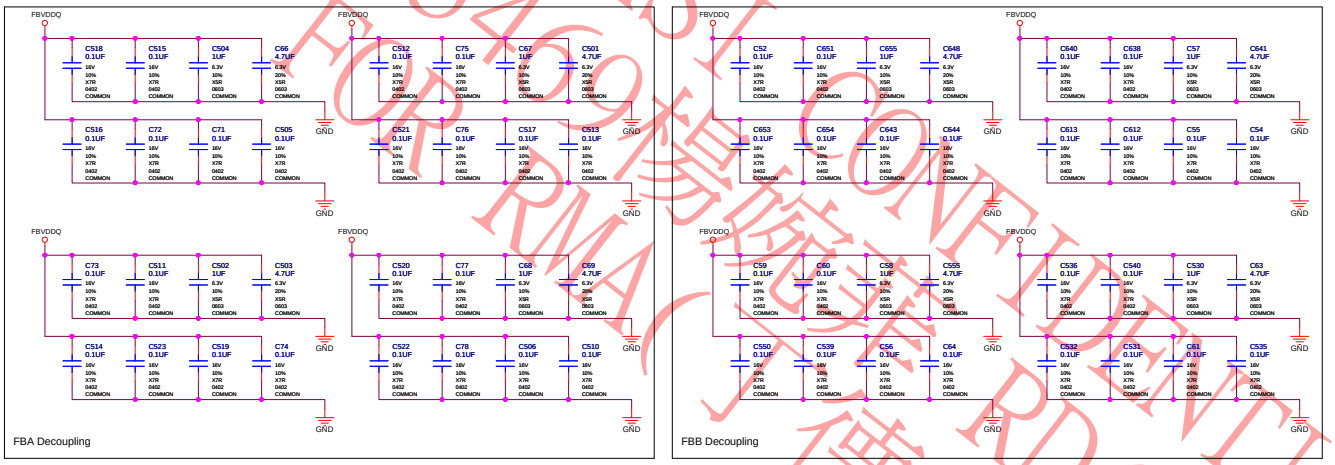
Memory Decoupling



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Memory Decoupling

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NV_PN	600-11071-BASE-SCH		
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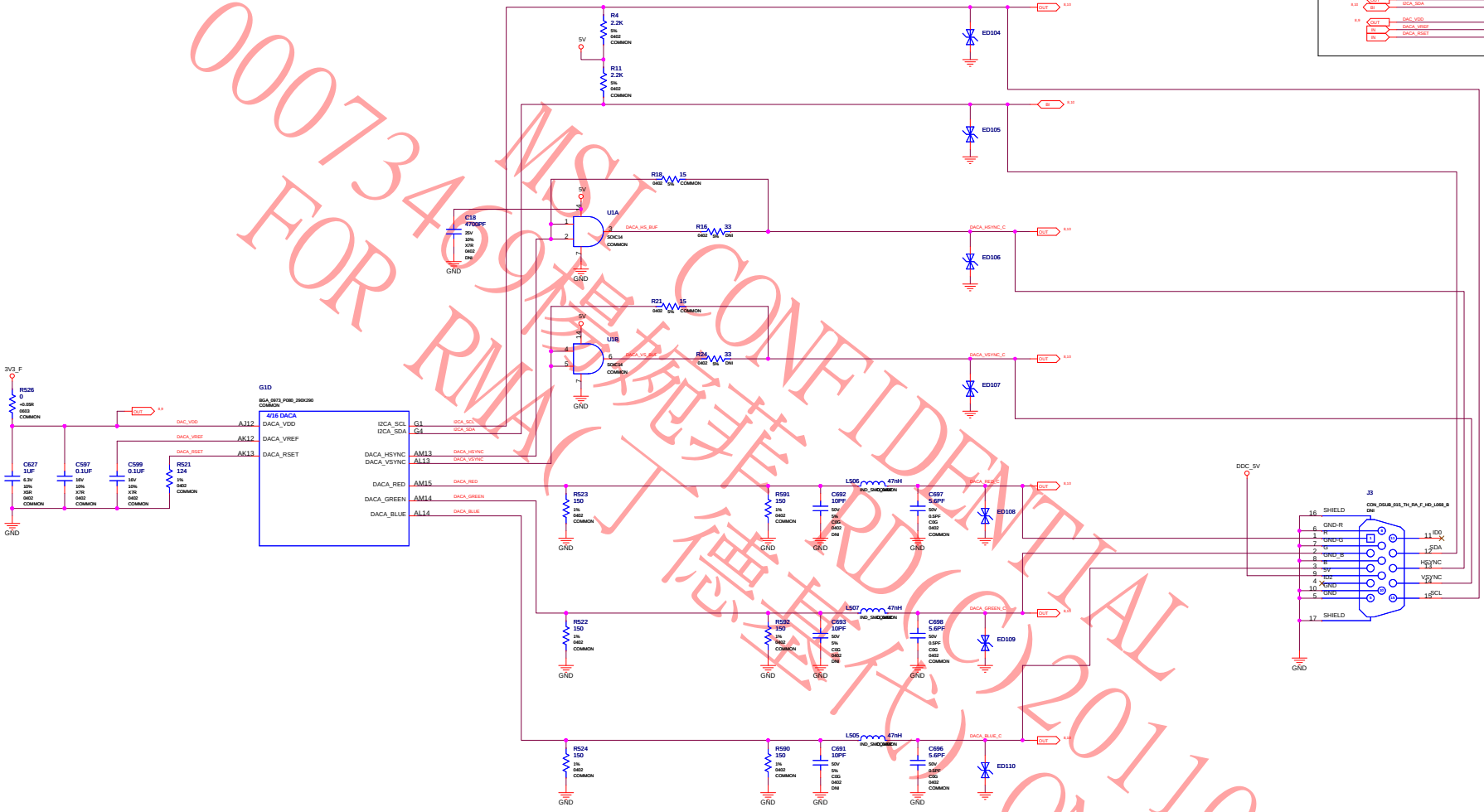
DACA VGA



Net Name	DIFF_PAIR	CRITICAL	IMPEDANCE
DACA_RED		1	50OHM
DACA_RED_C		1	50OHM
DACA_GREEN		1	50OHM
DACA_GREEN_C		1	50OHM
DACA_BLUE		1	50OHM
DACA_BLUE_C		1	50OHM
DACA_HSYNC		2	50OHM
DACA_HS_BUF		2	50OHM
DACA_HSYNC_C		2	50OHM
DACA_VSYNC		2	50OHM
DACA_VS_BUF		2	50OHM
DACA_VSYNC_C		2	50OHM

Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
I2CA_SCL			
I2CA_SDA			
DAC_VDD	12MIL		
DACA_VREF	12MIL		
DACA_RSET	12MIL		

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACA VGA

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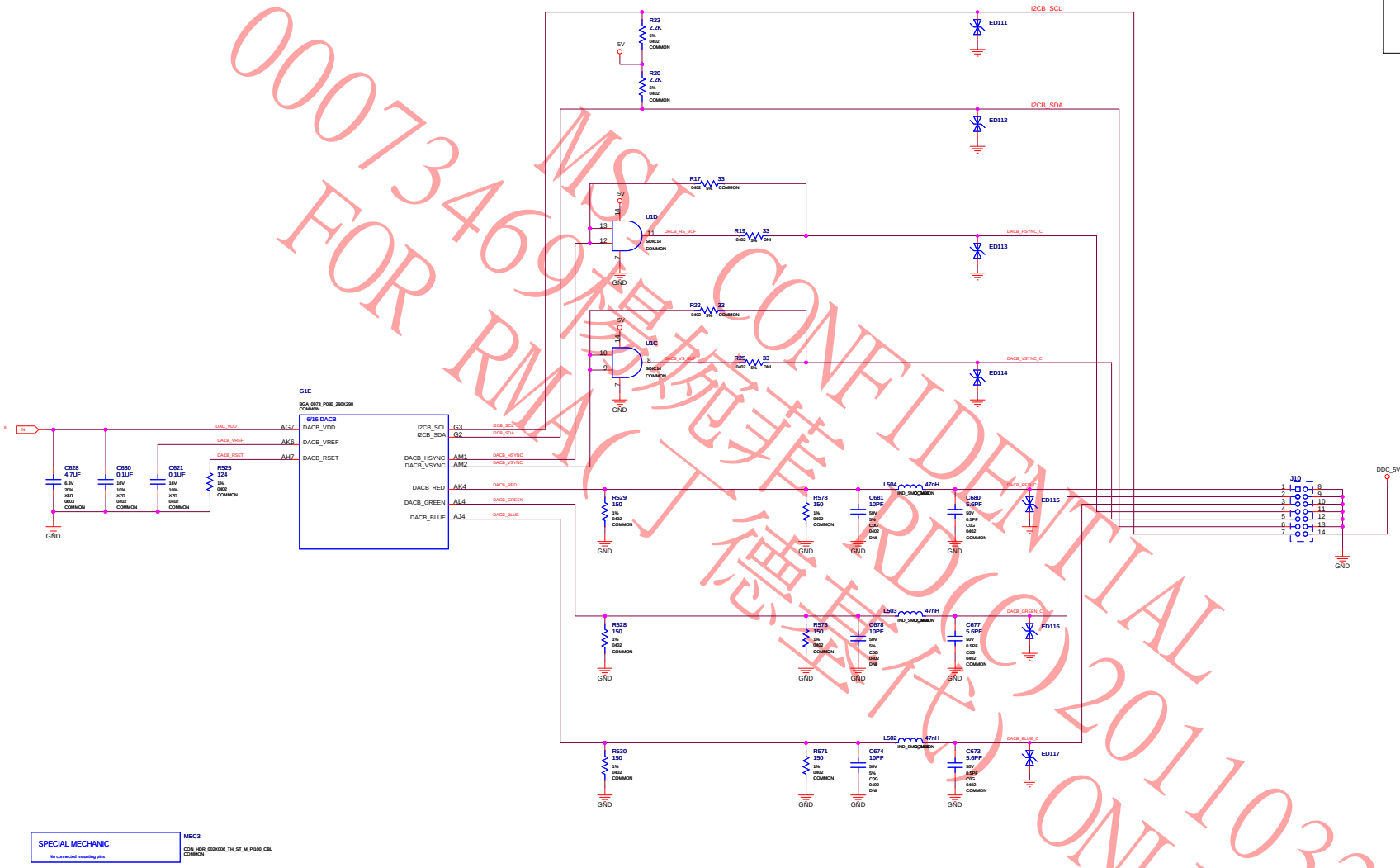
DACB VGA Header



Net Name	DIFF_PAIR	CRITICAL	IMPEDANCE
DACB_RED		1	50OHM
DACB_RED_C		1	50OHM
DACB_GREEN		1	50OHM
DACB_GREEN_C		1	50OHM
DACB_BLUE		1	50OHM
DACB_BLUE_C		1	50OHM
DACB_HSYNC		2	50OHM
DACB_HS_BUF		2	50OHM
DACB_HSYNC_C		2	50OHM
DACB_VSYNC		2	50OHM
DACB_VS_BUF		2	50OHM
DACB_VSYNC_C		2	50OHM

Net Name	MIN_WIDTH	VOLTAGE	MAX_CURRENT
I2CB_SCL			
I2CB_SDA			
DACB_VREF	12MIL		
DACB_RSET	12MIL		

SPECIAL MECHANIC
No connected mounting pins
MEC3
CON_HDR_0029X06_TH_ST_M_P0.00_CBL
COMMON

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	DACB VGA Header

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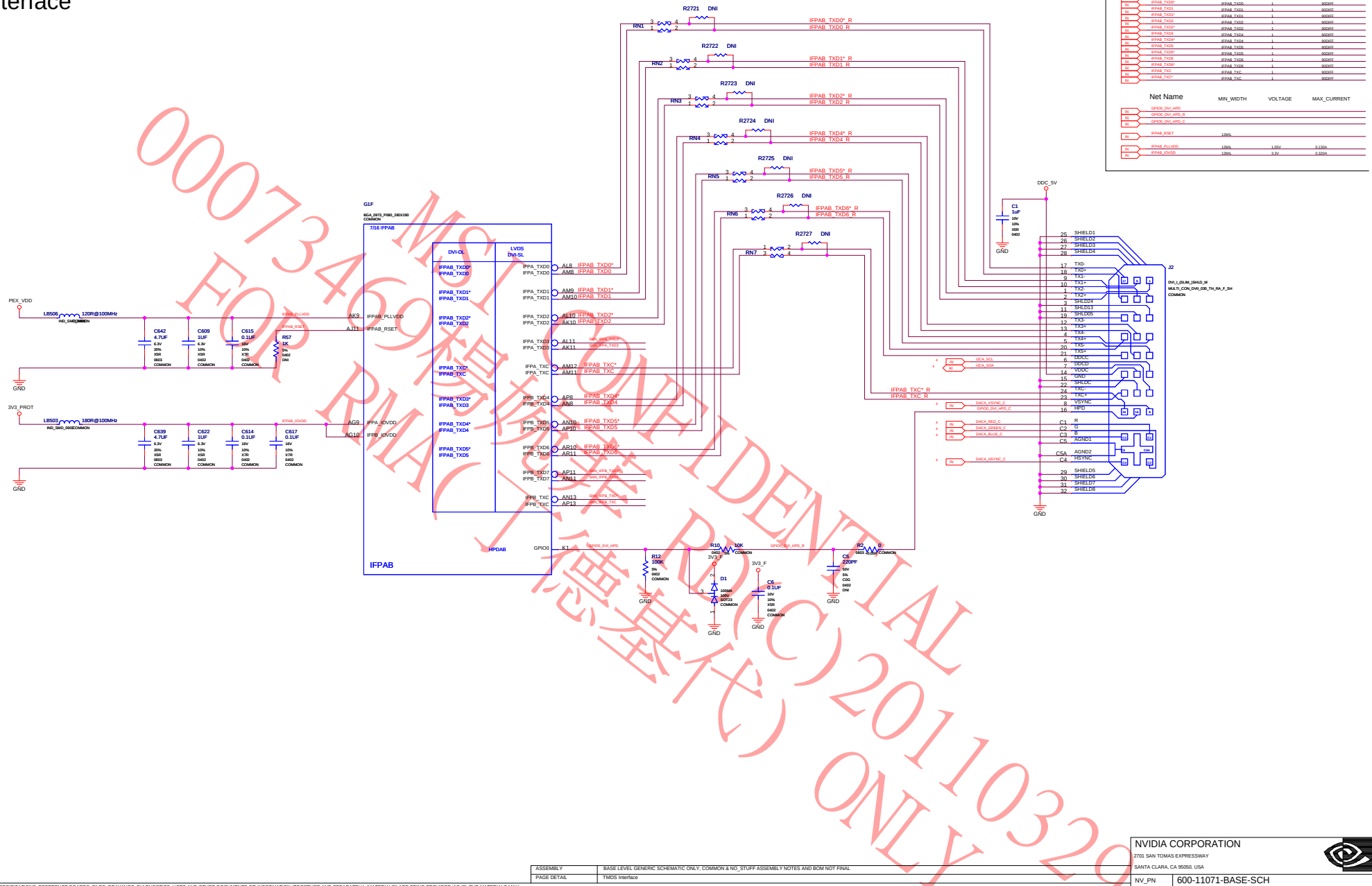
TMDS Interface



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	TMDS Interface

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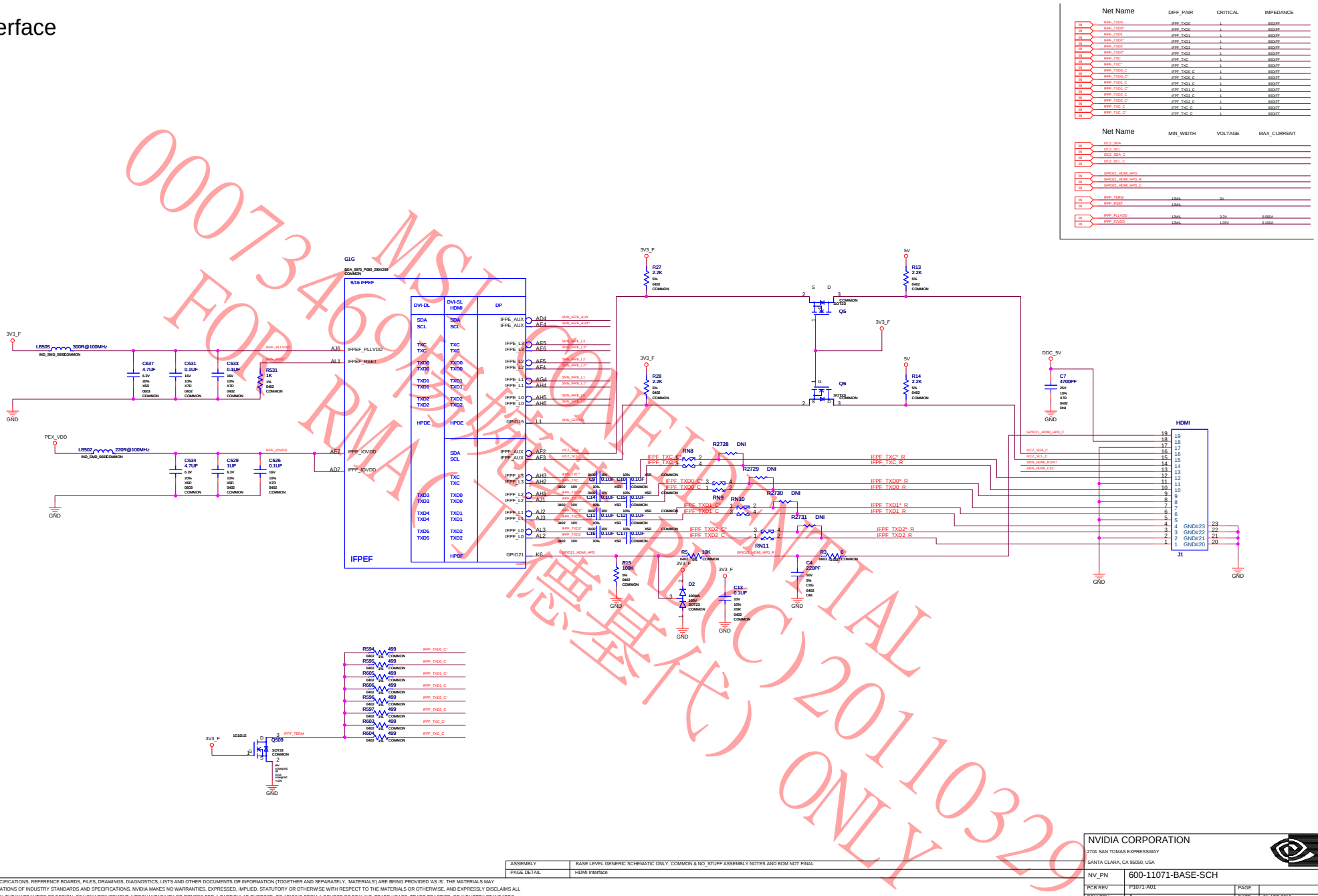
HDMI Interface



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	HDMI Interface

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NV_PN	600-11071-BASE-SCH		
PCB REV	P1071-A01	PAGE	
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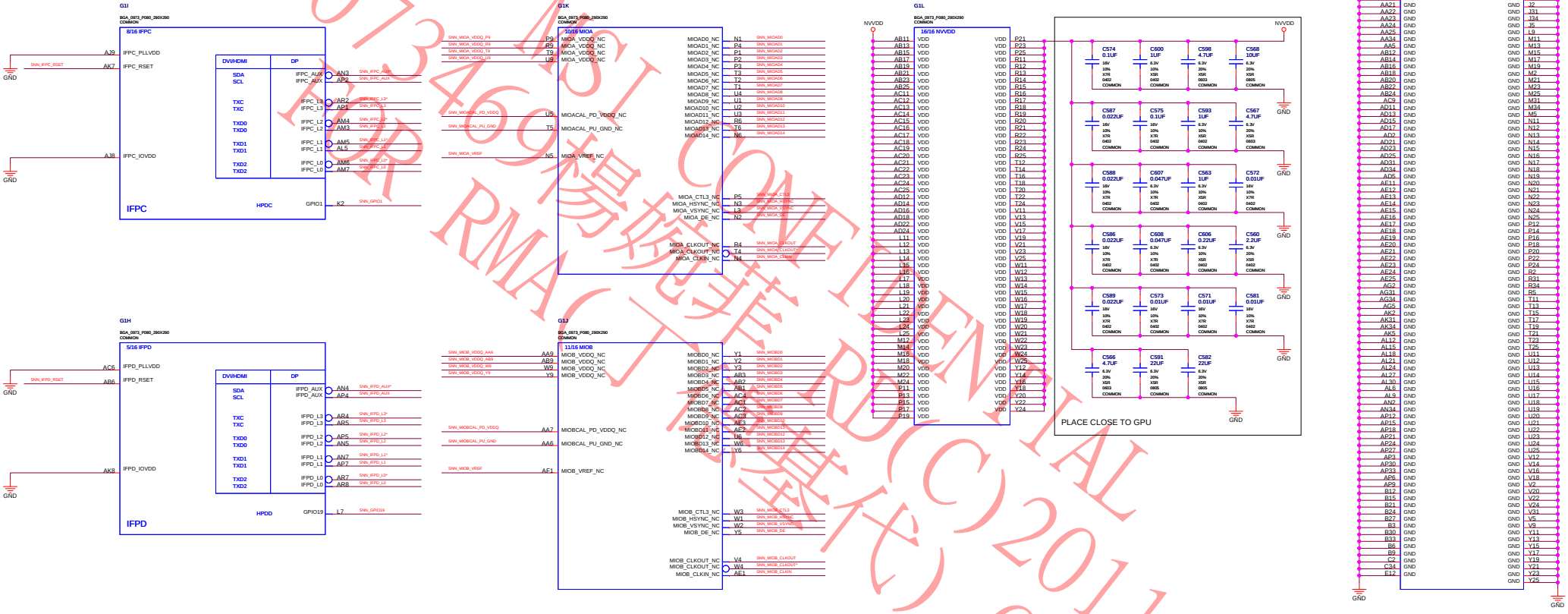
Unused Interfaces, NVVDD Decoupling



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Unused Interfaces, NVVDD Decoupling

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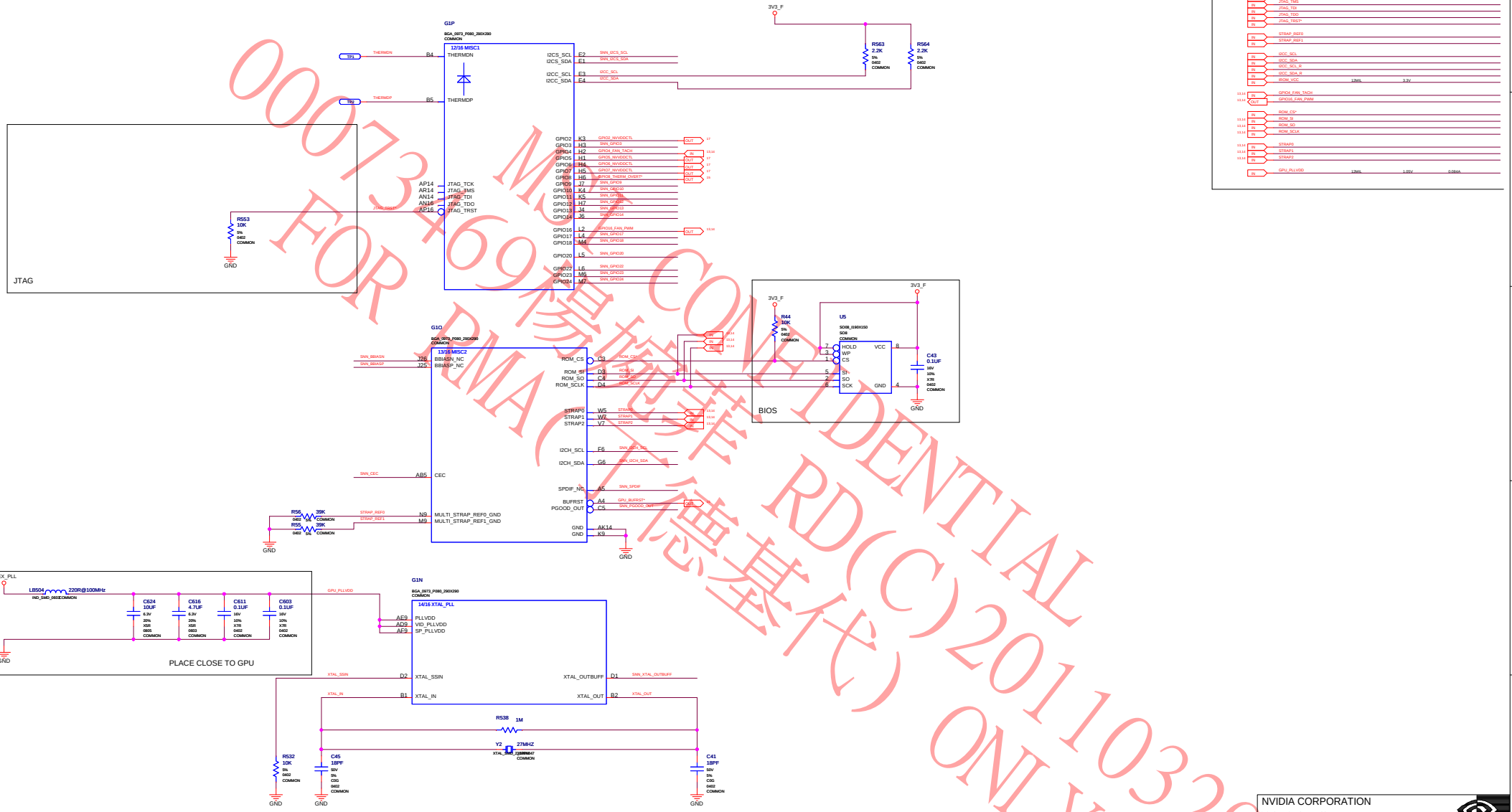
XTAL, ROM, JTAG, Misc



ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	XTAL, ROM, JTAG, Misc

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NV_PN	600-11071-BASE-SCH		
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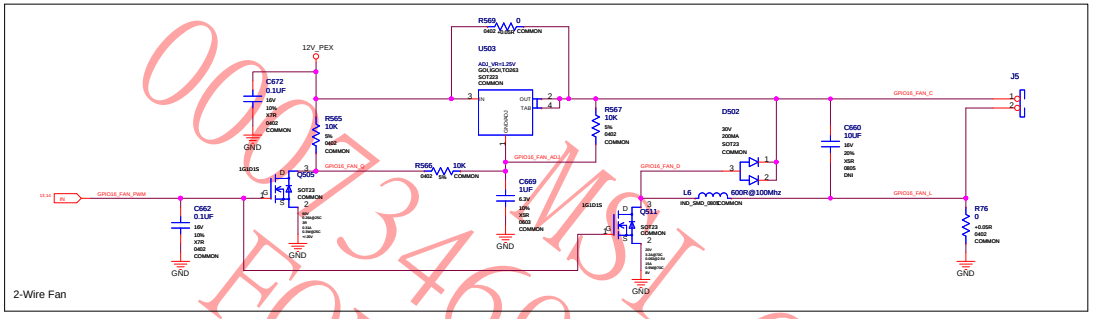
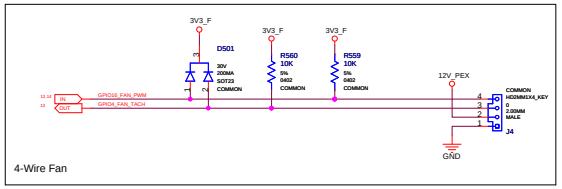
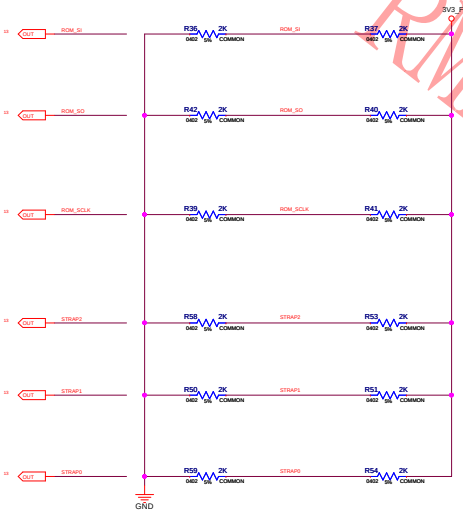
Straps, Thermal, Mechanical



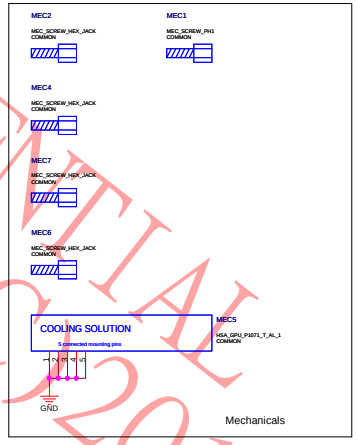


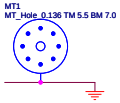
GF108 Straps

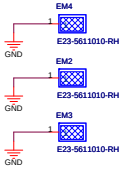


Bit	Signal	Values			
03:	RAMCFG[3]	0000			
02:	RAMCFG[2]	0001	Micron		
		0010	Hynix		
		0011	Samsung		
01:	RAMCFG[1]	0100			
00:	RAMCFG[0]	0101			
		0110			
		0111			
02:	XCLK_417	0	Disable		
		1	Enable		
01:	FBIO	0			
		1	Default		
01:	SMB_ALT_ADDR	0	0x6C		
		1	0x2C		
00:	VGA_DEVICE	0	Disable		
		1	Enable		
03:	PCI_DEVID_EXT	0	GF108-300-A1		
		0	GF108-200-A1		
02:	SUB_VENDOR	0	No BIOS		
		1	BIOS		
01:	SLOT_CLK_CFG	0	Disable		
		1	Enable		
00:	PEX_PLL_EN_TERM100	0	Disable		
		1	Enable		
03:	PCI_DEVID[3]	0001	GF108-300-A1		
		0010	GF108-200-A1		
02:	PCI_DEVID[2]				
01:	PCI_DEVID[1]				
00:	PCI_DEVID[0]				
03:	3GIO_PADCFG_LUT_ADR[3]	0000	DSKTOP_DEFAULT	1000	DSKTOP_HTHRS
		0001	MOBILE_DEFAULT	1001	MOBILE_HTHRS_NAMP
02:	3GIO_PADCFG_LUT_ADR[2]	0010	MOBILE_NTHRS_LLAMP	1010	MOBILE_HTHRS_LLAMP
		0011	MOBILE_NTHRS_LAMP	1011	MOBILE_HTHRS_LAMP
01:	3GIO_PADCFG_LUT_ADR[1]	0100	MOBILE_NTHRS_HAMP	1100	MOBILE_HTHRS_HAMP
		0101	MOBILE_NTHRS_HHAMP	1101	MOBILE_HTHRS_HHAMP
00:	3GIO_PADCFG_LUT_ADR[0]	0110	MOBILE_NTHRS_HHHAMP	1110	MOBILE_HTHRS_HHHAMP
		0111	MOBILE_NTHRS_HHHHAMP	1111	MOBILE_HTHRS_HHHHAMP
03:	USER[3]	0000	Default		
02:	USER[2]				
01:	USER[1]				
00:	USER[0]				

Multilevel Straps	
5K to GND	0000
10K to GND	0001
15K to GND	0010
20K to GND	0011
25K to GND	0100
30K to GND	0101
35K to GND	0110
45K to GND	0111
5K to VCC	1000
10K to VCC	1001
15K to VCC	1010
20K to VCC	1011
25K to VCC	1100
30K to VCC	1101
35K to VCC	1110
45K to VCC	1111







ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Straps, Thermal, Mechanical

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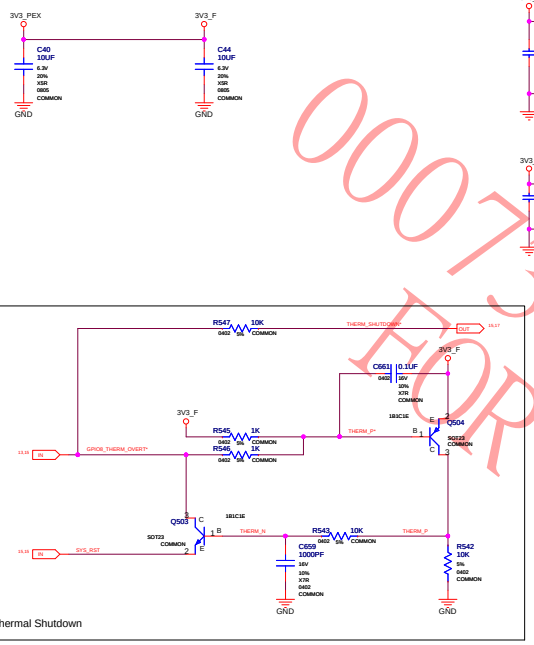
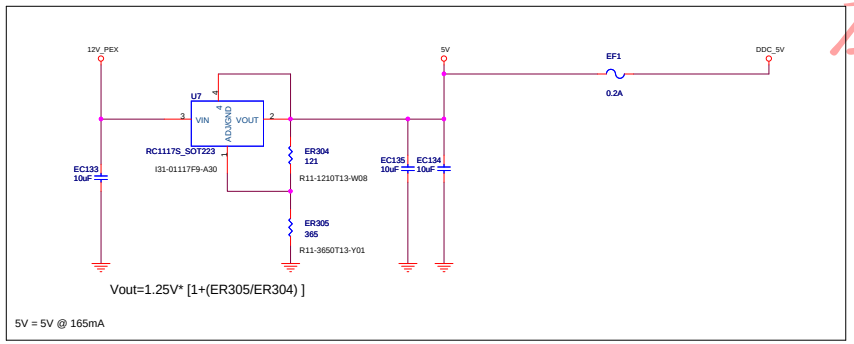
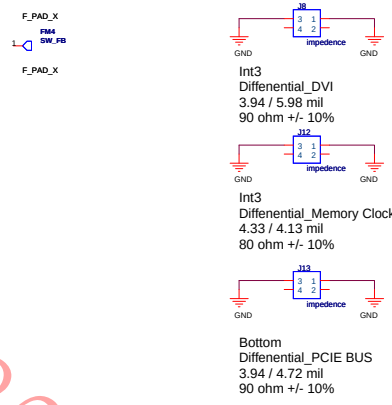
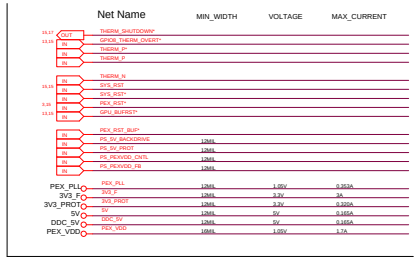
NV_PN	600-11071-BASE-SCH		
PCB REV	P1071-A01	PAGE	
BOM REV	A	DATE	01-APR-2010

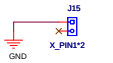
Power Supply I: Misc Power, Thermal Shutdown



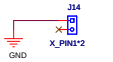




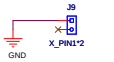




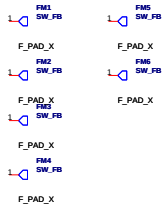
Top
Single Ended_Memory CMD
6.6929 mil
40 ohm +/- 10%

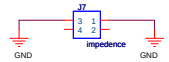


Int3
Single Ended_RGB
4.76377 mil
50 ohm +/- 10%



Bottom
Single Ended_Memory Data
3.937 mil
53 ohm +/- 10%





Top
Diffenential_PCIE BUS
3.94 / 4.72 mil
90 ohm +/- 10%

Int3
Diffenential_DVI
3.94 / 5.98 mil
90 ohm +/- 10%

Int3
Diffenential_Memory Clock
4.33 / 4.13 mil
80 ohm +/- 10%

Bottom
Diffenential_PCIE BUS
3.94 / 4.72 mil
90 ohm +/- 10%

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Power Supply I: Misc Power, Thermal Shutdown

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NV_PN	600-11071-BASE-SCH		
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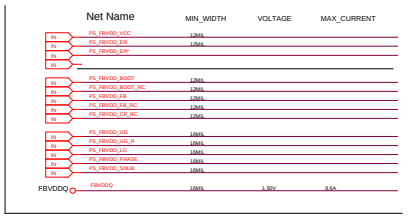
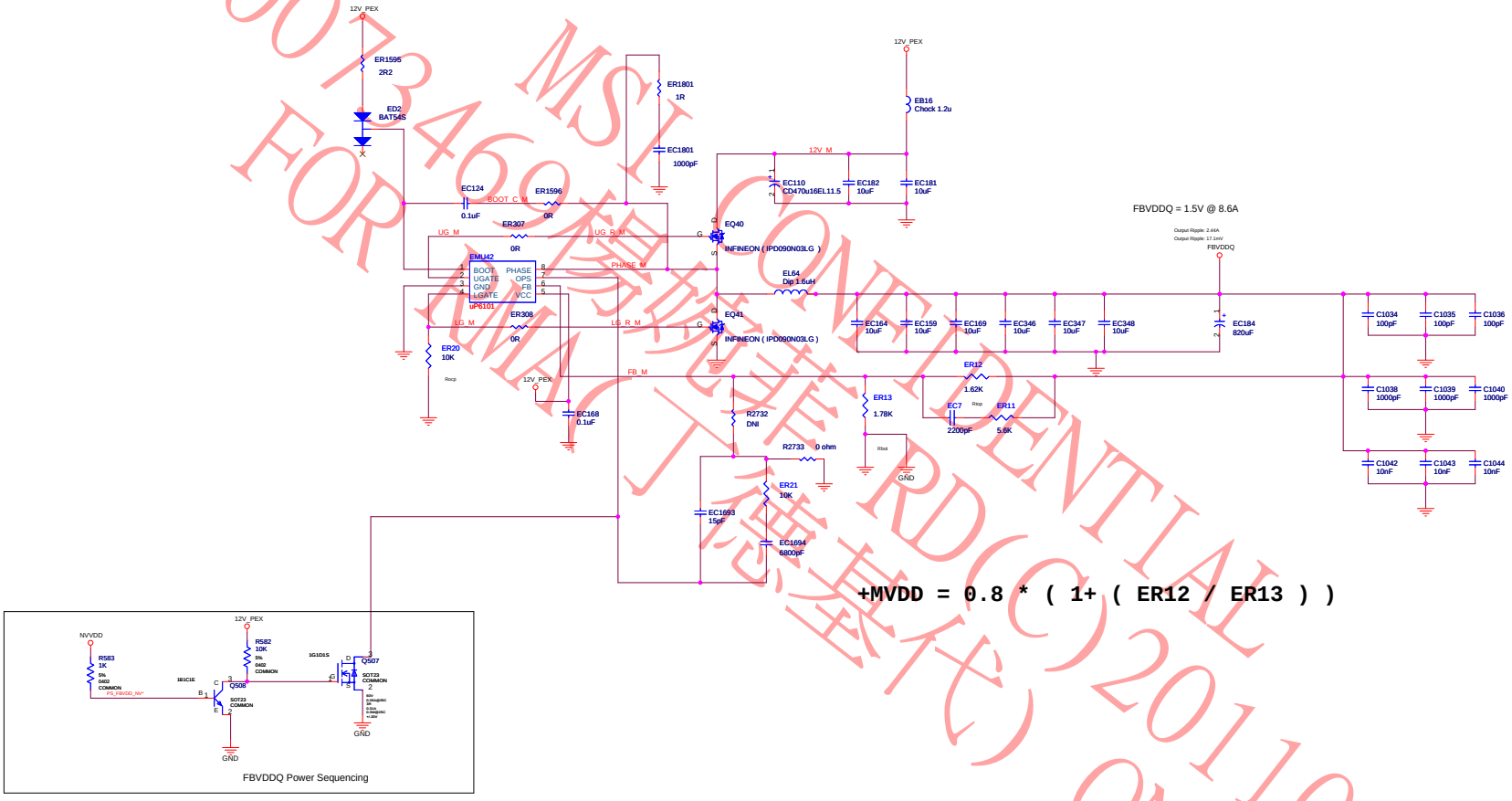
BOM REV	A	DATE	01-APR-2010

Power Supply II: FBVDDQ





+MVDD = 0.8 * (1+ (ER12 / ER13))

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Power Supply II: FBVDDQ

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NV_PN	600-11071-BASE-SCH		
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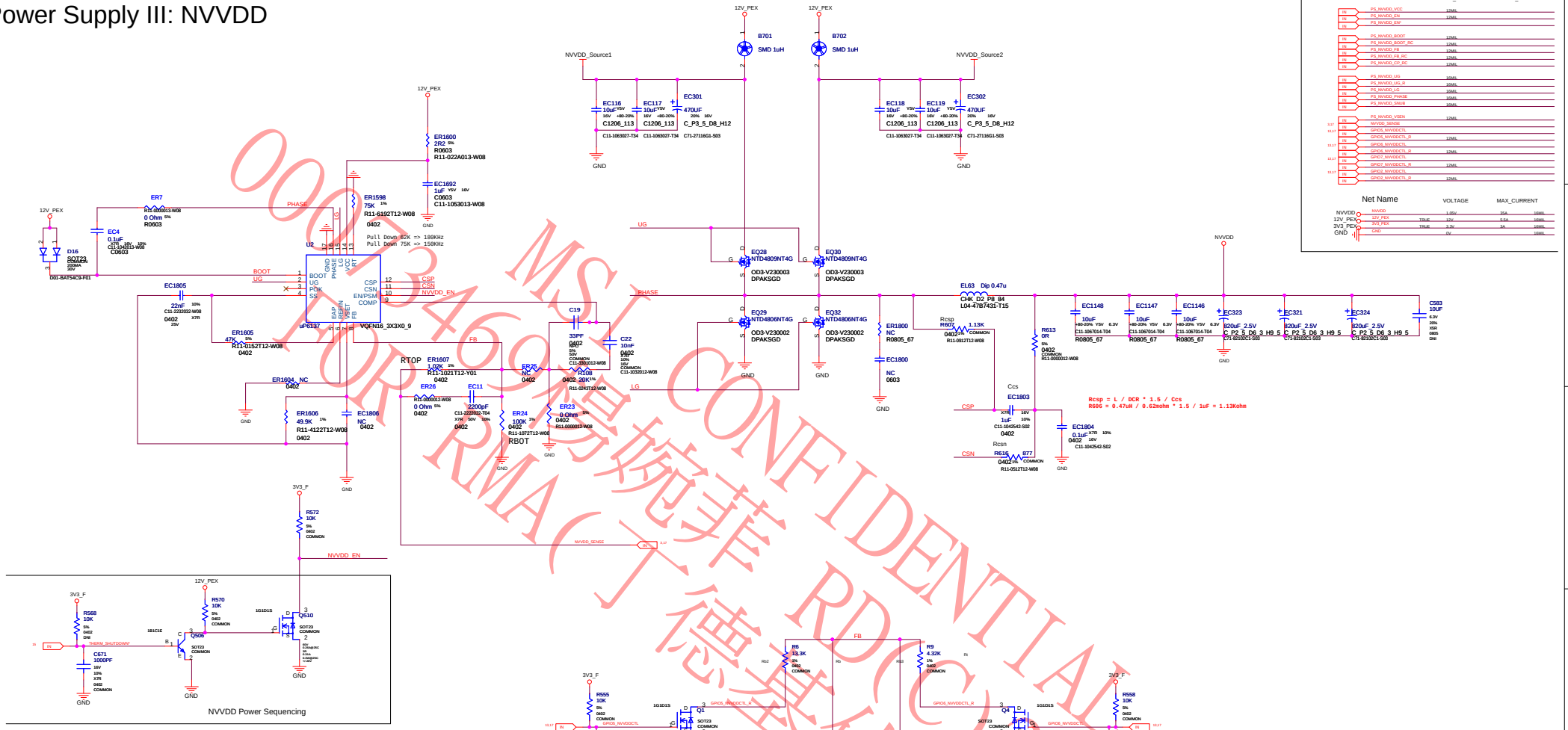
BOM REV	A	DATE	01-APR-2010

Power Supply III: NVVDD



GPIO[2,7:5]	NVVDD
0000	0.870V
0001	0.885V
0010	0.900V
0011	0.915V
0100	0.930V
0101	0.945V
0110	0.960V
0111	0.975V
1000	0.990V
1001	1.005V
1010	1.020V
1011	1.035V
1100	1.050V
1101	1.065V
1110	1.080V
1111	1.095V

Net Name | MIN_WIDTH | MAX_WIDTH

PS_NVVDD_VCC, PS_NVVDD_EN, PS_NVVDD_ENP, PS_NVVDD_BOOT, PS_NVVDD_BOOT_RC, PS_NVVDD_FB, PS_NVVDD_FB_RC, PS_NVVDD_CP_RC, PS_NVVDD_UG, PS_NVVDD_UG_R, PS_NVVDD_LG, PS_NVVDD_PHASE, PS_NVVDD_SNUB, PS_NVVDD_VSEN, NVVDD_SENSE, GPIO5_NVVDDCTL, GPIO5_NVVDDCTL_R, GPIO6_NVVDDCTL, GPIO6_NVVDDCTL_R, GPIO7_NVVDDCTL, GPIO7_NVVDDCTL_R, GPIO2_NVVDDCTL, GPIO2_NVVDDCTL_R — 12MIL / 16MIL

Net Name | VOLTAGE | MAX_CURRENT

NVVDD	NVVDD	1.05V	35A	16MIL
12V_PEX	12V_PEX	TRUE	12V	5.5A	16MIL
3V3_PEX	3V3_PEX	TRUE	3.3V	3A	16MIL
GND	GND		0V		16MIL

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	Power Supply III: NVVDD

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NV_PN	600-11071-BASE-SCH		
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BOM REV	A	DATE	01-APR-2010

Title: Basenet Report
Design: p1071_a01
Date: Mar 31 14:19:50 2010

Base nets and synonyms for
p1071_a01_lib:P1071_A01(@p1071_a01_lib.p
1071_a01(sch_1))
Base Signal Location([Zone][dir])

3V3_F 15.1G
3V3_PEX 17.2H
3V3_PROT 15.1G
5V 15.1G
12V_PEX 17.2H
DACA_BLUE 8.1G< 8.4C
DACA_BLUE_C 8.1G< 8.5F> 10.3F<
DACA_GREEN 8.1G< 8.3C
DACA_GREEN_C 8.1G> 8.4F> 10.3F<
DACA_HSYNC 8.1G< 8.3C
DACA_HSYNC_C 8.1G> 8.2F> 10.3F<
DACA_HS_BUF 8.1G< 8.2D
DACA_RED 8.1G< 8.3C
DACA_RED_C 8.1G> 8.3F> 10.3F<
DACA_RSET 8.1G< 8.3B
DACA_VREF 8.1G< 8.3B
DACA_VSYNC 8.1G< 8.3C
DACA_VSYNC_C 8.1G> 8.3F> 10.3F<
DACA_VS_BUF 8.1G< 8.3D
DACB_BLUE 9.1G< 9.4C
DACB_BLUE_C 9.1G< 9.5E
DACB_GREEN 9.1G< 9.3C
DACB_GREEN_C 9.1G< 9.4E
DACB_HSYNC 9.1G< 9.3C
DACB_HSYNC_C 9.1G> 9.2E
DACB_HS_BUF 9.1G< 9.2D
DACB_RED 9.1G< 9.3C
DACB_RED_C 9.1G< 9.3E
DACB_RSET 9.1G< 9.3B
DACB_VREF 9.1G< 9.3B
DACB_VSYNC 9.1G< 9.3C
DACB_VSYNC_C 9.1G> 9.3E
DACB_VS_BUF 9.1G< 9.2D
DAC_VDD 8.1G> 8.3B> 9.3A<
DDC_5V 15.1G
DP_MODE 11.2F
DP_MODE* 11.2G
DP_MODE_C 11.4H
FBA_CLK0 4.1G> 4.4C> 5.2A<
5.2C< 5.4B<
FBA_CLK0* 4.1G> 4.4C< 5.2A<
5.2C< 5.4B<
FBA_CLK0_T 5.1G< 5.4B
FBA_CLK1 4.1G> 4.4C> 5.2E<
5.2F< 5.5B<
FBA_CLK1* 4.1G> 4.4C> 5.2E<
5.2F< 5.5T<
FBA_CLK1_T 5.1G< 5.5B
FBA_CMD<0> 4.3C 5.1A 5.1C
FBA_CMD<30..0> 4.1G> 4.3D> 5.1A<
FBA_CMD<2> 4.3C 4.3C 5.3A 5.3C
FBA_CMD<3> 4.3C 4.3D 5.2A 5.2C
FBA_CMD<5> 4.3C 4.4D 5.3A 5.3C
5.3E 5.3F
FBA_CMD<6> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<7> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<8> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<9> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<10> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<11> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<12> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<13> 4.3C 5.1A 5.1C 5.1E
5.1F
FBA_CMD<15> 4.3C 5.1A 5.1C 5.1E
5.1F
FBA_CMD<16> 4.3C 5.1E 5.1F
FBA_CMD<18> 4.3C 4.3C 5.3E 5.3F
FBA_CMD<19> 4.3C 4.3D 5.2E 5.2F
FBA_CMD<20> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<21> 4.3C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<22> 4.4C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<23> 4.4C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<24> 4.4C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<25> 4.4C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<26> 4.4C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<27> 4.4C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<28> 4.4C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<29> 4.4C 5.2A 5.2C 5.2E
5.2F
FBA_CMD<30> 4.4C 5.1A 5.1C 5.1E
5.1F
FBA_CMD<31> 4.4C 5.1A 5.1C 5.1E
5.1F
FBA_D<0> 4.1A 5.4D
FBA_D<63..0> 4.1A<> 4.1G<> 5.4C<>
FBA_D<1> 4.1A 5.4D
FBA_D<2> 4.1A 5.4D
FBA_D<3> 4.1A 5.4D
FBA_D<4> 4.1A 5.4D
FBA_D<5> 4.1A 5.4D
FBA_D<6> 4.1A 5.4D
FBA_D<7> 4.1A 5.4D
FBA_D<8> 4.1A 5.5D
FBA_D<9> 4.1A 5.5D
FBA_D<10> 4.1A 5.5D
FBA_D<11> 4.1A 5.5D
FBA_D<12> 4.1A 5.5D
FBA_D<13> 4.1A 5.5D
FBA_D<14> 4.2A 5.5D
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FBA_D<16> 4.2A 5.4E
FBA_D<17> 4.2A 5.4E
FBA_D<18> 4.2A 5.4E
FBA_D<19> 4.2A 5.4E
FBA_D<20> 4.2A 5.4E
FBA_D<21> 4.2A 5.4E
FBA_D<22> 4.2A 5.4E
FBA_D<23> 4.2A 5.4E
FBA_D<24> 4.2A 5.5E
FBA_D<25> 4.2A 5.5E
FBA_D<26> 4.2A 5.5E
FBA_D<27> 4.2A 5.5E
FBA_D<28> 4.2A 5.5E
FBA_D<29> 4.2A 5.5E
FBA_D<30> 4.2A 5.5E
FBA_D<31> 4.2A 5.5E
FBA_D<32> 4.2A 5.4F
FBA_D<33> 4.2A 5.4F
FBA_D<34> 4.2A 5.4F
FBA_D<35> 4.2A 5.4F
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FBA_D<38> 4.2A 5.4F
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FBA_D<41> 4.2A 5.5F
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FBA_D<53> 4.3A 6.4G
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FBA_D<55> 4.3A 6.4G
FBA_D<56> 4.3A 6.5G
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FBA_D<59> 4.3A 6.5G
FBA_D<60> 4.3A 6.5G
FBA_D<61> 4.3A 6.5G
FBA_D<62> 4.3A 6.5G
FBA_D<63> 4.3A 6.5G
FBA_DBEBUG0 4.2G< 4.4G
FBA_DEBUG1 4.2G< 4.4G
FBA_DQM<0> 4.3E 6.4D
FBA_DQM<1> 4.3E 6.5D
FBA_DQM<2> 4.3E 6.4E
FBA_DQM<3> 4.3E 6.5E
FBA_DQM<4> 4.3E 6.4F
FBA_DQM<5> 4.3E 6.5F
FBA_DQM<6> 4.3E 6.4G
FBA_DQM<7> 4.3E 6.5G
FBA_DQS_RN<0> 4.4E 6.4D 7.1G
FBA_DQS_RN<7..0> 4.4E<> 6.4C<> 7.1G<>
FBA_DQS_RN<1> 4.4E 6.5D 7.1G
FBA_DQS_RN<2> 4.4E 6.4E 7.1G
FBA_DQS_RN<3> 4.4E 6.5E 7.1G
FBA_DQS_RN<4> 4.4E 6.4F 7.2G
FBA_DQS_RN<5> 4.4E 6.5F 7.2G
FBA_DQS_RN<6> 4.4E 6.4G 7.2G
FBA_DQS_RN<7> 4.4E 6.5G 7.2G
FBA_DQS_WP<0> 4.4E 6.4D 7.1G
FBA_DQS_WP<7..0> 4.4E<> 6.4C<> 7.1G<>
FBA_DQS_WP<1> 4.4E 6.5D 7.1G
FBA_DQS_WP<2> 4.4E 6.4E 7.1G
FBA_DQS_WP<3> 4.4E 6.5E 7.1G
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FBA_DQS_WP<5> 4.4E 6.5F 7.2G
FBA_DQS_WP<6> 4.4E 6.4G 7.2G
FBA_DQS_WP<7> 4.4E 6.5G 7.2G
FBA_VREF0 5.1G< 5.5D
FBA_VREF1 5.1G< 5.3H
FBA_ZQ0 5.1G< 5.3C
FBA_ZQ1 5.1G< 5.3E
FBA_ZQ2 5.1G< 5.3E
FBA_ZQ3 5.1G< 5.3F
FBB_CLK0 4.1G> 4.4G> 6.2A<
6.2C< 6.4B<
FBB_CLK0* 4.1G> 4.4G> 6.2A<
6.2C< 6.4B<
FBB_CLK0_T 6.1G< 6.4B
FBB_CLK1 4.1G> 4.4G> 6.2E<
6.2F< 6.5B<
FBB_CLK1* 4.1G> 4.4G> 6.2E<
6.2F< 6.5B<
FBB_CLK1_T 6.1G< 6.5B
FBB_CMD<0> 4.3G 6.1A 6.1C
FBB_CMD<30..0> 4.1G> 4.3H> 6.1A<
FBB_CMD<2> 4.3G 4.3G 6.3A 6.3C
FBB_CMD<3> 4.3G 4.3H 6.2A 6.2C
FBB_CMD<5> 4.3G 4.4H 6.3A 6.3C
6.3E 6.3F
FBB_CMD<6> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<7> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<8> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<9> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<10> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<11> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<12> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<13> 4.3G 6.1A 6.1C 6.1E
6.1F
FBB_CMD<15> 4.3G 6.1A 6.1C 6.1E
6.1F
FBB_CMD<16> 4.3G 6.1E 6.1F
FBB_CMD<18> 4.3G 4.3G 6.3E 6.3F
FBB_CMD<19> 4.3G 4.3H 6.2E 6.2F
FBB_CMD<20> 4.3G 6.3A 6.2C 6.2E
6.2F
FBB_CMD<21> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<22> 4.3G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<23> 4.4G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<24> 4.4G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<25> 4.4G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<26> 4.4G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<27> 4.4G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<28> 4.4G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<29> 4.4G 6.2A 6.2C 6.2E
6.2F
FBB_CMD<30> 4.4G 6.1A 6.1C 6.1E
6.1F
FBB_D<0> 4.1E 6.4D
FBB_D<63..0> 4.1E<> 4.1G<> 6.4C<>
FBB_D<1> 4.1E 6.4D
FBB_D<2> 4.1E 6.4D
FBB_D<3> 4.1E 6.4D
FBB_D<4> 4.1E 6.4D
FBB_D<5> 4.1E 6.4D
FBB_D<6> 4.1E 6.4D
FBB_D<7> 4.1E 6.4D
FBB_D<8> 4.1E 6.5D
FBB_D<9> 4.1E 6.5D
FBB_D<10> 4.1E 6.5D
FBB_D<11> 4.1E 6.5D
FBB_D<12> 4.1E 6.5D
FBB_D<13> 4.1E 6.5D
FBB_D<14> 4.1E 6.5D
FBB_D<15> 4.2E 6.5D
FBB_D<16> 4.2E 6.4E
FBB_D<17> 4.2E 6.4E
FBB_D<18> 4.2E 6.4E
FBB_D<19> 4.2E 6.4E
FBB_D<20> 4.2E 6.4E
FBB_D<21> 4.2E 6.4E
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FBB_D<23> 4.2E 6.4E
FBB_D<24> 4.2E 6.5E
FBB_D<25> 4.2E 6.5E
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FBB_DQS_WP<6> 4.4E 6.4G 7.2G
FBB_DQS_WP<7> 4.4E 6.5G 7.2G
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FBB_ZQ1 5.1G< 5.3C
FBB_ZQ2 5.1G< 5.3C
FBB_ZQ3 5.1G< 5.3D
FBVDDQ 16.1G
FB_CAL_PD_VDDQ 4.1G< 4.5G
FB_CAL_PU_GND 4.1G< 4.5G
FB_CAL_TERM_GND 4.1G< 4.5G
FB_PLLAVDD 4.2G< 4.5C
GPIO0_DVI_HPD 10.1G< 10.4D
GPIO0_DVI_HPD_C 10.1G< 10.3F
GPIO0_DVI_HPD_R 10.1G< 10.4E
GPIO2_NVVDDCTL 13.2D> 17.1G< 17.5G<
GPIO2_NVVDDCTL_R 17.2G< 17.5F
GPIO4_FAN_TACH 13.2D< 13.2G< 14.2F>
GPIO5_NVVDDCTL 13.2D> 17.1G< 17.4G<
GPIO5_NVVDDCTL_R 17.1G< 17.4E
GPIO6_NVVDDCTL 13.2D> 17.1G< 17.4G<
GPIO6_NVVDDCTL_R 17.1G< 17.4E
GPIO7_NVVDDCTL 13.2D> 17.1G< 17.5D<
GPIO7_NVVDDCTL_R 17.1G< 17.5E
GPIO8_THERM_OVERT* 13.2D> 15.1G< 15.2B<
GPIO16_FAN_ADJ 14.1G< 14.2C
GPIO16_FAN_C 14.1G< 14.2E
GPIO16_FAN_D 14.2D
GPIO16_FAN_L 14.1G< 14.2E
GPIO16_FAN_PWM 13.2D> 13.2G> 14.2B<
14.2F<
GPIO16_FAN_Q 14.1G< 14.2C
GPIO21_HDMI_HPD 11.2G< 11.4D
GPIO21_HDMI_HPD_C 11.2G< 11.3G
GPIO21_HDMI_HPD_R 11.2G< 11.4E
GPU_BUFRST* 13.4D> 15.1G< 15.3E<
GPU_PLLVDD 13.2G< 13.4B
GPU_TESTMODE 3.1G< 3.5F
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I2CA_SDA 8.1G<> 8.2F<> 10.3F<>
I2CB_SCL 9.1G< 9.3C
I2CB_SDA 9.1G< 9.3C
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I2CC_SCL_R 13.1F 13.1G<
I2CC_SDA 13.1D 13.1G<
I2CC_SDA_R 13.1F 13.1G<
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I2CZ_SDA_C 11.1G< 11.3G
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IFPF_TXD2_C* 11.1G< 11.3G 11.4D
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JTAG_TCK 13.1G< 13.2C
JTAG_TDI 13.1G< 13.2C
JTAG_TDO 13.1G< 13.2C
JTAG_TMS 13.1G< 13.2C
JTAG_TRST* 13.1G< 13.2C
NVVDD 17.2H
NVVDD_SENSE 3.4F> 17.1G< 17.4H<
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PEX_PLLVDD 3.1G< 3.4F
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PEX_REFCLK* 3.2C 3.5A<
PEX_RST* 3.1D> 15.1G< 15.2E<
15.3E<
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PEX_RX0 3.2C 3.4A<
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PS_5V_PROT 15.1G< 15.4B
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PS_FBVDD_BOOT_RC 16.1G< 16.2D
PS_FBVDD_CP_RC 16.1G< 16.4C
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PS_FBVDD_EN* 16.1G< 16.4B
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PS_FBVDD_FB_R 16.4F
PS_FBVDD_FB_RC 16.1G< 16.3F
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PS_PEXVDD_FB 15.1G< 15.4F
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ROM_SI 13.2G< 13.3D< 14.3B>
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ROM_SO 13.2G< 13.3D< 14.3B>
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SNN_BBASP 13.3C
SNN_CEC 13.3C
SNN_DP_CEC 11.4H
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SNN_FBA0_A11 5.3A
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SNN_FBB0_L10 6.3A
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SNN_FBB1_L2 6.3C
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SNN_FBB1_T1 6.3C
SNN_FBB1_T8 6.2C
SNN_FBB1_T11 6.3C
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SNN_FBB2_T1 6.3E
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SNN_FBB3_J10 6.3F
SNN_FBB3_L2 6.3F
SNN_FBB3_L10 6.3F
SNN_FBB3_M8 6.2F
SNN_FBB3_T1 6.3F
SNN_FBB3_T8 6.2F
SNN_FBB3_T11 6.3F

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	<edit here to insert page detail>

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NV_PN	600-11071-BASE-SCH		
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BOM REV	A	DATE	01-APR-2010

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SNN_FBB_WCK0 4.4E
SNN_FBB_WCK1 4.4E
SNN_FBB_WCK2 4.4E
SNN_FBB_WCK3 4.4E
SNN_FBB_WCKN0 4.4E
SNN_FBB_WCKN1 4.4E
SNN_FBB_WCKN2 4.4E
SNN_FBB_WCKN3 4.4E
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SNN_FB_PLLAVDD 4.5C
SNN_FB_VREF 4.5A
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SNN_GPIO3 13.2D
SNN_GPIO9 13.2D
SNN_GPIO10 13.2D
SNN_GPIO11 13.2D
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SNN_GPIO22 13.2D
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SNN_GPU_A7 3.2F
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
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