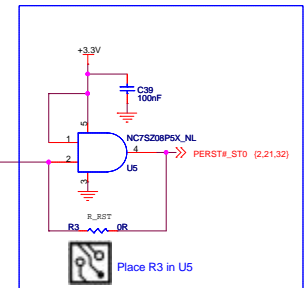
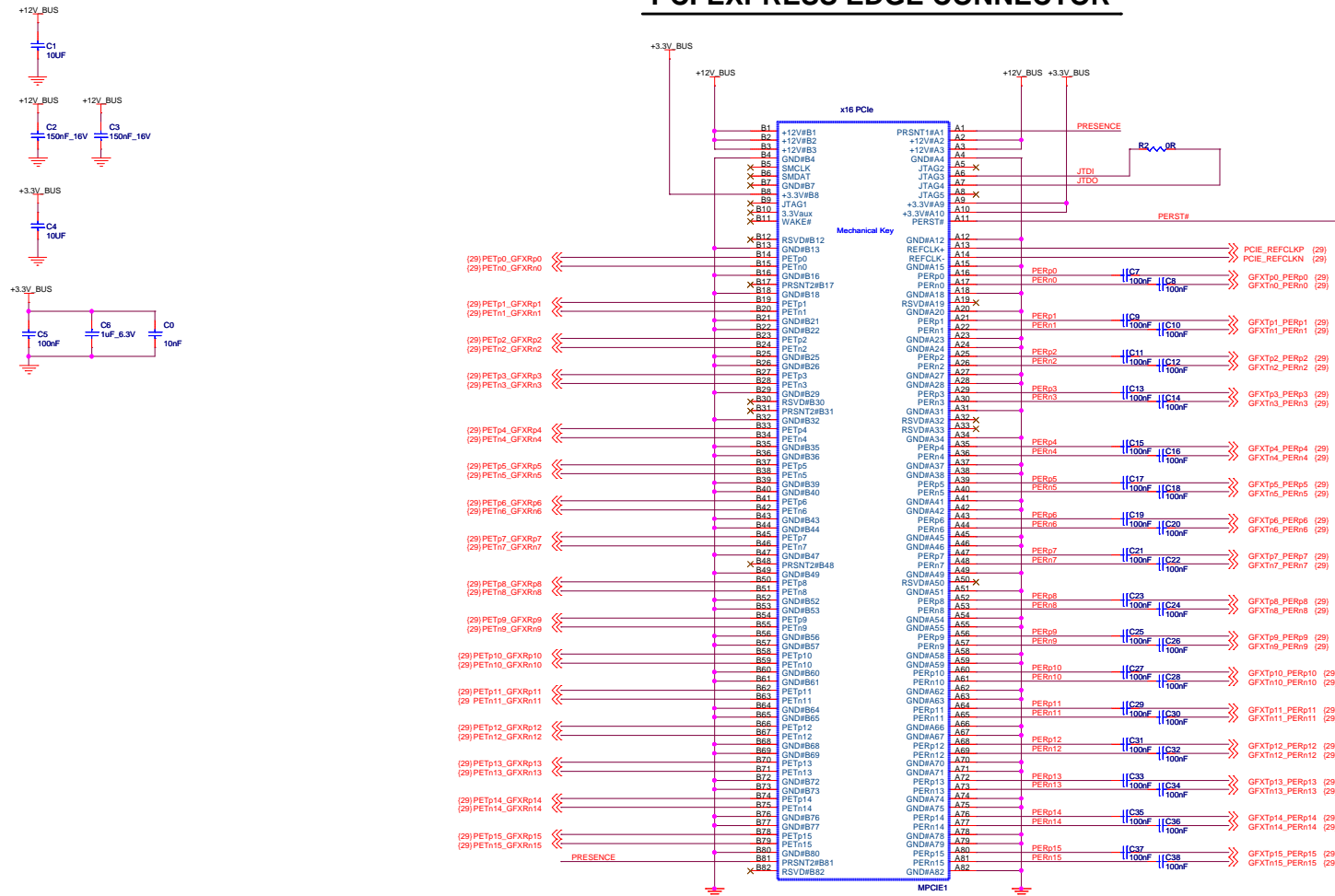


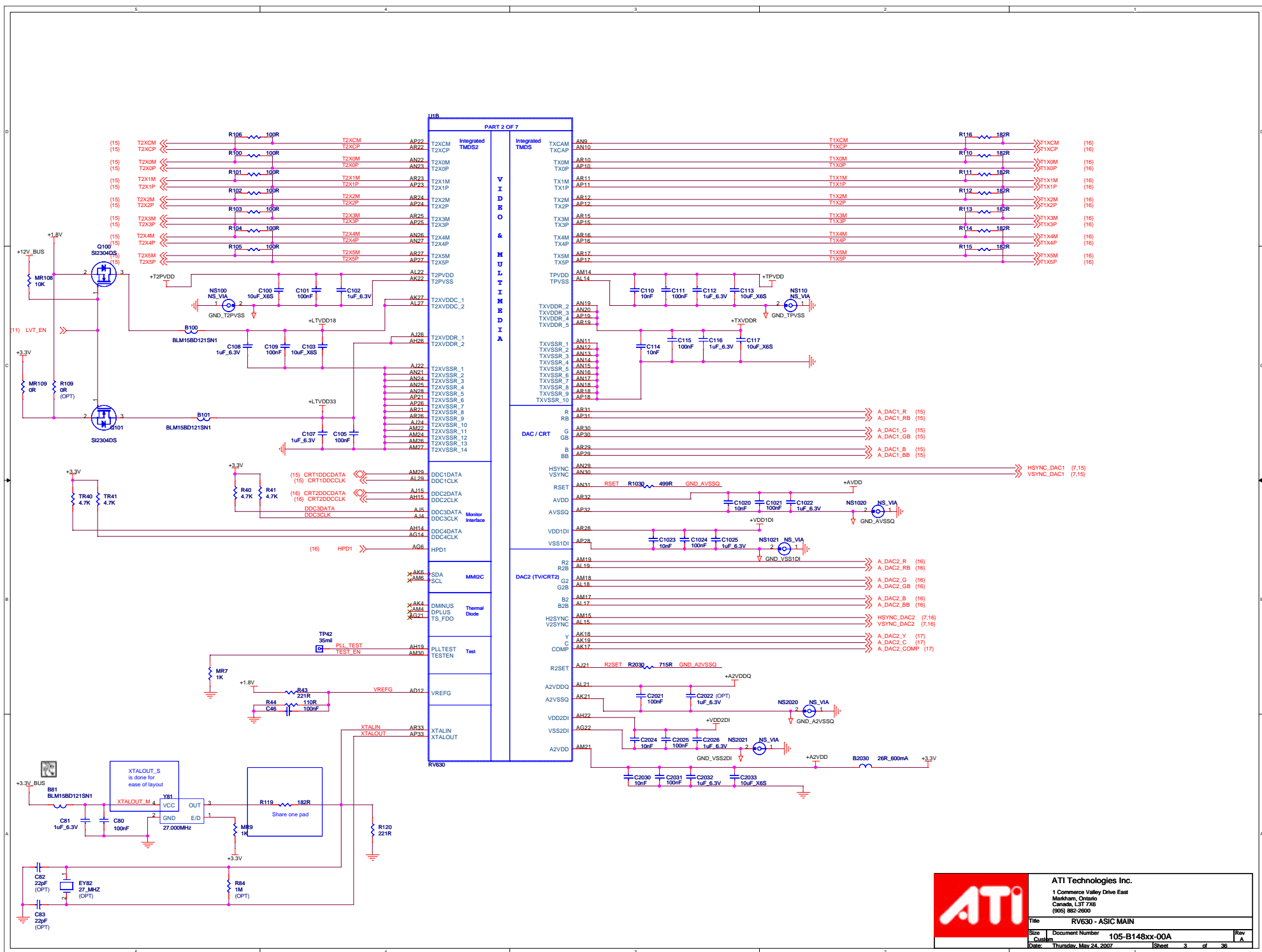
PCI-EXPRESS EDGE CONNECTOR



SYMBOL LEGEND	
DNI	DO NOT INSTALL
#	ACTIVE LOW
	DIGITAL GROUND
	ANALOG GROUND
BUO	BRING UP ONLY

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Title		PEX8547+RV630 GDDR3 VGA CARD	
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Title				RV630 - ASIC MAIN			
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Recommended caps:
(see BOM for qualified values/vendors)
10uF , X6S, 10%, 0805, 6.3V, 1.4MM MAX THICK
1uF, X6S, 10%, 0402, 6.3V
100nF, X7R, 10%, 0402
10nF , X7R, 10%, 0402

PCIE - Express

Core

Selected PLL's

Mechanical Pins

Memory I/O

POWER

RV630

PCIE - Express

Core

Selected PLL's

Mechanical Pins

Memory I/O

POWER

RV630

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RV630 GDDR3 - ASIC Power

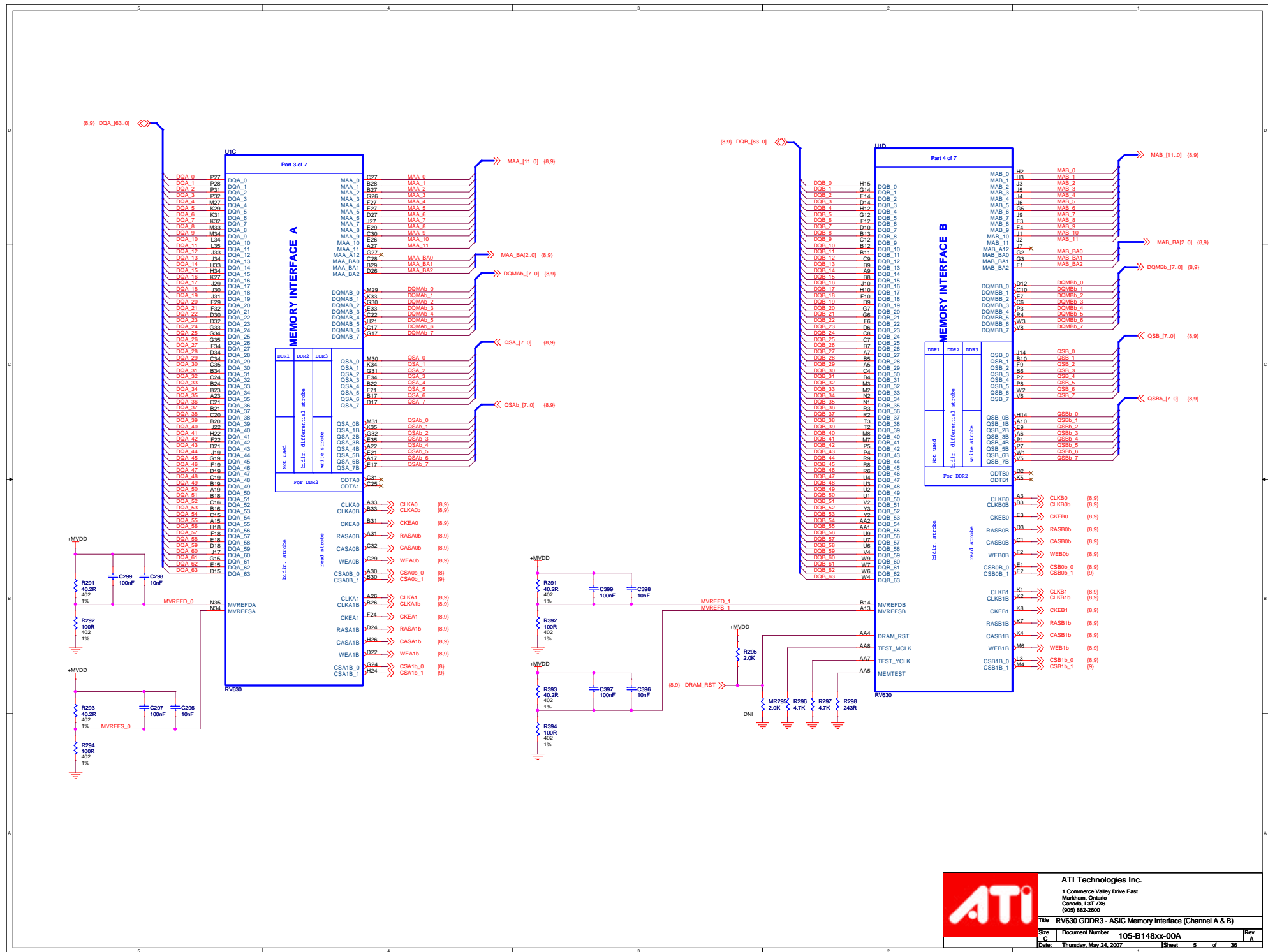
Size C Document Number **105-B148xx-00A**

Date: Thursday, May 24, 2007

Rev A

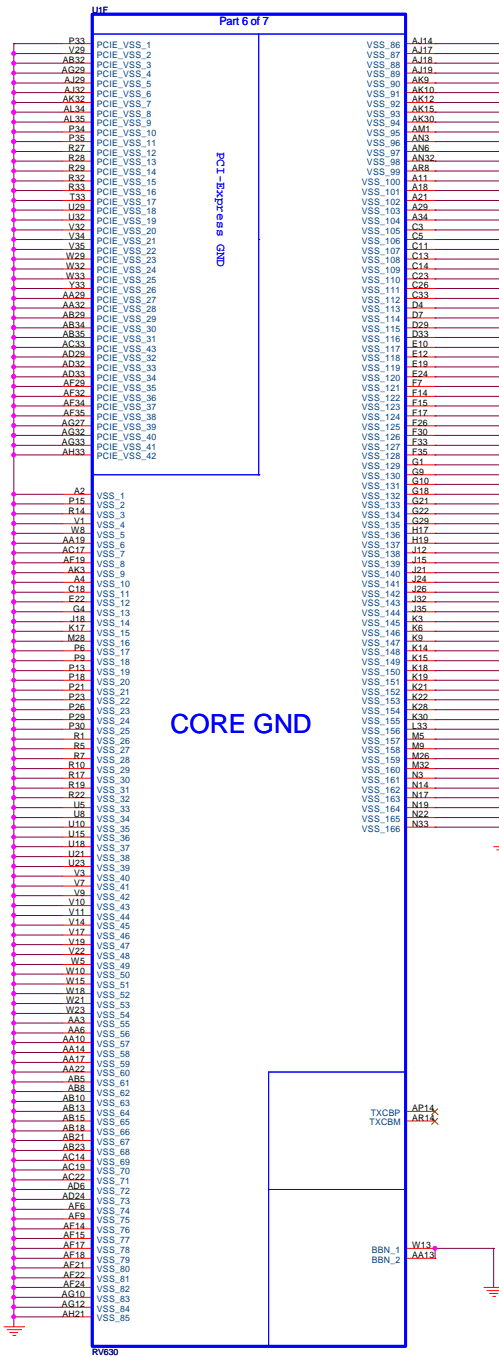


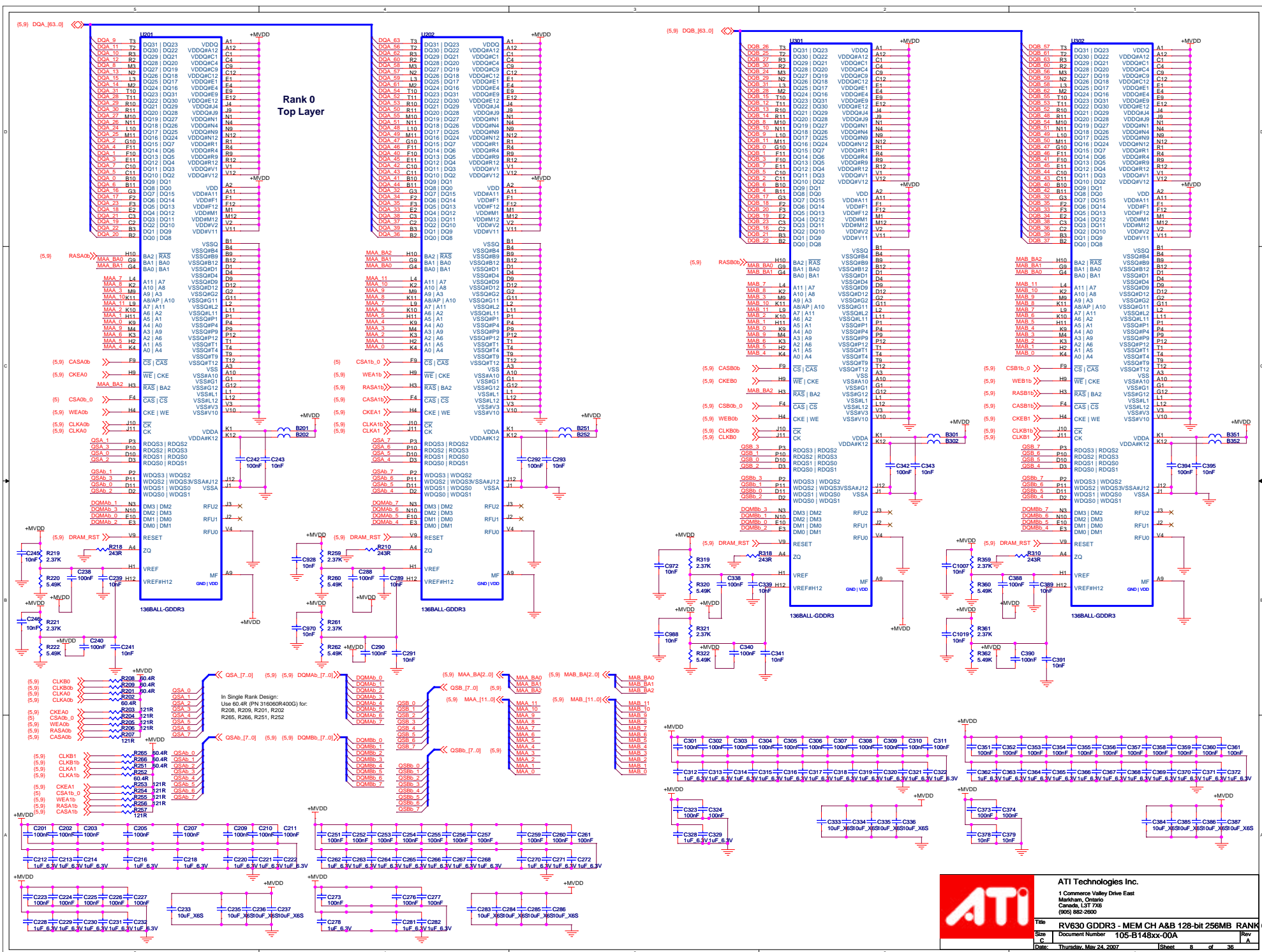
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Size	Document Number	Rev	
C	105-B148xx-00A	A	
Date:	Thursday, May 24, 2007	Sheet	4 of 36



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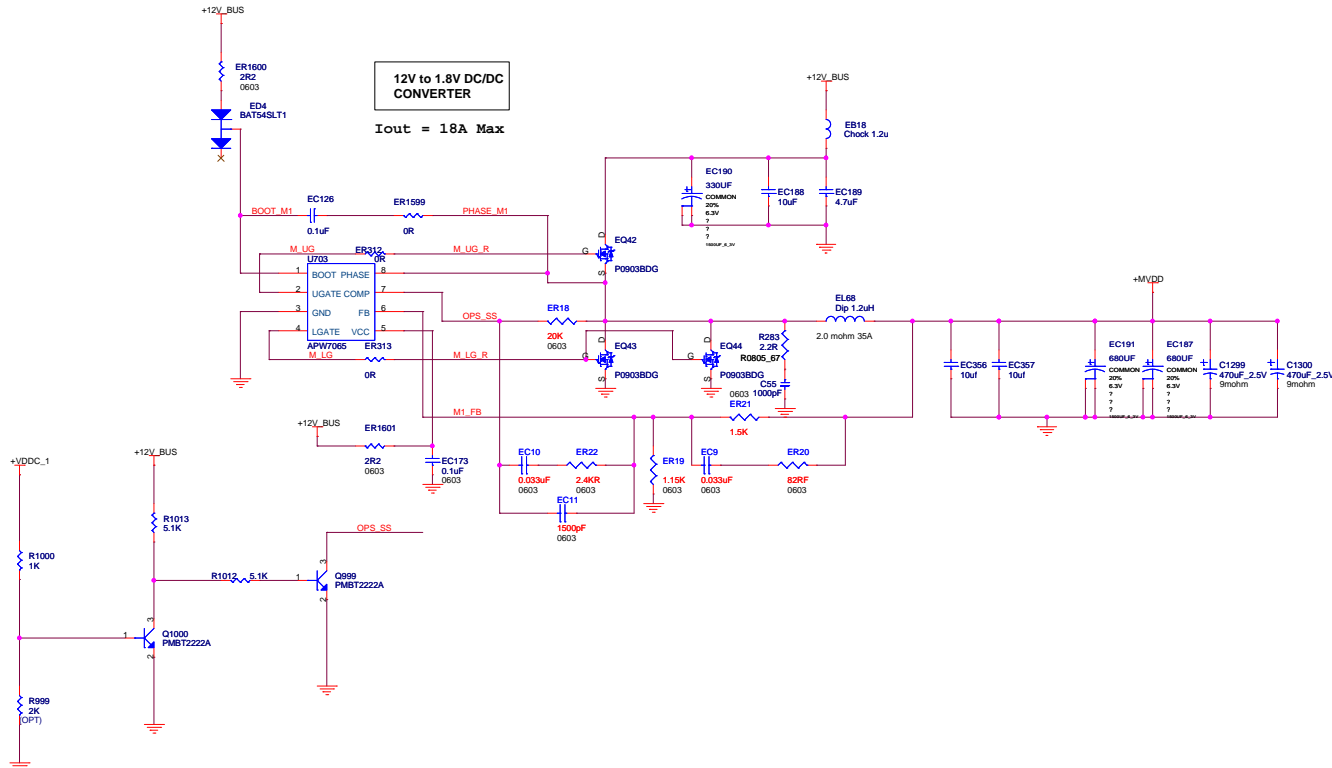
Title: RV630 GDDR3 - ASIC Memory Interface (Channel A & B)
Size: C Document Number: 105-B148xx-00A Rev: A
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CORE REGULATOR VDDC



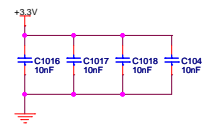
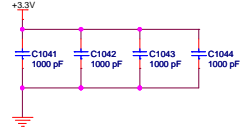
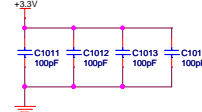
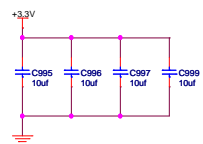
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Title RV630 GDDR3 - MVDD SMPS 01

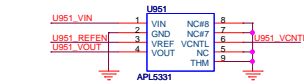
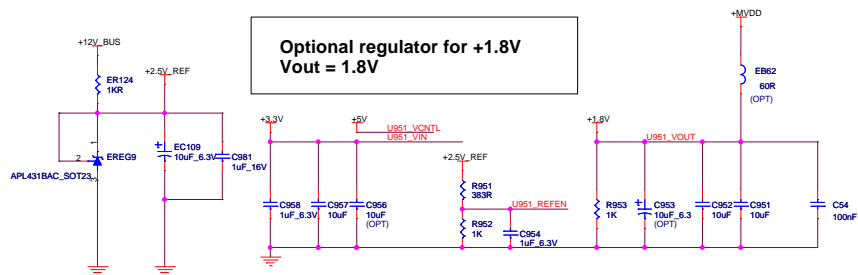
Size	Document Number	105-B148xx-00A
Custom		

Rev

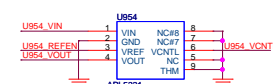
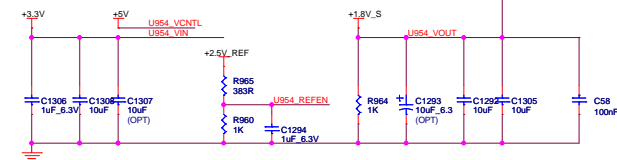


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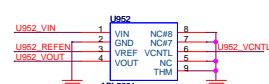
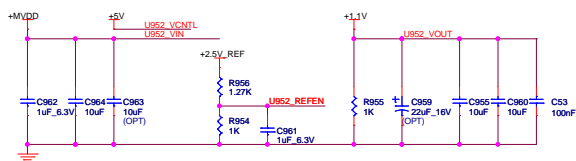
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Size	Document Number	Rev	
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Date:	Thursday, May 24, 2007	Sheet	13 of 36



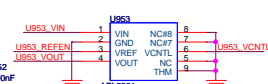
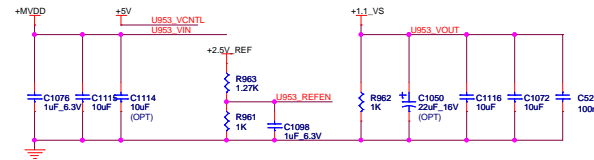
Optional regulator for +1.8V
Vout = 1.8V



Optional regulator for +1.1V
Vout = 1.1V



Optional regulator for +1.1V
Vout = 1.1V

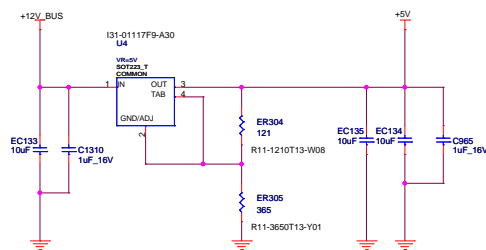


GPU1



Shared Power Rails

GPU2



$$V_{out} = 1.25V \cdot [1 + (R_{305}/R_{304})]$$



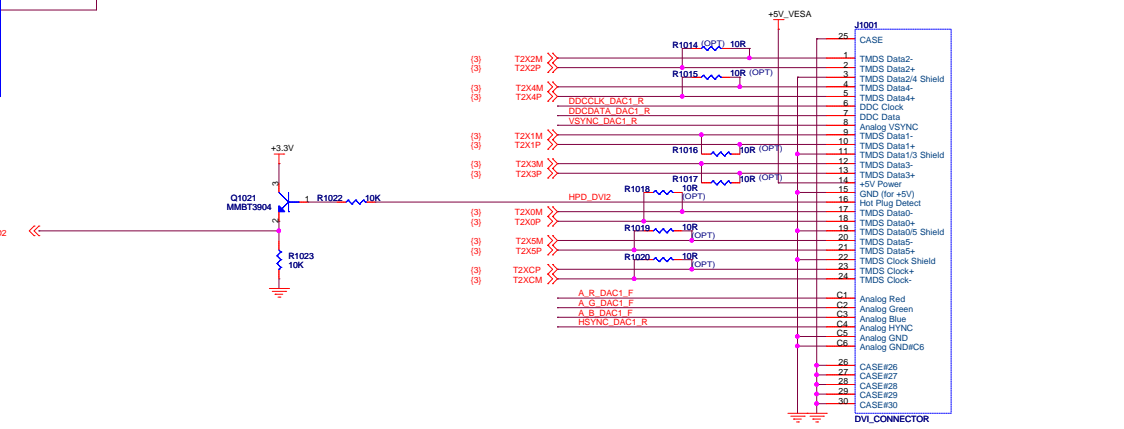
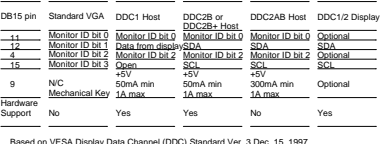
ATI Technologies Inc.
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(905) 882-2500

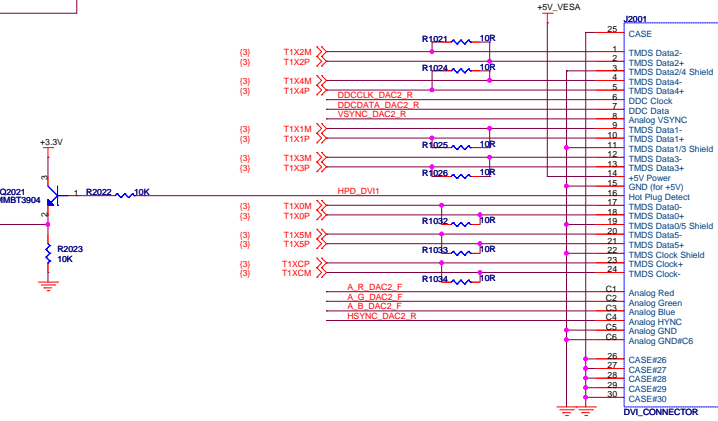
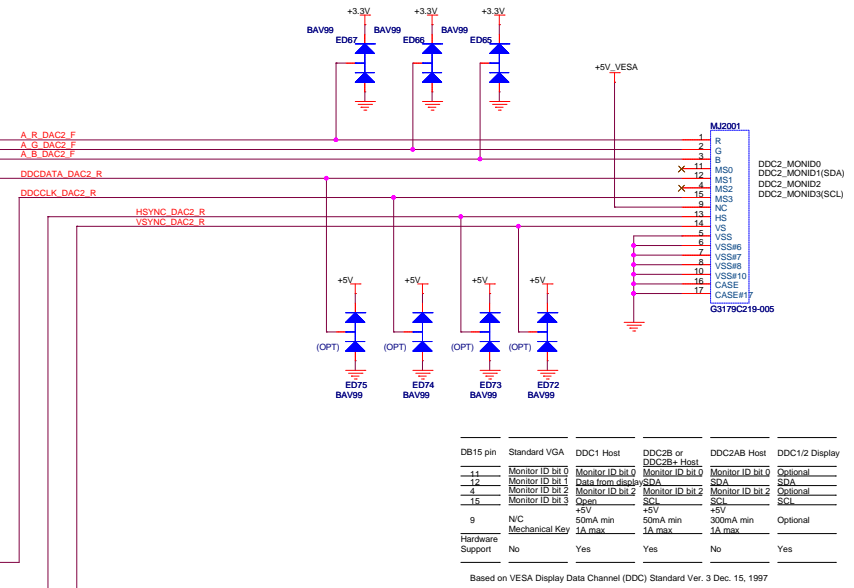
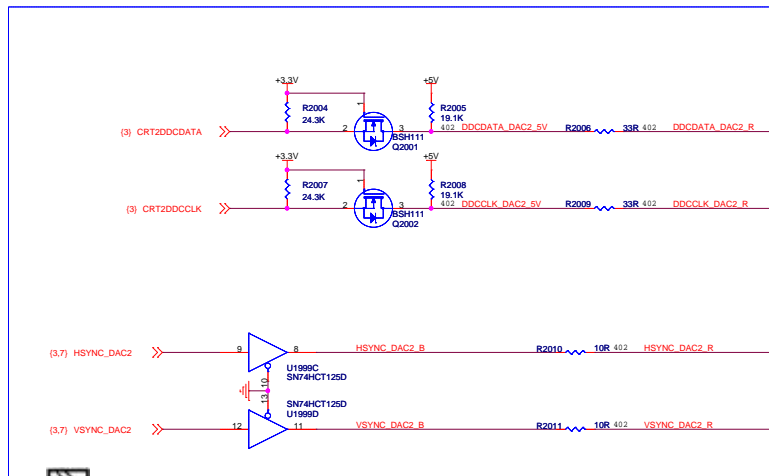
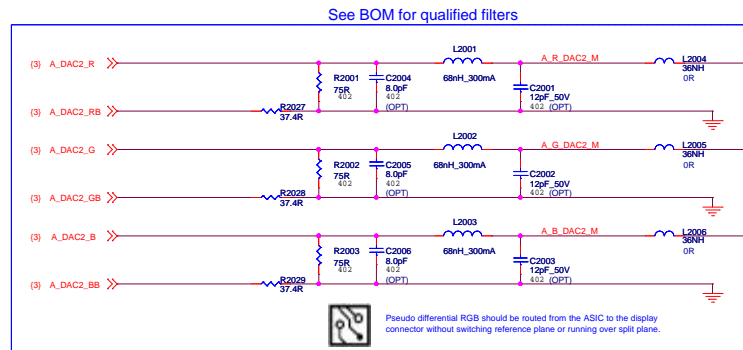
Title: RV630 GDDR3 - Linear Regulators

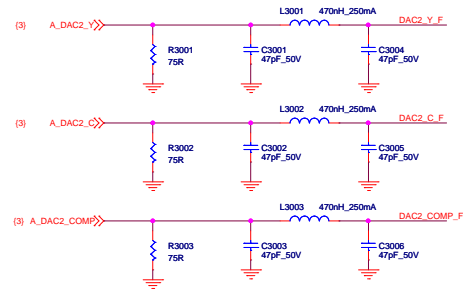
Size: C Document Number: 105-B148xx-00A

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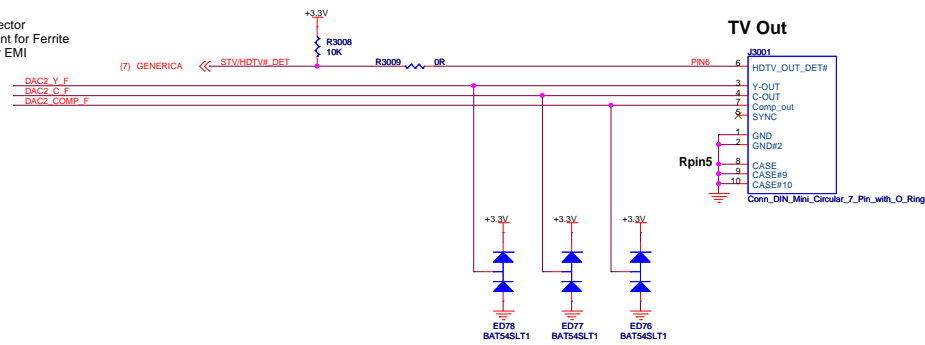
Rev







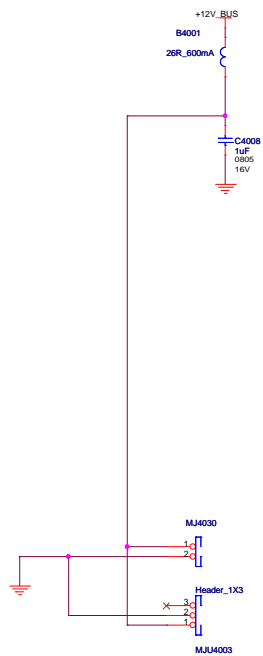
Place near connector
OR leaves footprint for Ferrite
Beads if req'd for EMI



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Title	RV630 GDDR3 - TVO		
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DVI/DVI SCREWS with top tab

MEC4

MEC_HEX_JACK_SCREW
COMMON



MEC3

MEC_HEX_JACK_SCREW
COMMON



MEC2

MEC_HEX_JACK_SCREW
COMMON



MEC1

MEC_HEX_JACK_SCREW
COMMON



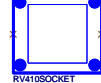
BKT1

BRACKET

8020042500G

DNI

SK1



RV410SOCKET

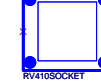
PCB1



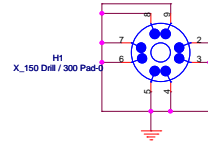
109-GN882-00A

DNI

SK2



RV410SOCKET



Fiducial Point

<Variant Name>



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Title RV630 GDDR3- Mechanical

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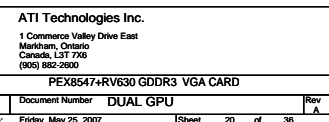
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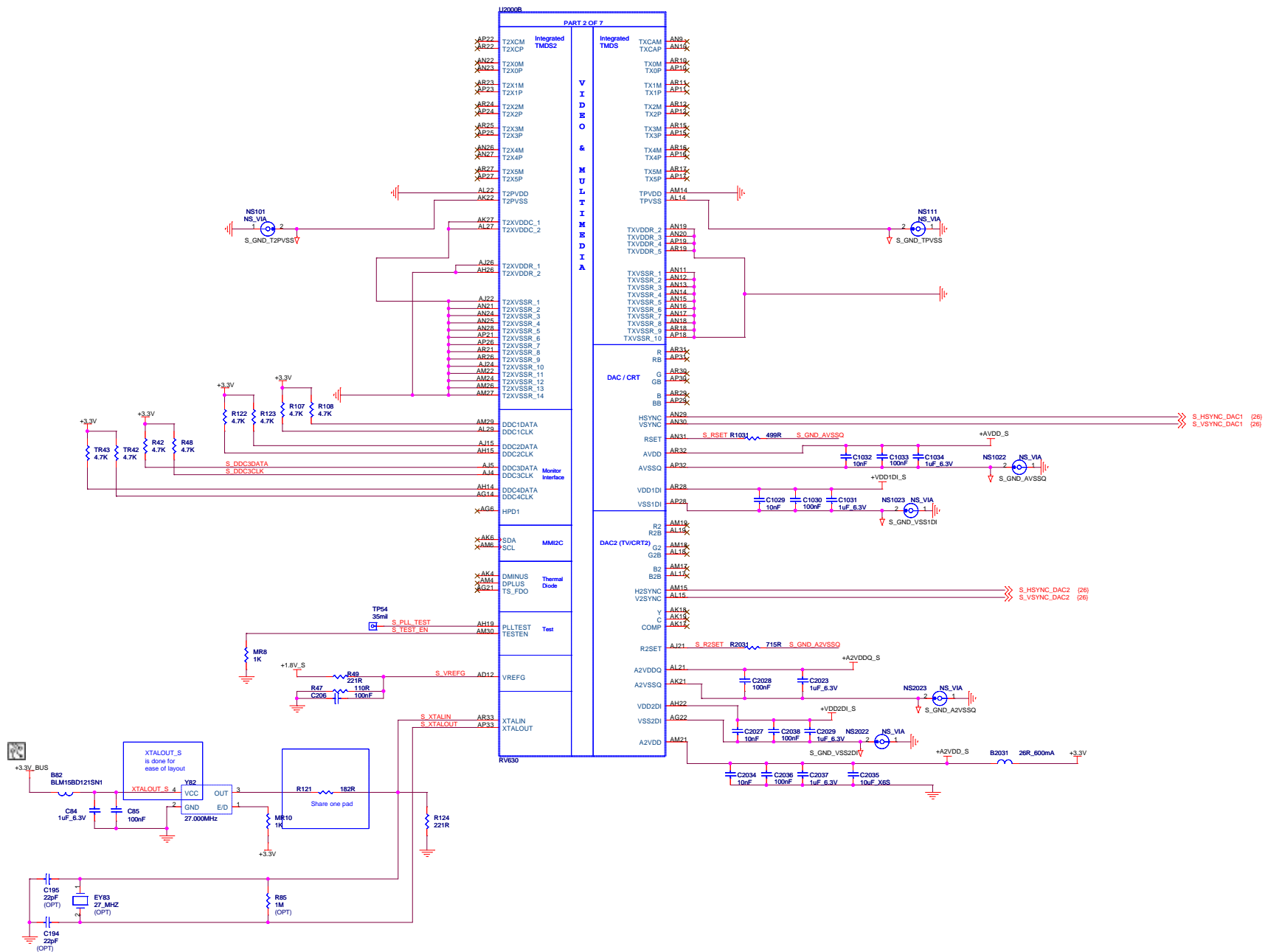
A

5 4 3 2 1



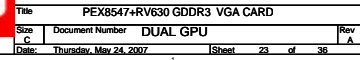
7+RV630 GDDR3 VGA CAR	
DUAL GPU	
007	Sheet 2

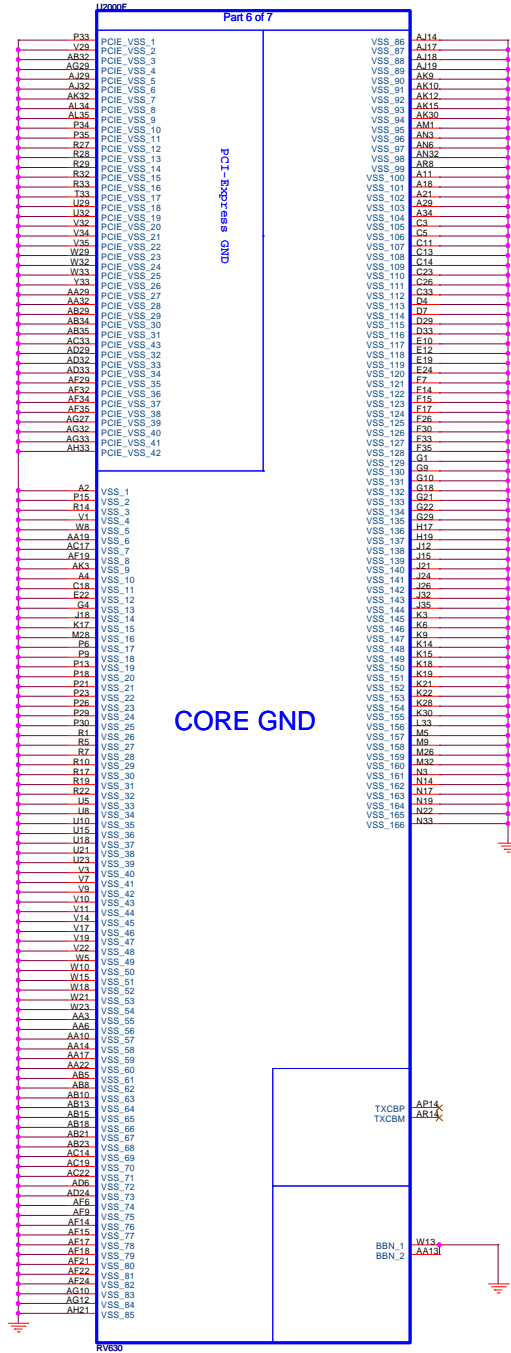




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Title			
PEX8547+RV630 GDDR3 VGA CARD			
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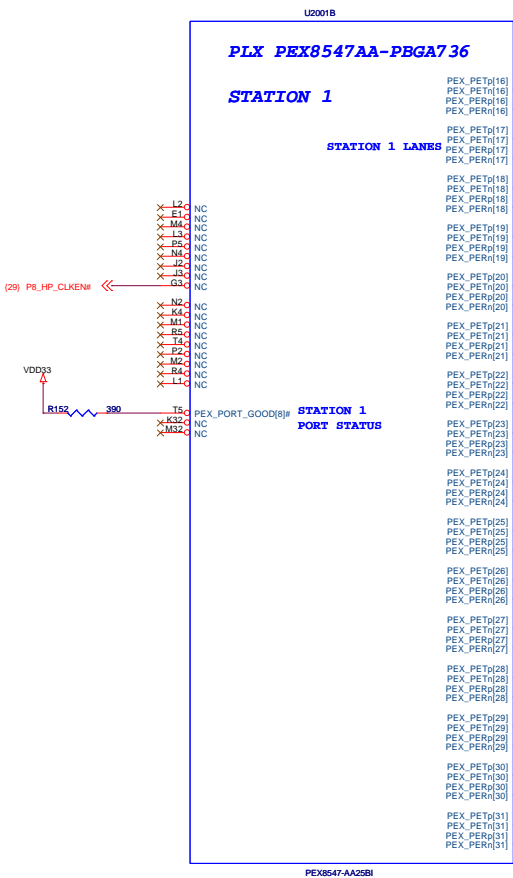
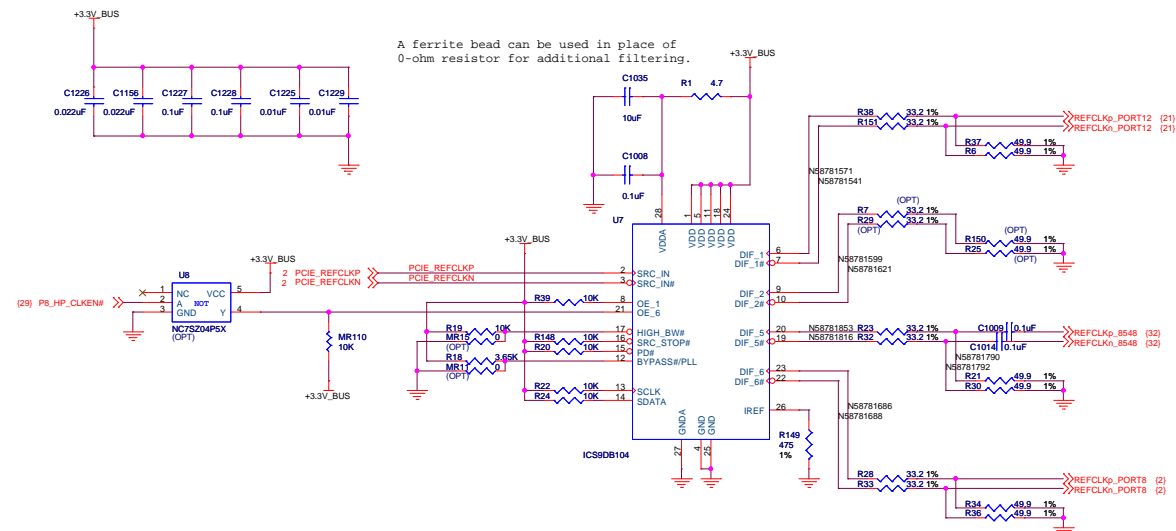
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PEX_Pet[19]	B5	GFXtp15_Perp15	GFXtp15_Perp15	1
PEX_Pet[16]	A5	GFXtp15_Perp15	GFXtp15_Perp15	1
PEX_Pet[16]	E5	Pet15_GFXR15	Pet15_GFXR15	1
PEX_Pet[16]	E8	Pet15_GFXR15	Pet15_GFXR15	1
PEX_Pet[17]	B7	GFXtp14_Perp14	GFXtp14_Perp14	1
PEX_Pet[17]	A7	GFXtp14_Perp14	GFXtp14_Perp14	1
PEX_Pet[17]	E7	Pet14_GFXR14	Pet14_GFXR14	1
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PEX_Pet[18]	B9	GFXtp13_Perp13	GFXtp13_Perp13	1
PEX_Pet[18]	A9	GFXtp13_Perp13	GFXtp13_Perp13	1
PEX_Pet[18]	E9	Pet13_GFXR13	Pet13_GFXR13	1
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PEX_Pet[19]	A11	GFXtp12_Perp12	GFXtp12_Perp12	1
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5 4 3 2 1

D

C

B

A

5 4 3 2 1

A

5 4 3 2 1

A

U2001C
PLX PEX8547AA-PBGA736

STATION 2

STATION 2 LANBS

STATION 2
PORT
STATUS



L36
X L35
X L33C
PEX_PORT_GOOD[12]
NC

PEX_PETp[32]	T35	P8_Tp0	C1077	0.1uF	PETp0_GFXRp0_M	X	PETp0_GFXRp0_M	2
PEX_PETn[32]	C136	P8_Tn0			PETn0_GFXRn0_M	X	PETn0_GFXRn0_M	2
PEX_PERp[32]	T33	P8_PER0	C1127	0.1uF	C1073	0.1uF	PERp0_PER0_M	2
PEX_PERn[32]	T32	P8_ERn0	C1128	0.1uF			PERn0_PERn0_M	2
PEX_PETp[33]	V35	P8_Tp1	C1074	0.1uF	PETp1_GFXRp1_M	X	PETp1_GFXRp1_M	2
PEX_PETn[33]	V36	P8_Tn1	C1135	0.1uF	C1076	0.1uF	PETn1_GFXRn1_M	2
PEX_PERp[33]	V33	P8_PER1			PERp1_PERp1_M	X	PERp1_PERp1_M	2
PEX_PERn[33]	V32	P8_ERn1	C1157	0.1uF			PERn1_PERn1_M	2
PEX_PETp[34]	V35	P8_Tp2	C1037	0.1uF	PETp2_GFXRp2_M	X	PETp2_GFXRp2_M	2
PEX_PETn[34]	V36	P8_Tn2	C1158	0.1uF	C1038	0.1uF	PETn2_GFXRn2_M	2
PEX_PERp[34]	V33	P8_PER2			PERp2_PERp2_M	X	PERp2_PERp2_M	2
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PEX_PERp[36]	AD33	P8_PER4			PERp4_PERp4_M	X	PERp4_PERp4_M	2
PEX_PERn[36]	AD32	P8_ERn4	C1163	0.1uF			PERn4_PERn4_M	2
PEX_PETp[37]	AE35	P8_Tp5	C1066	0.1uF	PETp5_GFXRp5_M	X	PETp5_GFXRp5_M	2
PEX_PETn[37]	AE36	P8_Tn5	C1164	0.1uF	C1067	0.1uF	PETn5_GFXRn5_M	2
PEX_PERp[37]	AE33	P8_PER5			PERp5_PERp5_M	X	PERp5_PERp5_M	2
PEX_PERn[37]	AE32	P8_ERn5	C1165	0.1uF			PERn5_PERn5_M	2
PEX_PETp[38]	AH35	P8_Tp6	C1068	0.1uF	PETp6_GFXRp6_M	X	PETp6_GFXRp6_M	2
PEX_PETn[38]	AH36	P8_Tn6	C1166	0.1uF	C1069	0.1uF	PETn6_GFXRn6_M	2
PEX_PERp[38]	AH33	P8_PER6			PERp6_PERp6_M	X	PERp6_PERp6_M	2
PEX_PERn[38]	AH32	P8_ERn6	C1167	0.1uF			PERn6_PERn6_M	2
PEX_PETp[39]	AK35	P8_Tp7	C1070	0.1uF	PETp7_GFXRp7_M	X	PETp7_GFXRp7_M	2
PEX_PETn[39]	AK36	P8_Tn7			PETn7_GFXRn7_M	X	PETn7_GFXRn7_M	2
PEX_PERp[39]	AK33	P8_PER7	C1230	0.1uF	C1071	0.1uF	PERp7_PERp7_M	2
PEX_PERn[39]	AK32	P8_ERn7	C1231	0.1uF			PERn7_PERn7_M	2
PEX_PETp[40]	AR34	P8_Tp8	C1062	0.1uF	PETp8_GFXRp8_M	X	PETp8_GFXRp8_M	2
PEX_PETn[40]	AR34	P8_Tn8			PETn8_GFXRn8_M	X	PETn8_GFXRn8_M	2
PEX_PERp[40]	AM34	P8_PER8	C1232	0.1uF	C1060	0.1uF	PERp8_PERp8_M	2
PEX_PERn[40]	AM34	P8_ERn8	C1233	0.1uF			PERn8_PERn8_M	2
PEX_PETp[41]	AR32	P8_Tp9	C1061	0.1uF	PETp9_GFXRp9_M	X	PETp9_GFXRp9_M	2
PEX_PETn[41]	AR32	P8_Tn9			PETn9_GFXRn9_M	X	PETn9_GFXRn9_M	2
PEX_PERp[41]	AM32	P8_PER9	C1234	0.1uF	C1063	0.1uF	PERp9_PERp9_M	2
PEX_PERn[41]	AM32	P8_ERn9	C1235	0.1uF			PERn9_PERn9_M	2
PEX_PETp[42]	AR30	P8_Tp10	C1047	0.1uF	PETp10_GFXRp10_M	X	PETp10_GFXRp10_M	2
PEX_PETn[42]	AR30	P8_Tn10			PETn10_GFXRn10_M	X	PETn10_GFXRn10_M	2
PEX_PERp[42]	AM30	P8_PER10	C1236	0.1uF	C1048	0.1uF	PERp10_PERp10_M	2
PEX_PERn[42]	AM30	P8_ERn10	C1237	0.1uF			PERn10_PERn10_M	2
PEX_PETp[43]	AR28	P8_Tp11	C1049	0.1uF	PETp11_GFXRp11_M	X	PETp11_GFXRp11_M	2
PEX_PETn[43]	AR28	P8_Tn11			PETn11_GFXRn11_M	X	PETn11_GFXRn11_M	2
PEX_PERp[43]	AM28	P8_PER11	C1238	0.1uF	C1051	0.1uF	PERp11_PERp11_M	2
PEX_PERn[43]	AM28	P8_ERn11	C1239	0.1uF			PERn11_PERn11_M	2
PEX_PETp[44]	AR26	P8_Tp12	C1052	0.1uF	PETp12_GFXRp12_M	X	PETp12_GFXRp12_M	2
PEX_PETn[44]	AR26	P8_Tn12			PETn12_GFXRn12_M	X	PETn12_GFXRn12_M	2
PEX_PERp[44]	AM26	P8_PER12	C1240	0.1uF	C1053	0.1uF	PERp12_PERp12_M	2
PEX_PERn[44]	AM26	P8_ERn12	C1241	0.1uF			PERn12_PERn12_M	2
PEX_PETp[45]	AR24	P8_Tp13	C1054	0.1uF	PETp13_GFXRp13_M	X	PETp13_GFXRp13_M	2
PEX_PETn[45]	AR24	P8_Tn13			PETn13_GFXRn13_M	X	PETn13_GFXRn13_M	2
PEX_PERp[45]	AM24	P8_PER13	C1242	0.1uF	C1055	0.1uF	PERp13_PERp13_M	2
PEX_PERn[45]	AM24	P8_ERn13	C1243	0.1uF			PERn13_PERn13_M	2
PEX_PETp[46]	AR22	P8_Tp14	C1056	0.1uF	PETp14_GFXRp14_M	X	PETp14_GFXRp14_M	2
PEX_PETn[46]	AR22	P8_Tn14			PETn14_GFXRn14_M	X	PETn14_GFXRn14_M	2
PEX_PERp[46]	AM22	P8_PER14	C1244	0.1uF	C1058	0.1uF	PERp14_PERp14_M	2
PEX_PERn[46]	AM22	P8_ERn14	C1246	0.1uF			PERn14_PERn14_M	2
PEX_PETp[47]	AR20	P8_Tp15	C1058	0.1uF	PETp15_GFXRp15_M	X	PETp15_GFXRp15_M	2
PEX_PETn[47]	AR20	P8_Tn15			PETn15_GFXRn15_M	X	PETn15_GFXRn15_M	2
PEX_PERp[47]	AM20	P8_PER15	C1245	0.1uF	C1059	0.1uF	PERp15_PERp15_M	2
PEX_PERn[47]	AM20	P8_ERn15	C1247	0.1uF			PERn15_PERn15_M	2

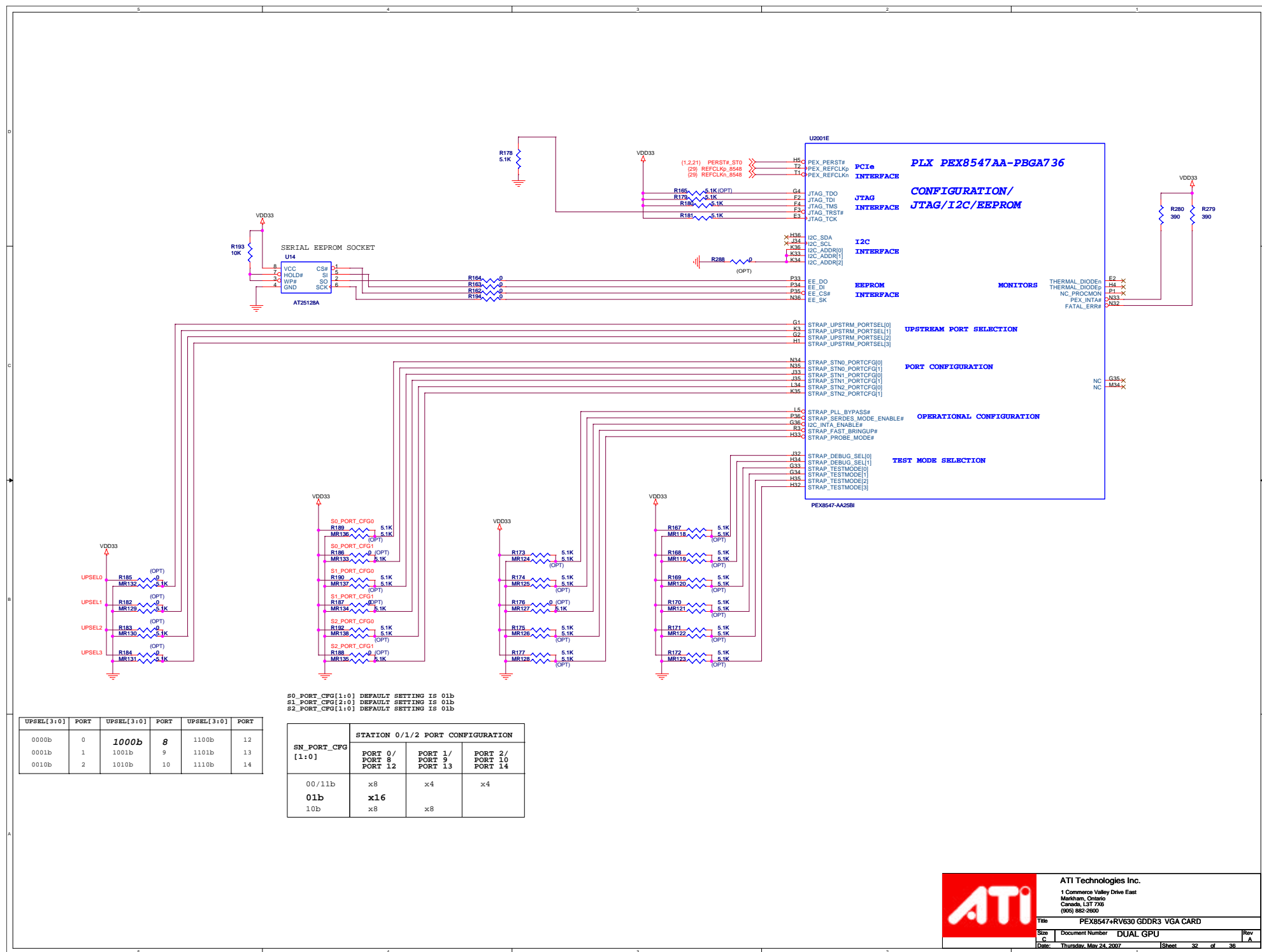
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VP033
R195 0
A ferrite bead can be used in place of
0-ohm resistor for additional filtering.
CAP to be placed
near ball
C113 0.1uF
U200D



PLX PEX8547AA-PBGA736

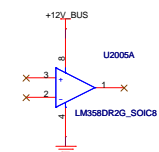
POWER

PEX8547-AA25BI



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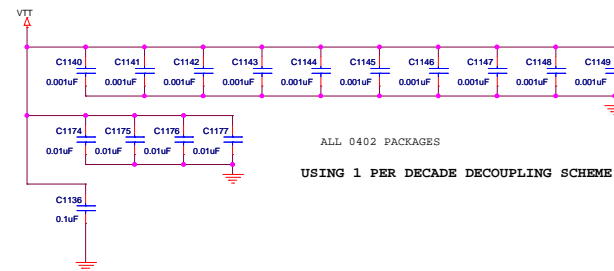
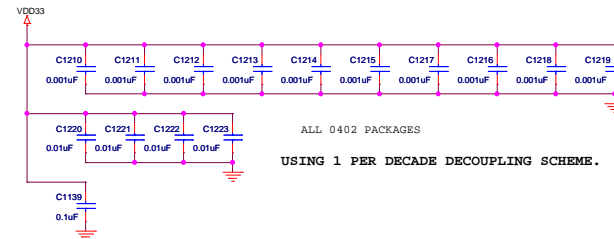
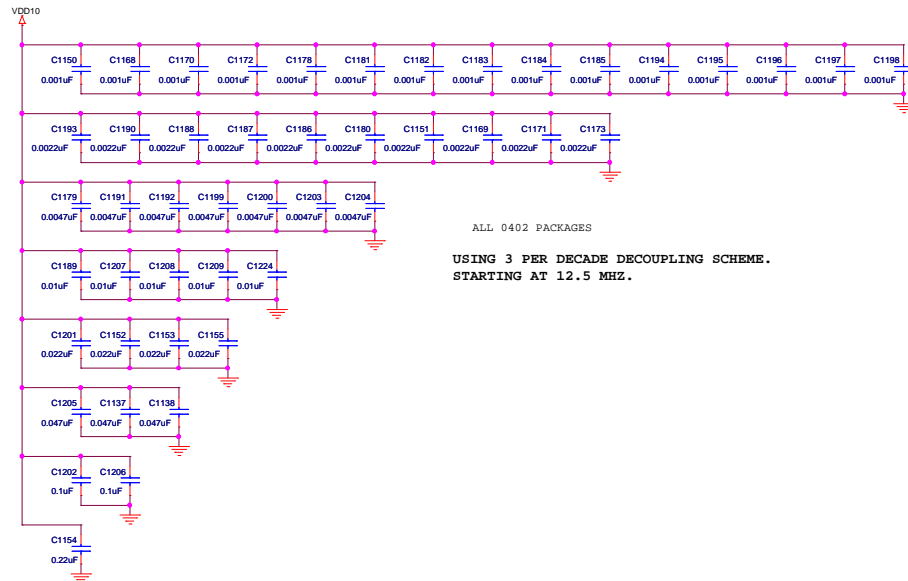


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