

PG161-A00

TU106 6GB GDDR6, 192b, X16
DVI-D/DP + DP + HDMI

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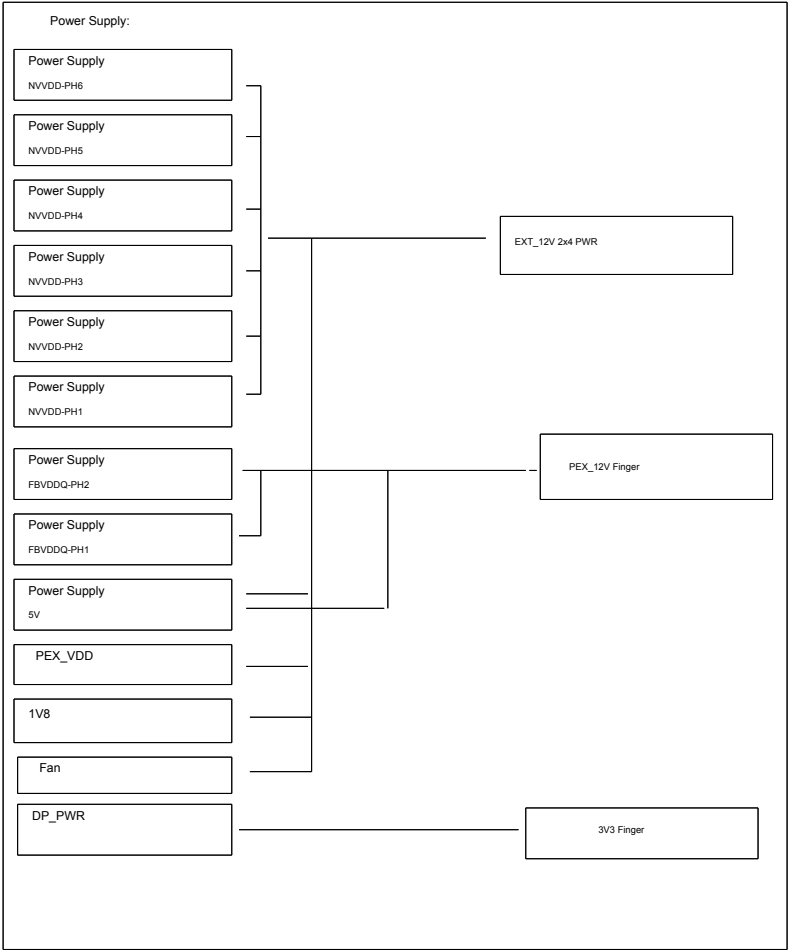
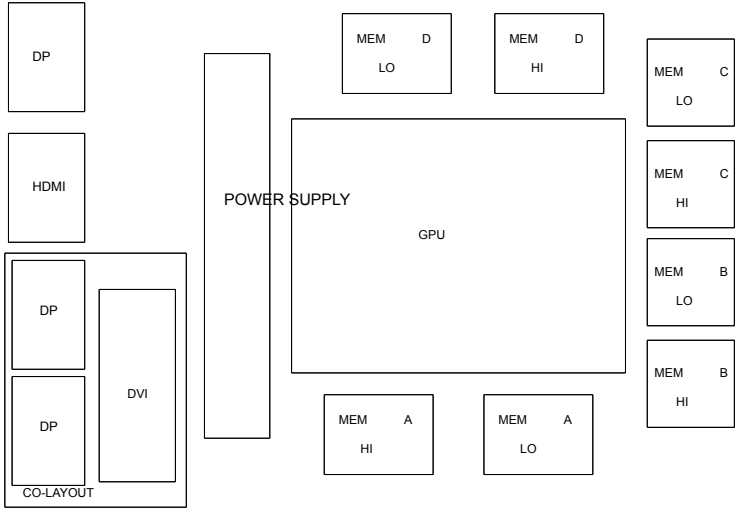
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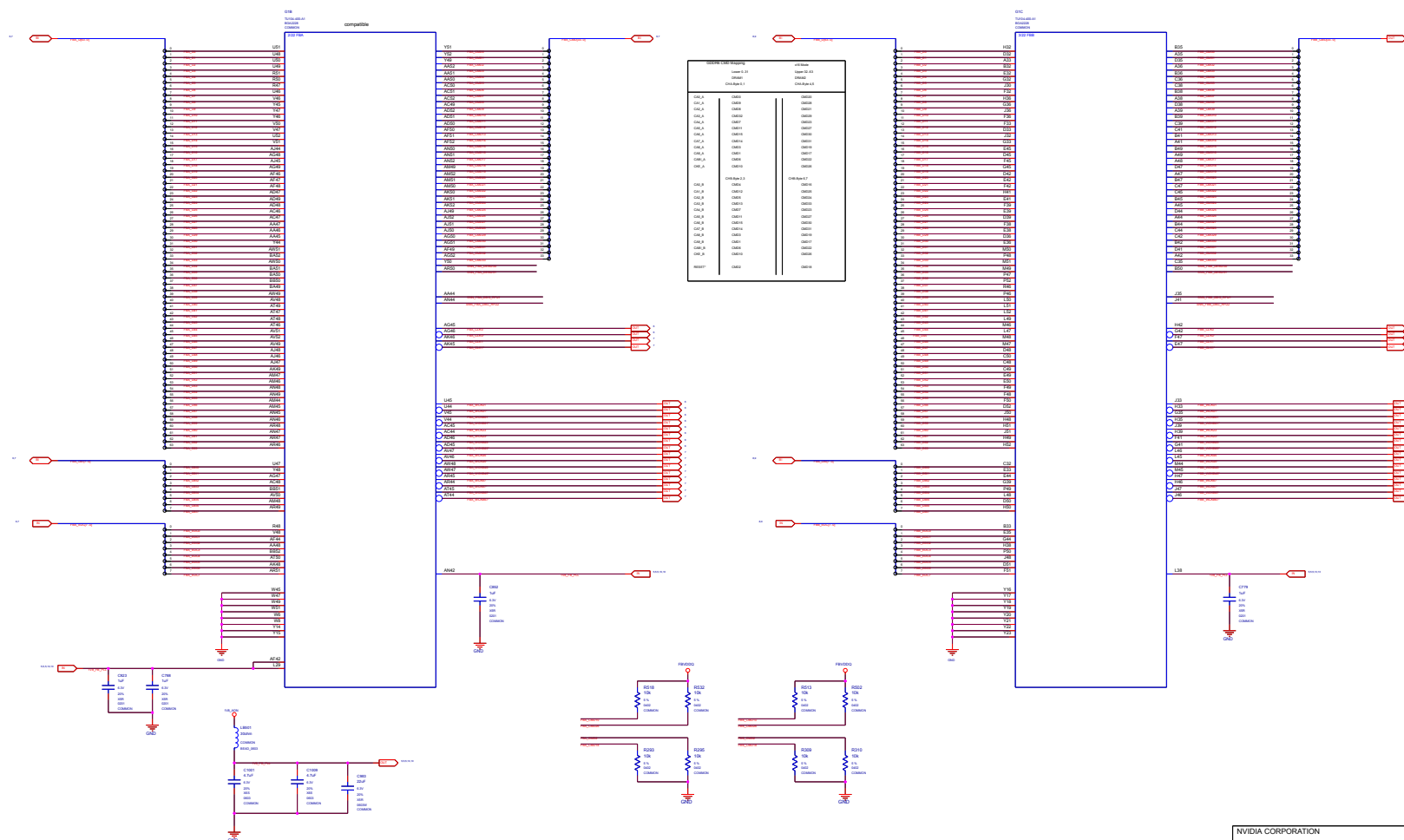
NVIDIA CORPORATION			
2011 SAN JOSE, CALIFORNIA			
NVIDIA CLARK, CA 94501			
			
REV. 001	800-1G161-BASE-000	10/10	10/10
REV. 001	REV. 001	REV. 001	REV. 001



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ASSEMBLY	COMPONENT DESCRIPTION
POWER SUPPLY	Power Supply

NVIDIA CORPORATION			
2001 SAN TOME AVENUE, SANTA CLARA, CA 95050, USA			
			
PNV_PTH	800-1G161-BASE-000		
REV	1.0	DATE	1/1/2018



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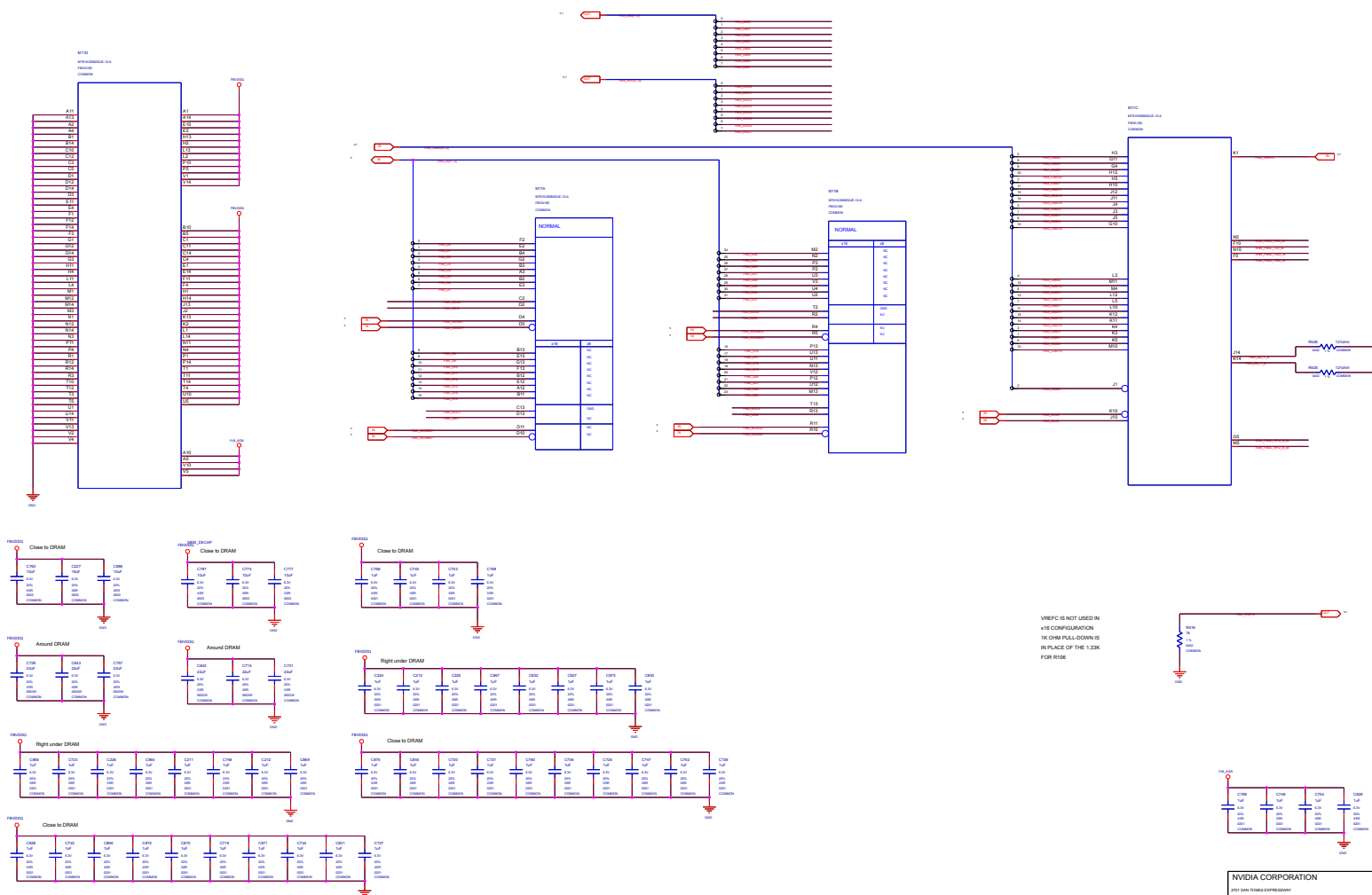
NVIDIA CORPORATION

2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA



	NV_P/N	600-1G161-BASE-000
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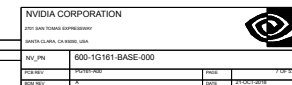
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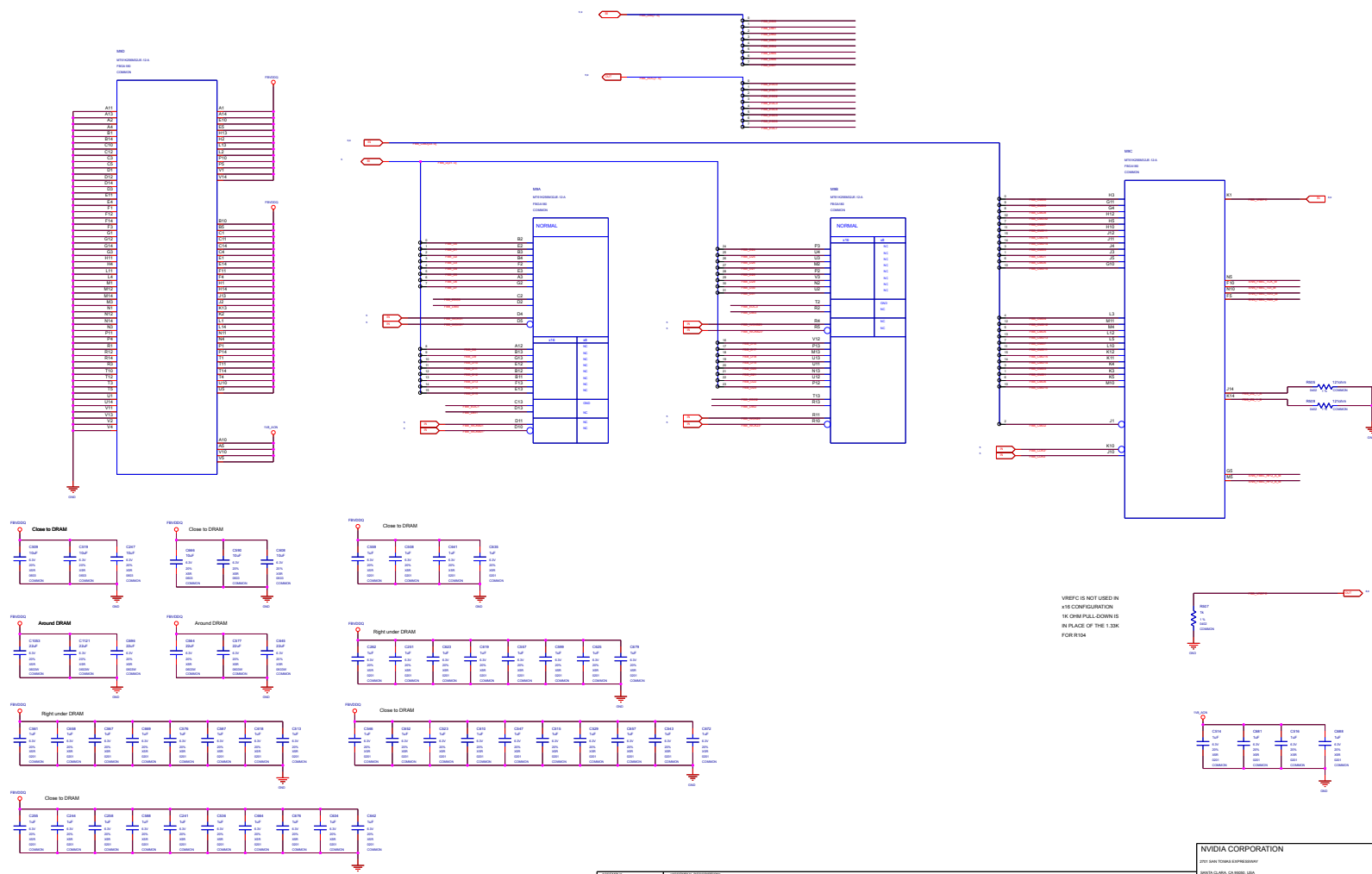


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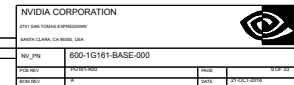
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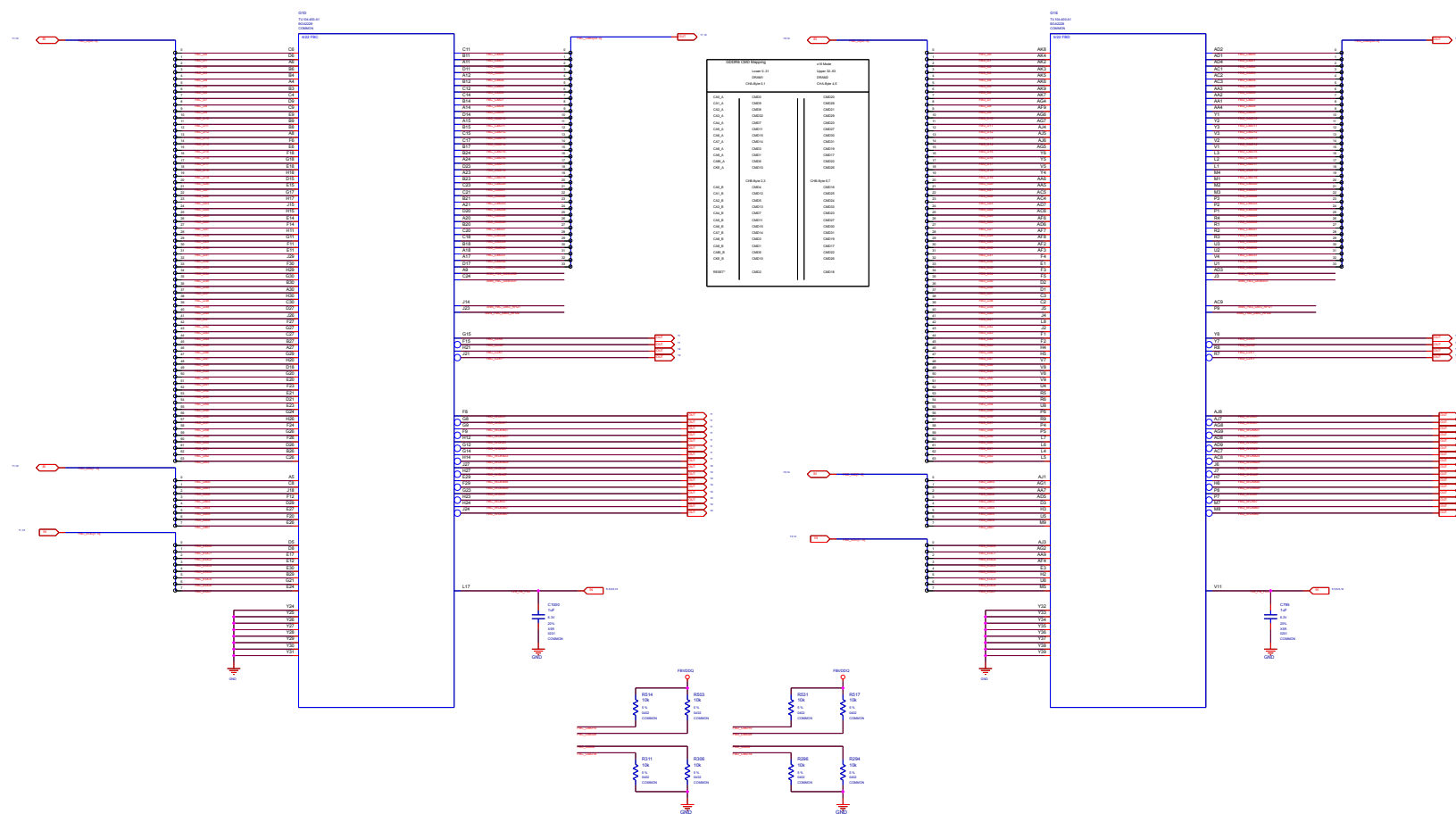




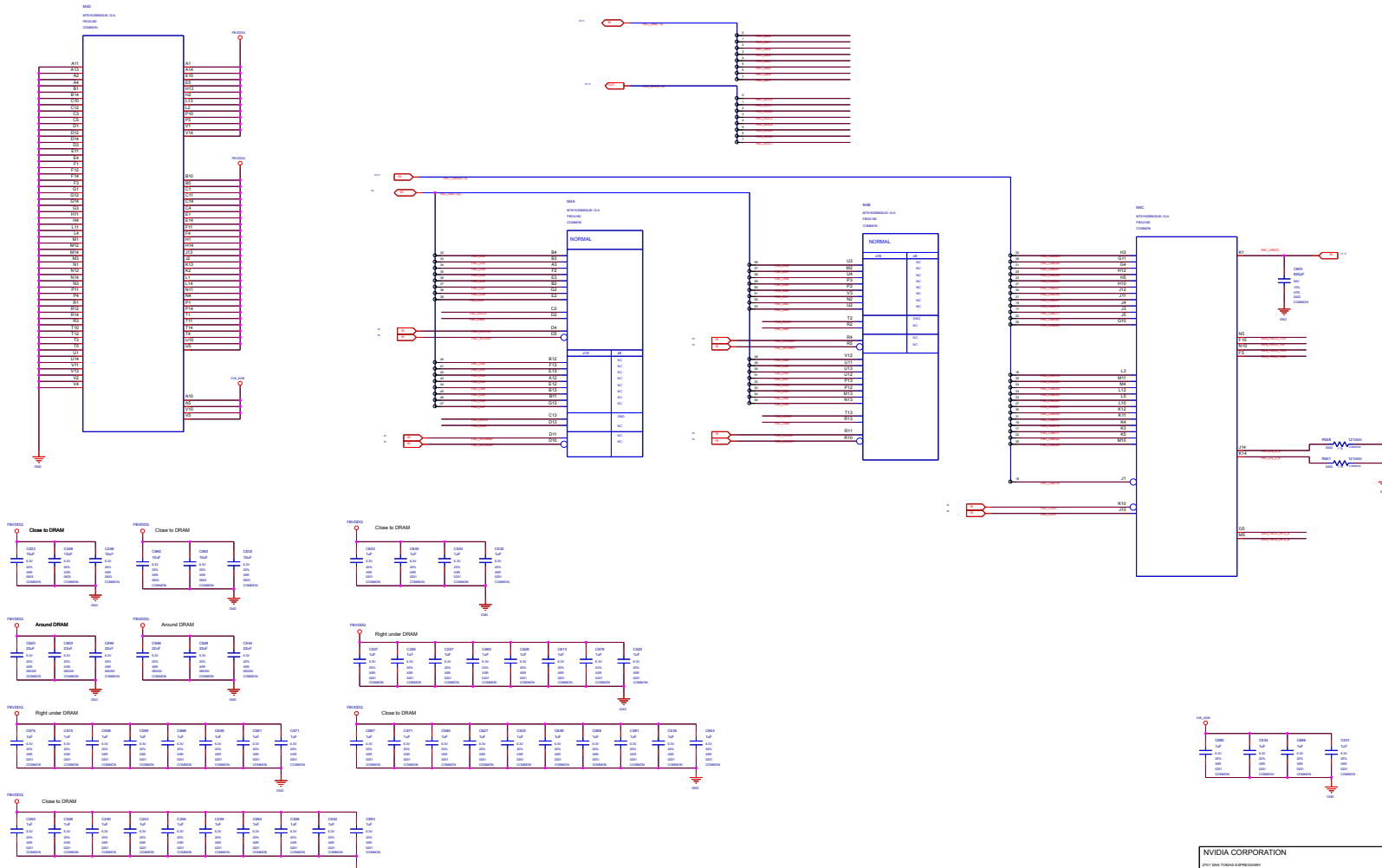


VREFC IS NOT USED IN
x16 CONFIGURATION
1K OHM PULL-DOWN IS
IN PLACE OF THE 1.33K
FOR R104





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Case 1_51		0.09
Case		



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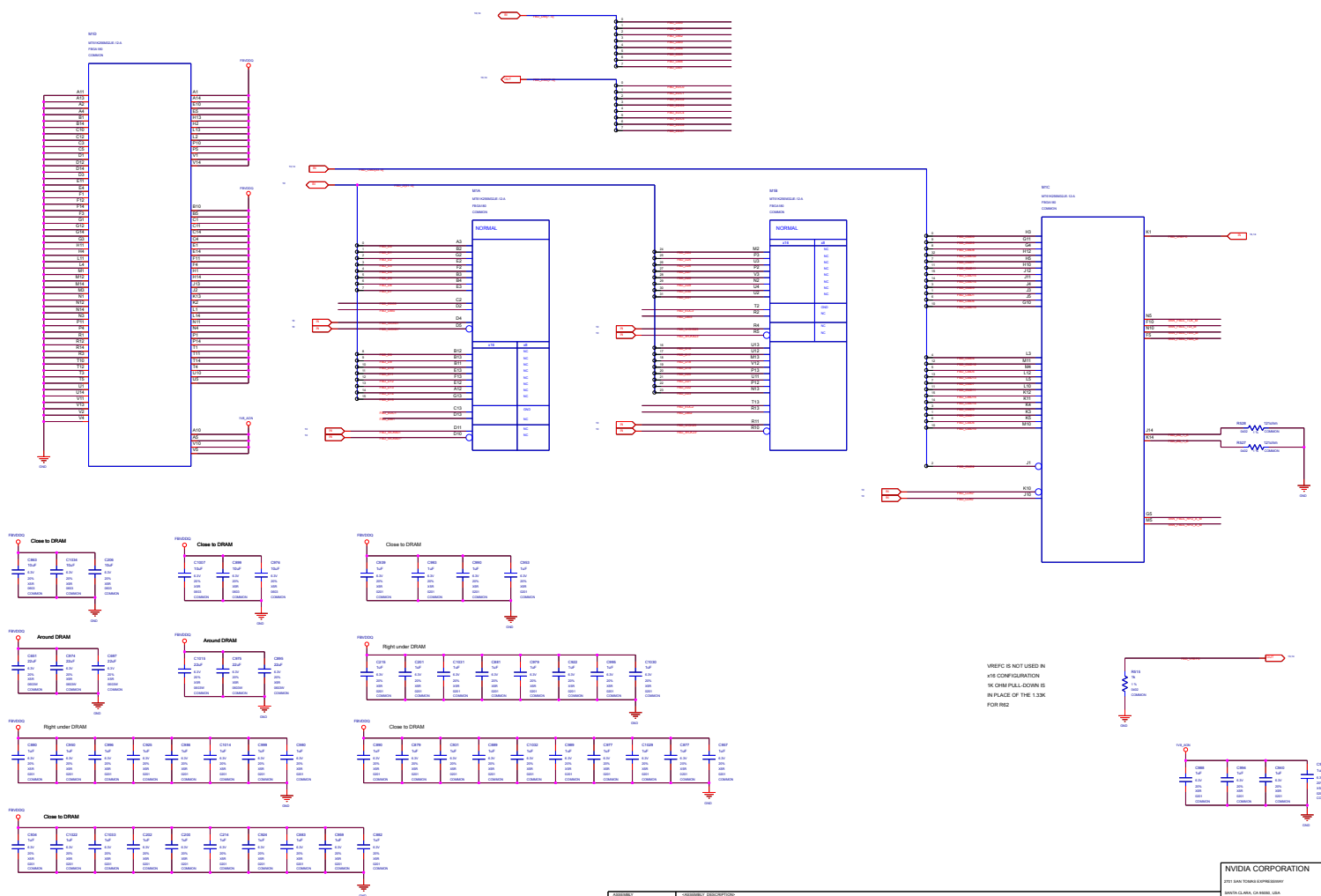
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2701 SAN TOMAS EXPRESSWAY
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NV_P/N	600-1G161-BASE-000
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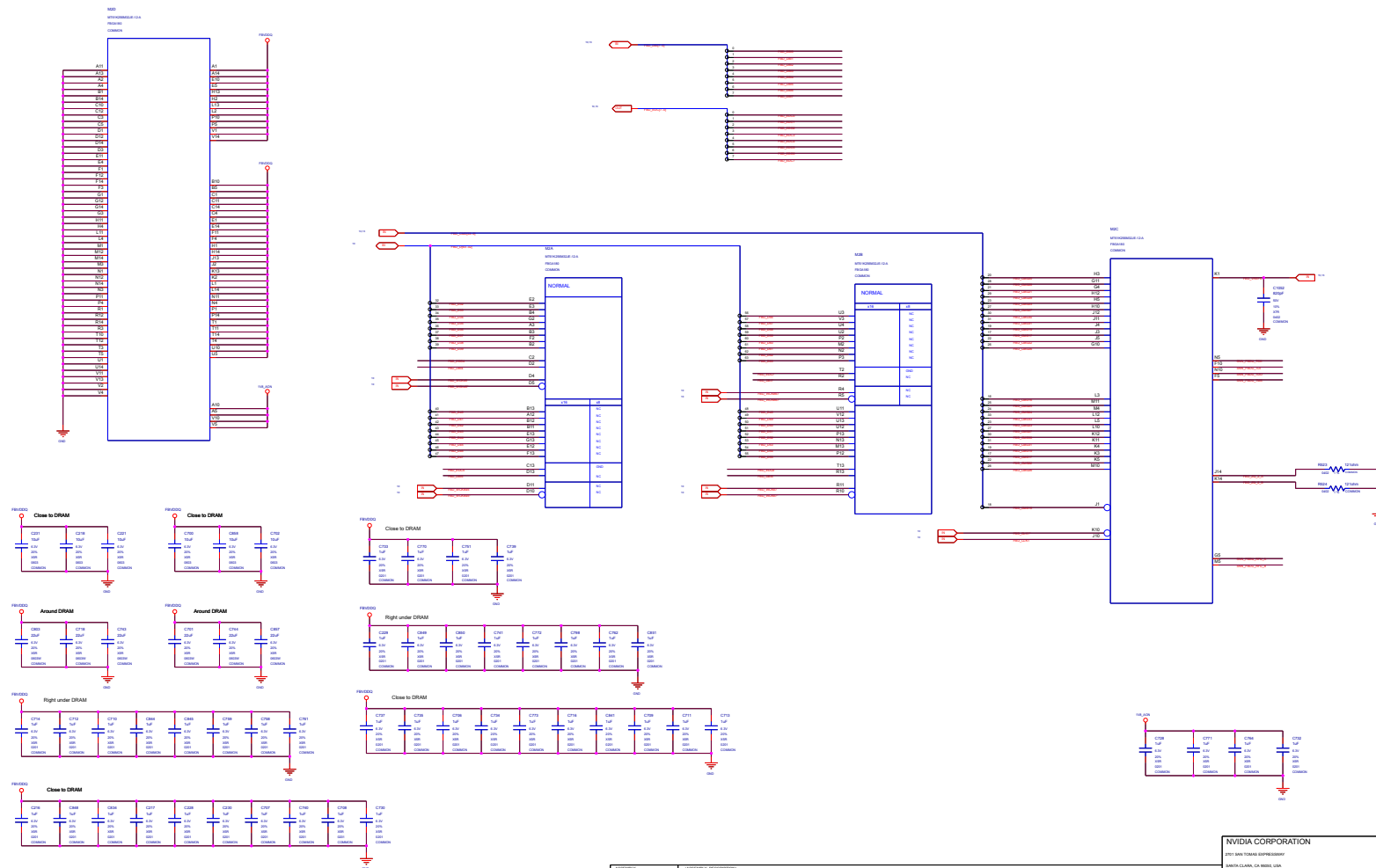


VREFC IS NOT USED IN
x16 CONFIGURATION
1K OHM PULL-DOWN IS
IN PLACE OF THE 1.33K
FOR R52

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SANTA CLARA, CA 95050, USA				
NV_P/N		600-1G161-BASE-000		
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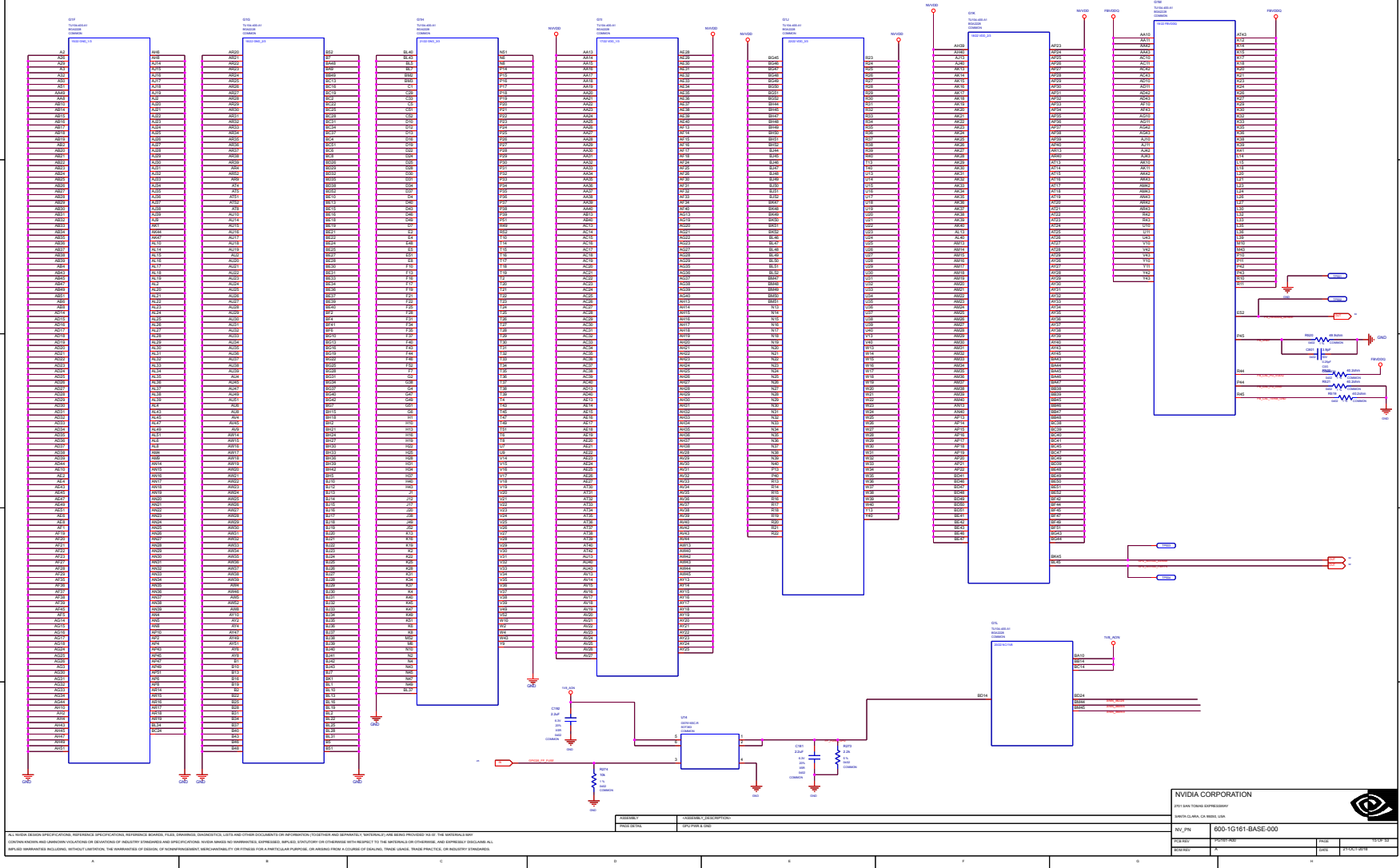
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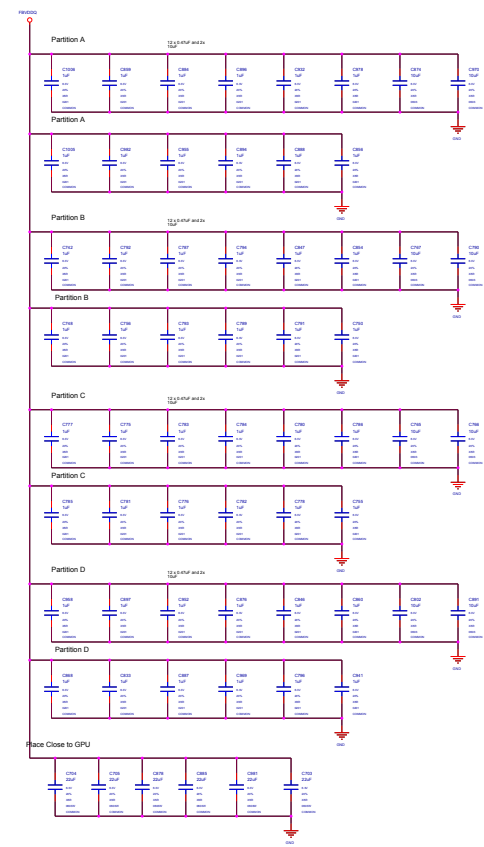
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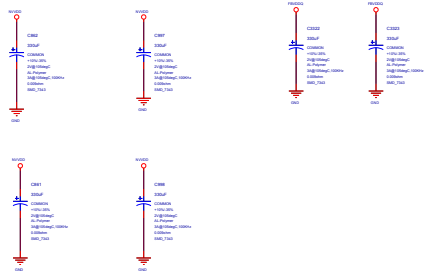
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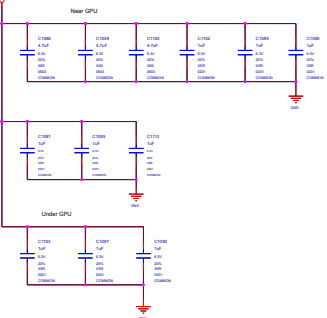
FBVDDQ



NVDD



1V8_AON



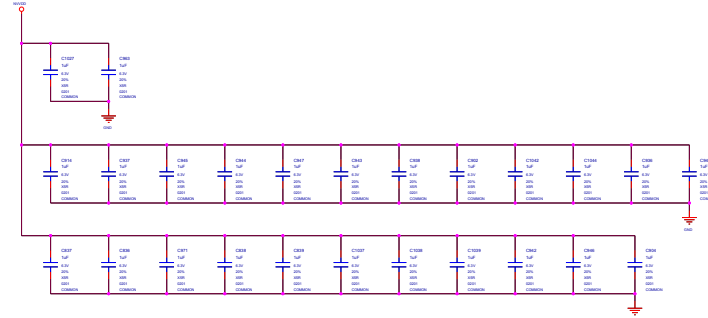
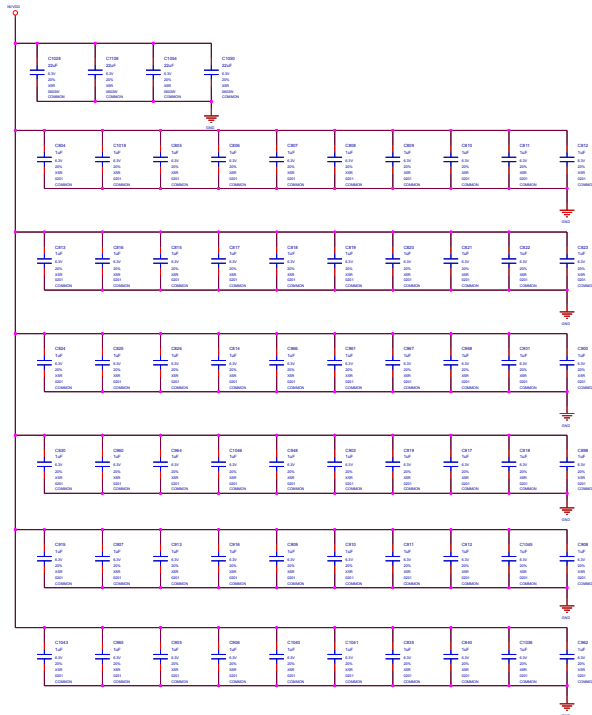
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PRODUCT	PRODUCT ID: 600-1G161-000
PRODUCT ID	600-1G161-000

NVIDIA CORPORATION	
2700 LAKE TOWER DRIVE	
SAN JOSE, CA 95134, USA	
REV. PIN	600-1G161-000
REV. PIN	600-1G161-000
REV. PIN	600-1G161-000



NVDD



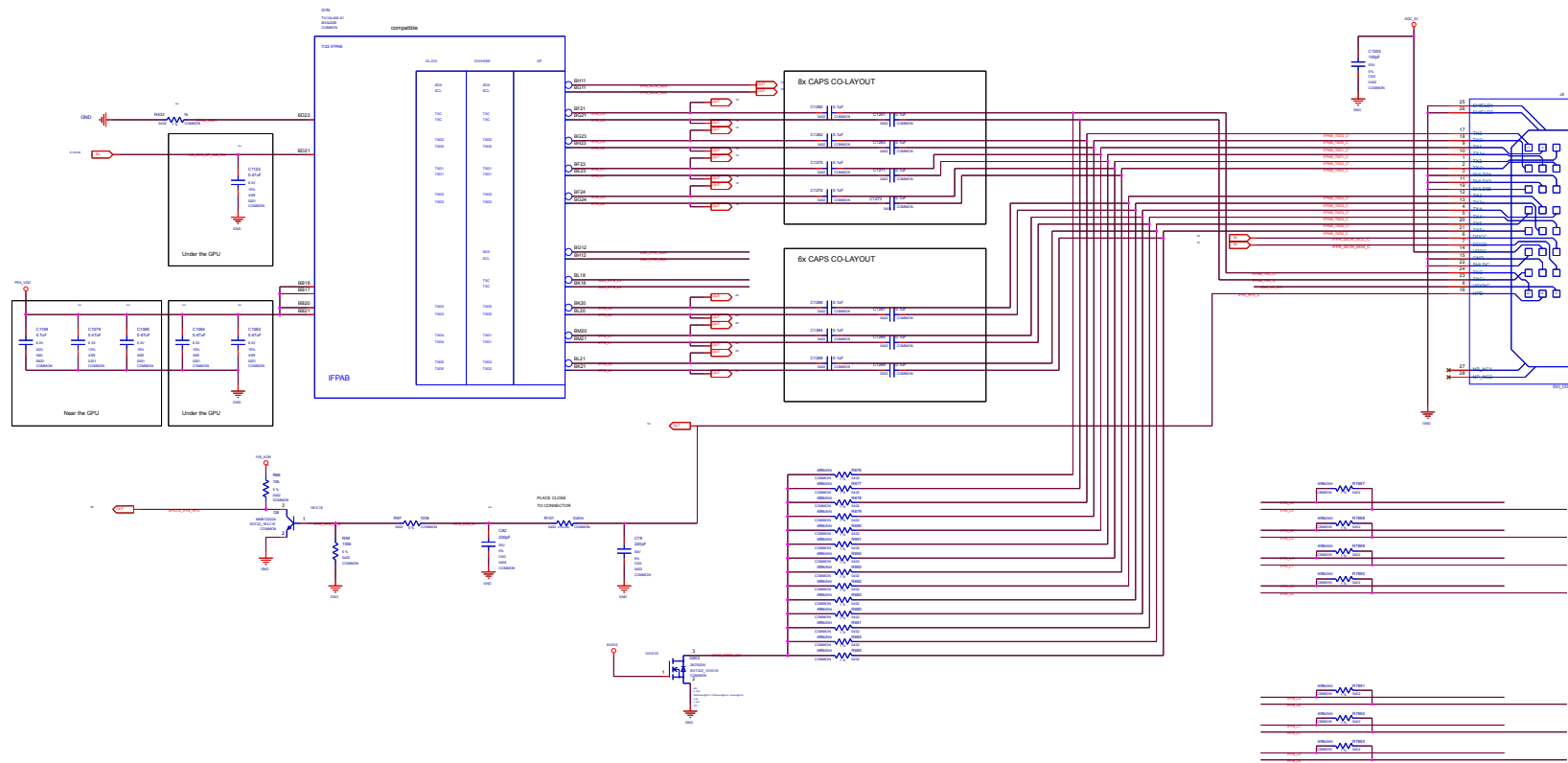
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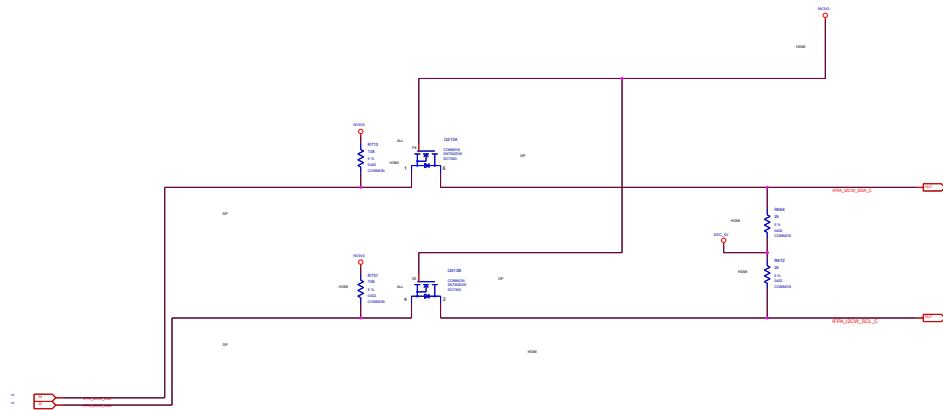
FIGURE 1	FIGURE 2: GPU DECOUPLING
FIGURE 1	FIGURE 2

NVIDIA CORPORATION			
2700 SAN JUAN EXPRESSWAY			
SANTA CLARA, CA 95058, USA			
NVIDIA CORPORATION			
600-1G161-BASE-000			
FIGURE 1	FIGURE 2	DATE	11/01/2018
REVISION	1	DATE	11/01/2018



IFPAB DVI-D-DL CO-LAYOUT WITH 2x DP




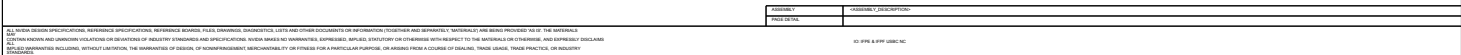


IFPA DP CO-LAYOUT WITH IFPAB DVI-D-DL

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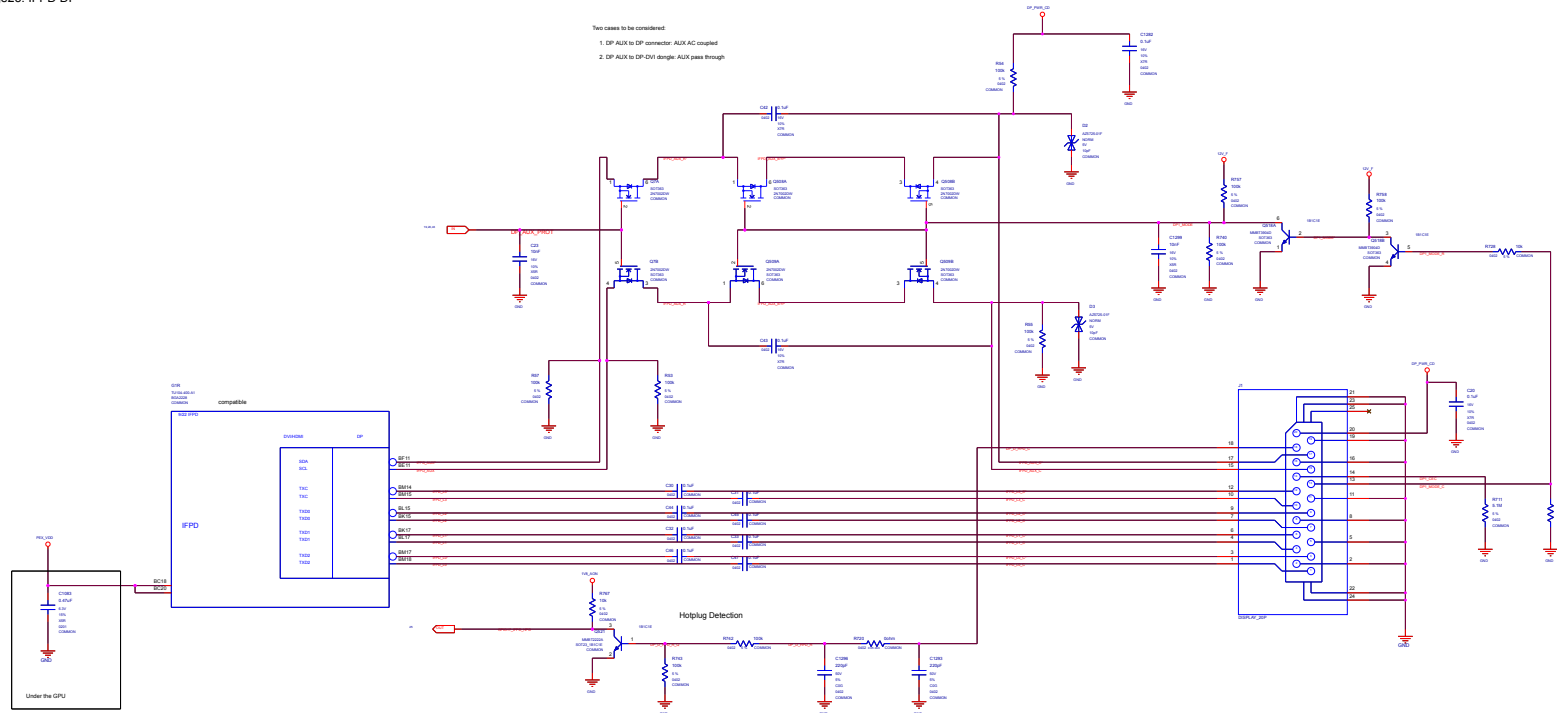
REVISION	DESCRIPTION
1.0	Initial Release

NVIDIA CORPORATION	
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SANTA CLARA, CA 95051, USA	
	
Part Number	800-1G181-BASE-000
Version	1.0
Page	19 OF 21

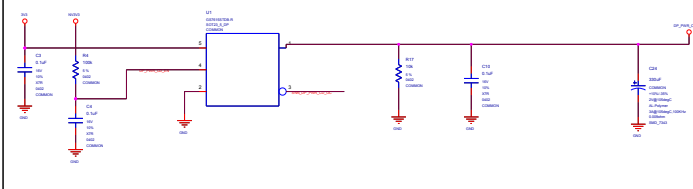


Two cases to be considered

1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI adapter: AUX pass through



Fused DP_PWR



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PART NUMBER: DP-IFPD-DP

NVIDIA CORPORATION

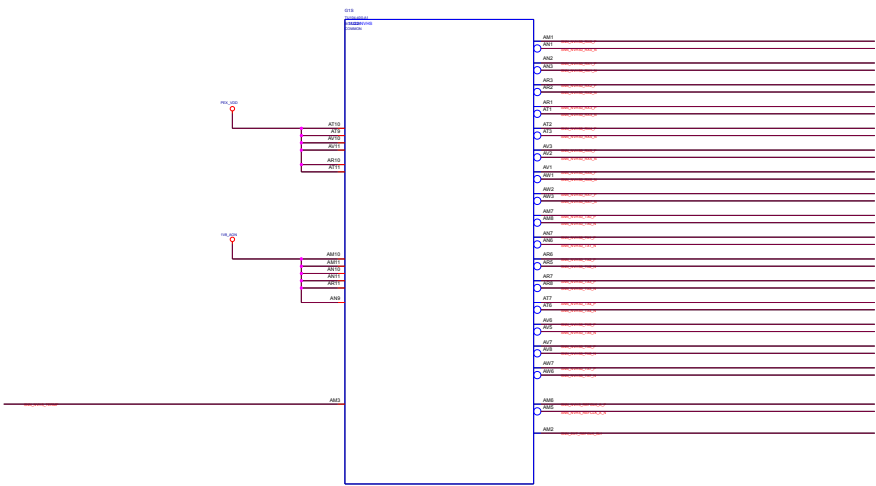
2013-2014 NVIDIA CORPORATION
NVIDIA CORPORATION, OR ITS AFFILIATES

MODEL: 600-1G161-BASE-000

REV: 1.0

DATE: 12/11/2013






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ASSEMBLY	ASSEMBLY DESCRIPTION
POWER SUPPLY	DC POWER SUPPLY AND FAN MODULE

NVIDIA CORPORATION

2701 SAN TOME EXPRESS DRIVE

30070 DUBLIN, CA 94568 USA



NV_PN

600-1G161-BASE-000

PRODUCT NAME

600-1G161-BASE-000

REV

1

DATE

27-02-2018

34 OF 37

STRAP2	STRAP1	STRAP0	RAMCFQ[4:0]
L	L	L	00000
L	L	H	00001
L	H	L	00010
L	H	H	00011
H	H	L	00110
H	H	H	00111
L	L	M	01000

RAMCFQ[4:0]	DENSITY	WIDTH	VENDOR	DIE
00000	8Gb	256-M	Samsung	C
00001	8Gb	256-M	Micro	A
00010	8Gb	256-M	Hynix	M
00011	8Gb	256-M	Samsung	C
00100	8Gb	256-M	Micro	A
00101	8Gb	256-M	Hynix	M
00110	16Gb	256-M	Samsung	M
00111				

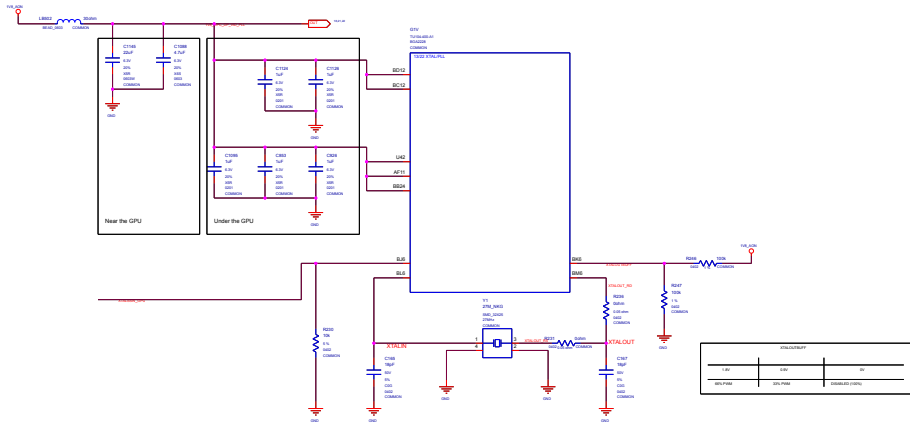
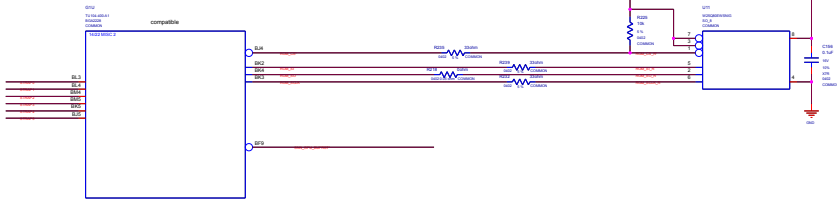
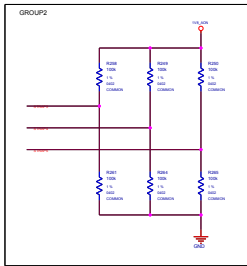
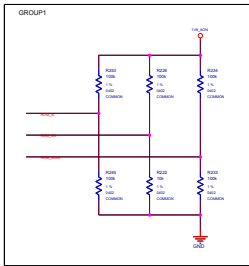
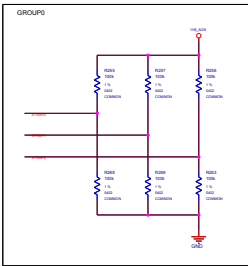
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L	L	L	XXX1	FS_OVERT ENABLE
L	L	M	XXX0	FS_OVERT DISABLE

STRAPS	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
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M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	L	M	1	0	0	1
L	L	L	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1 DEFAULT
L	L	L	0	0	0	0

Hi-High :Tied to 1.0V
Mid-Mid :Tied to 0.5V
L-Low :Tied to 0V

1.SMB_ALT_ADDR ENABLE
0.SMB_ALT_ADDR DISABLE
1.DEVID_SEL REBRAND
0.DEVID_SEL ORIGINAL

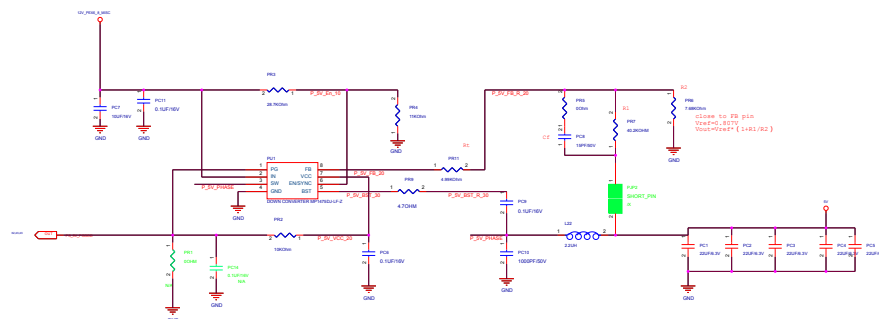
1.PCIE_CFG LOW POWER
0.PCIE_CFG HIGH POWER
1.VGA_DEVICE ENABLE
0.VGA_DEVICE DISABLE



XTAL	XTAL	XTAL
XTAL	XTAL	XTAL
XTAL	XTAL	XTAL


REV	REV	REV	REV
REV	REV	REV	REV
REV	REV	REV	REV

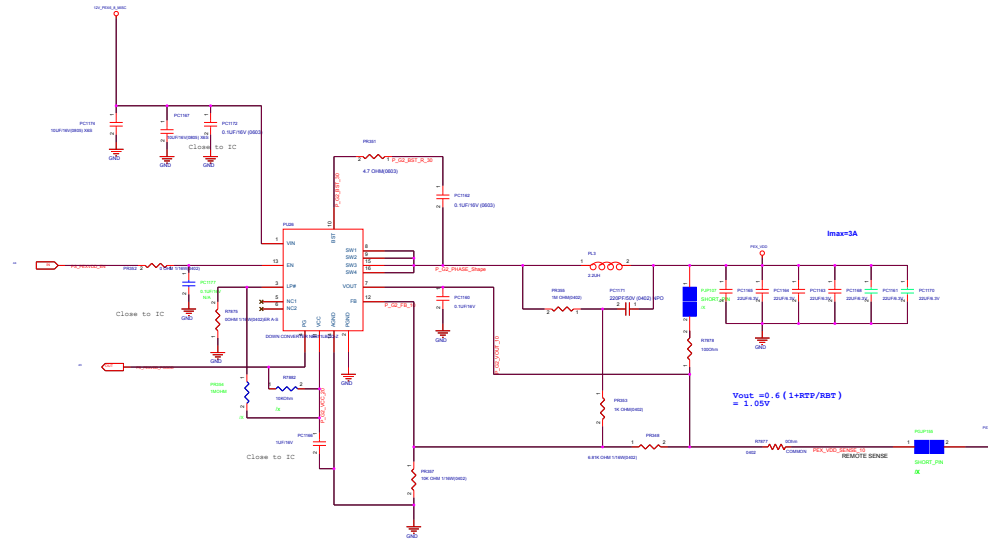
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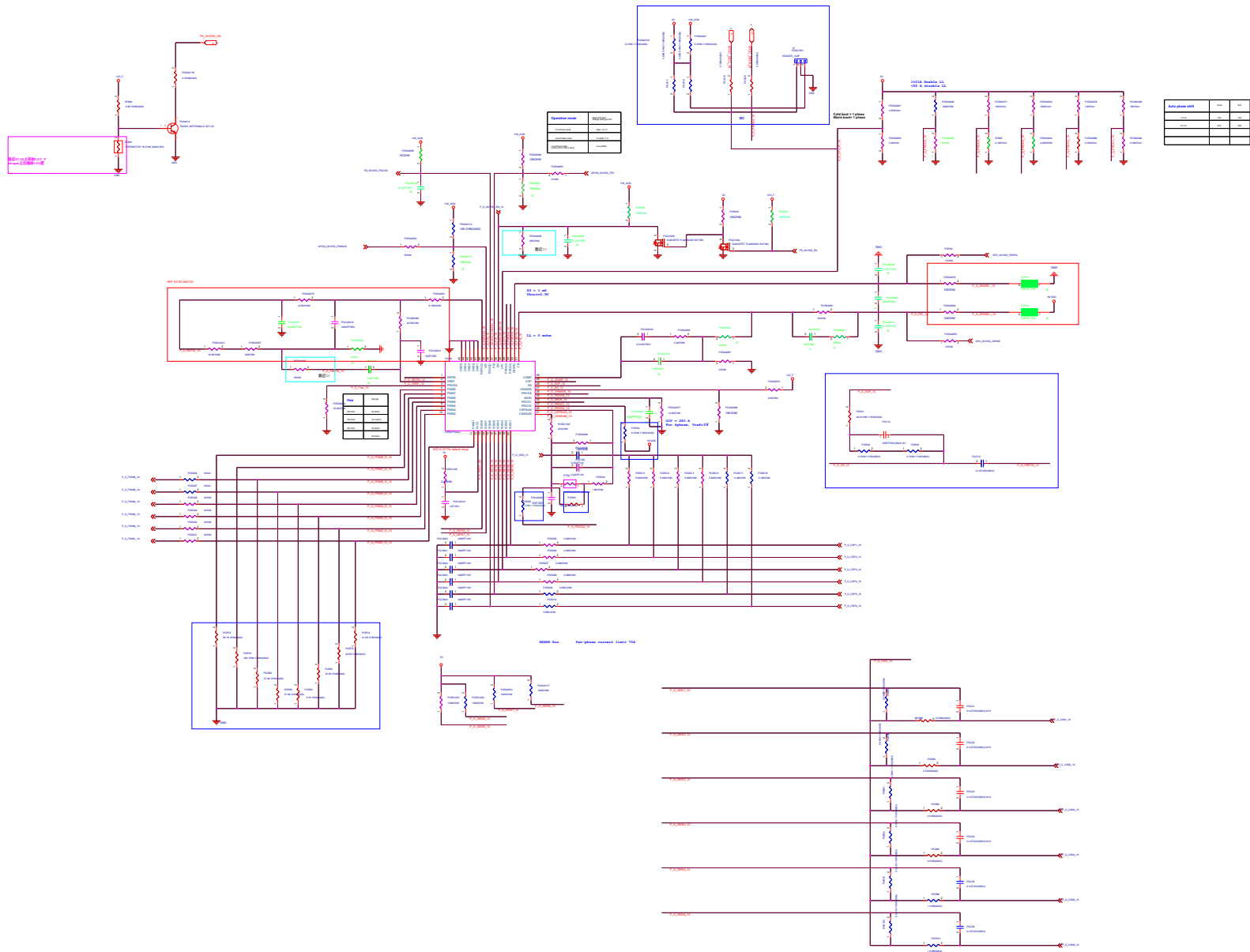


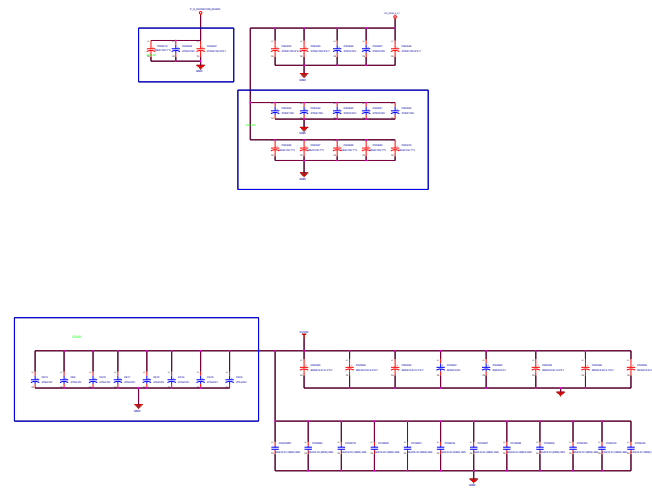
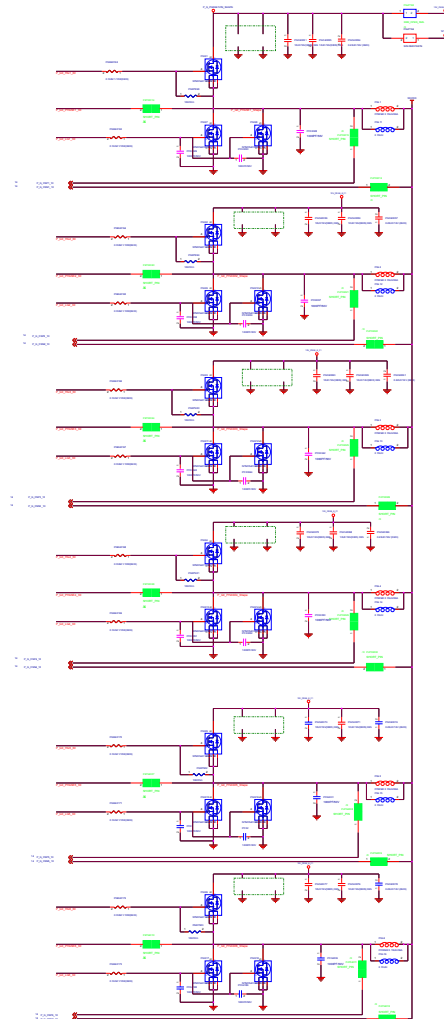
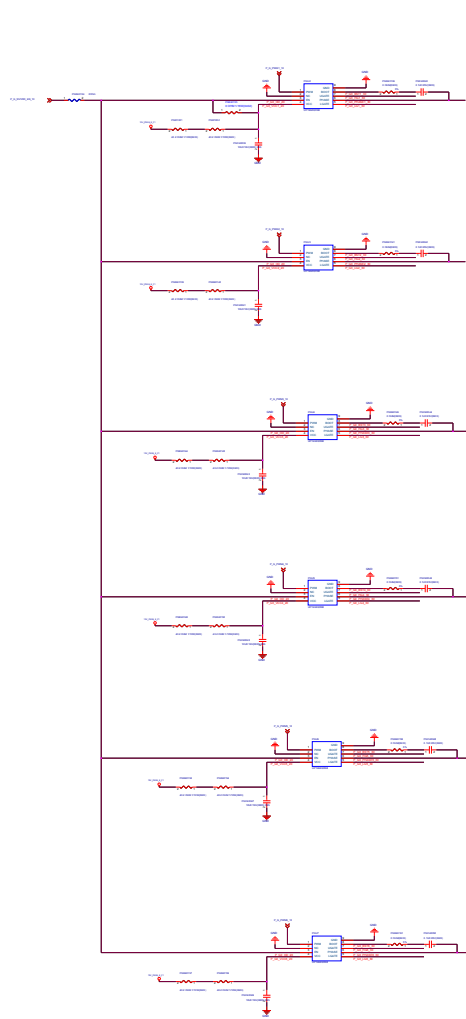
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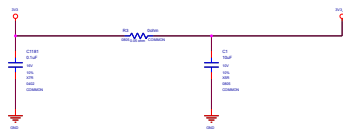
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NV_PN		600-1G161-BASE-000		
PCB REV	PG151-A02	PAGE	28 OF 53	
ECN REV	A	DATE	21-Oct-2018	



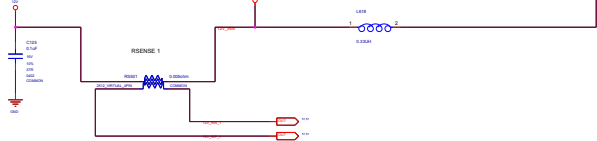




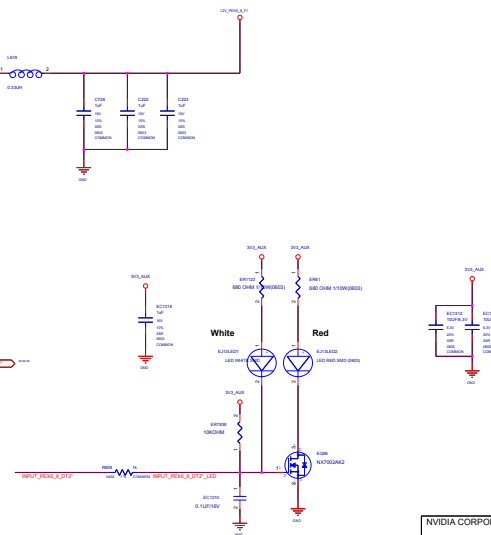
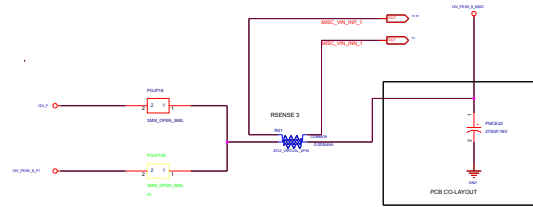
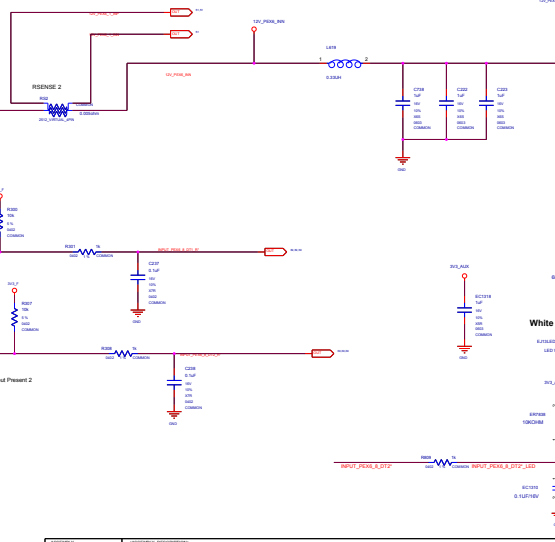
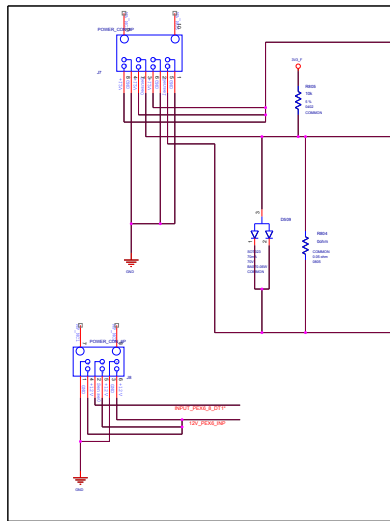
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PEX_12V INPUT - 66W



PEX8 INPUT 1 - 2x4 PCIE CON 150W



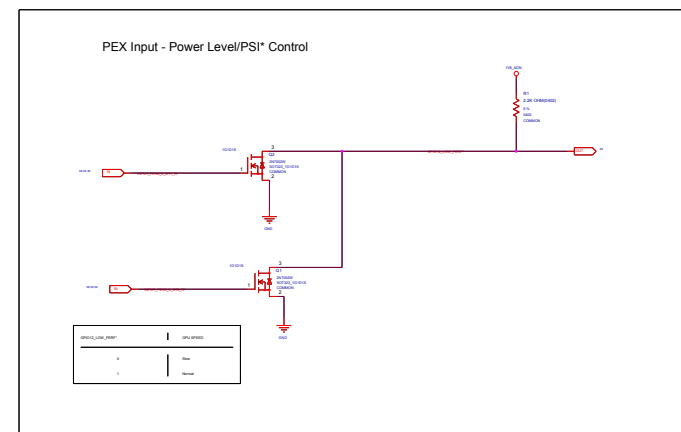
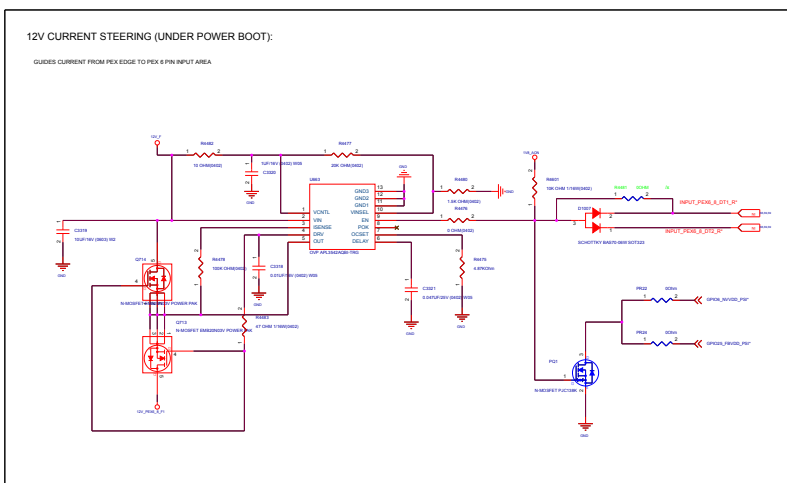
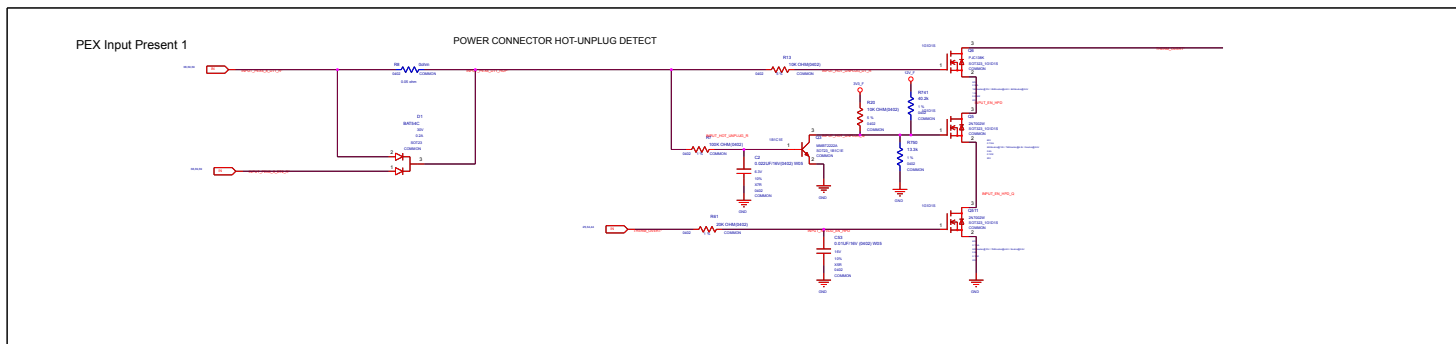
NVIDIA CORPORATION

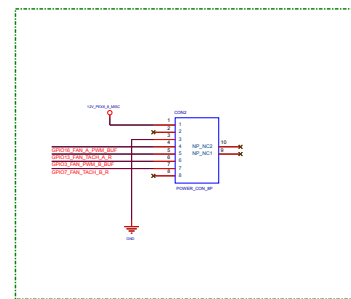
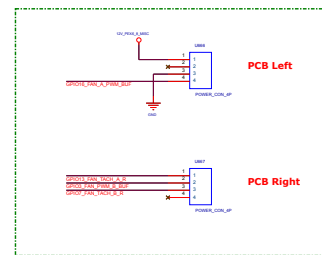
2700 LAKE DRIVE
SANTA CLARA, CA 95050, USA



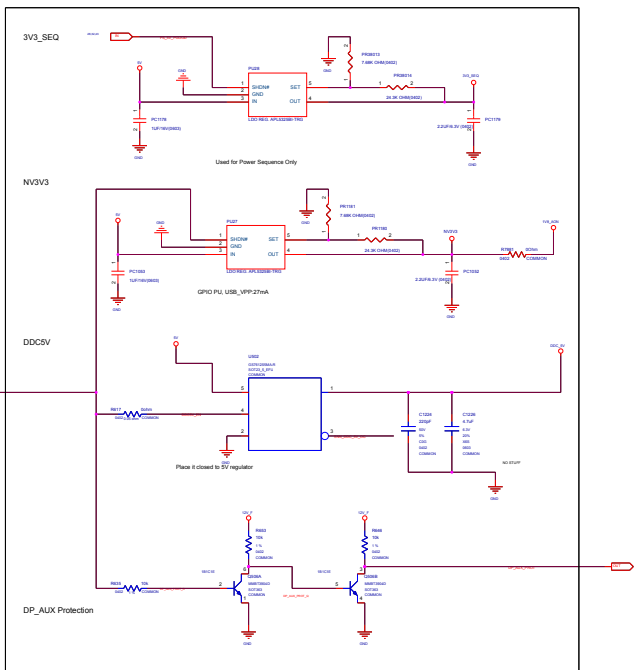
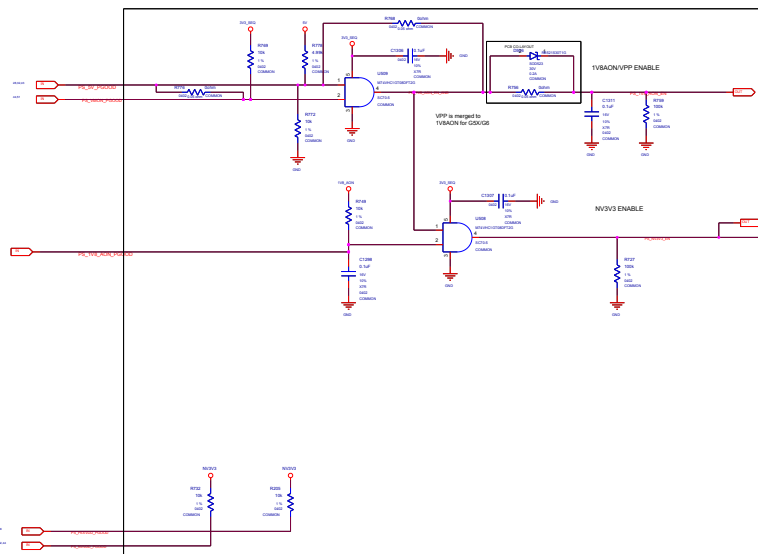
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PCBREV	01/10/10	DATE	01/10/10
REV	1	DATE	01/10/10

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DATE: 10/10/2014

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DATE: 10/10/2014

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DALLAS, TEXAS 75201

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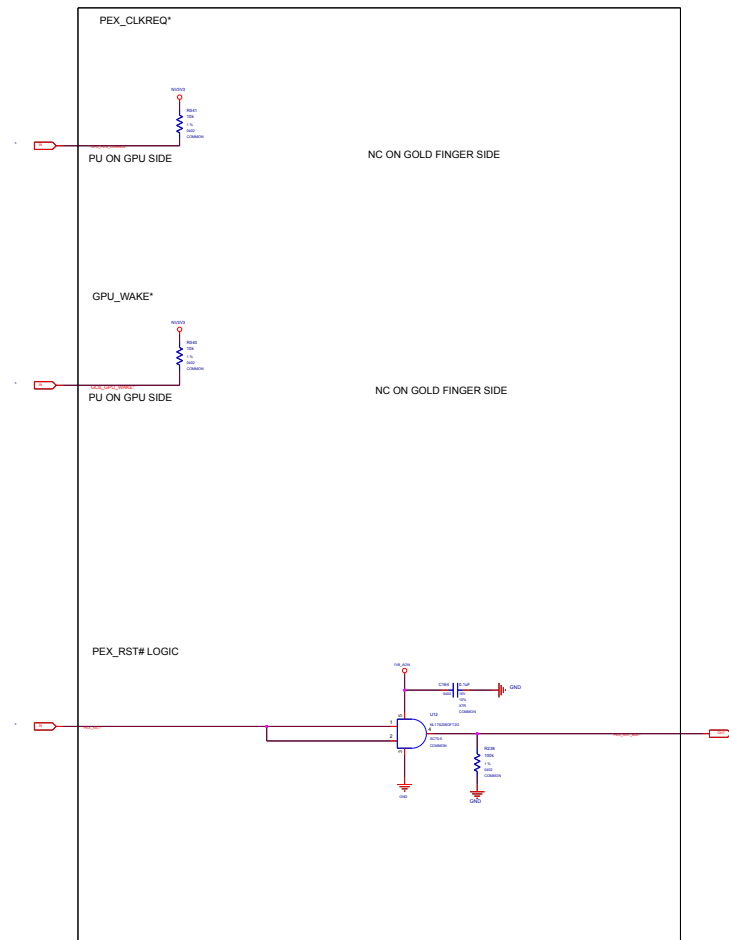
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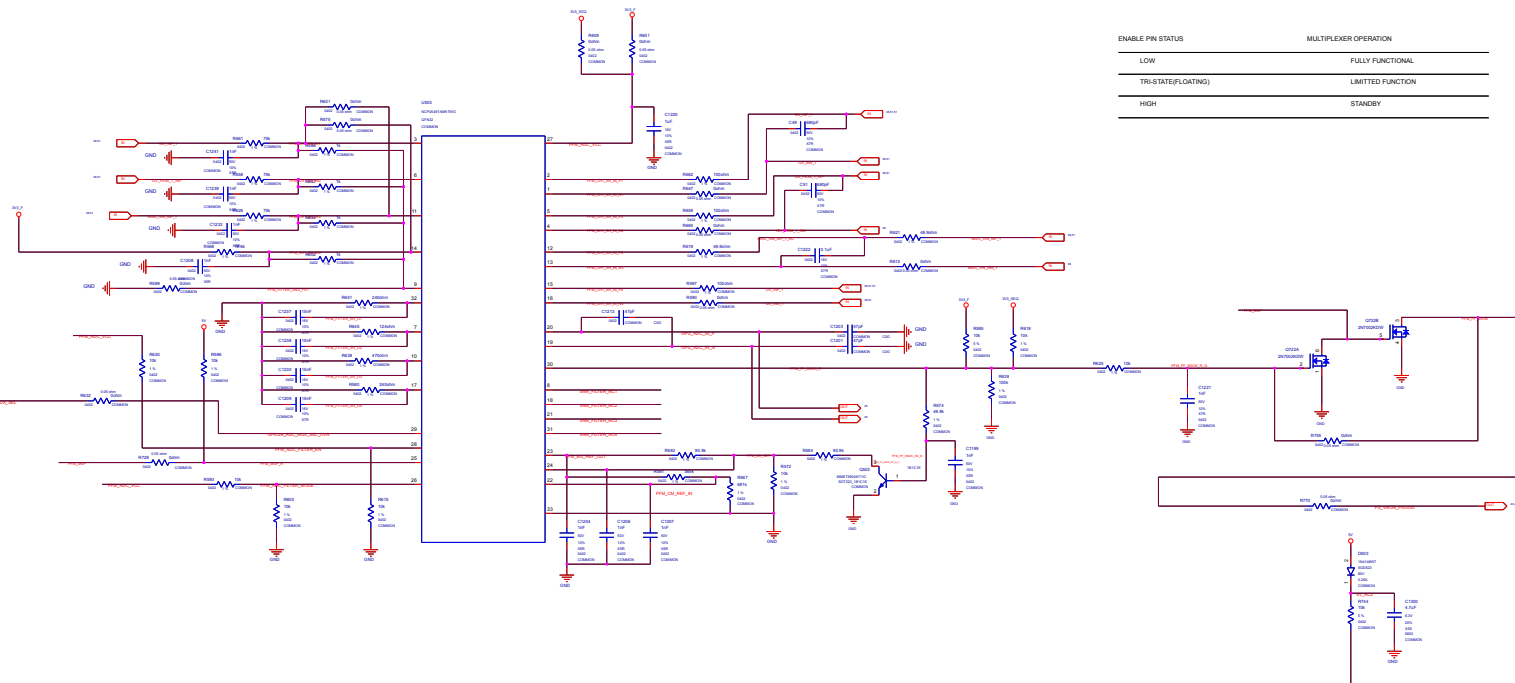


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ASSEMBLY	DESCRIPTION
PEX_RST#	PEX_RST#

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2701 SAN TOME AVE. SUITE 1000	
SANTA CLARA, CA 95050, USA	
NVIDIA CORPORATION	
600-1G161-BASE-000	
REV. 001	27 OCT 07
REV. 001	27 OCT 07






MODE PIN STATUS	MULTIPLEXER OPERATION
LOW	DEVICE A
TRI-STATE(FLOATING)	STAND-ALONE
HIGH	DEVICE B

ENABLE PIN STATUS	MULTIPLEXER OPERATION
LOW	FULLY FUNCTIONAL
TRI-STATE(FLOATING)	LIMITED FUNCTION
HIGH	STANDBY

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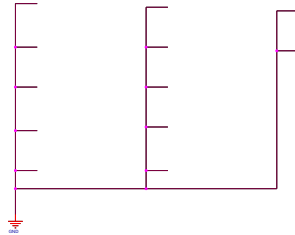
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PCB REV	PGT05-AGE	PAGE	51 OF
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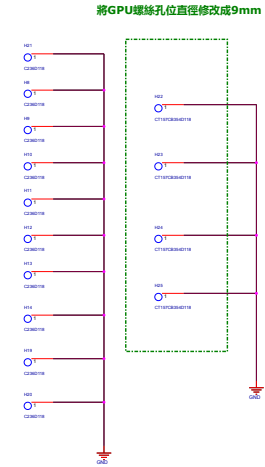
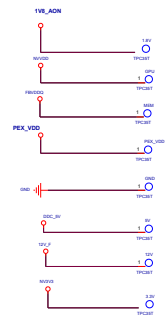
Bracket:




MOUNTING HOLES FOR HS:



BACK STIFFENER:



ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PRICE DETAIL	SEARCH

NVIDIA CORPORATION 2701 SAN TOMEA SUITE 500007 SANTA CLARA, CA 95050, USA			
NV_PN 600-1G161-BASE-000			
PCR REV 1	PCR PART	PROJ	12 OF 32
BCR REV A		DATE	21-OCT-2018

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