

PG161-A01

TU106 6GB GDDR6, 192b, X16  
DVI-D/DP + DP + HDMI

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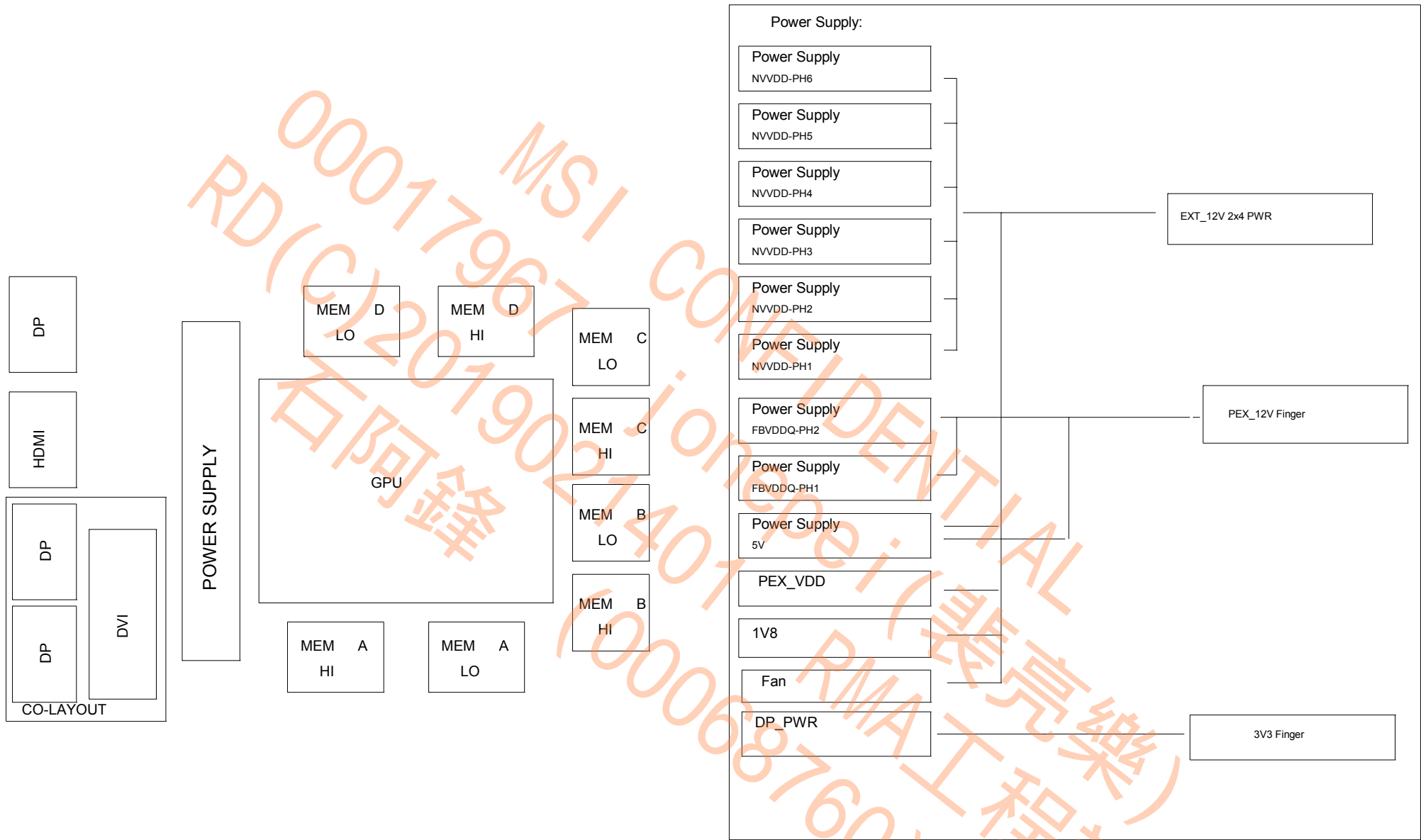
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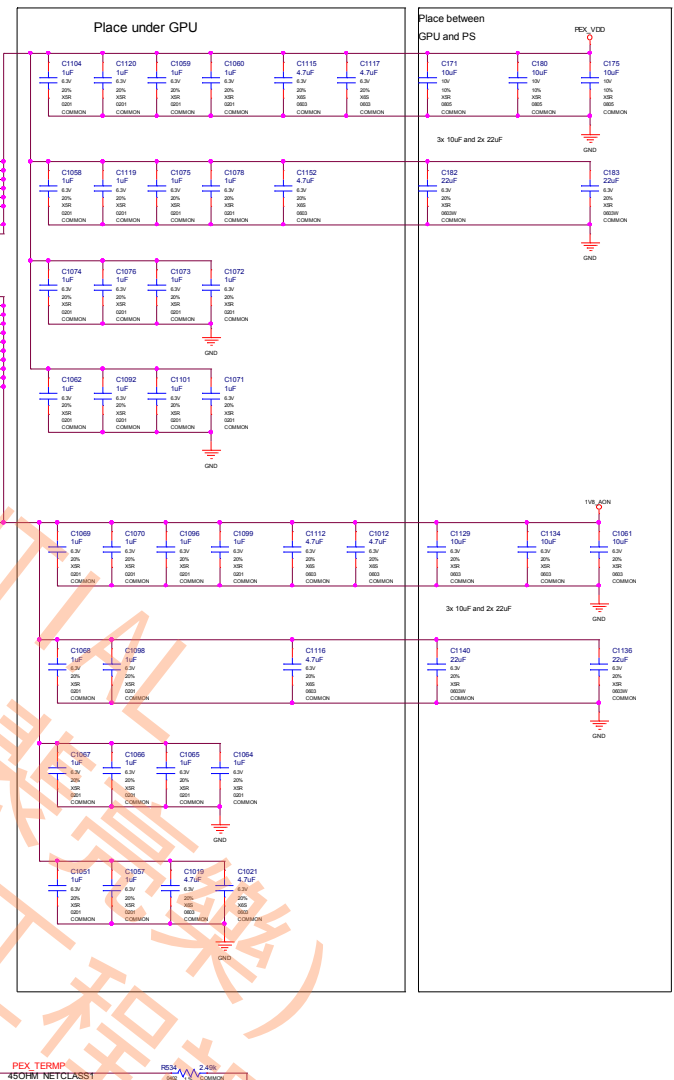
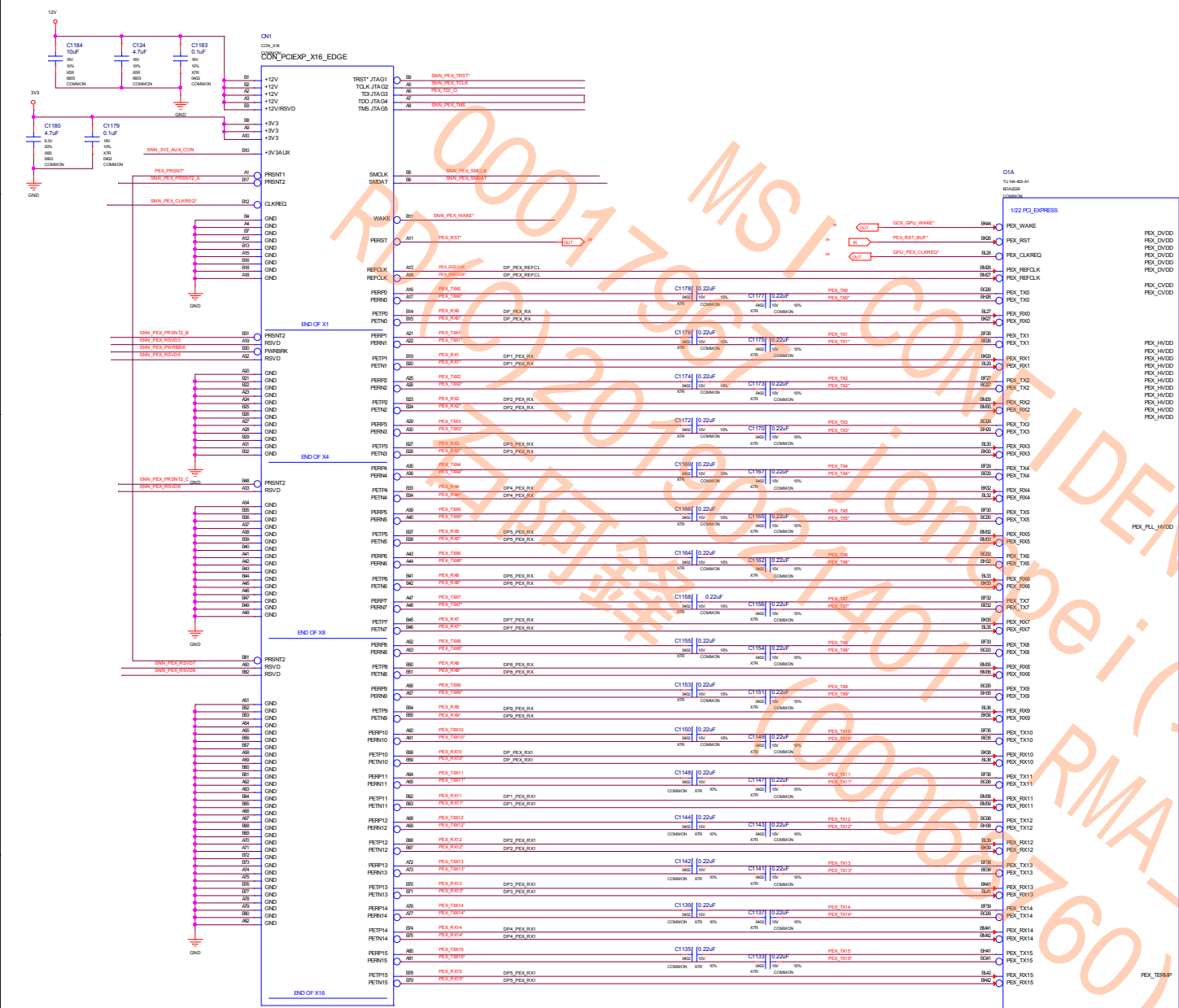
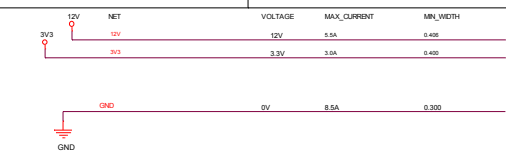
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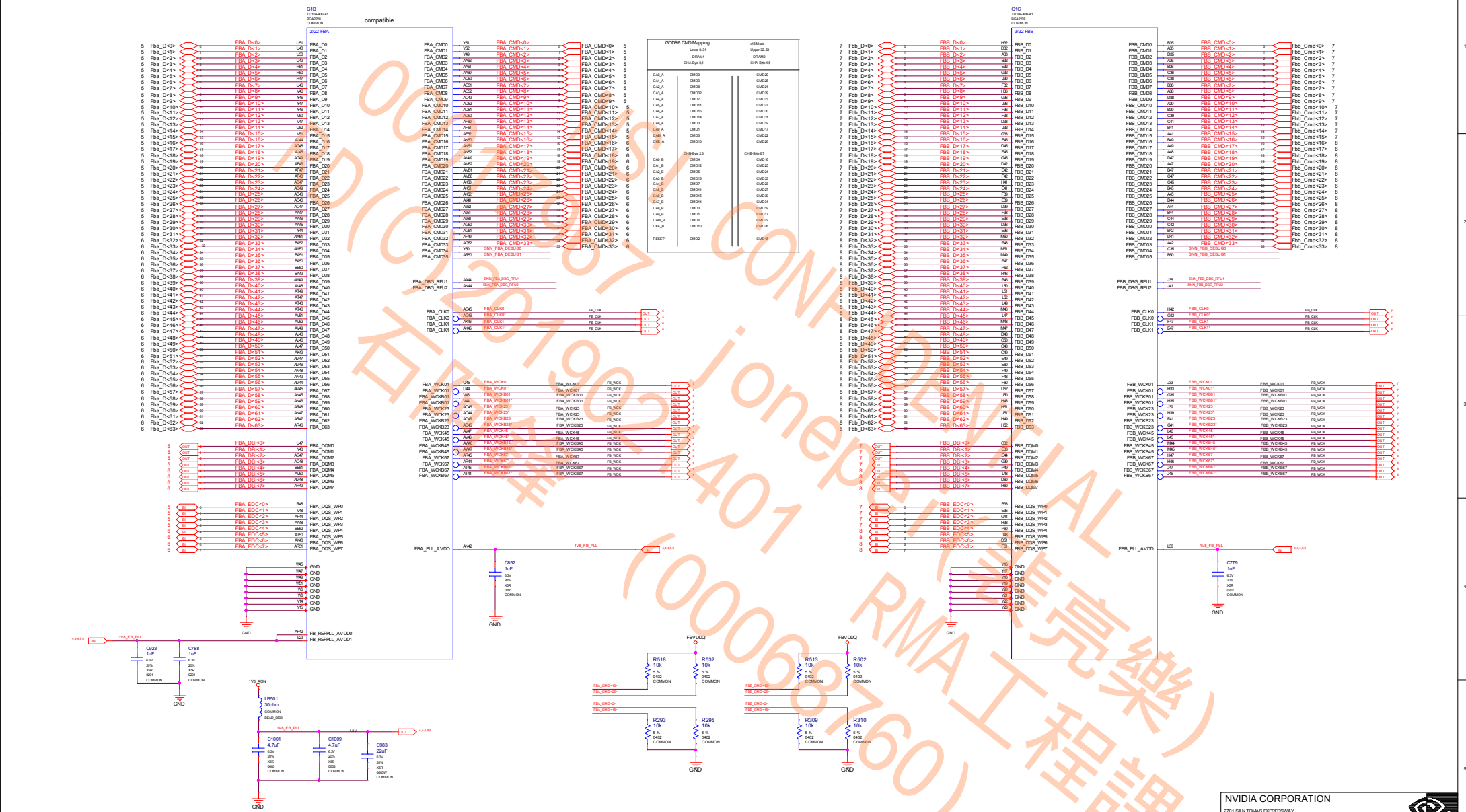
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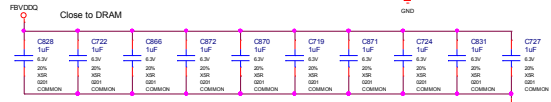
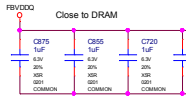
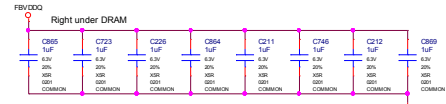
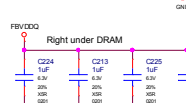
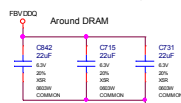
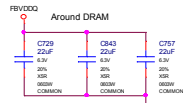
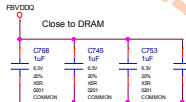
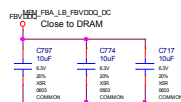
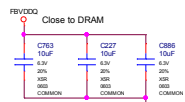
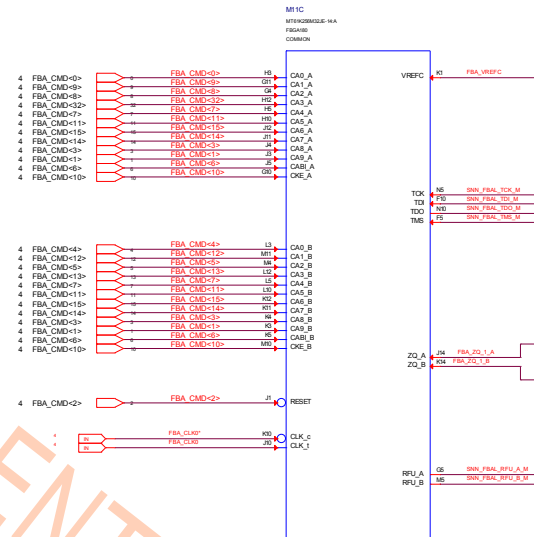
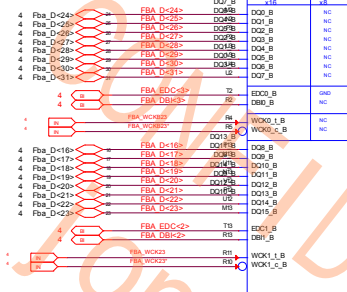
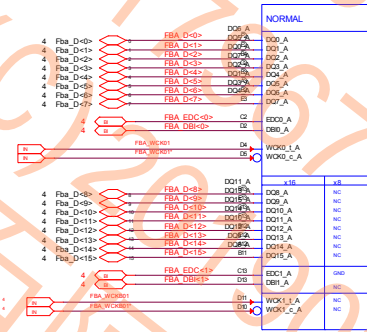
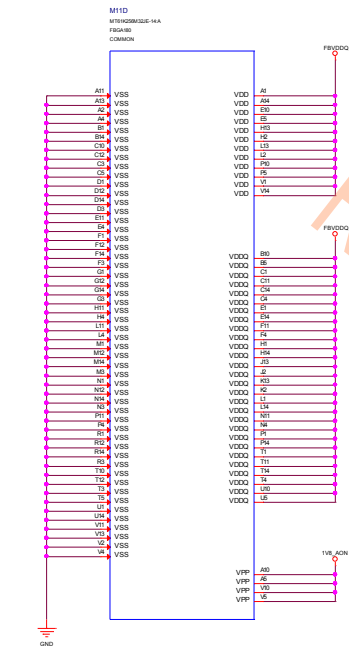


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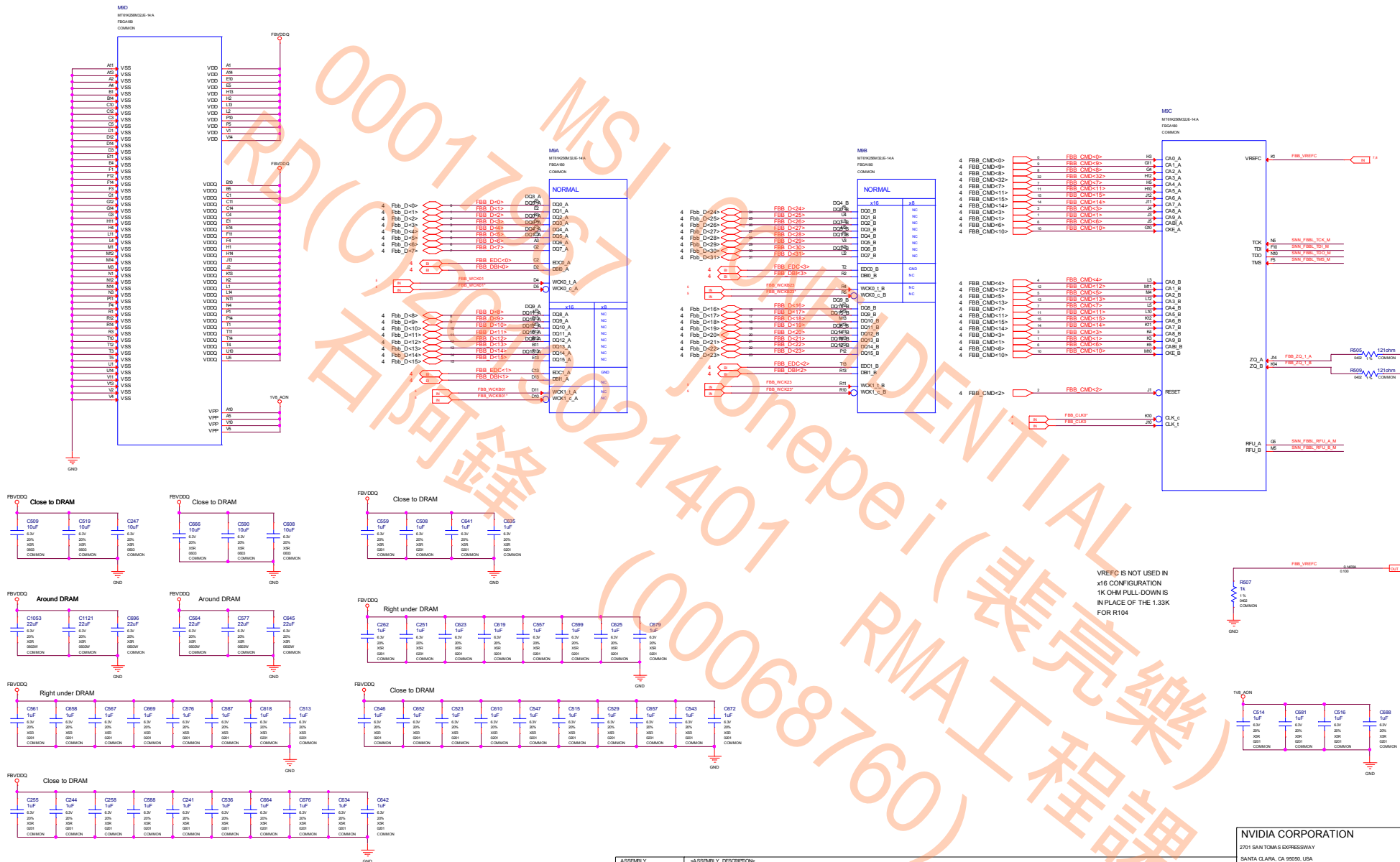
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VREFC IS NOT USED IN  
x16 CONFIGURATION  
1K OHM PULL-DOWN IS  
IN PLACE OF THE 1.33K  
FOR R104

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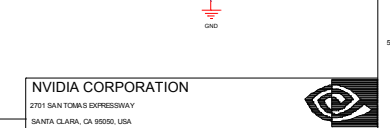
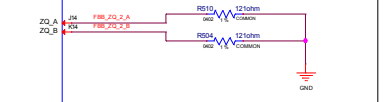
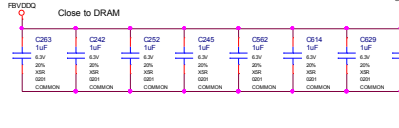
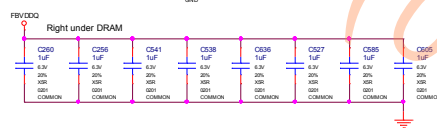
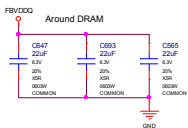
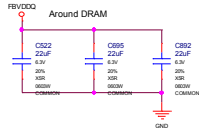
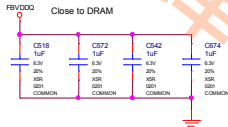
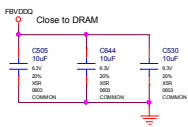
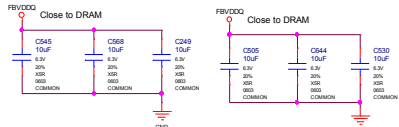
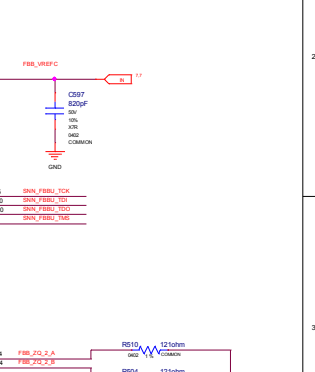
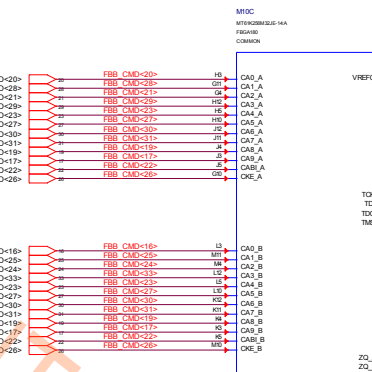
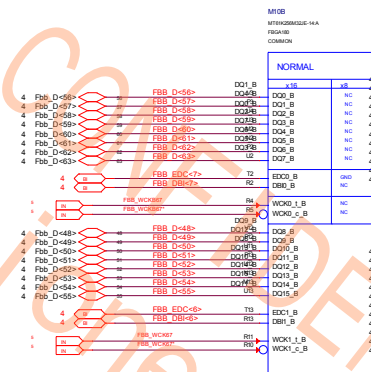
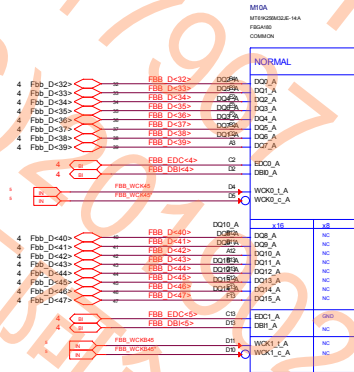
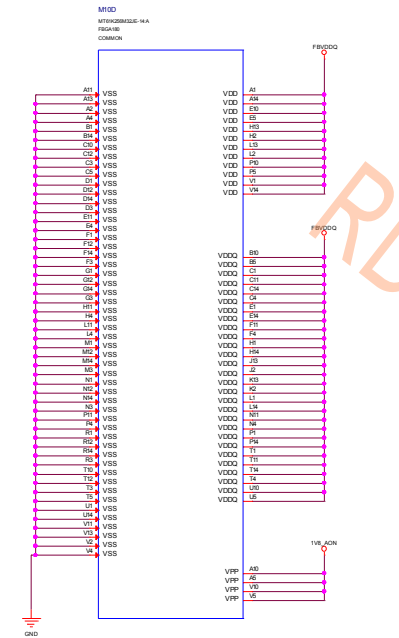
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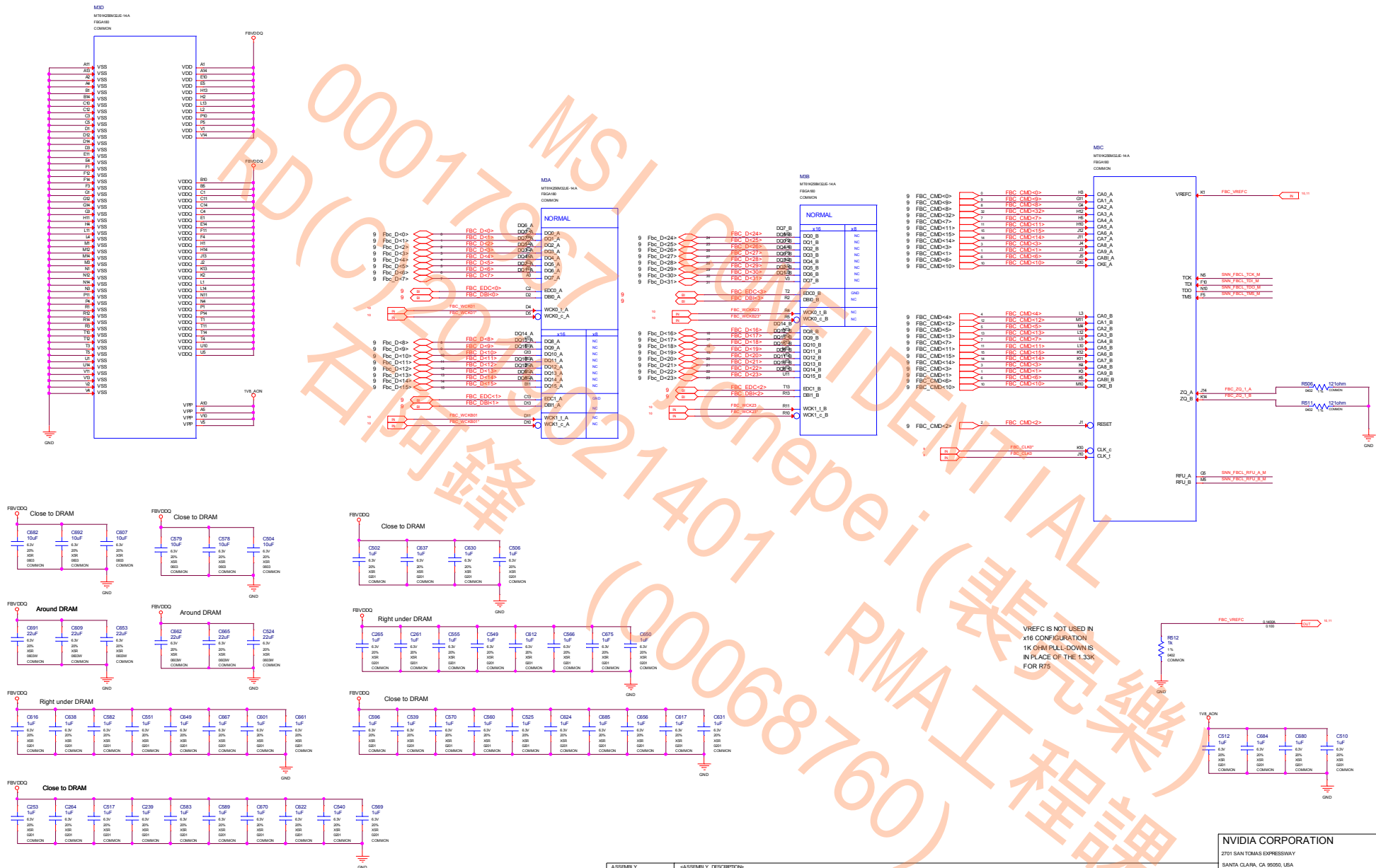
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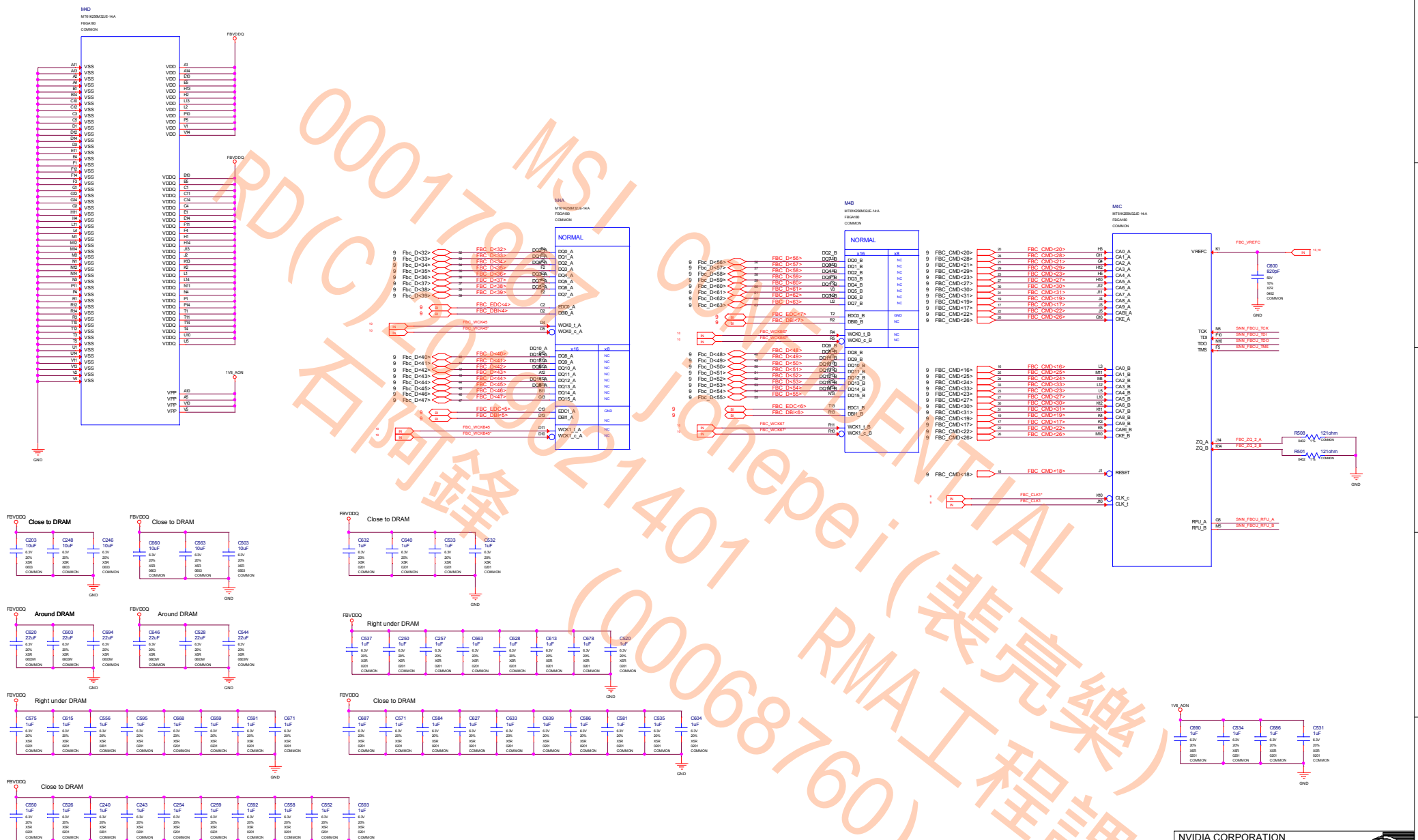
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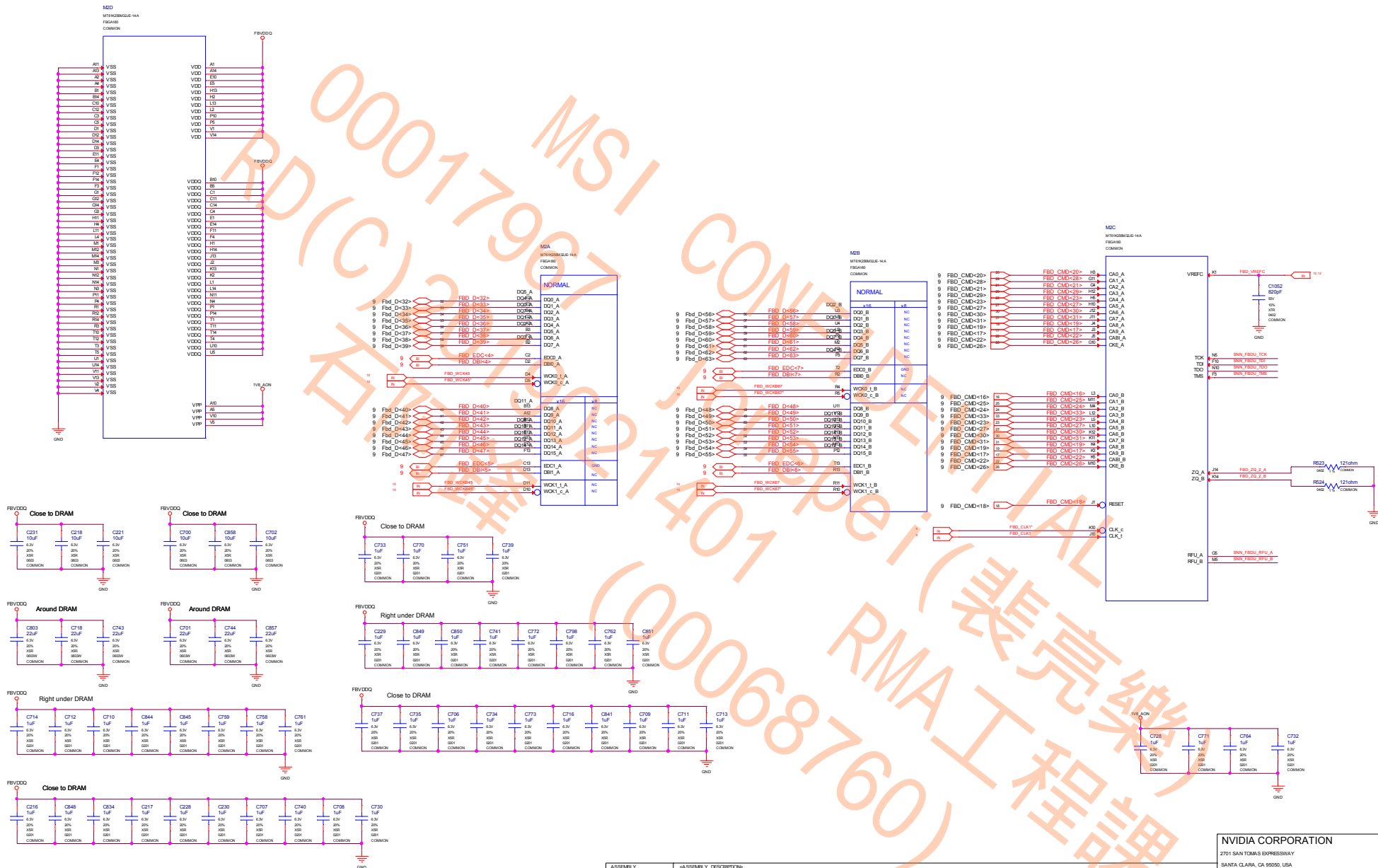
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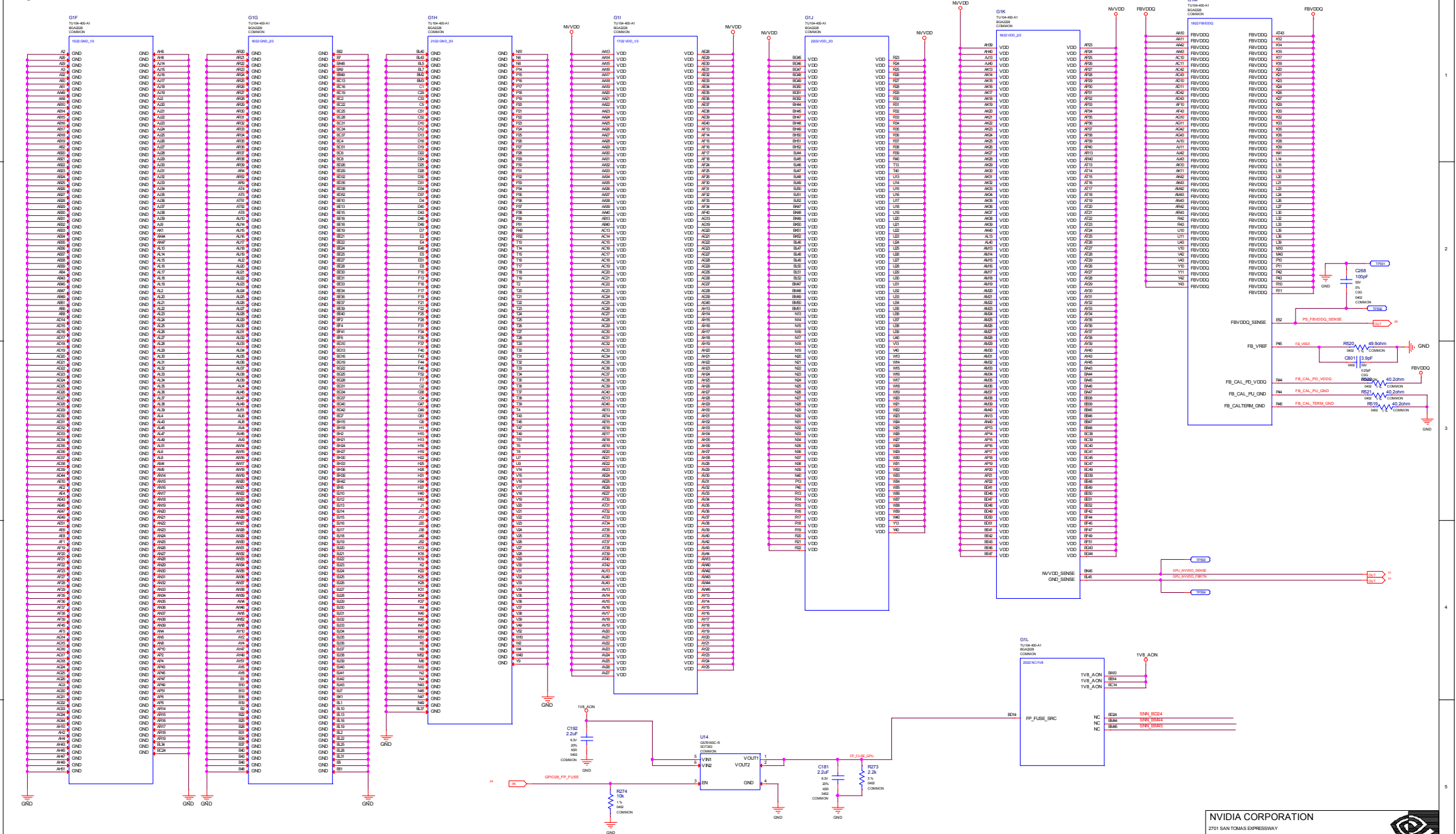


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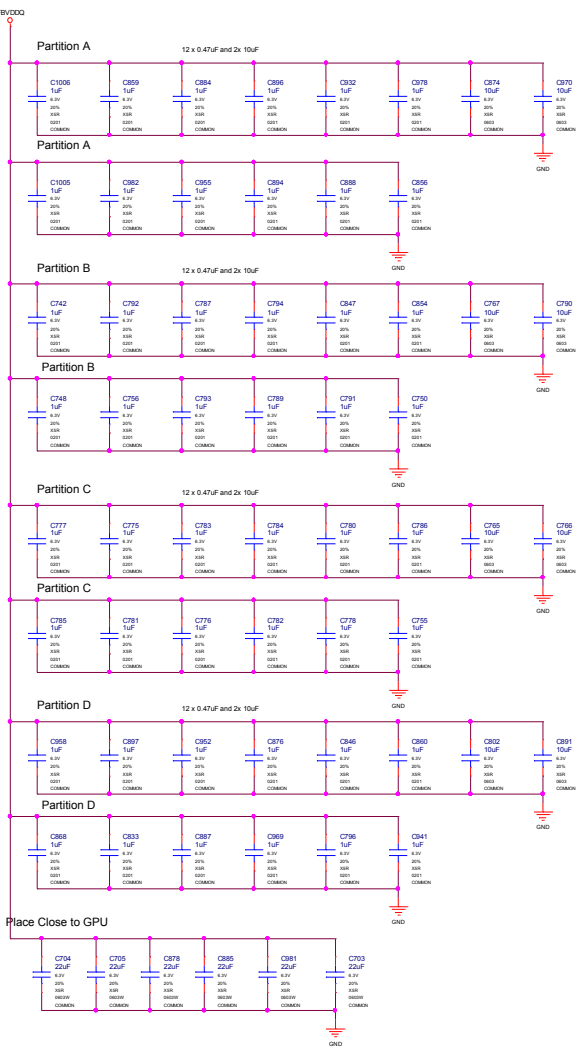




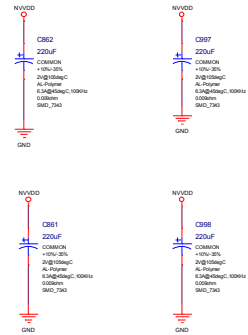
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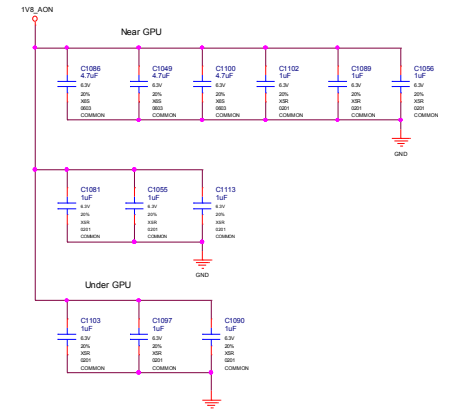
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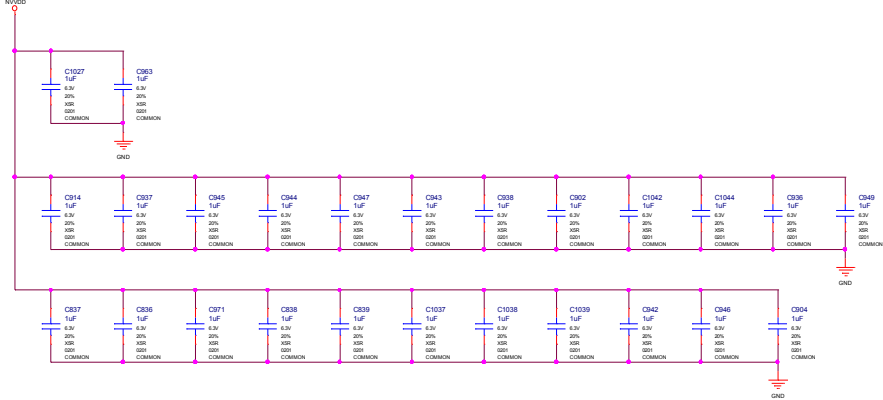
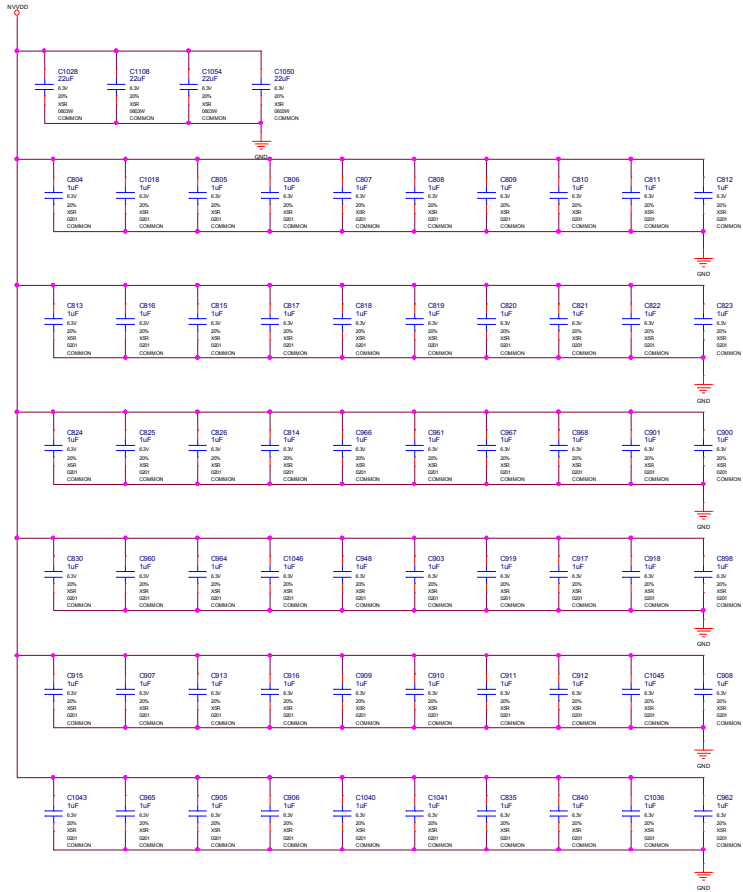


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# IFPB DP CO-LAYOUT WITH IFPAB DVI-D-DL

Two cases to be considered:  
1. DP ALX to DP connector: ALX AC coupled  
2. DP ALX to DP-DVI dongle: ALX pass through

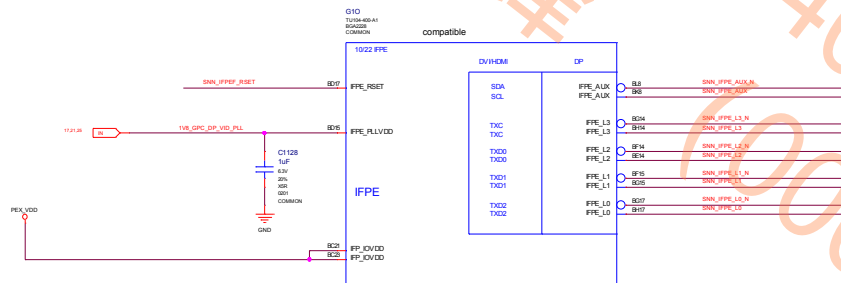
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ROM REV	1000A	DATE	19-NOV-2018



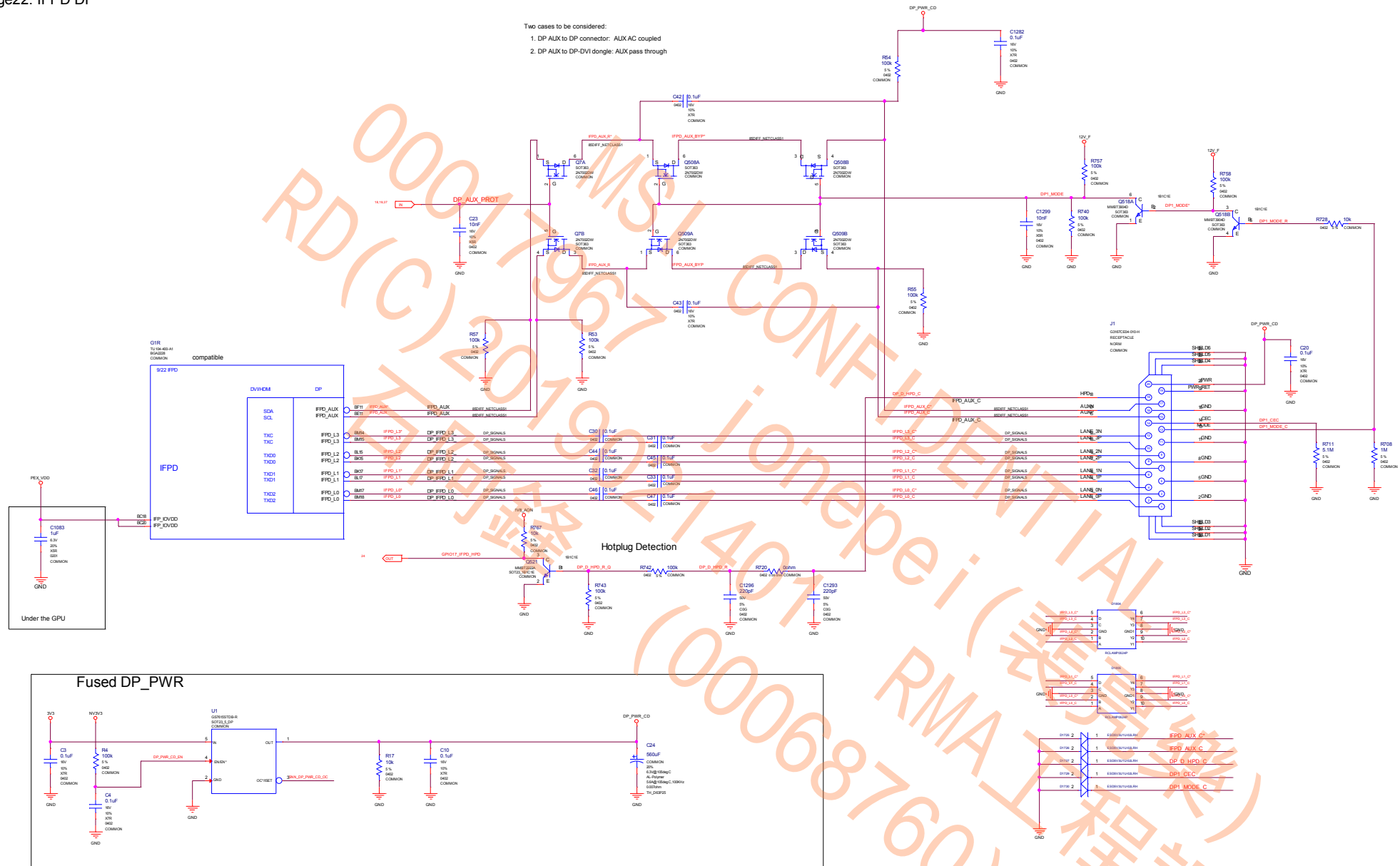






Two cases to be considered:

1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through



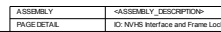
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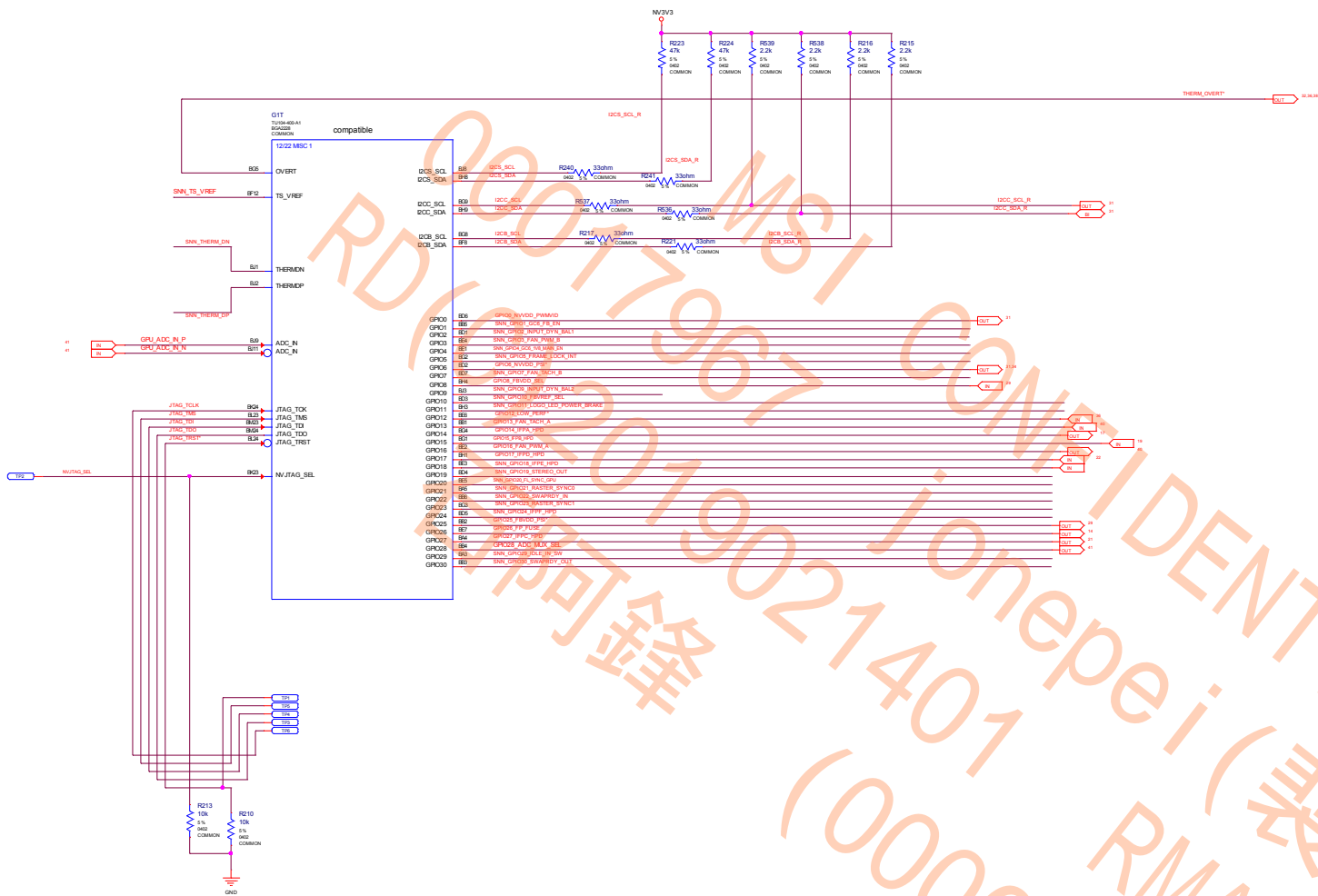
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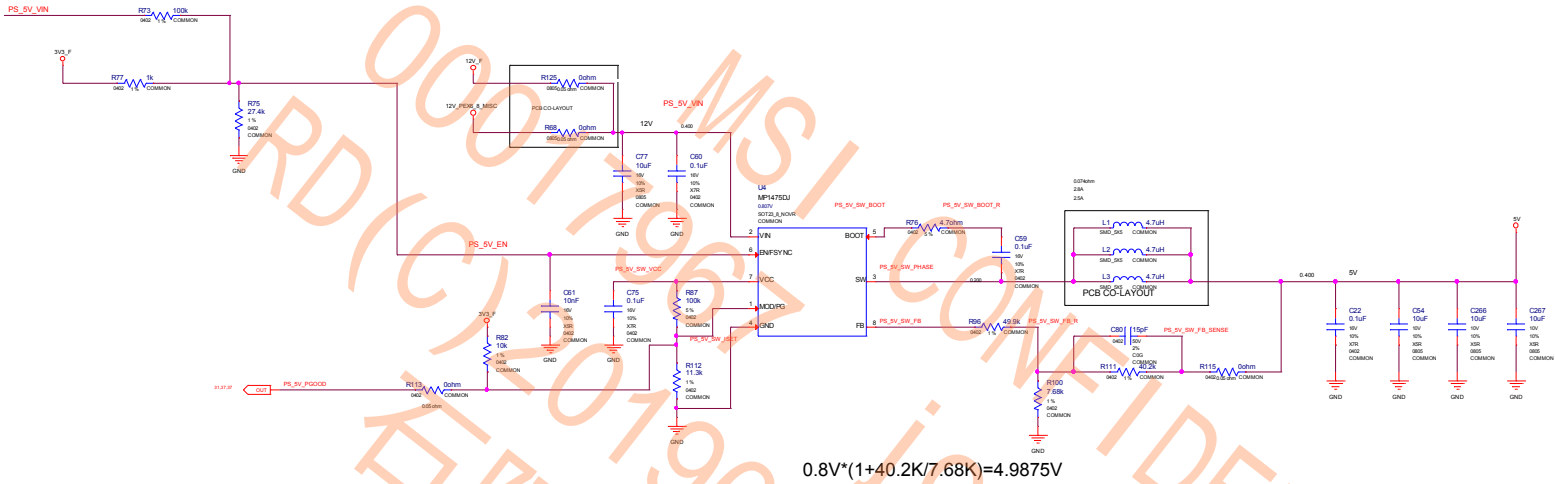
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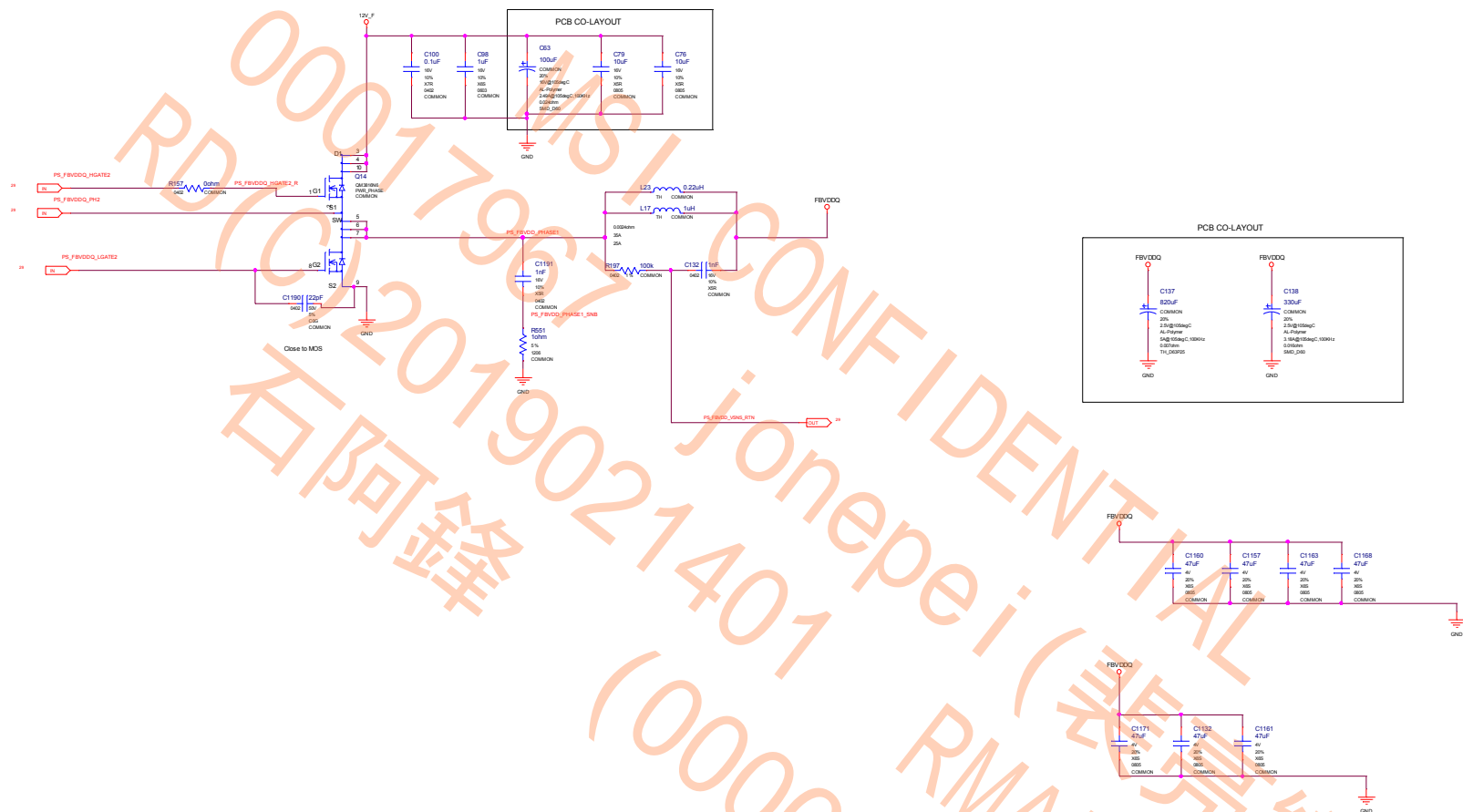


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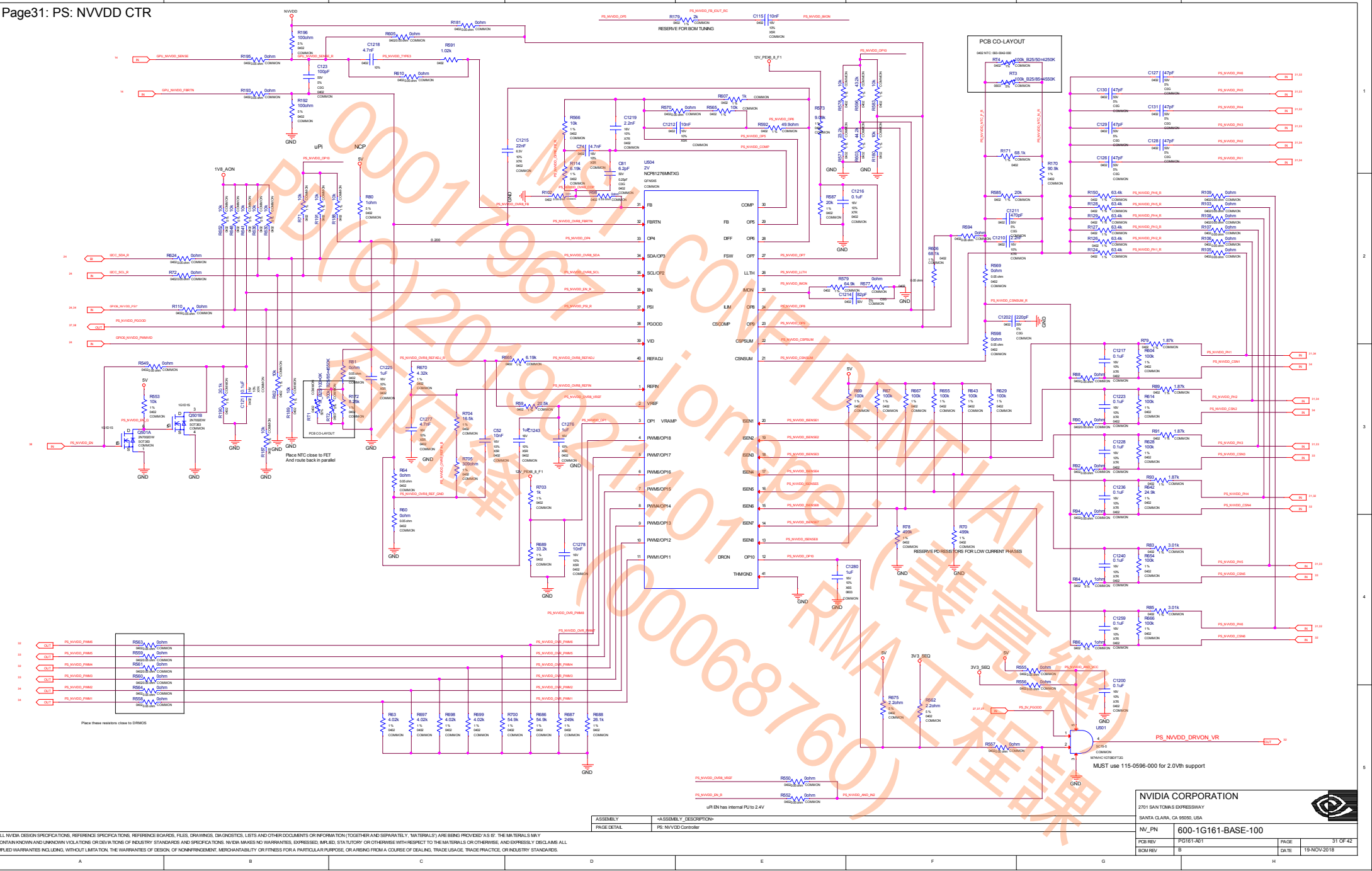
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PAGE DETAIL	PS: NVVDD Controller

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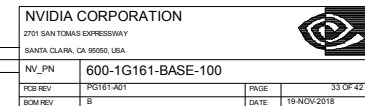




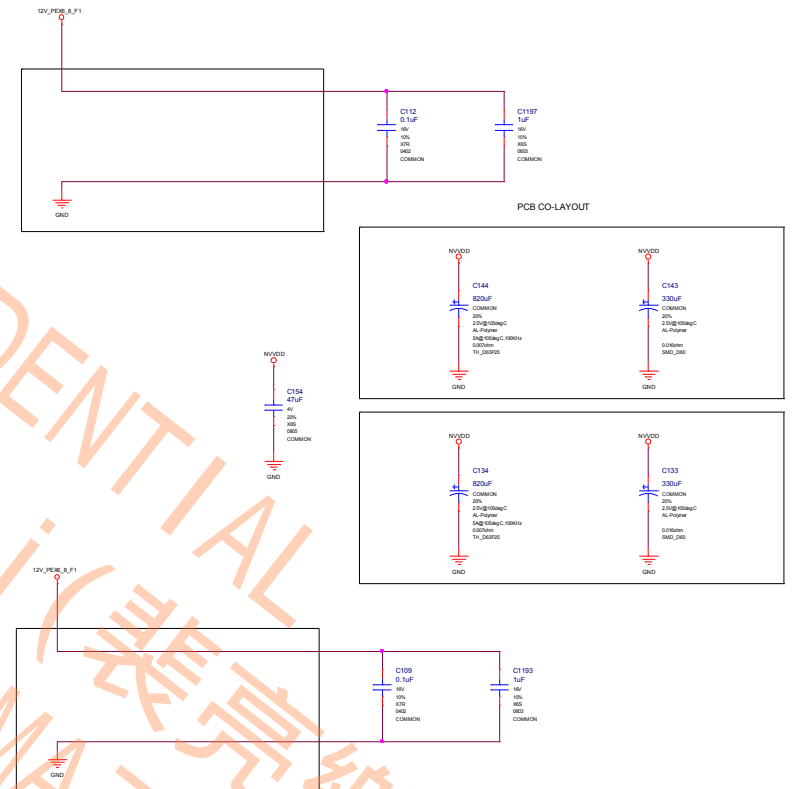
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<b>NV_PN</b>		<b>600-1G161-BASE-100</b>		
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ASSEMBLY	ASSEMBLY DESCRIPTION	SANTA CLARA, CA 95050, USA	
PAGE DETAIL	PS: NV/VDI Phase 3.5	NV_PN	600-1G161-BASE-100
		FOR REV	PD181AD1
		DATE	19-NOV-2018
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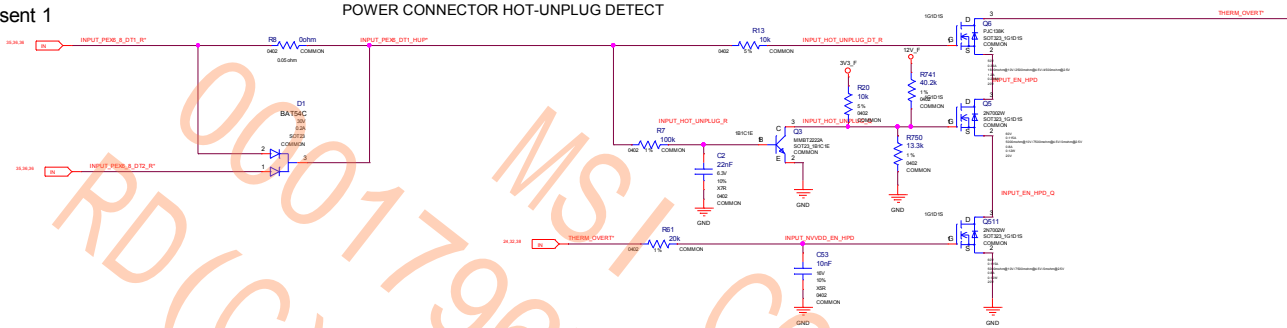


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BOM REV	B



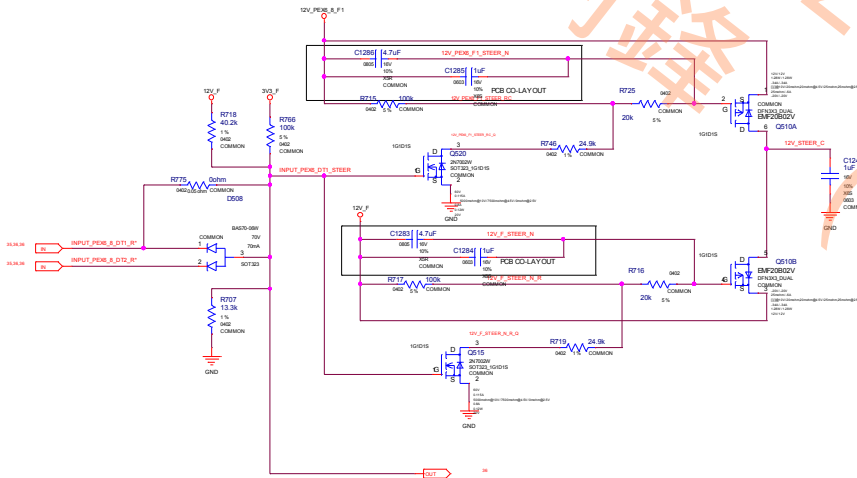
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<b>NV_PN</b> 600-1G161-BASE-100			
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PEX Input Present 1

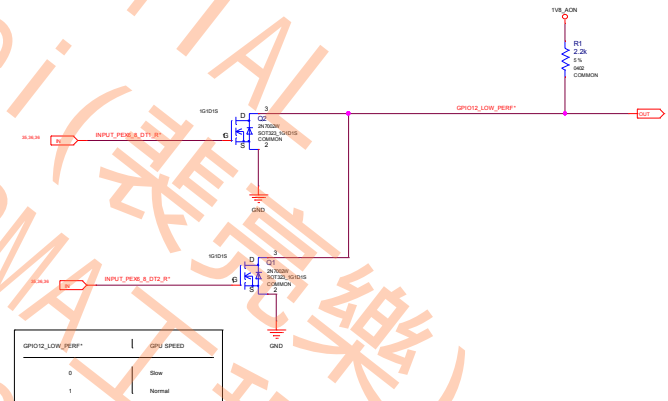


12V CURRENT STEERING (UNDER POWER BOOT):

GUIDES CURRENT FROM PEX EDGE TO PEX 6 PIN INPUT AREA

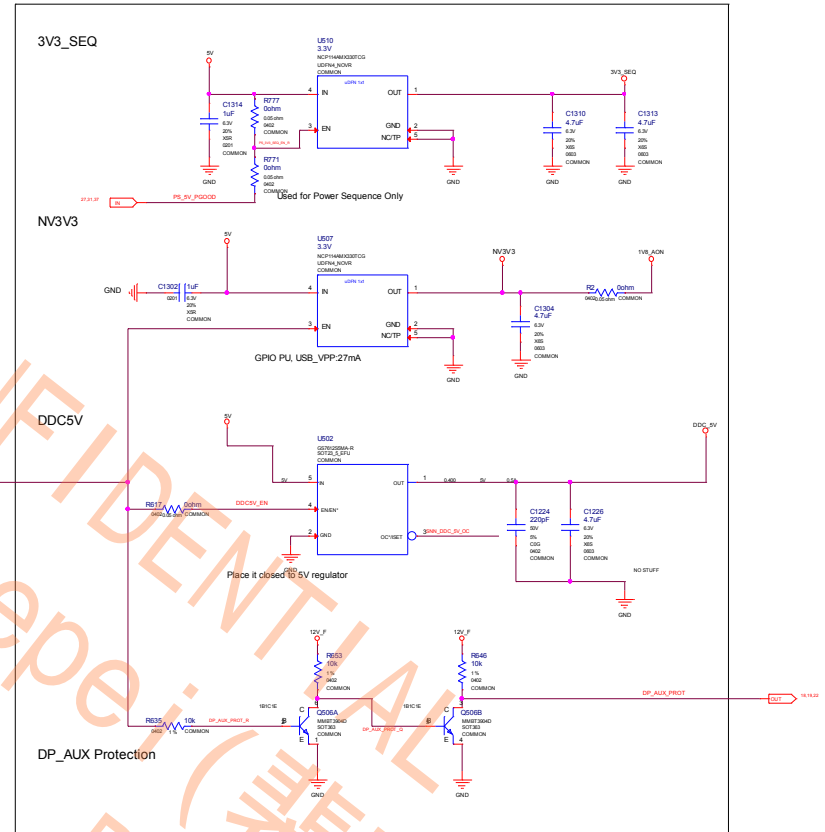
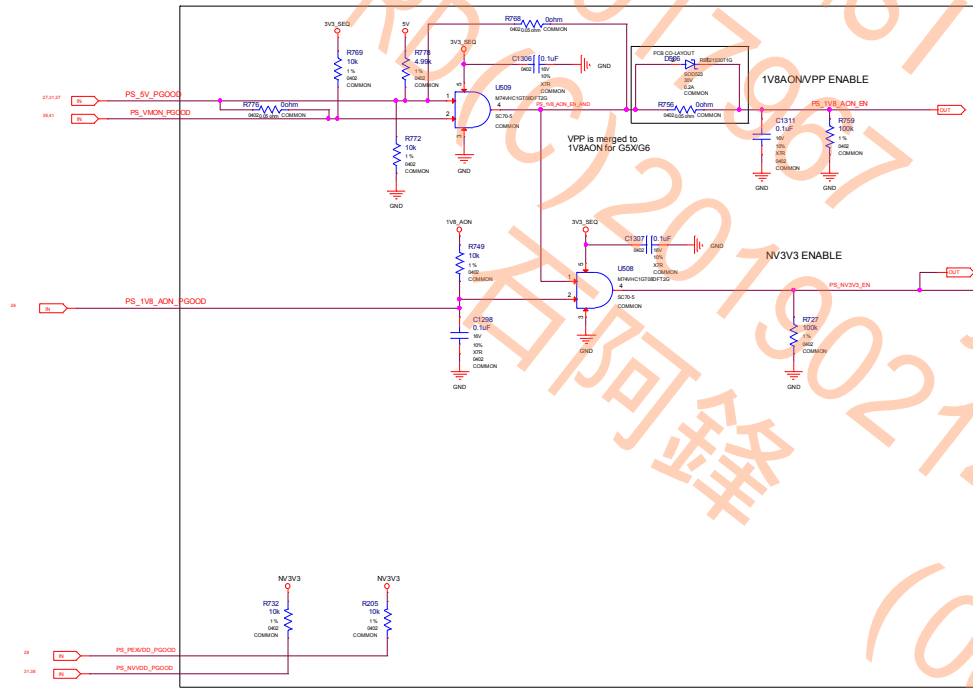


PEX Input - Power Level/PSI\* Control



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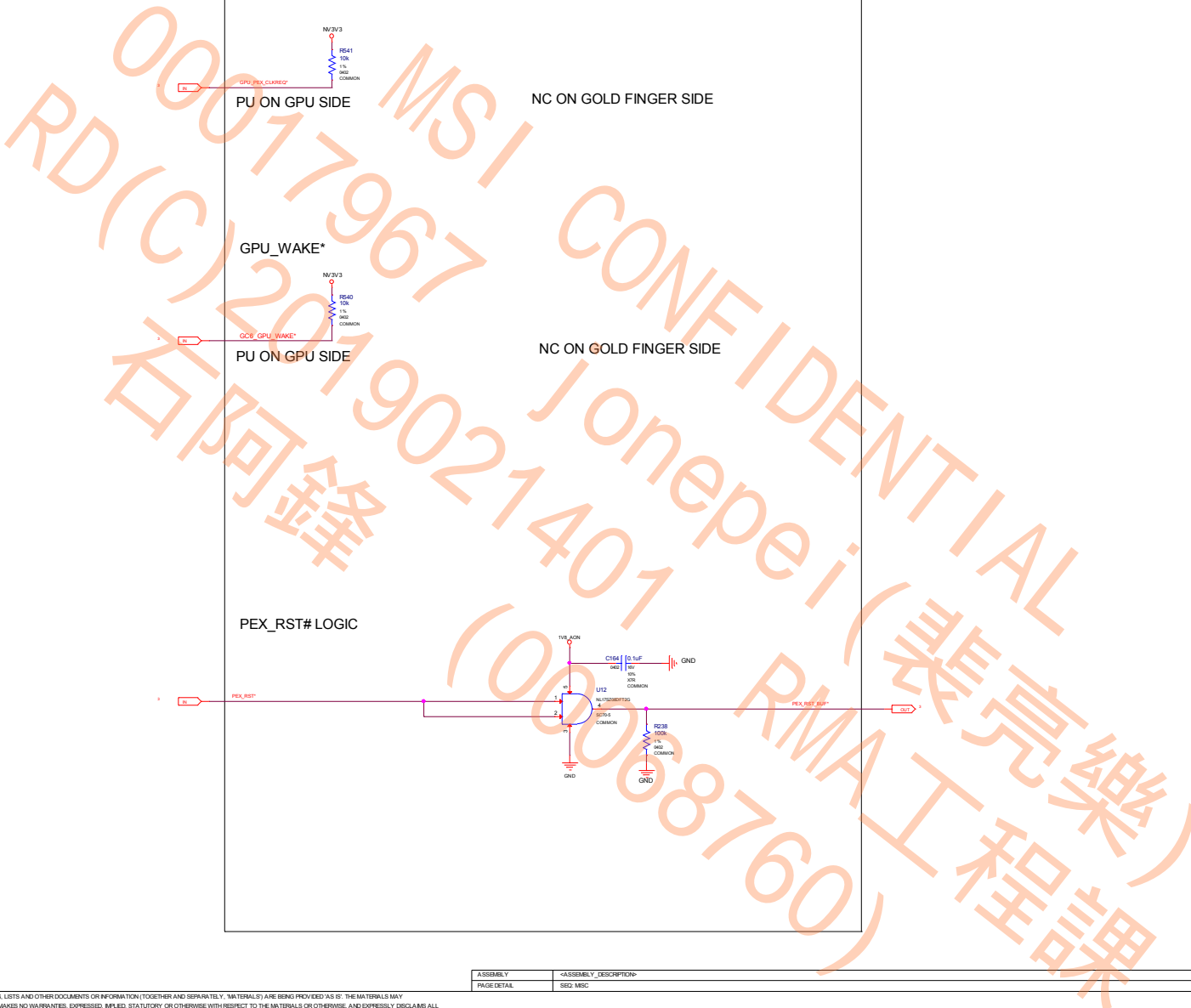
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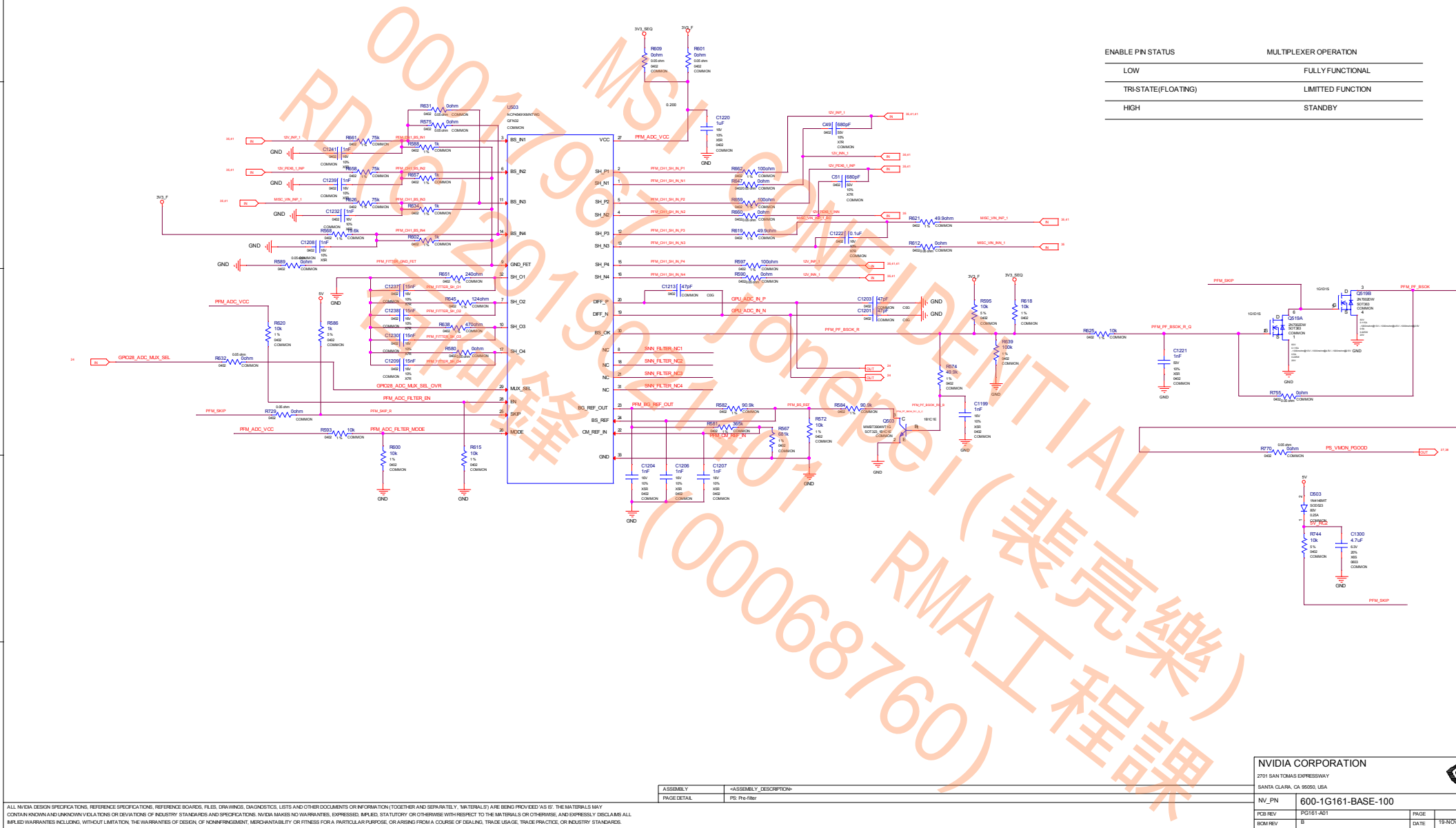


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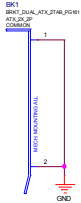


MODE PIN STATUS	MULTIPLEXER OPERATION
LOW	DEVICE A
TRI-STATE(FLOATING)	STAND-ALONE
HIGH	DEVICE B

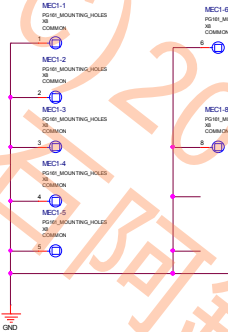
ENABLE PIN STATUS	MULTIPLEXER OPERATION
LOW	FULLY FUNCTIONAL
TRI-STATE(FLOATING)	LIMITED FUNCTION
HIGH	STANDBY

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Bracket:



MOUNTING HOLES FOR HS:



BACK STIFFENER:



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