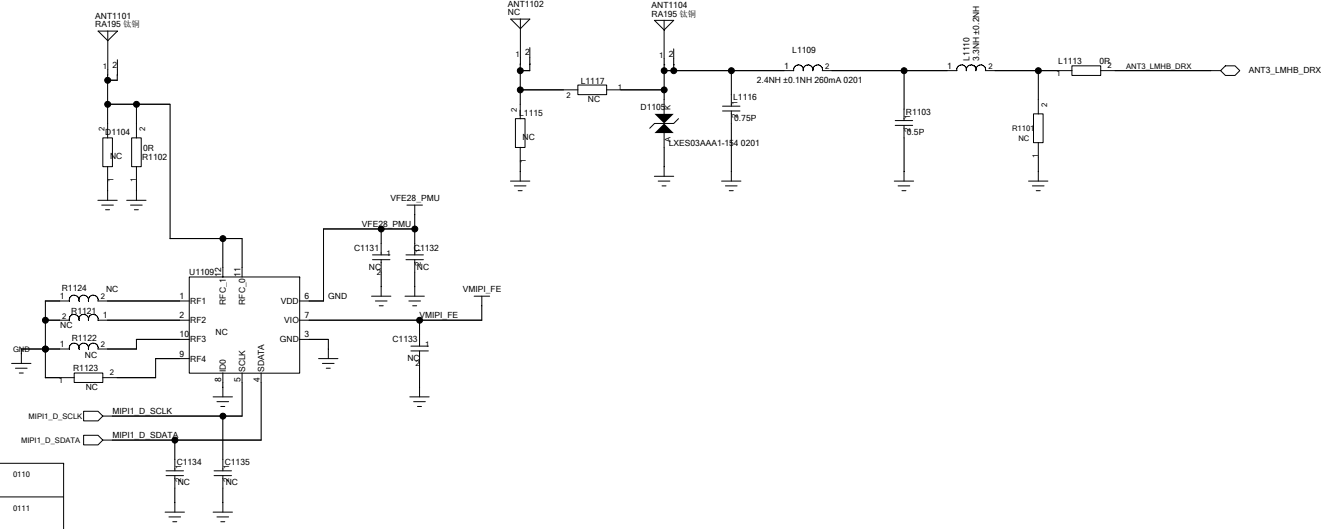
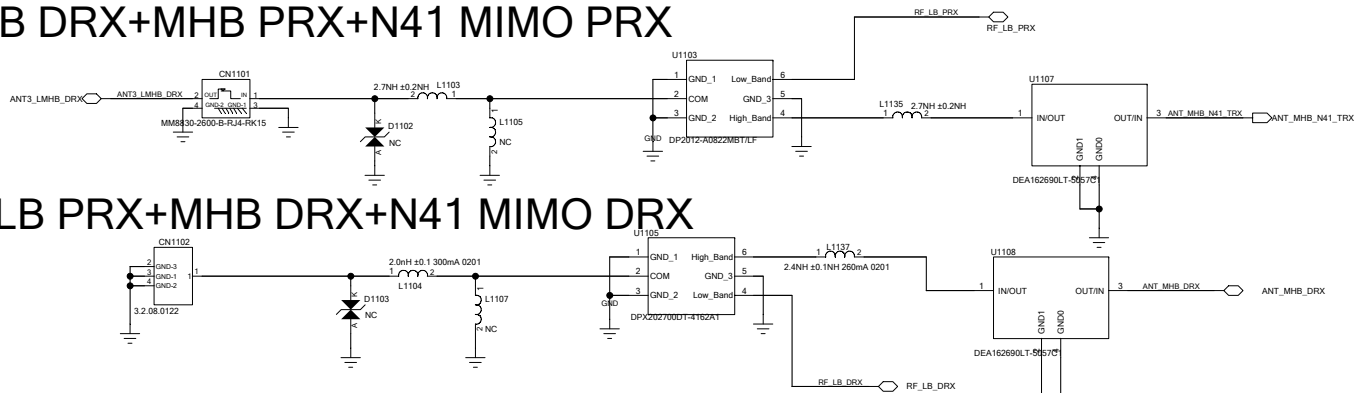


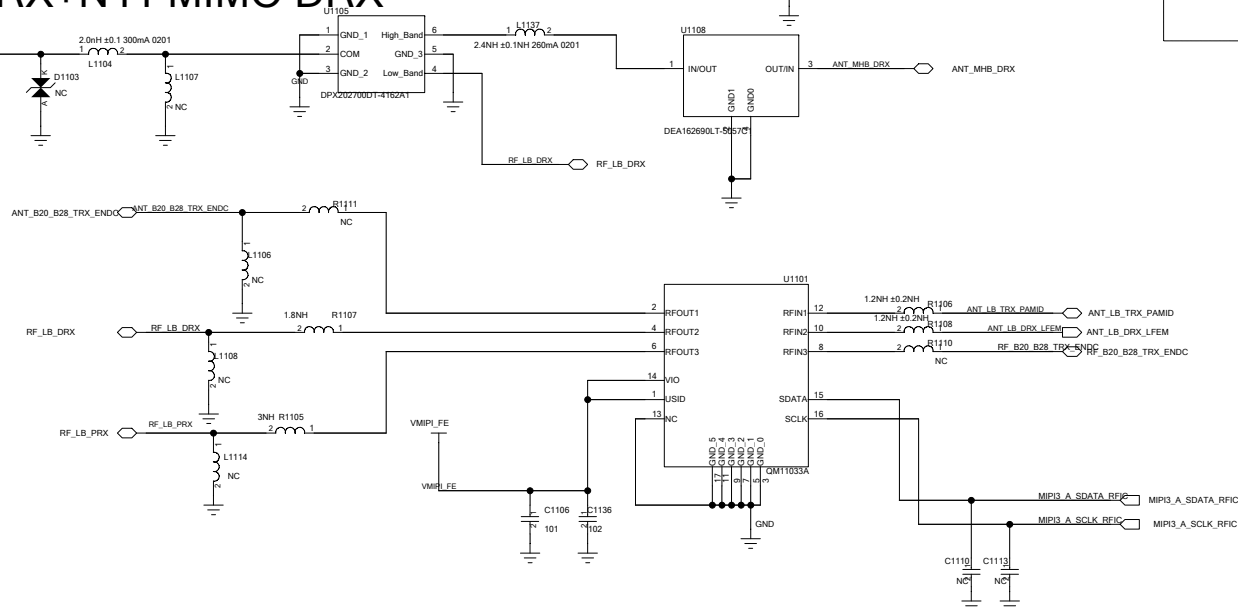
Ant0 LB DRX+MHB PRX+N41 MIMO PRX 上天线



Ant0 LB DRX+MHB PRX+N41 MIMO PRX

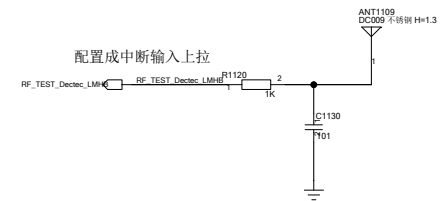



ANT4 LB PRX+MHB DRX+N41 MIMO DRX



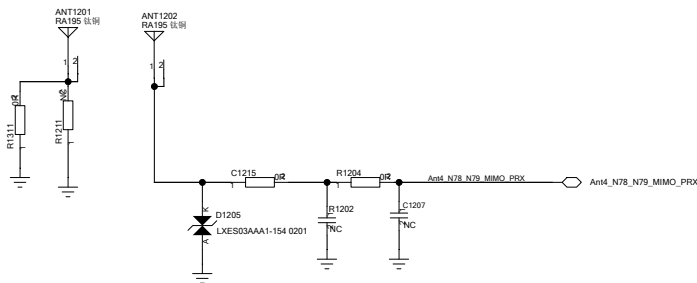
压板传导测试检测电路

配置成中断输入上拉

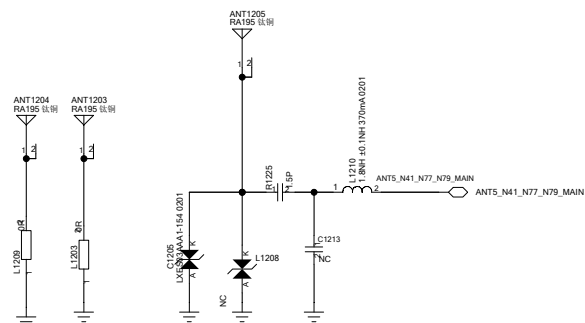


	Sign	Date	Type:	
Drawn By			Draw No: Schematic	
Checked By			Sheet of 50	Ver:
Standard By				
Approved By				

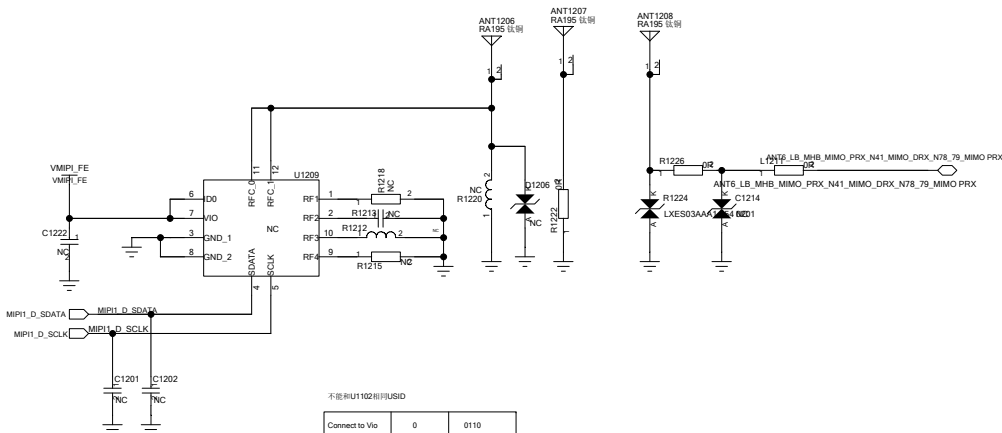
Ant8 N78/79 PRX



Ant2 N41 PRX+N78/79 DRX



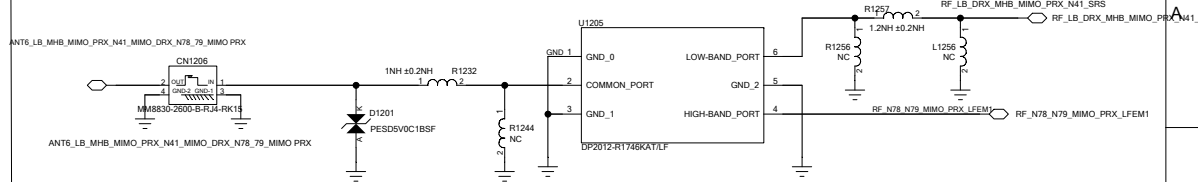
Ant6 MHB MIMO PRX+N41 MIMO DRX+N78/79 MIMO PRX



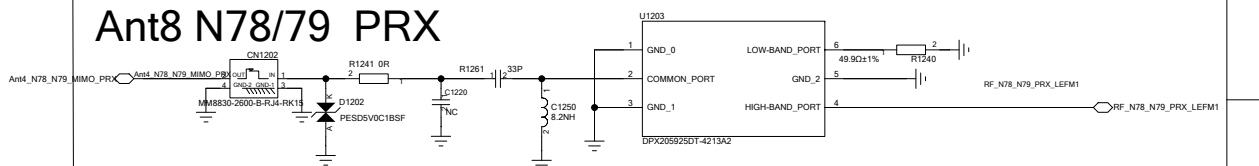
不能和U102共用USID

Connect to Vio	0	0110
Connect to GND	1	0111

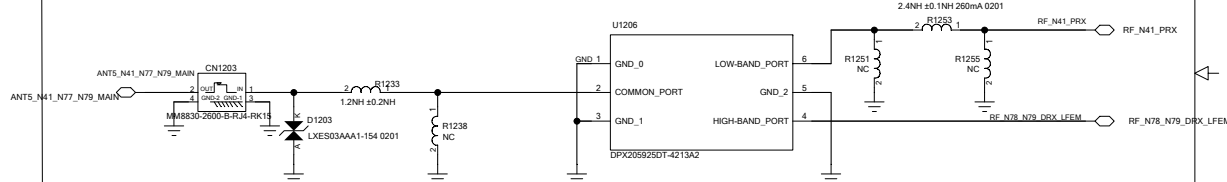
Ant6 MHB MIMO PRX+N41 MIMO DRX+N78/79 MIMO PRX



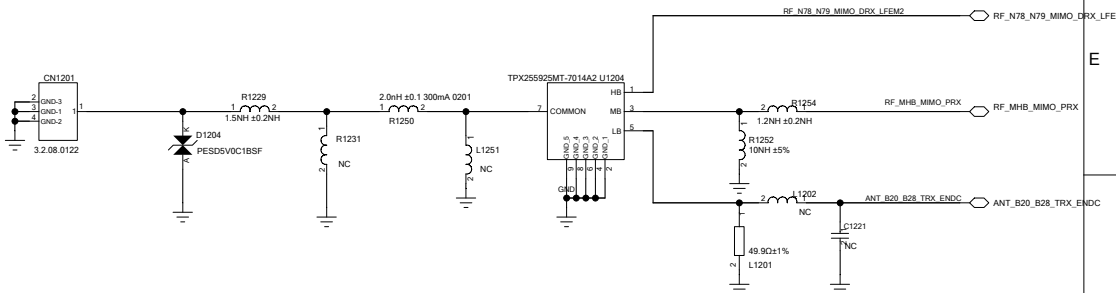
Ant8 N78/79 PRX



Ant2 N41 PRX+N78/79 DRX



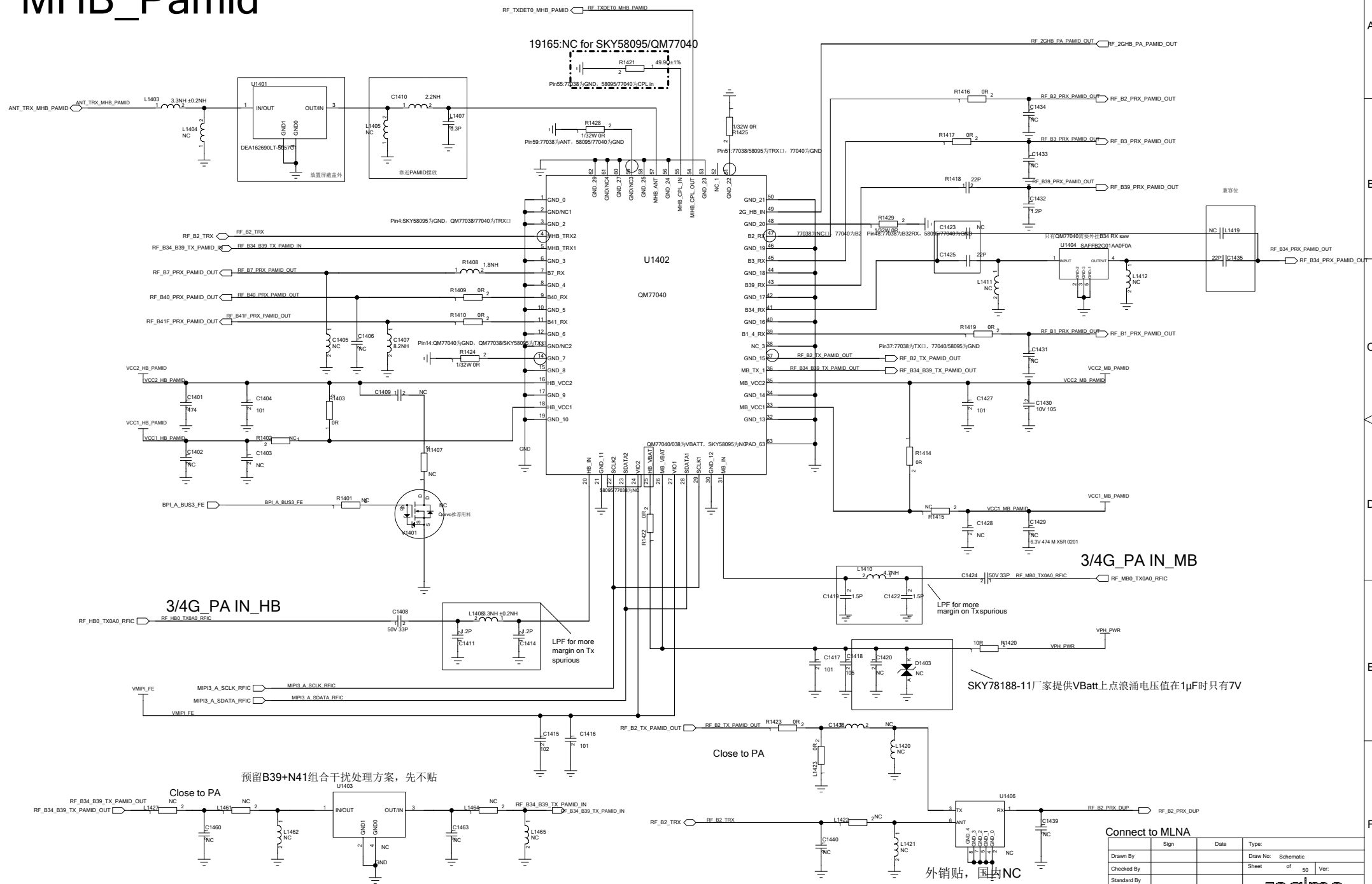
Ant3 MHB MIMO DRX+N41 DRX+ N78/79 MIMO DRX



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Standard By		
Approved By		


realme

MHB_Pamid



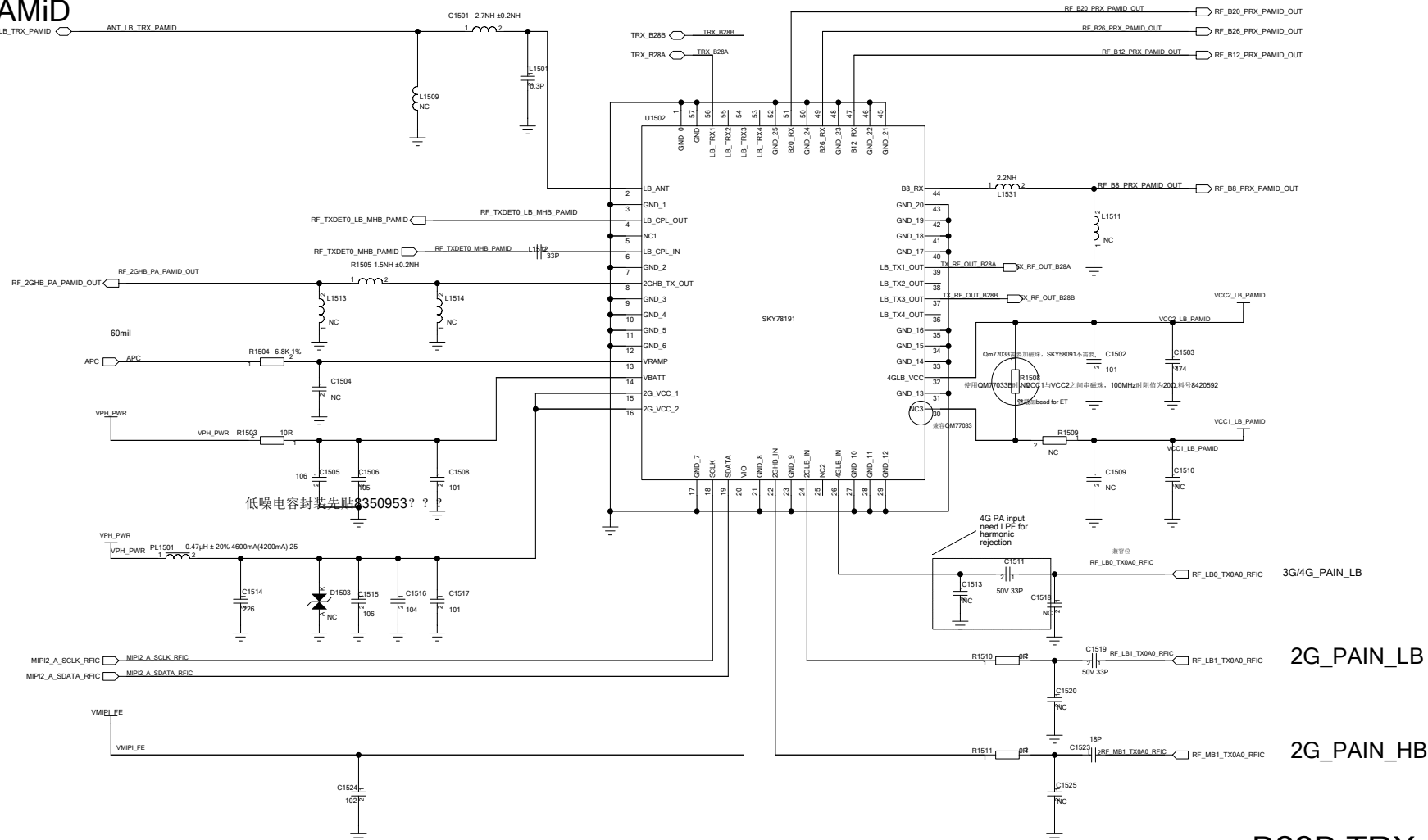
外销贴，国内NC

Connect to MLNA

	Sign	Date	Type:	
Drawn By			Draw No:	Schematic
Checked By			Sheet	of 50 Ver:
Standard By				
Approved By				

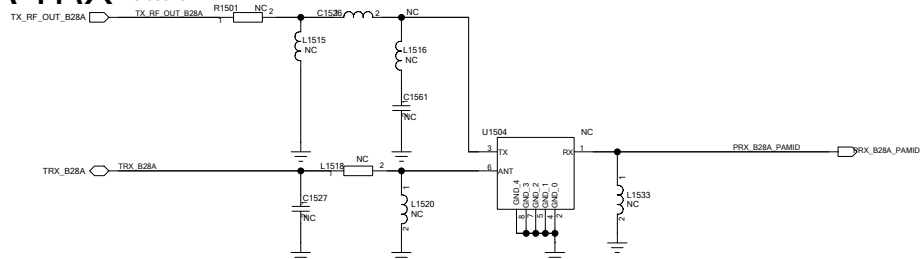
realme

LB PAMiD



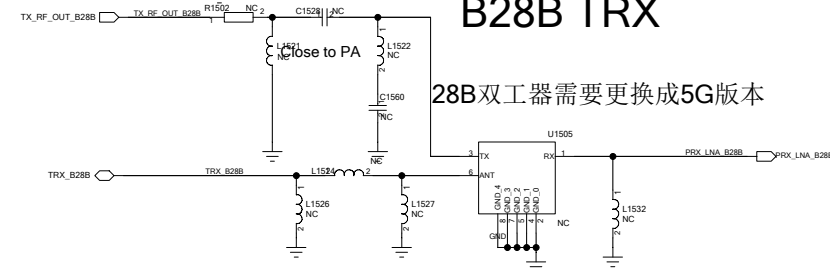
B28A TRX


Close to PA

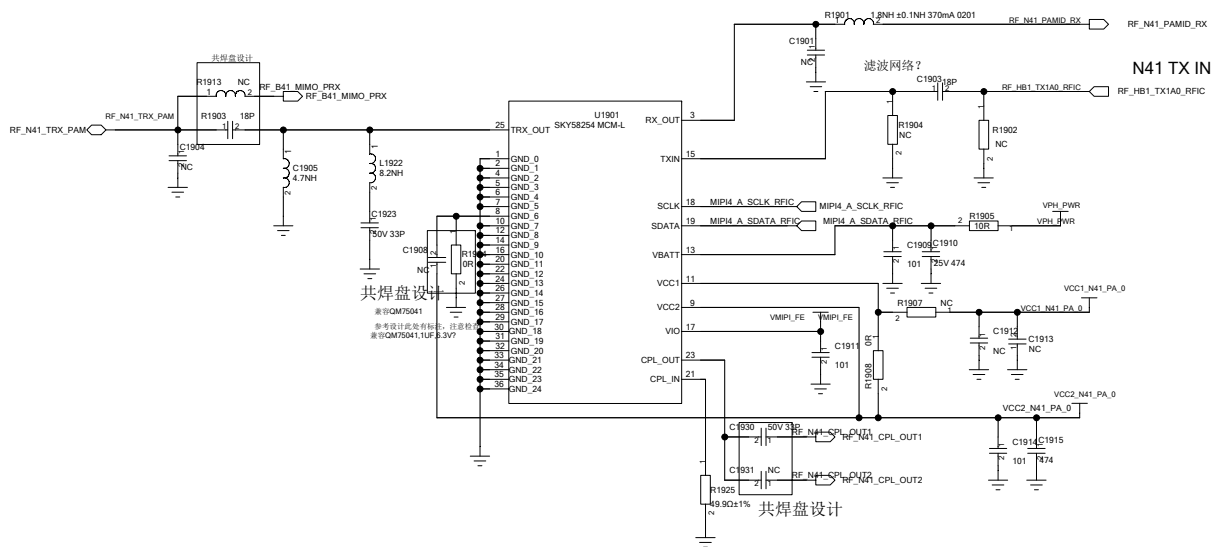


B28B TRX

28B双工器需要更换成5G版本



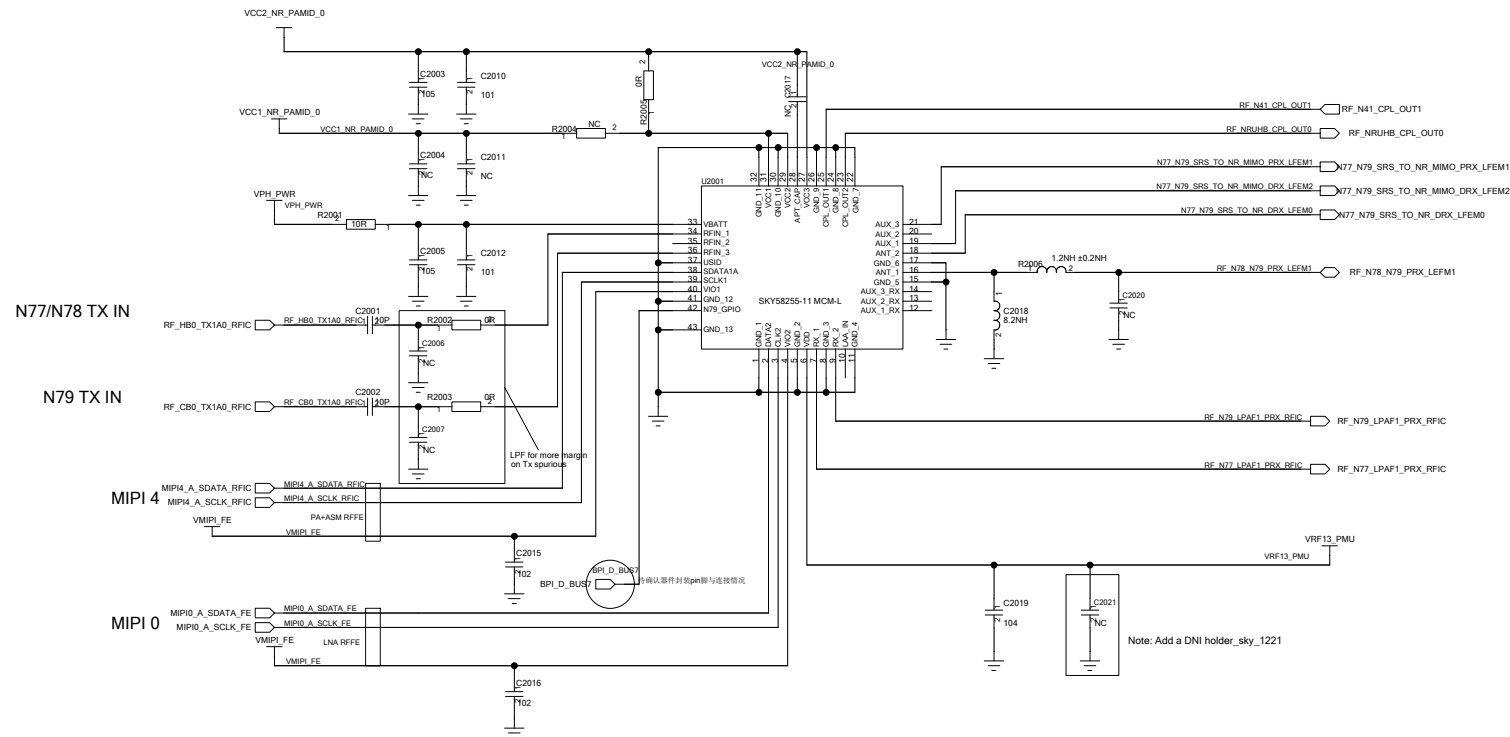
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Drawn By			Draw No: Schematic	
Checked By			Sheet	of 50 Ver:
Standard By				
Approved By				




	Sign	Date	Type:
Drawn By			Draw No: Schematic
Checked By			Sheet of 50 Ver:
Standard By			
Approved By			

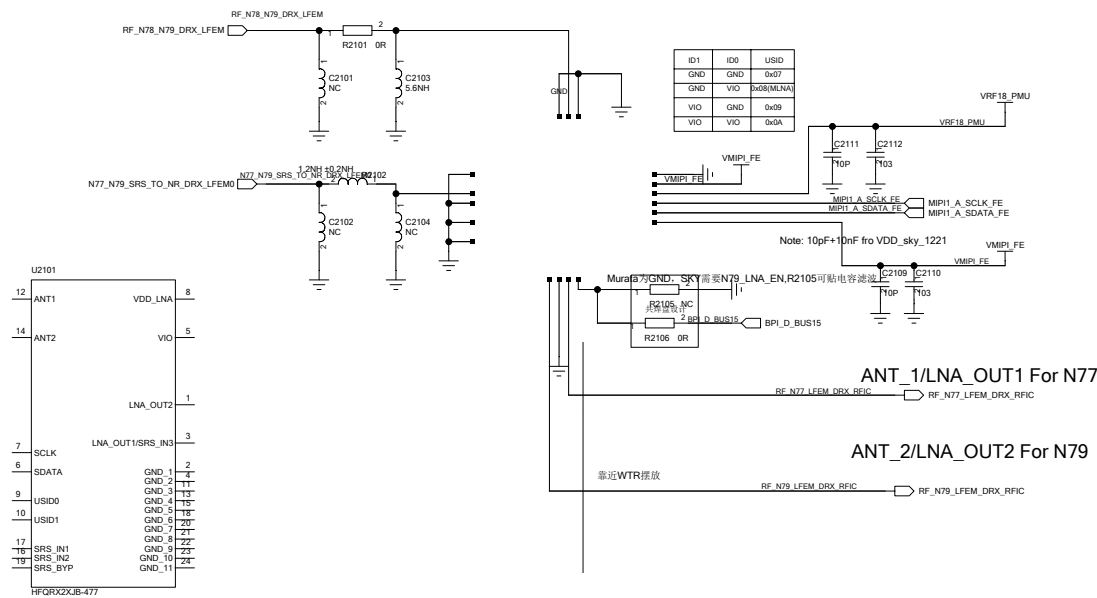
realme


NR PAMID 0(N77/79Main)

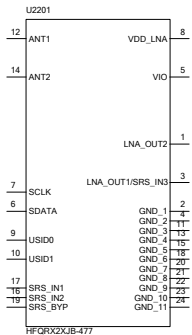


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Drawn By			Draw No: Schematic	
Checked By			Sheet of 50	Ver:
Standard By				
Approved By				

NR LFEM DRX




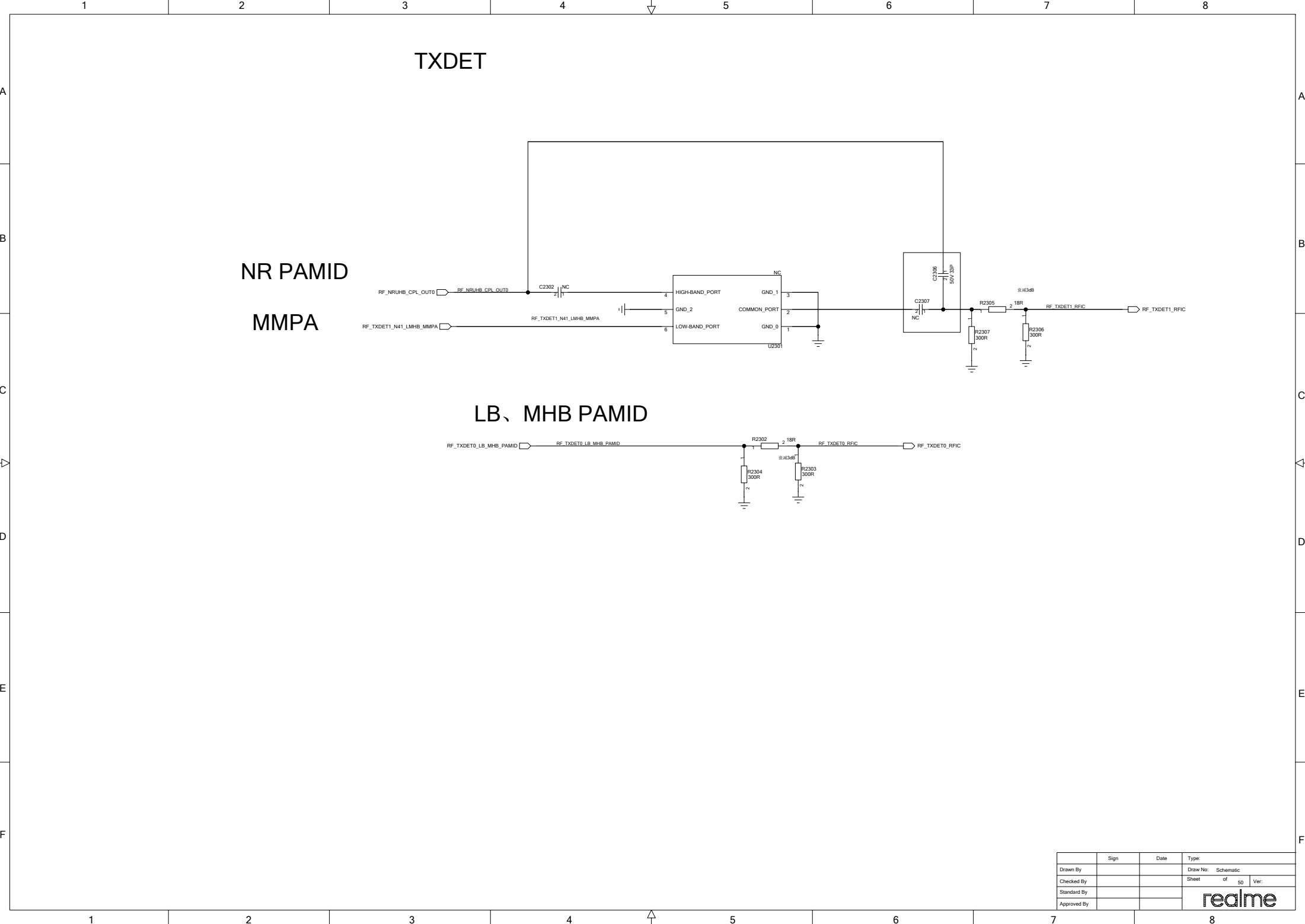
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Drawn By			Draw No:	Schematic	
Checked By			Sheet	of	50
Standard By			Ver:		
Approved By					

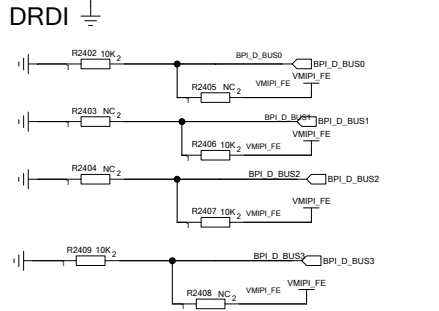
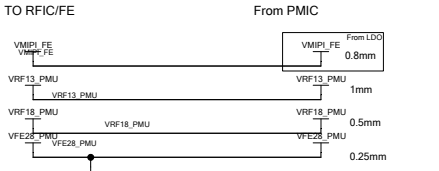
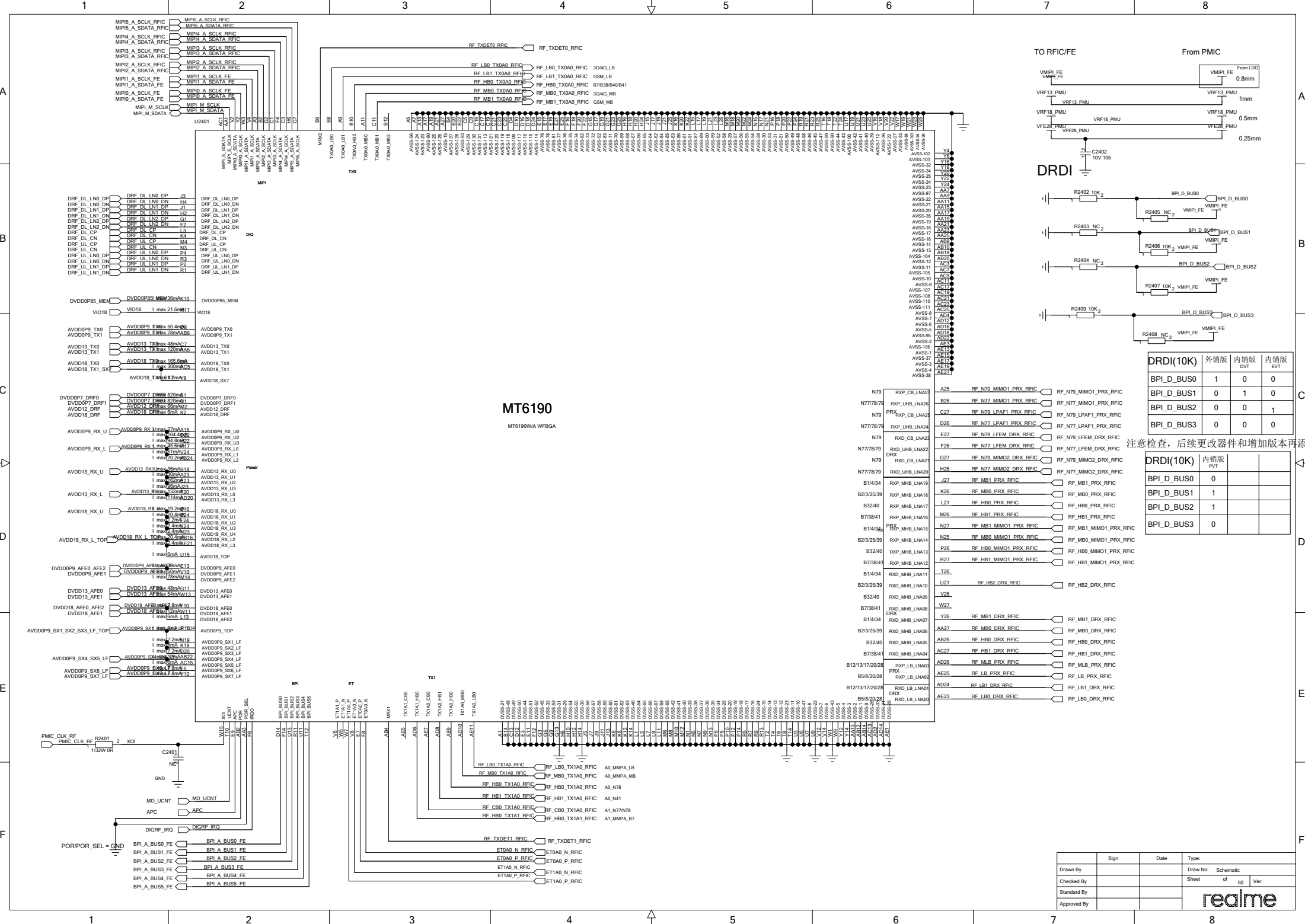
[illegible][illegible]

ID1	ID0	USID
GND	GND	0x0A
GND	VIO	0x08
VIO	GND	0x09
VIO	VIO	0x01

SKY53728		
ID1	ID0	USID
GND	GND	0x07(NR D
GND	VIO	0x08(MLNA
VIO	GND	0x09
VIO	VIO	0x0A

	Sign	Date	Type:
Drawn By			Draw No: Schematic
Checked By			Sheet of 50 Ver:
Standard By			
Approved By			





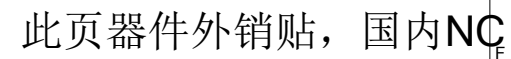
DRDI(10K)	外销版	内销版	内销版
BPI_D_BUS0	1	0	0
BPI_D_BUS1	0	1	0
BPI_D_BUS2	0	0	1
BPI_D_BUS3	0	0	0

注意检查，后续更改器件和增加版本再添加

DRDI(10K)	内销版	PVT
BPI_D_BUS0	0	
BPI_D_BUS1	1	
BPI_D_BUS2	1	
BPI_D_BUS3	0	

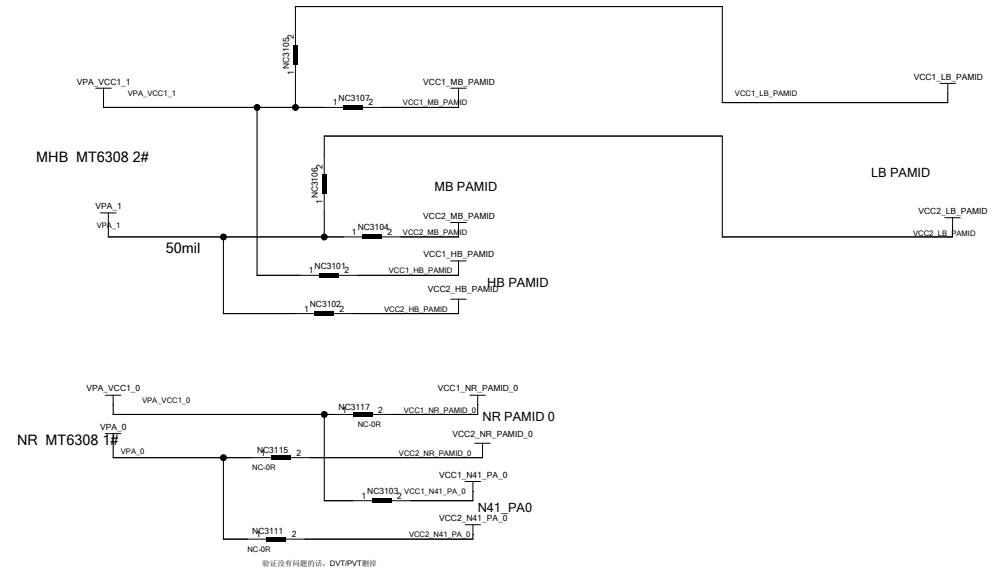
Sign	Date	Type
Drawn By		Draw No: Schematic
Checked By		Sheet of 50 Ver:
Standard By		
Approved By		






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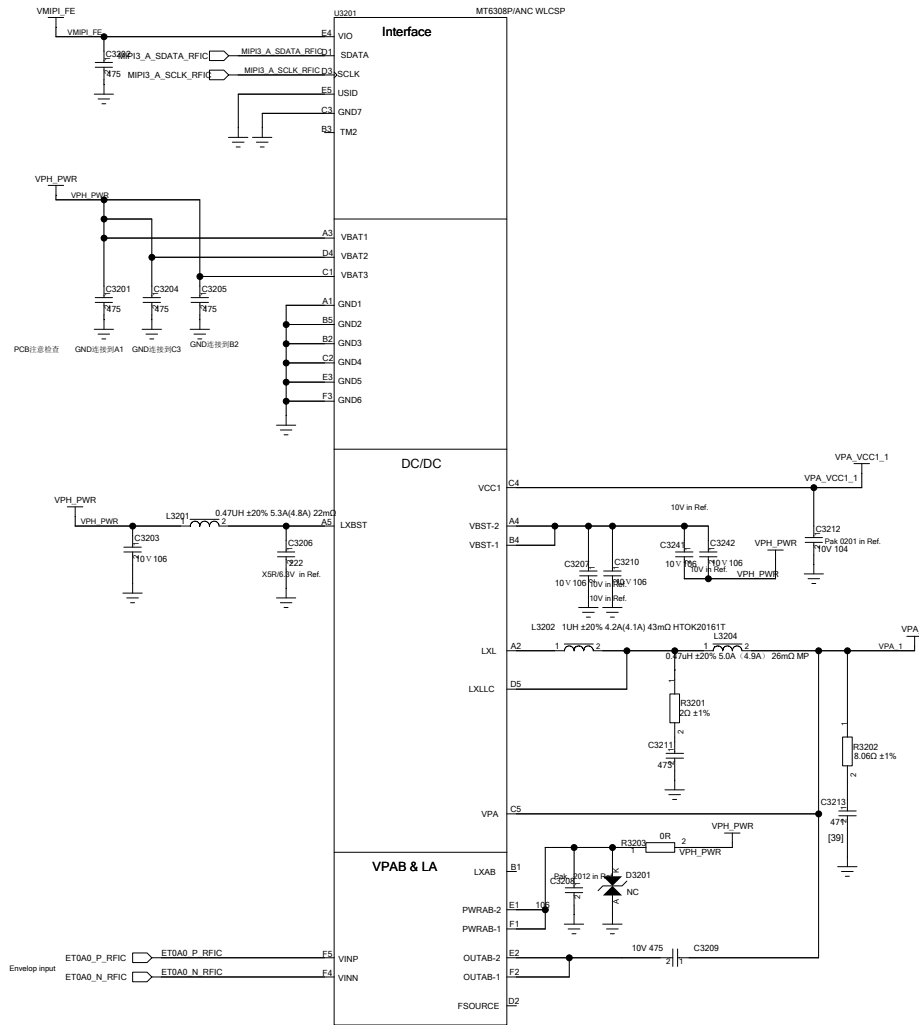
MT6308 ET供电转接



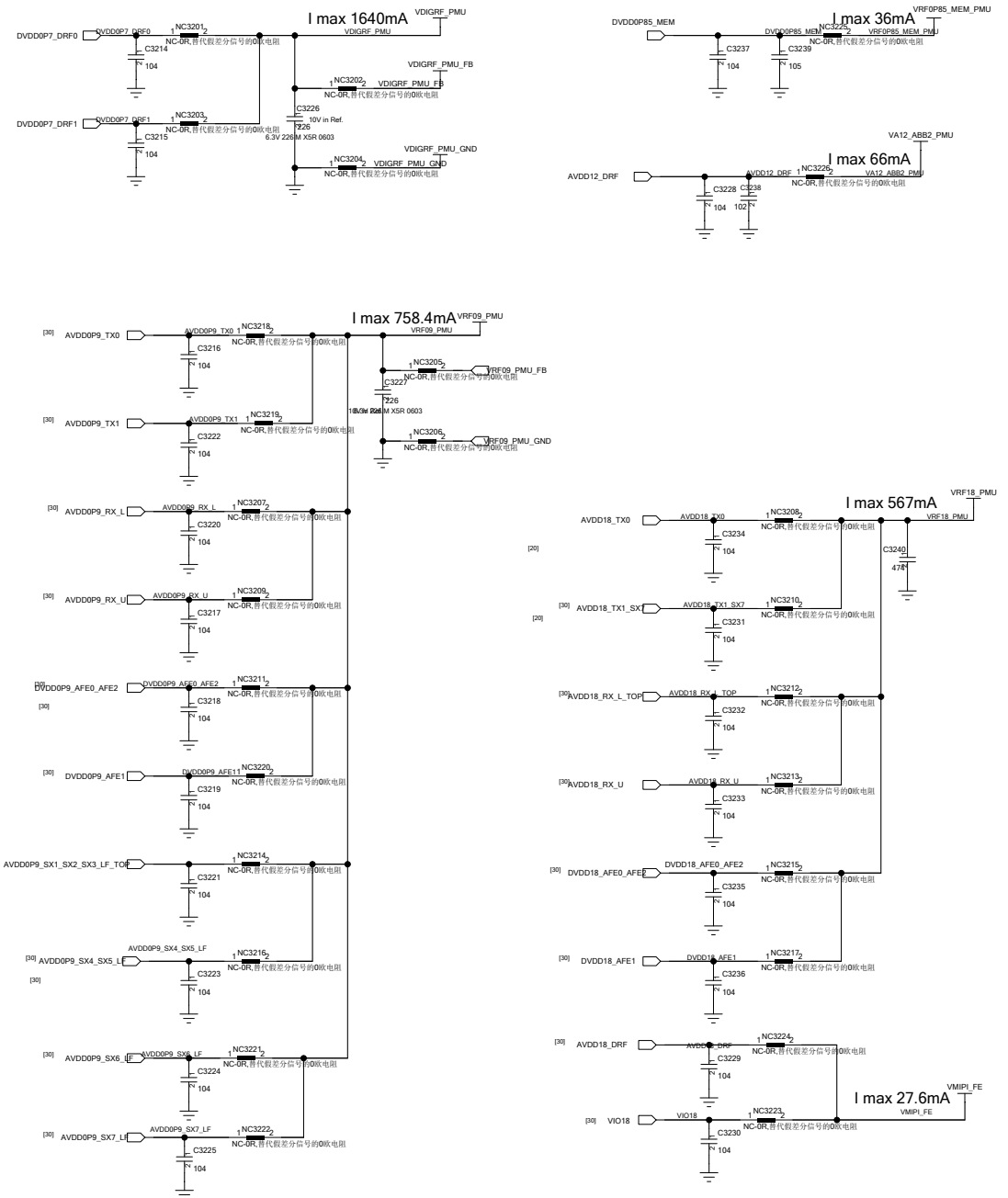
	Sign	Date	Type:	
Drawn By			Draw No: Schematic	
Checked By			Sheet of 50	Ver:
Standard By				
Approved By				

NR_PAMid_n41_SatPA_ET1_NR1/ET Ref design

LB/MHB_PAMID_ET MT6308 2#



MT6190供电部分

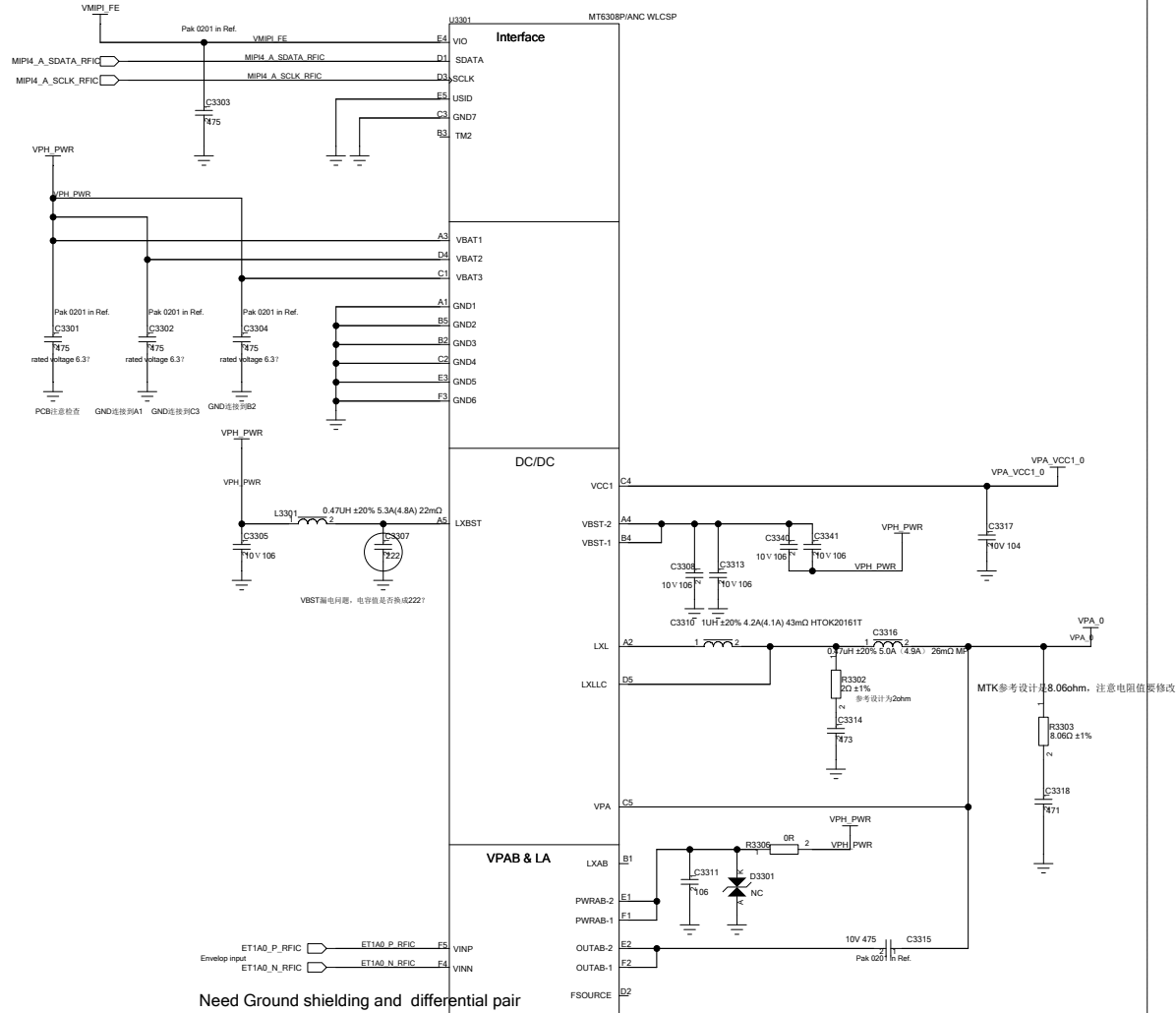


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Standard By		
Approved By		

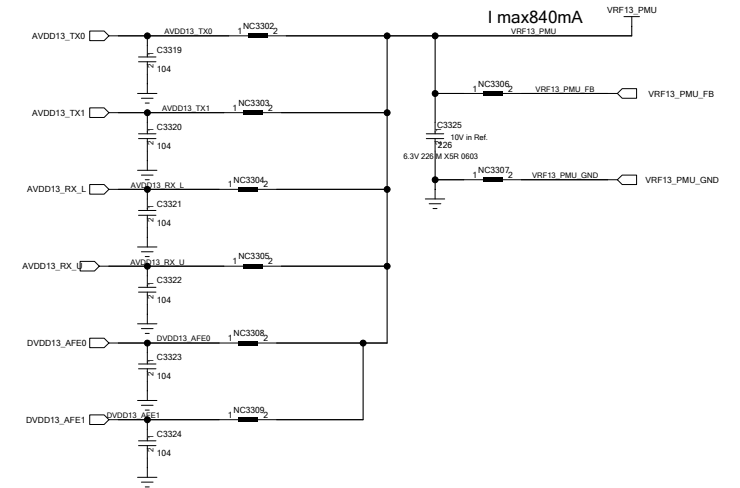
realme

NR_PAMID + N41 PAD /APT Ref Design

MT6308 1#



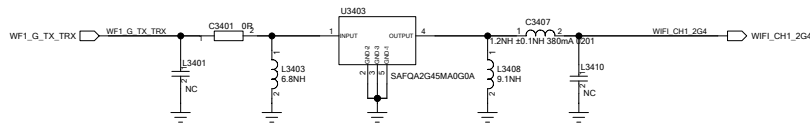
MT6190供电TX部分



Sign	Date	Type
Drawn By		Draw No: Schematic
Checked By		Sheet of 50 Ver:
Standard By		
Approved By		

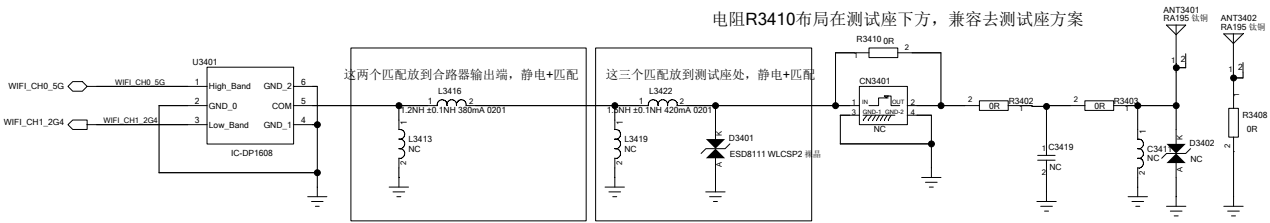
realme

CH1 2.4G

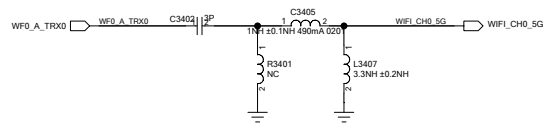


WIFI部分

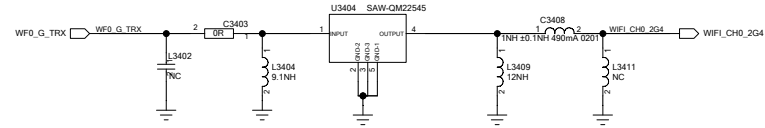
2G Chain1_5G_Chain0



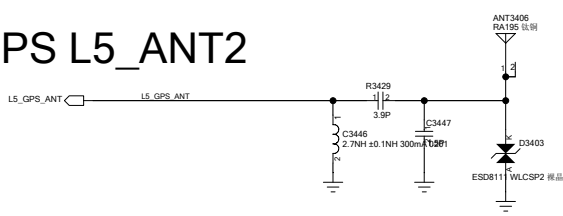
CH0 5G



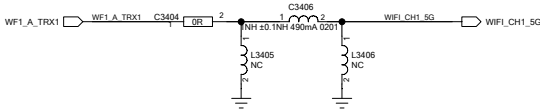
CH0 2.4G



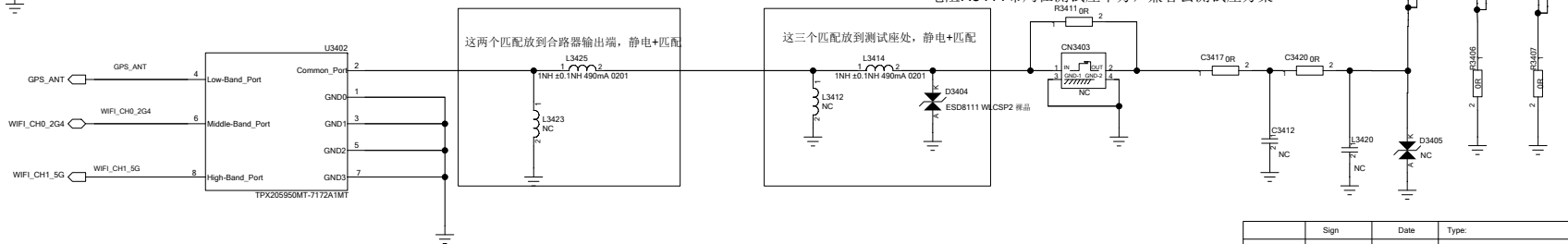
GPS L5_ANT2



CH1 5G



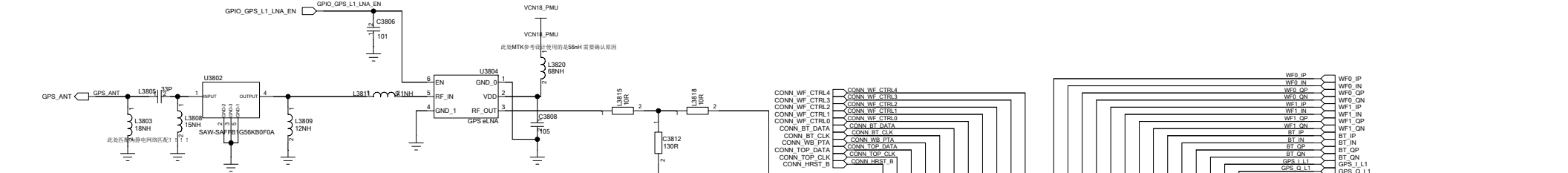
2G Chain0_5G Chain1+GPS L1



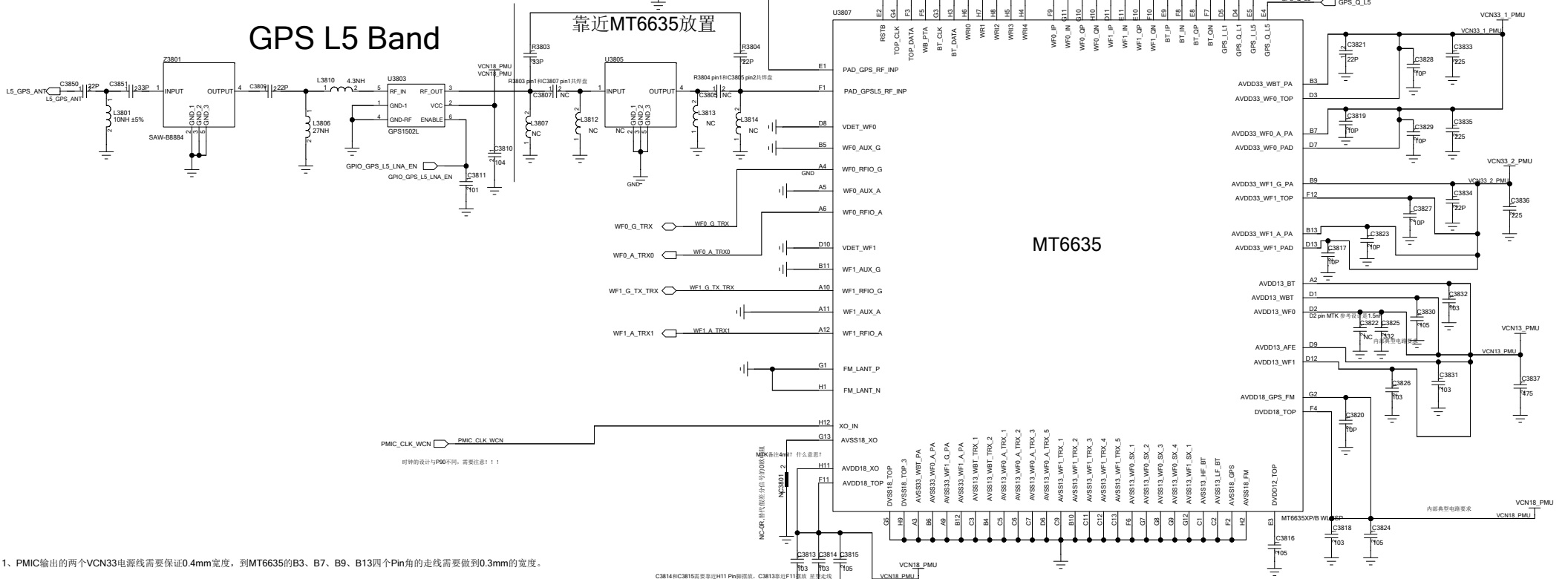
Drawn By	Sign	Date	Type
Checked By			Draw No: Schematic
Standard By			Sheet of 50 Ver:
Approved By			



GPS L1 Band



GPS L5 Band



1、PMIC输出的两个VCN33电源需要保证0.4mm宽度，到MT6635的B3、B7、B9、B13四个Pin角的布线需要做到0.3mm的宽度。

2、到MT6635 电源线到F11 Pin角的宽度需要做到0.15mm。

3、到MT6635其他电源脚（A3、D1、D2、D3、D7、D9、D12、D13、F4、F11、F12、G2、H11）布线宽度可以按照最小线宽来走

4、A3、A9、H9三个Pin与表层隔开单独接内层地

5、C1&C2表层连接到一起，再与表层其他地隔开，通过孔接到内层地；

6、F6、G7、G8、G9四个孔表层接在一起，与其他表层地分开，通过过孔接到内层GND

7、B4、B6、B11、B10、B12、C3、C5、C6、C7、C9、C11、C12、C13、D6、A5、A11、B5、B11可直接接表层GND

8、IQ布线需要保证10PF以内，走线长度尽量不要超过5CM；

9、PMIC到BB的CLK走线按照45欧姆阻抗设计，width=2.5mi，Space=?，长度8CM以内；

10、PMIC到MT6635的CLK走线按照30欧姆阻抗设计，Width=3min、Space=4mil，长度8CM以内；

	Sign	Date	Type
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A



C

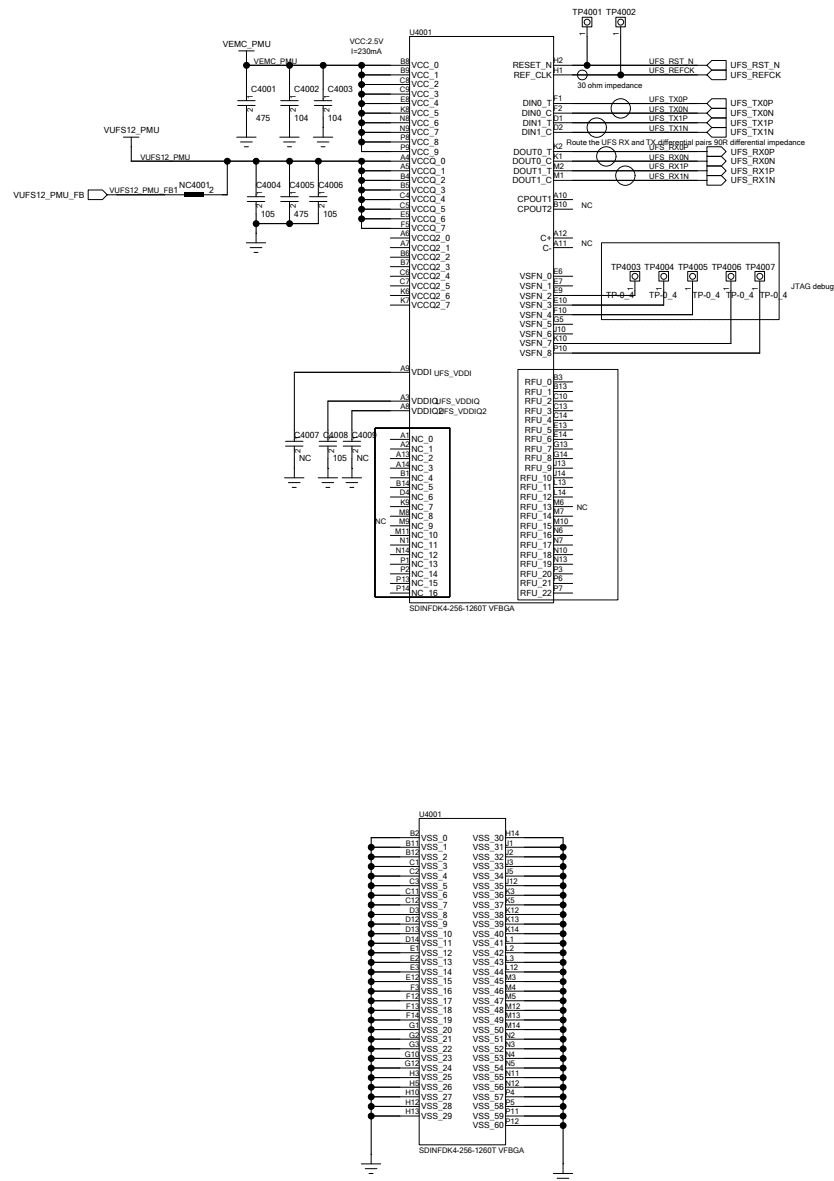
D

- F

8


UFS(256GB Sandisk)

UFS物料代码: 128G 9120283;256G 9120307

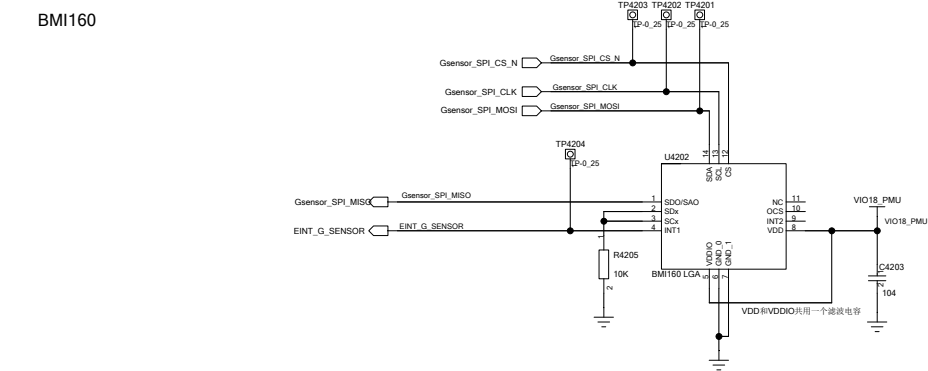


POP LPDDR4X(8GB)

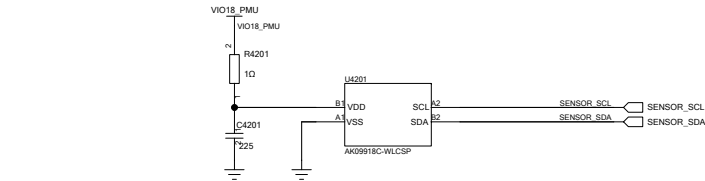


	Sign	Date	Type:
Drawn By			Draw No: Schematic
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Standard By			
Approved By			

G-Sensor/Gyo



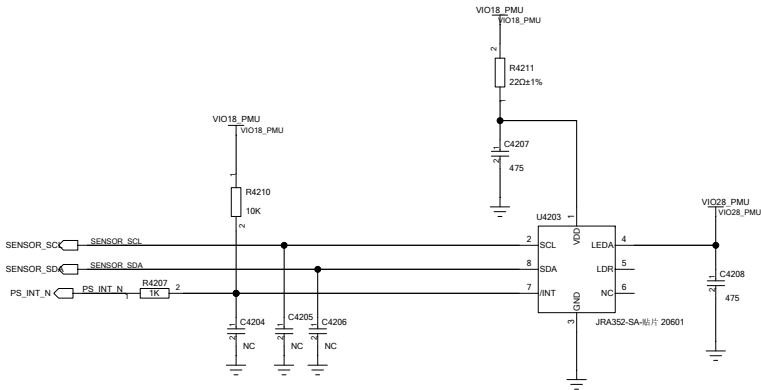
Magnetic Sensor



M-sensor	Write	Read
AK09918	0X18	0X19

P.S sensor module

A.L.S. sensor1



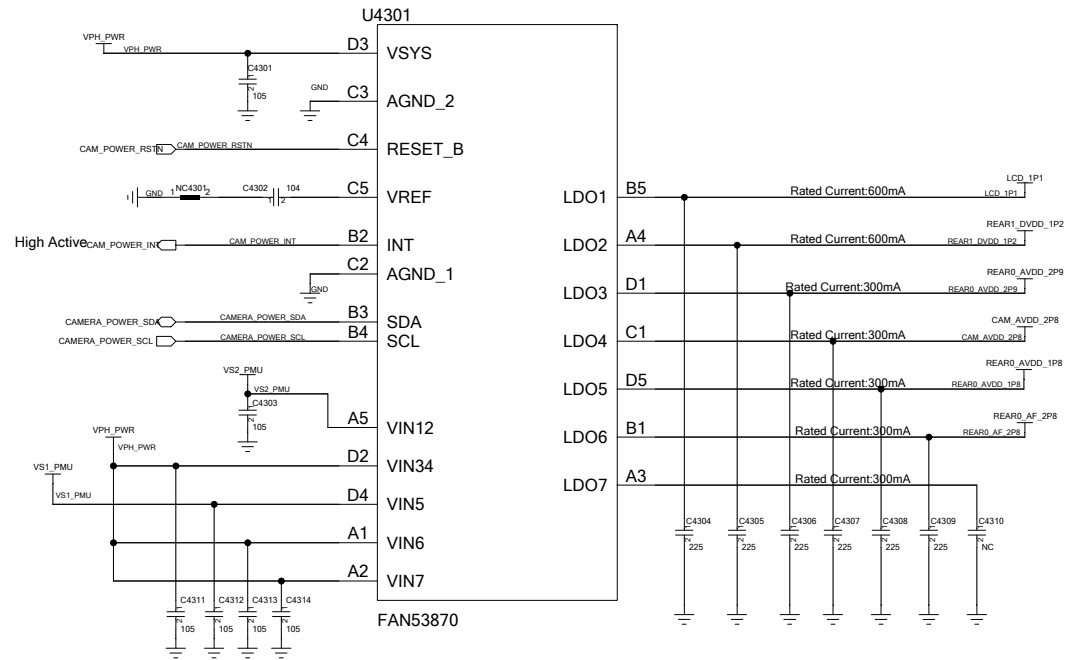
STK33502 读地址0x8D、写地址0x8C

ALS Sensor	Write	Read
STK33502	0X8C	0X8D

FAN53870	write	read
I2C	0X6A	0X6B

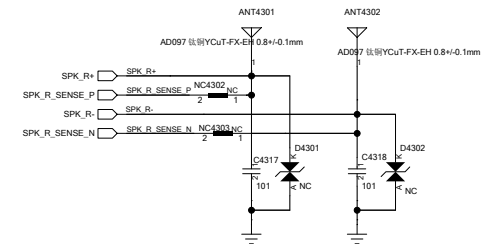
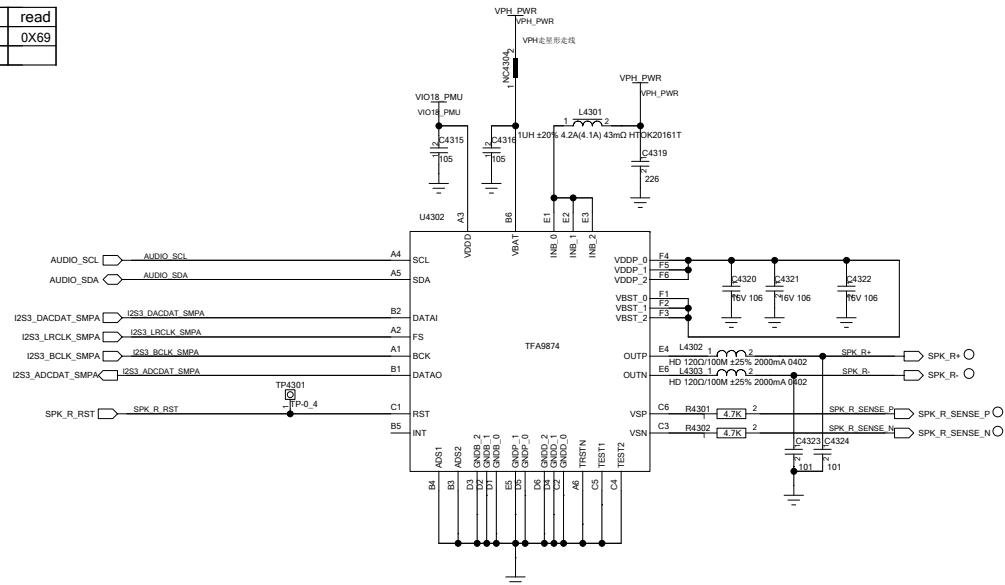
PM8008


PM8008 LDO W:0x12 R:0x13
PM8008 Other W:0x10 R:0x11



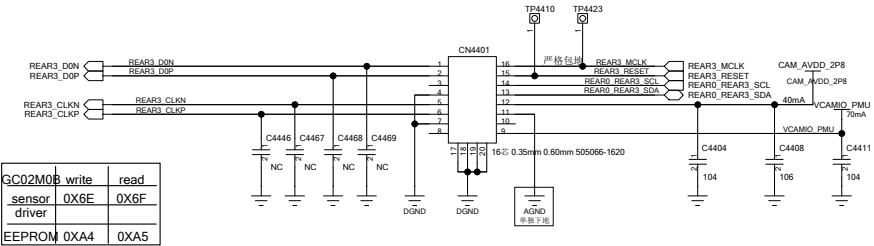
Smart PA TOP

TFA9894 TOP	write	read
I2C	0X68	0X69

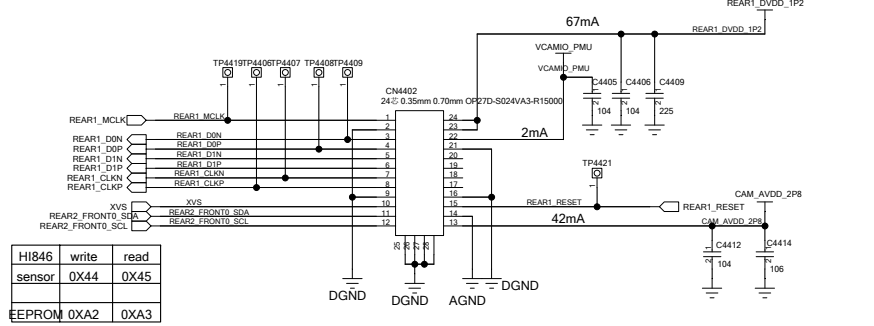


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Drawn By			Draw No: Schematic	
Checked By			Sheet of 50	Ver:
Standard By				
Approved By				

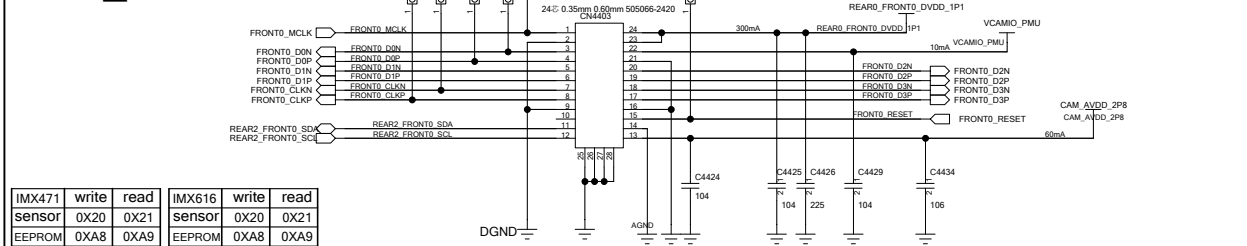
Rear_Camera3 (2M) MICRO



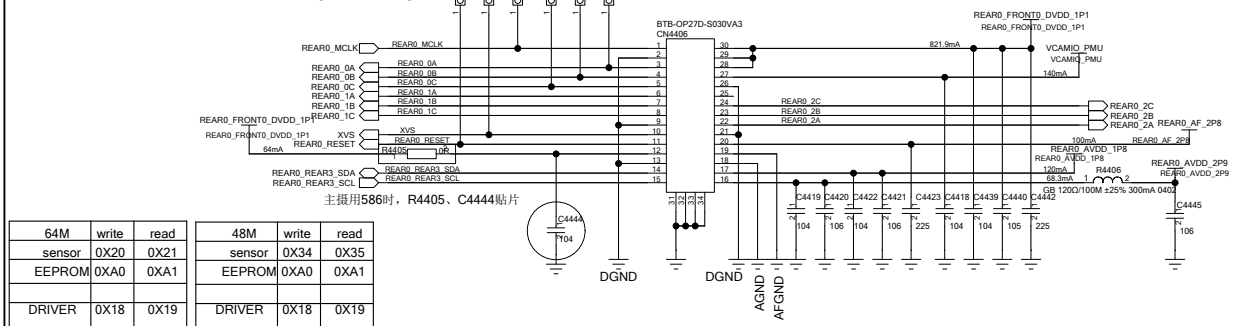
Rear_Camera1 (8M FF)



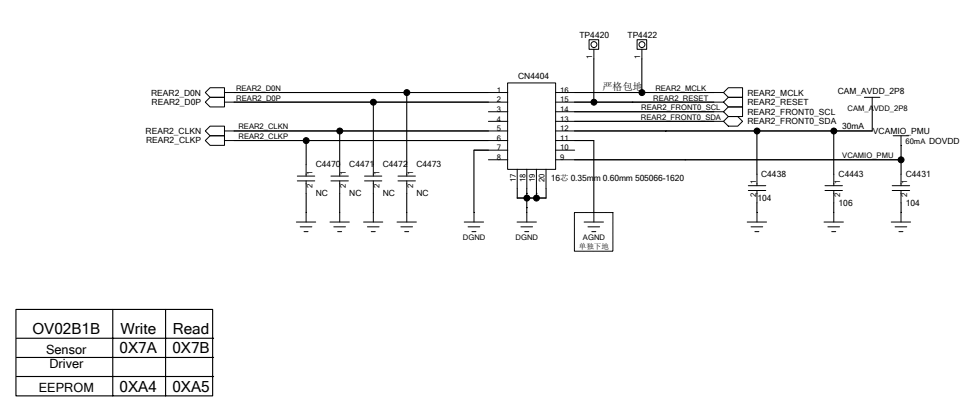
Front_Camera0 16M



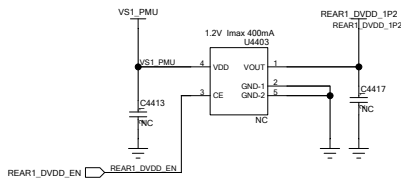
Rear_Camera0 (64M)



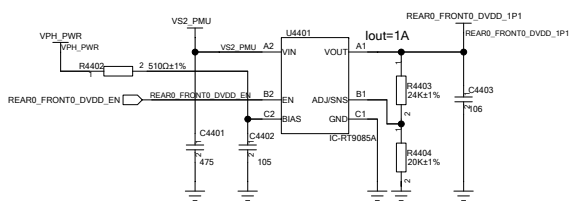
Rear_Camera2(2M MONO)



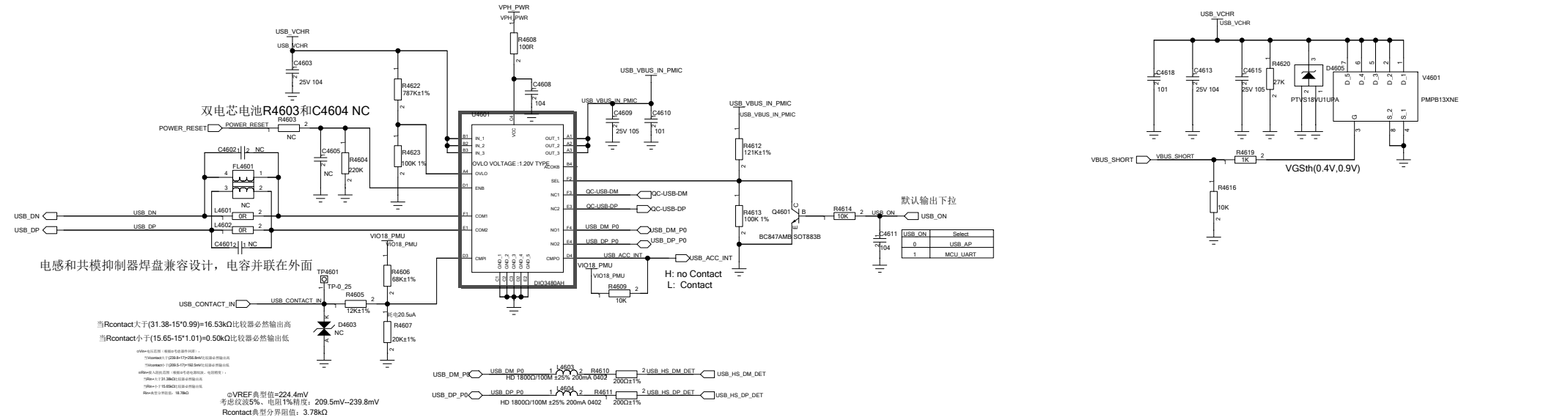
Rear_Camera1(8M)_DVDD_1P2



Main_Front_Camera_DVDD1P1



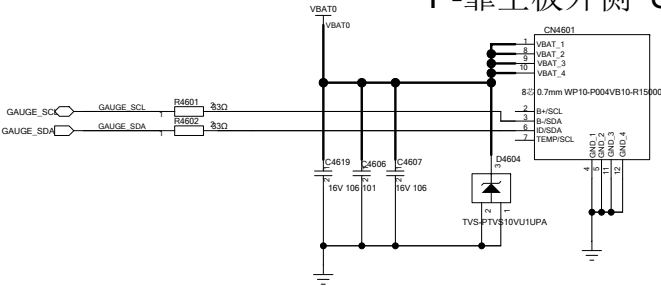
OVP SW Comparator



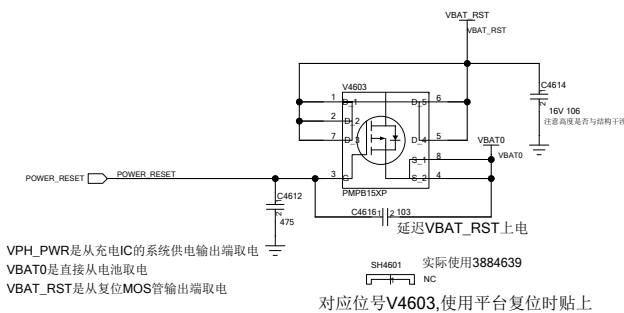
Battery Connector

Battery	Read	Write
28Z719	0XAB	0XAA

P-靠主板外侧 SCL靠电池



VBAT_MOSFET



	Sign	Date	Type:
Drawn By			Draw No: Schematic
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Standard By			
Approved By			realme

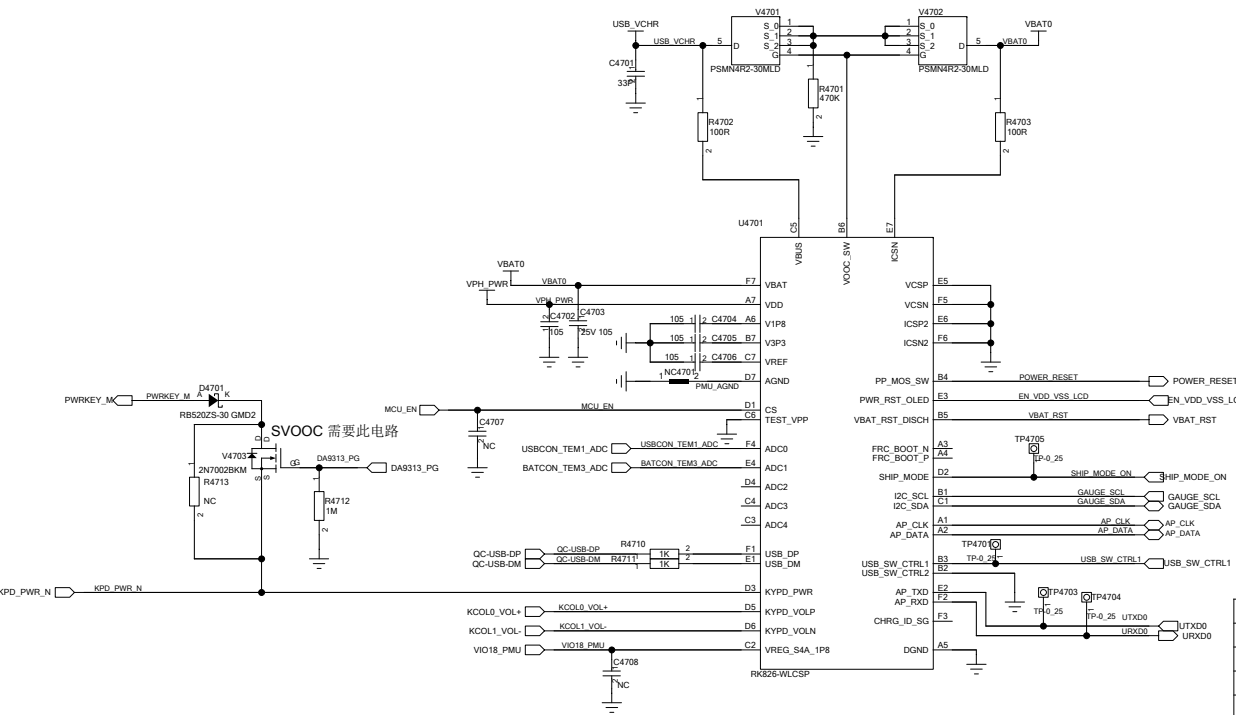
Fast Charge

MCU	Read	Write
RK826	0X15	0X14
SY6610	0X0D	0X0C
RT5125	0X1D	0X1C

ASIC	HW_ID_MCU
RK826	PU
OP10	PD
RT5123	High-Z

1. 开机时软件先设置输入pullup，读取IO状态0；再设置输入pulldown，读取IO状态1；最后设置为输入nopull
2. 上下拉电阻10K以内，不同平台需要确认所选GPIO上下拉电阻阻值，确保外部上下拉能拉高/低

Status		Control Pins	
USB_DP	USB_DM	SW_CTRL	USB_SW_CTRL2
AP_RXD	AP_TXD	L	L
MCU_RXD	MCU_TXD	H	L
CHRG_ID_SG		L	H



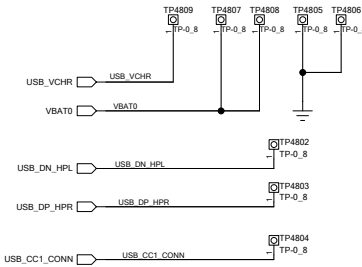
Sign	Date	Type
Drawn By		Draw No: Schematic
Checked By		Sheet of 50 Ver:
Standard By		
Approved By		

realme

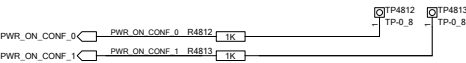
USB Switch

Test Point

JTAG TEST POINT



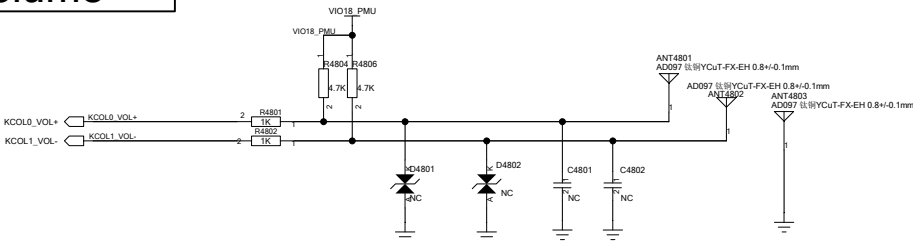
Test Confige



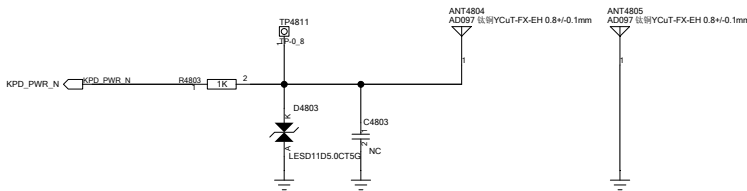
综测校准测试点

BOOT MODE		
CONF_0	CONF_1	Mode
0	0	Factory Mode
0	1	WLAN Final Test
1	0	RF mode
1	1	Ordinary Power On

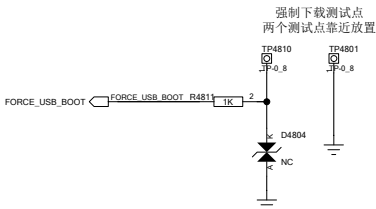
Key of Volume + -



Power Key



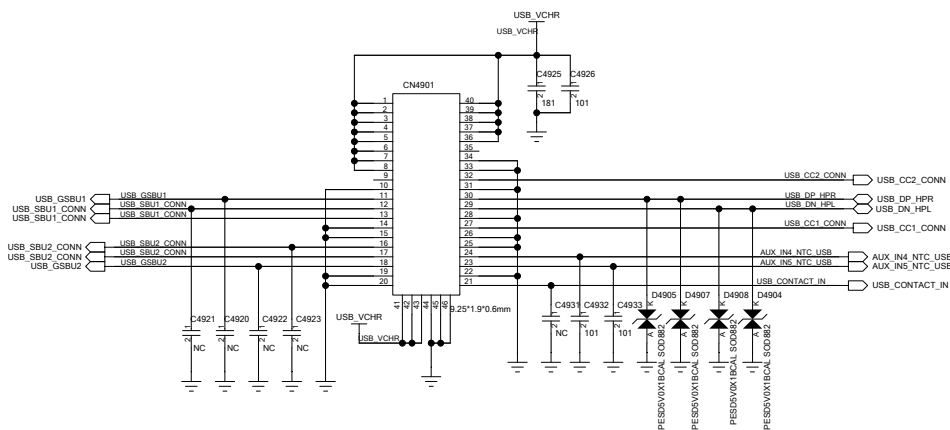
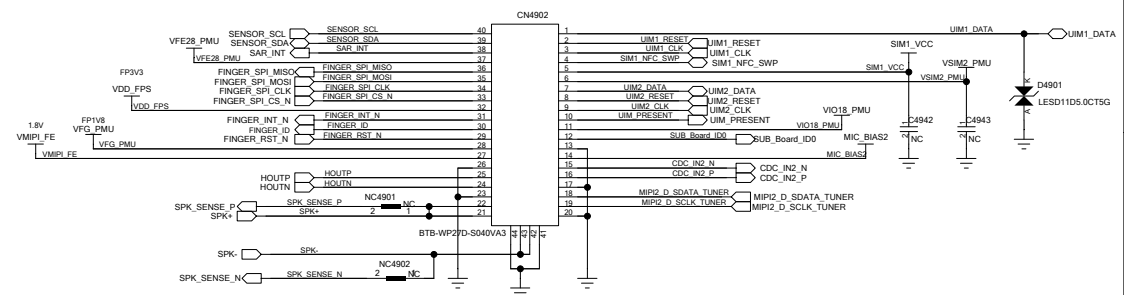
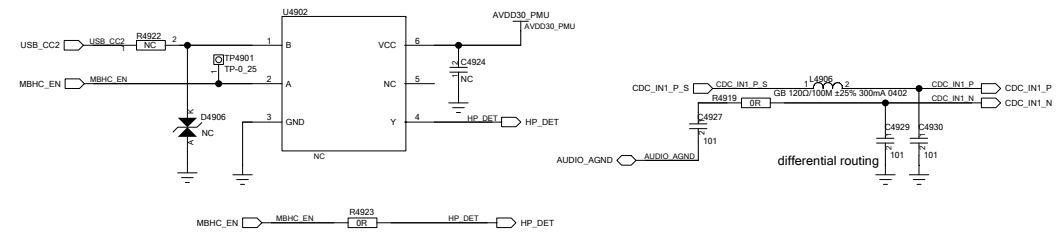
Force_USB_Boot




	Sign	Date	Type:	
Drawn By			Draw No:	Schematic
Checked By			Sheet	of 50 Ver:
Standard By				
Approved By				

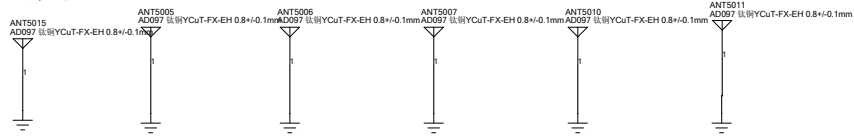
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EAR MIC



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Standard By			
Approved By			

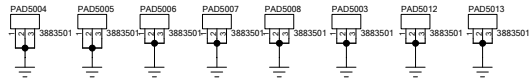
天线接地弹片



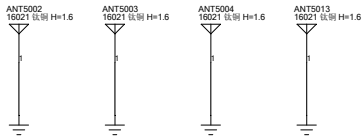
主板螺钉孔



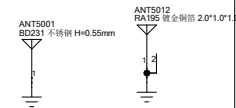
螺钉垫片



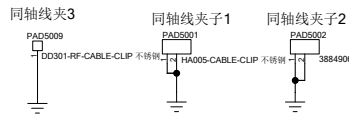
后摄装饰件接地弹片



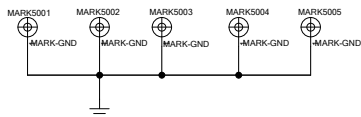
主摄支架接地弹片



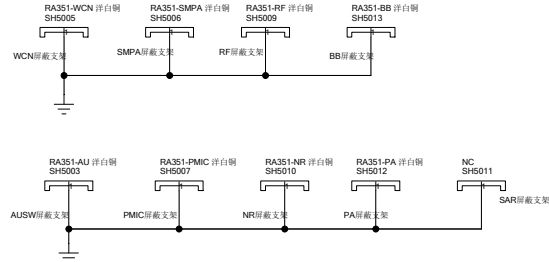
RF线夹



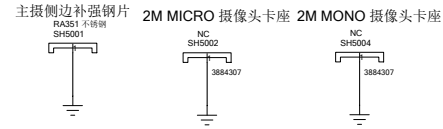
MARK POINT



屏蔽支架

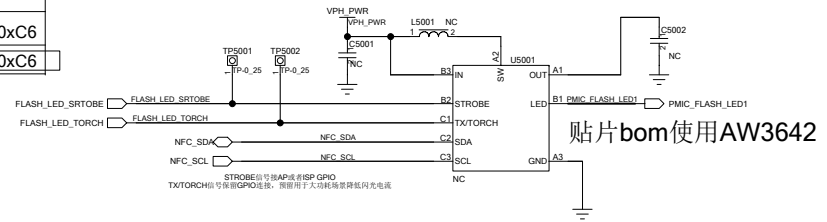


摄像头补强钢片

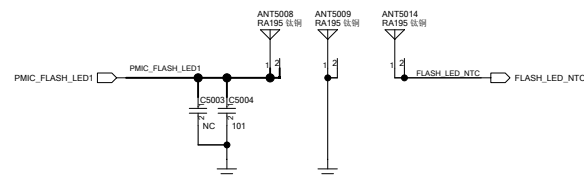


Flash LED

	Read	Write
LM3642	0xC7	0xC6
AW3642	0xC7	0xC6

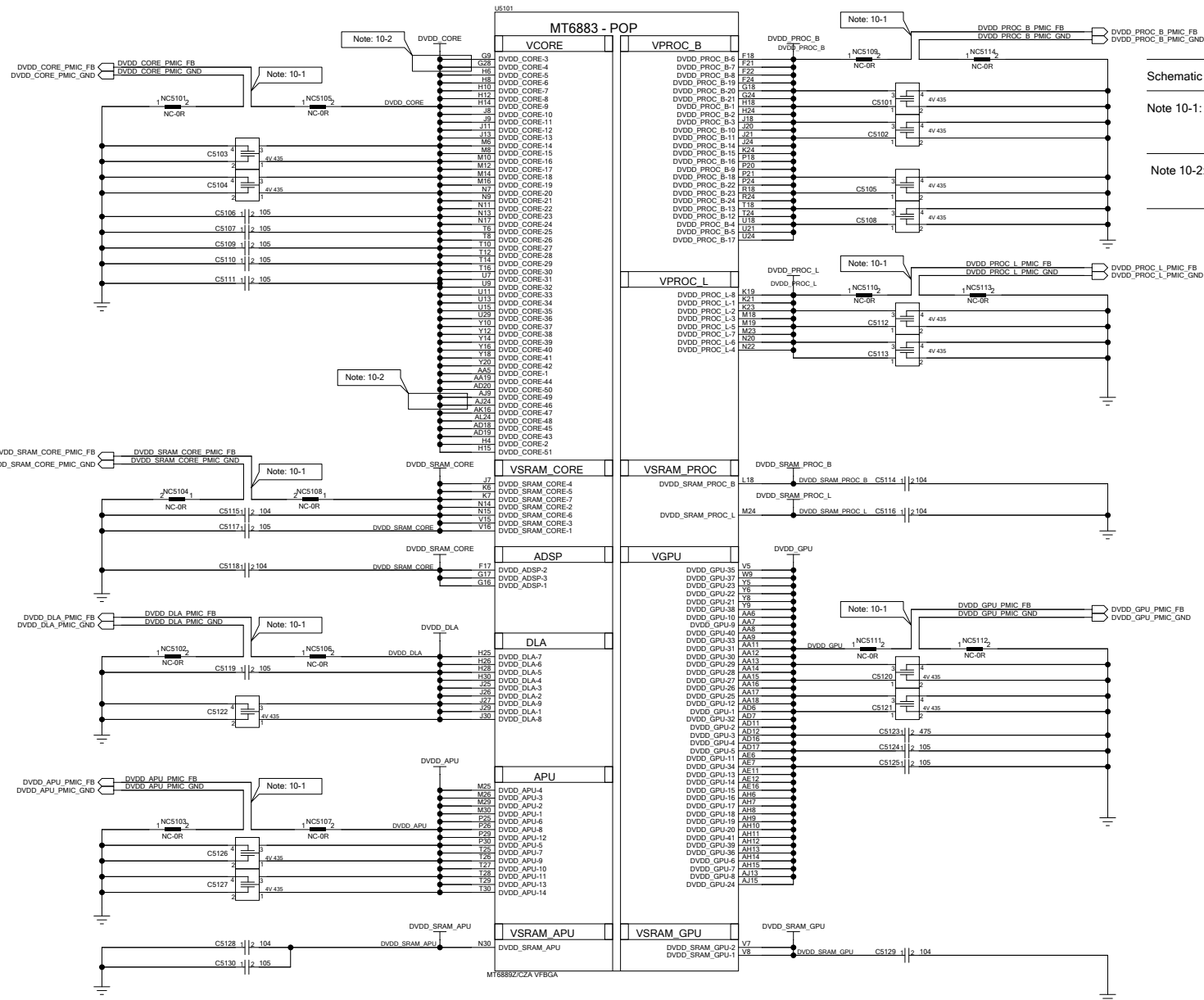


LED+



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Schematic design notice of "10_BB_POWER_PDN" page.

Note 10-1: Differential pair of PMIC remote sense must be close to BB's ball.
Remote sense trace with GND shielding to PMIC (Differential)

Note 10-2: DVDD_CORE PDN cap must be under BB's ball
G28/AJ24/AJ9/G9

	Sign	Date	Type:
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Approved By			

A

B

C

D

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F

A

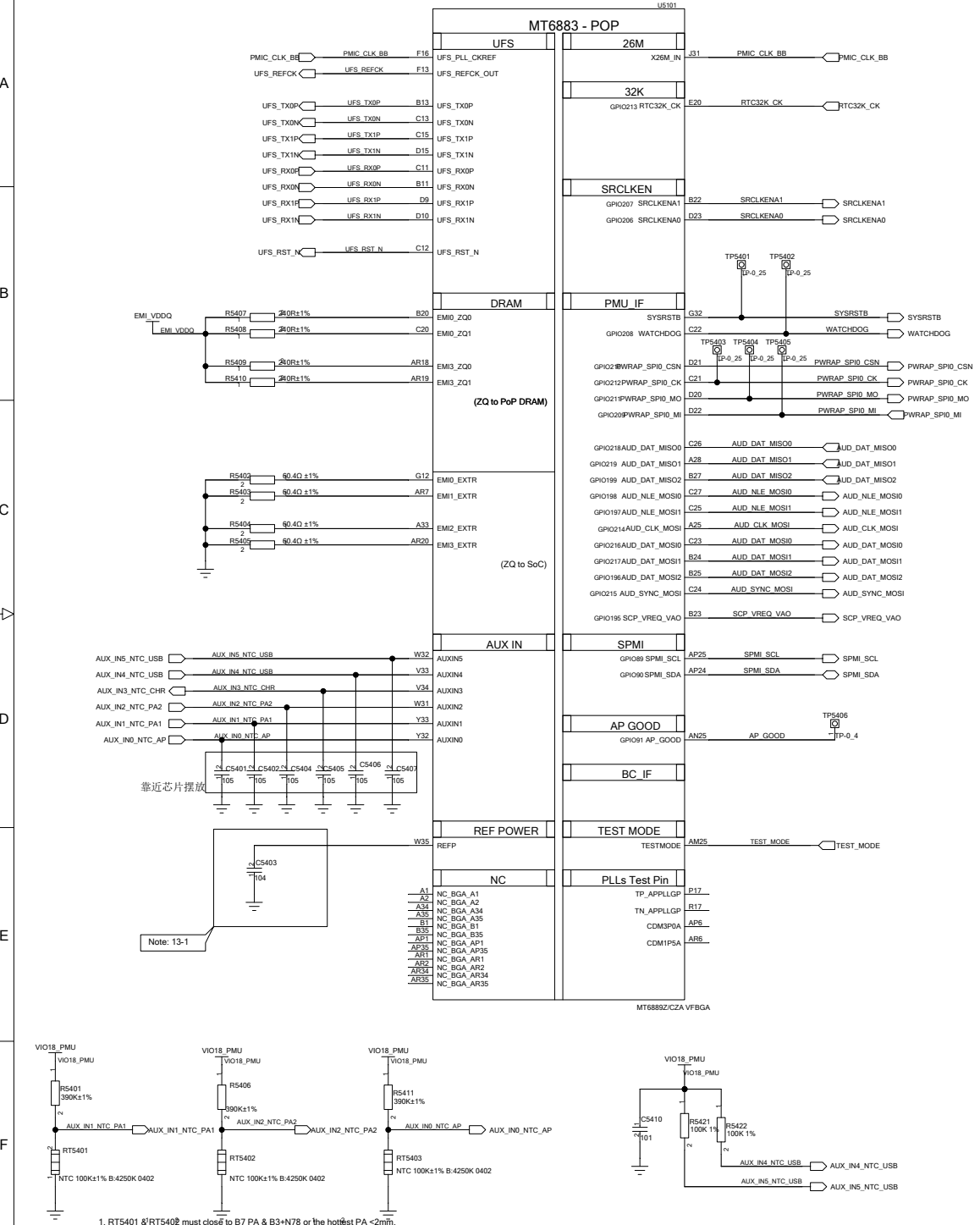
B

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Schematic design notice:

Note 13-1: The load cap. have to be placed as close to REFP ball as possible.

Note 13-2: "PWRAP_SPI0_CSN" and "AUD_DAT_MOS0" pin features in trapping pin to enable JTAG.

PWRAP_SPI0_CSN	AUD_DAT_MOS0	AP JTAG	IO JTAG
H (Default)	L (Default)	N/A	N/A
H	H (by external PU)	SPI0_CSB, SPI0_CLK, SPI0_MO, SPI0_MI, DMIC_CLK	N/A
L (by external PD)	L	SPI0_CSB, SPI0_CLK, SPI0_MO, SPI0_MI, DMIC_CLK	TDM_LRCK/TDM_BCK/TDM_MCK/TDM_DATA0/TDM_DATA1
L (by external PD)	H (by external PU)	MSDC1_CLK, MSDC1_CMD, MSDC1_DAT0, MSDC1_DAT1, MSDC1_DAT2	N/A

Note 13-3: "AUD_NLE_MOSI1" features in trapping pin to enable JTAG over USB 2.0 port.

AUD_NLE_MOSI1	USB JTAG
L (Default)	USB 2.0
H (by external PU)	DP/DM output JTAG

Note 13-4: "AUD_NLE_MOSI0" is trapping pin to select UFS booting only or eMMC booting only.

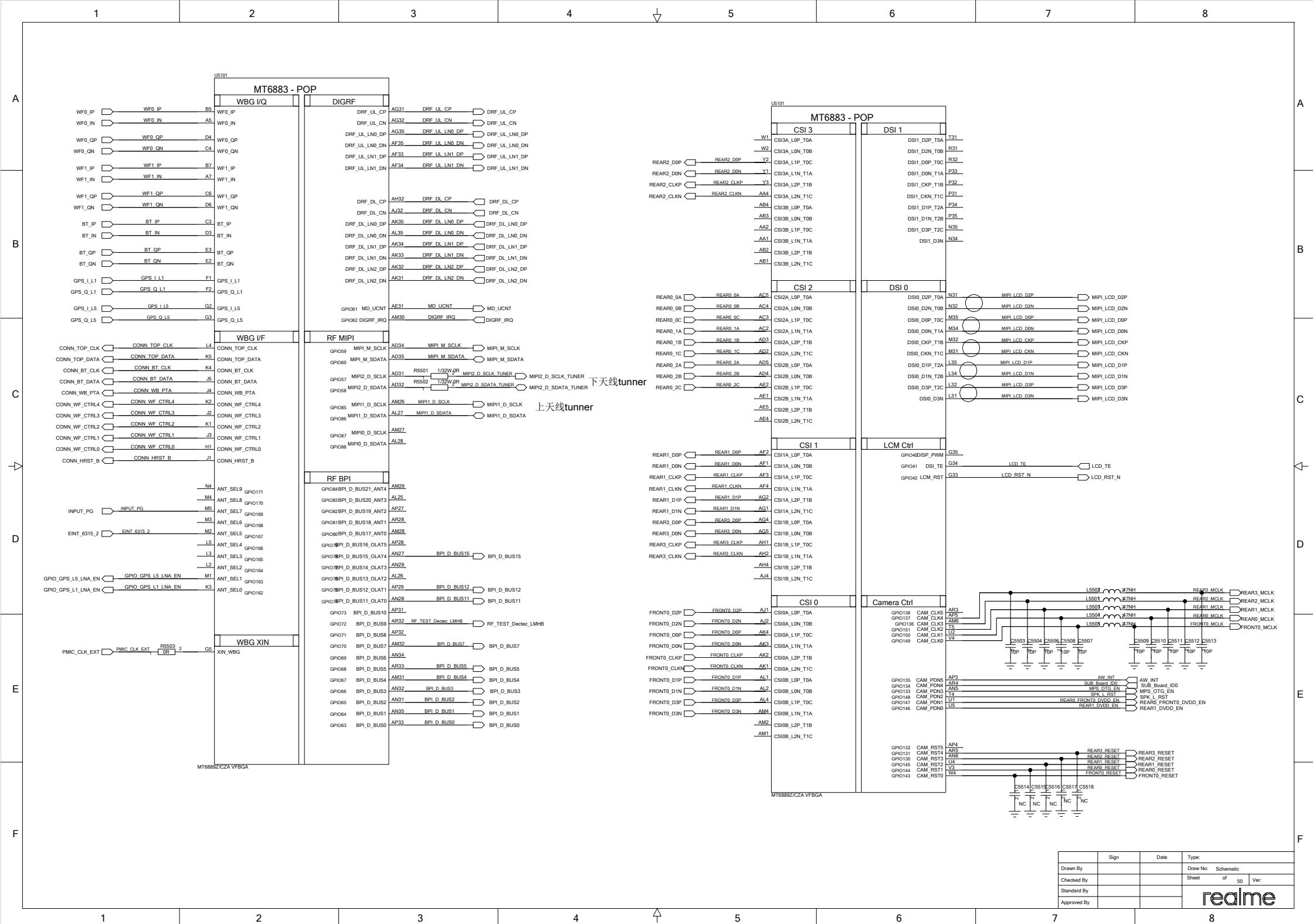
AUD_NLE_MOSI0	Storage Booting
L (Default)	UFS only
H (by external PU)	eMMC only

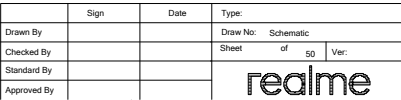
Note 13-5: "AUD_DAT_MISO1" is trapping pin to select VEMC Voltage.

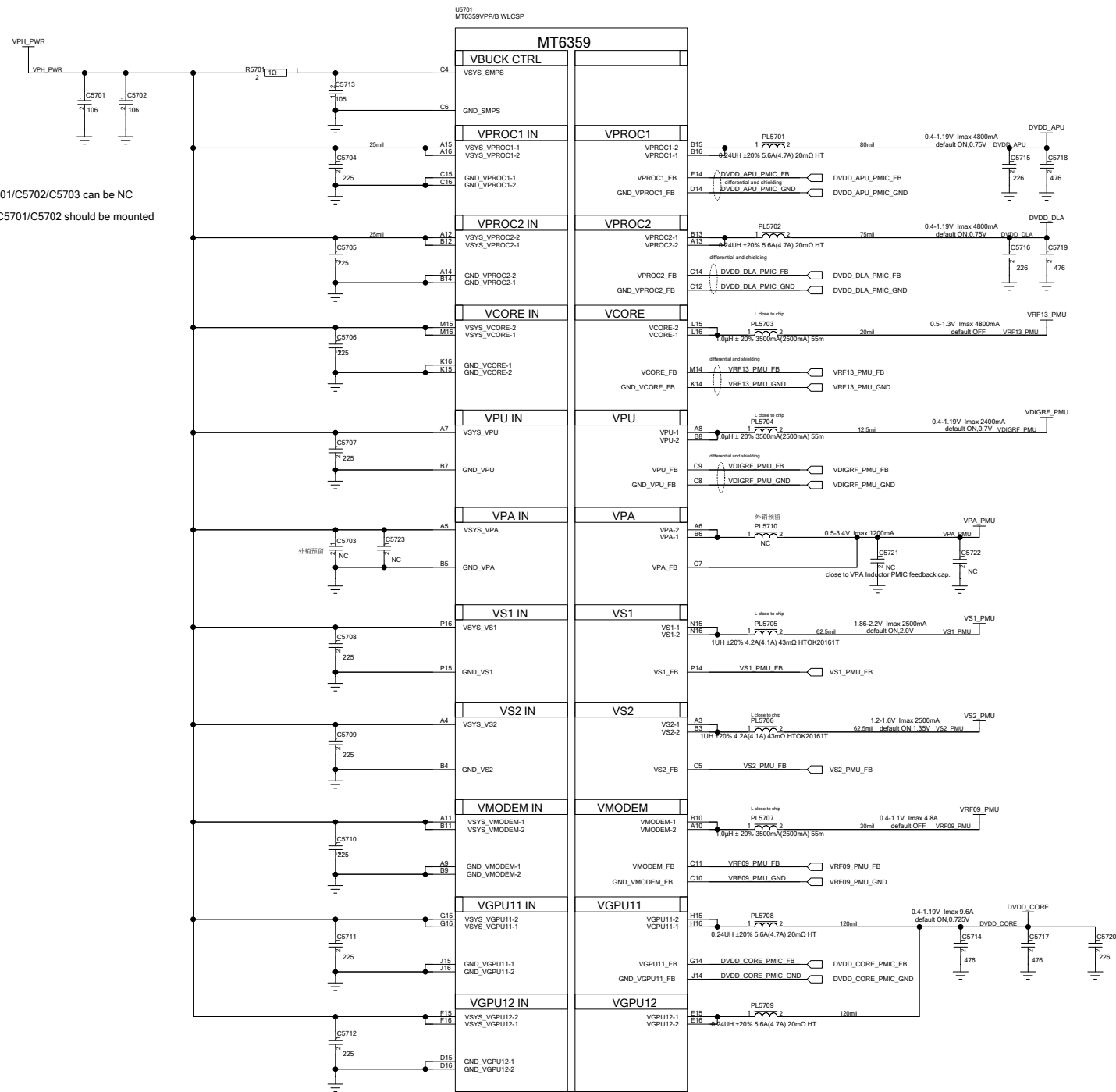
AUD_DAT_MISO1	VEMC Voltage
L (Default)	VEMC=3.0V
H (by external PU)	VEMC=2.5V


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Approved By			

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	Sign	Date	Type:
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Standard By			
Approved By			

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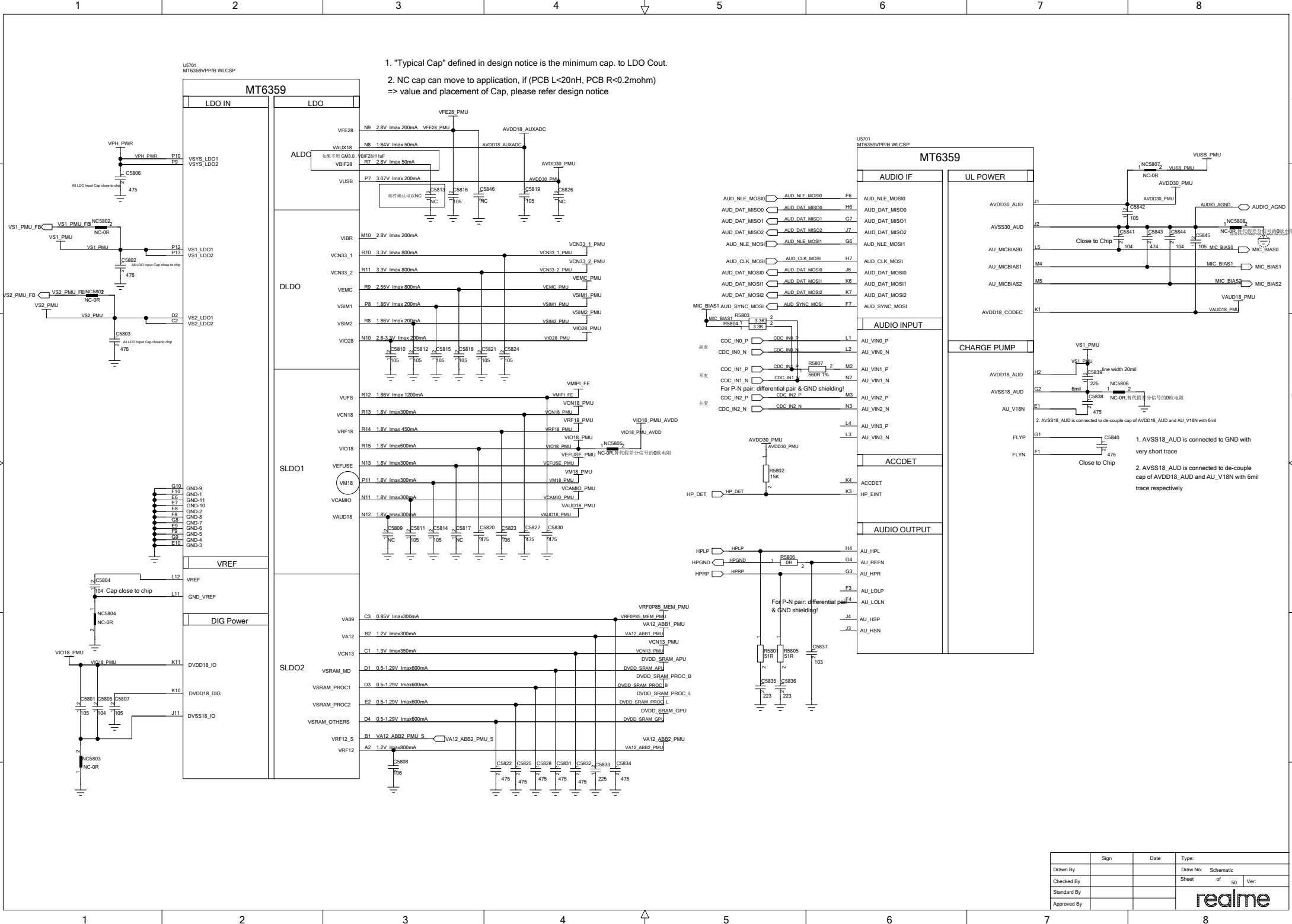
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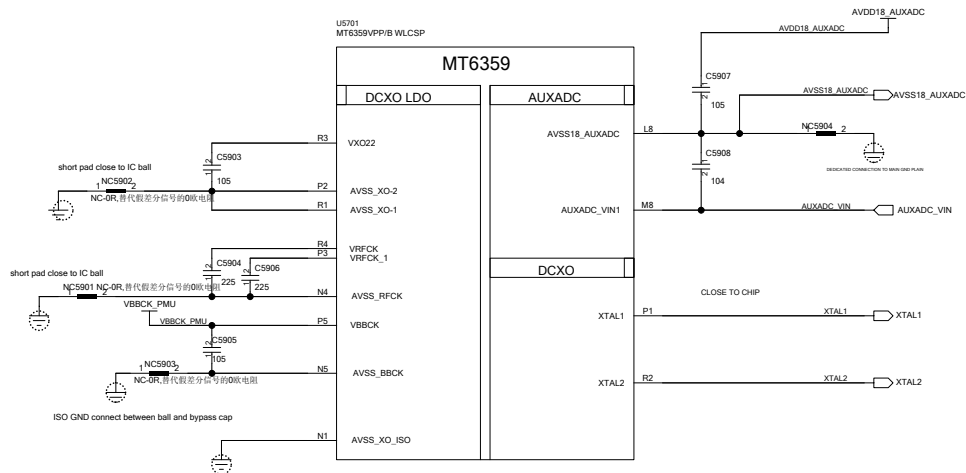


1. "Typical Cap" defined in design notice is the minimum cap. to LDO Cout.
2. NC cap can move to application, if (PCB L<20nH, PCB R<0.2mohm)
=> value and placement of Cap, please refer design notice

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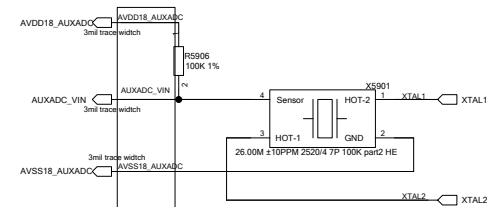
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A

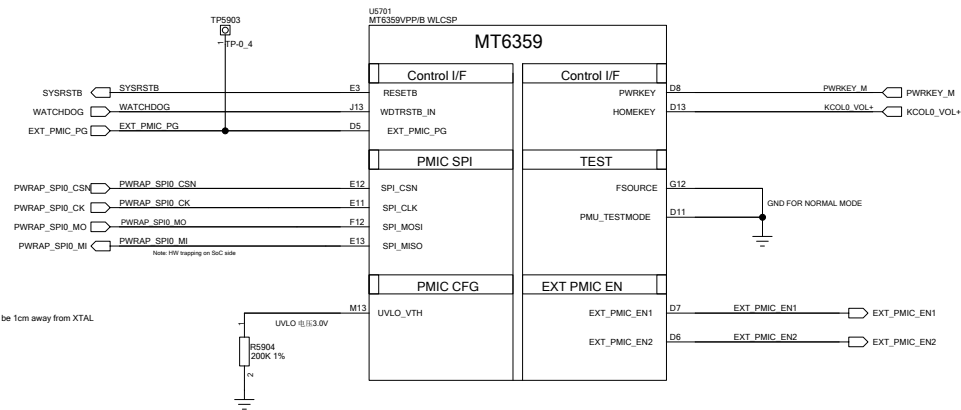


B

Route AVDD18_AUXADC/AUXADC_VIN (3 mil each) with well GND shielding

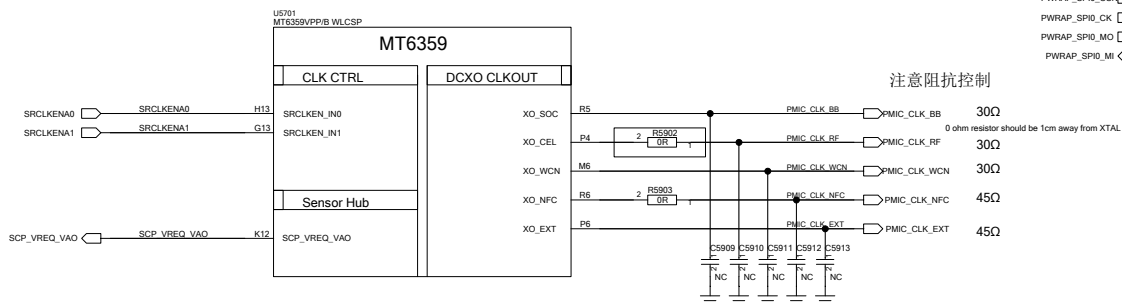


C

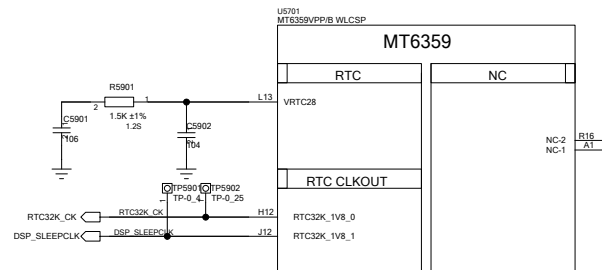


D

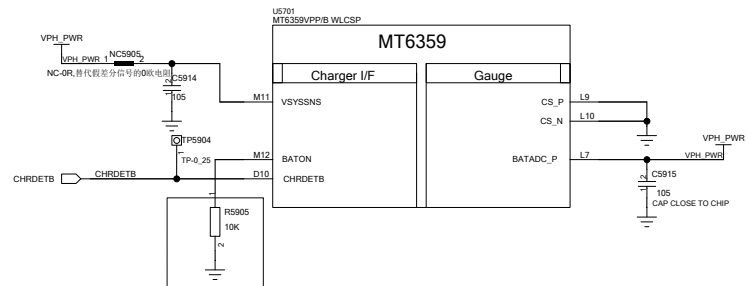
注意阻抗控制



E



F

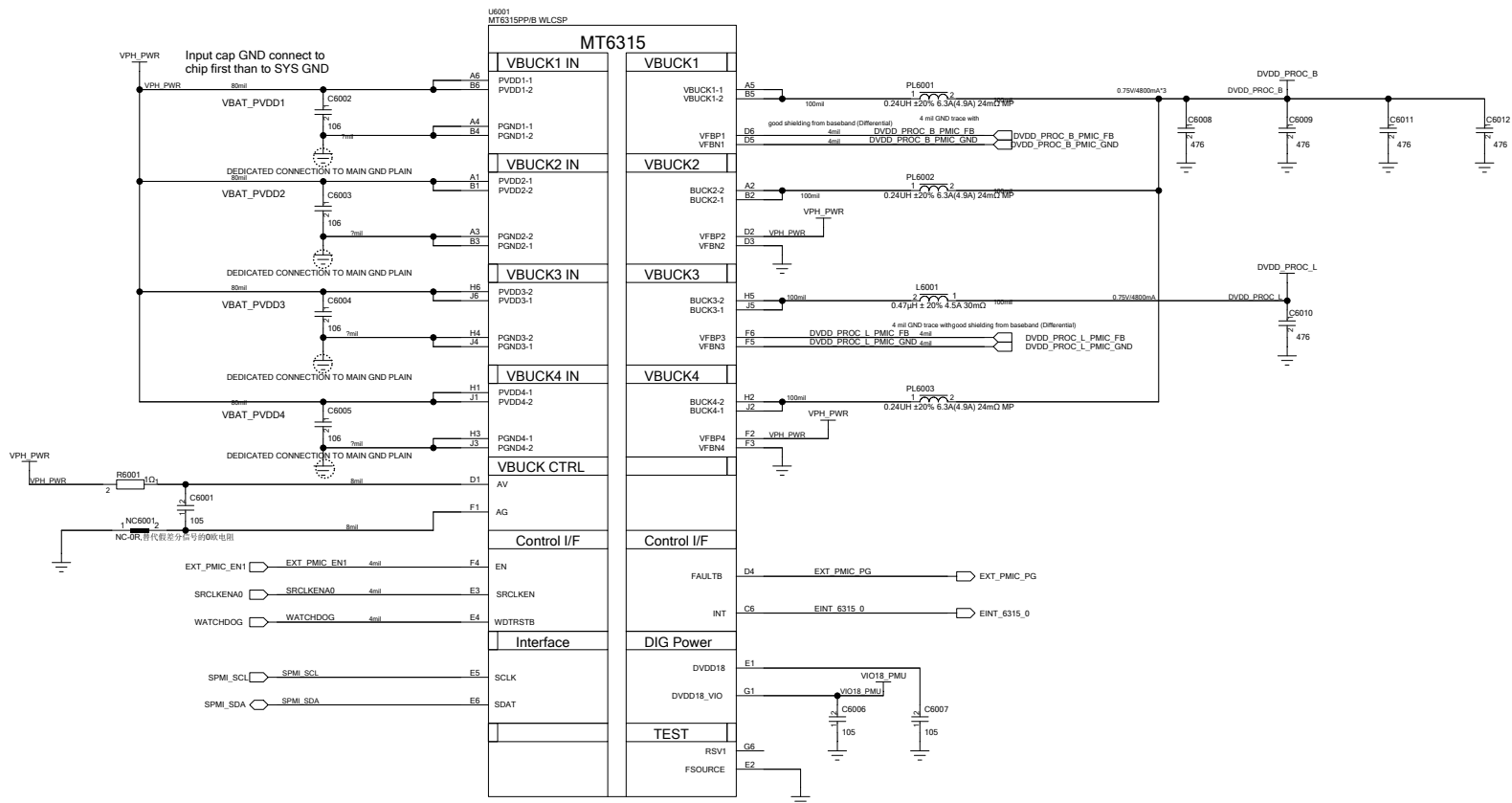


不用平台电池温度检测, 此pin面接10K下拉电阻到地

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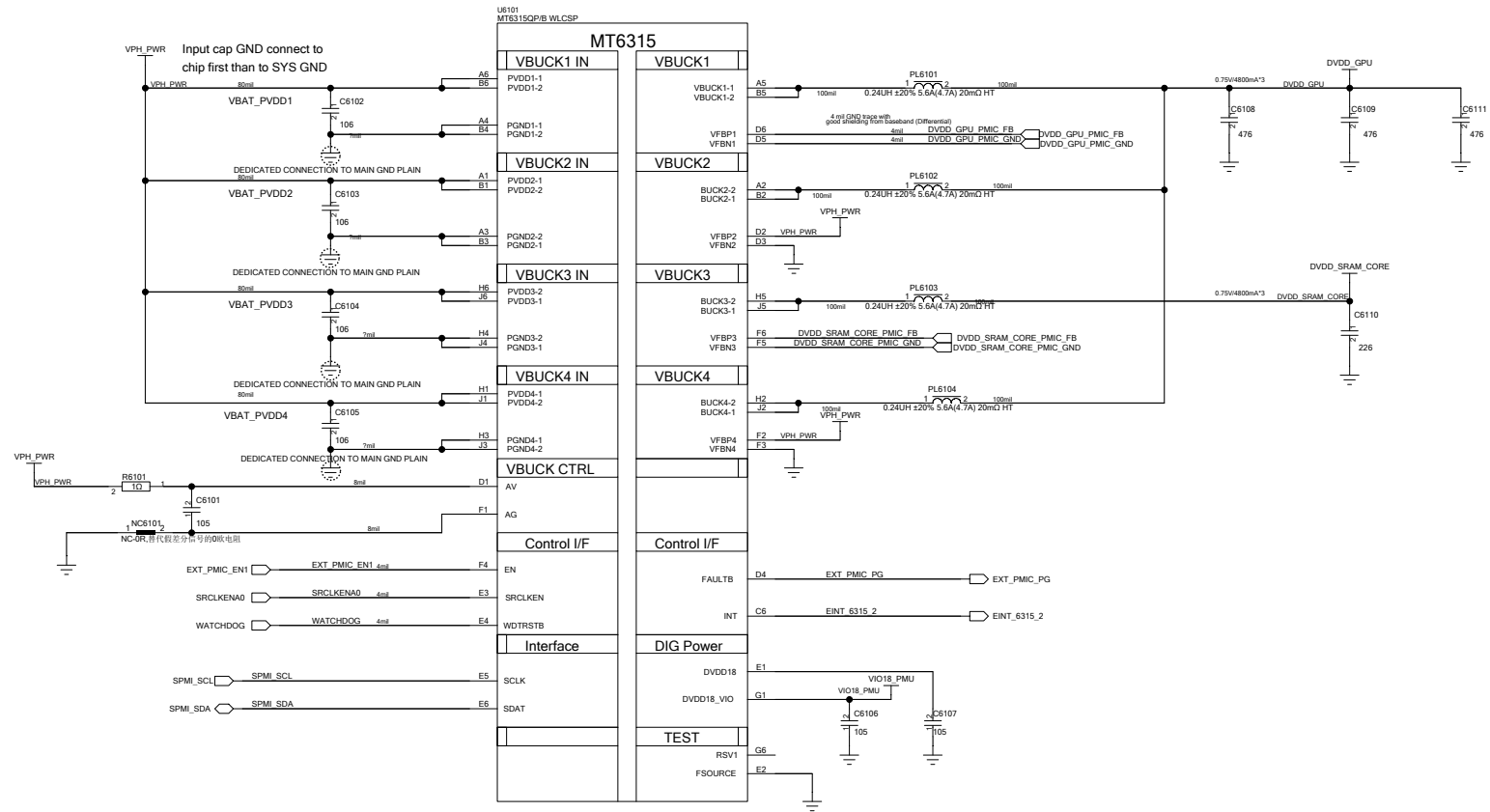
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MT6315 4-Phase Buck (CPU)



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Approved By			

MT6315 4-Phase Buck (GPU)



	Sign	Date	Type
Drawn By			Draw No: Schematic
Checked By			Sheet of 50 Ver:
Standard By			
Approved By			

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A

B

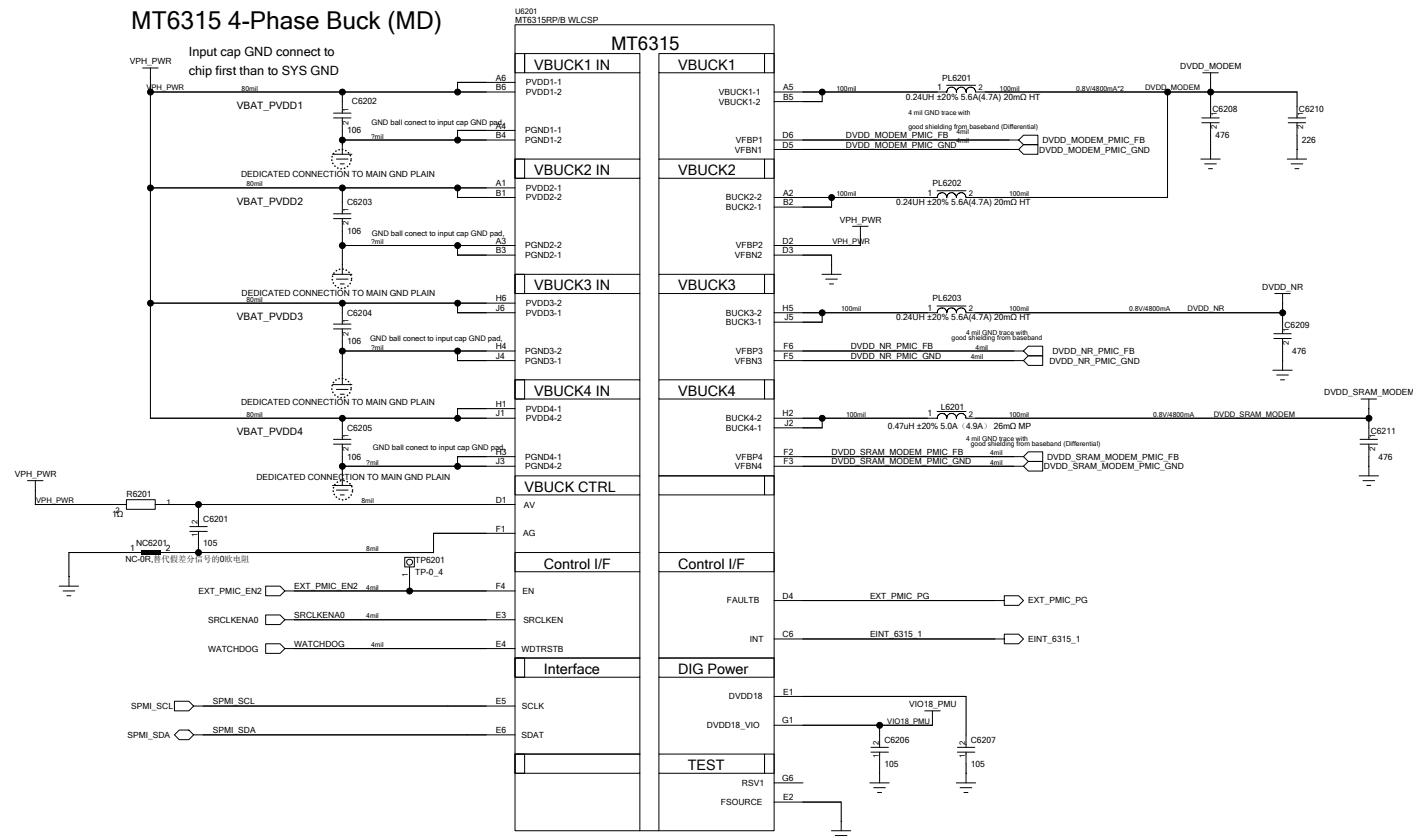
C


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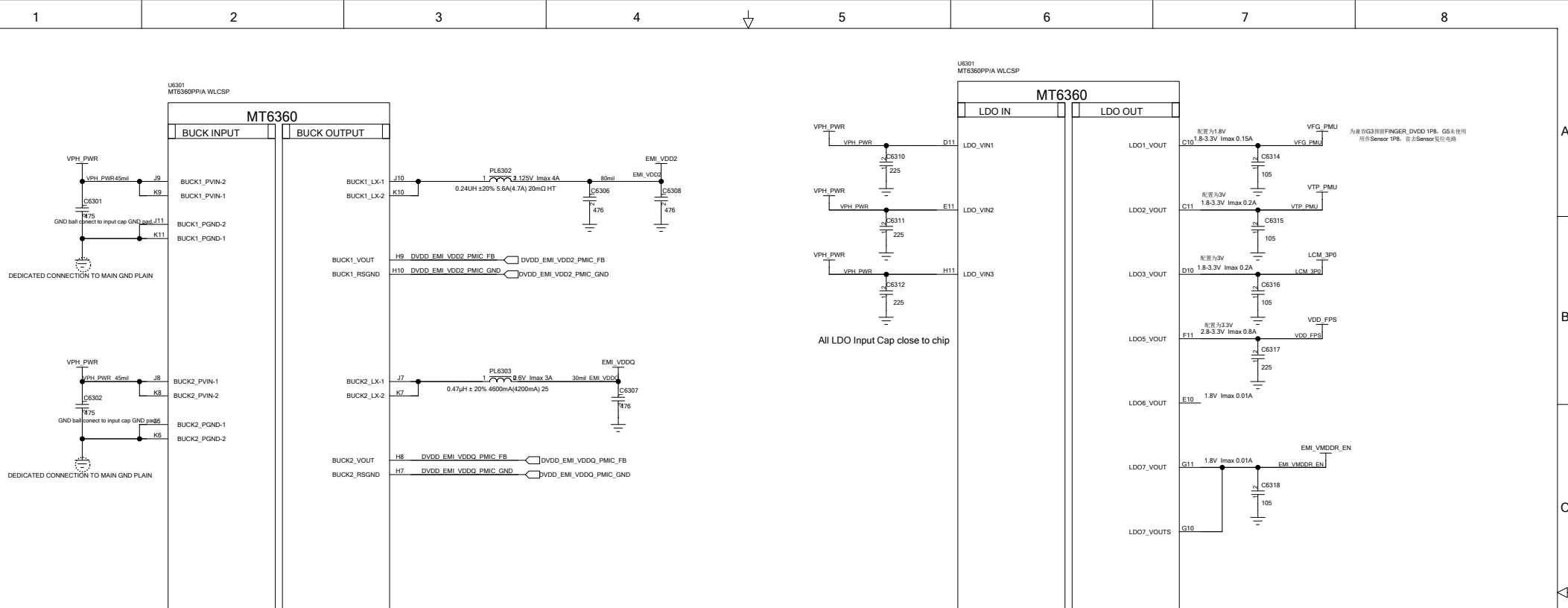
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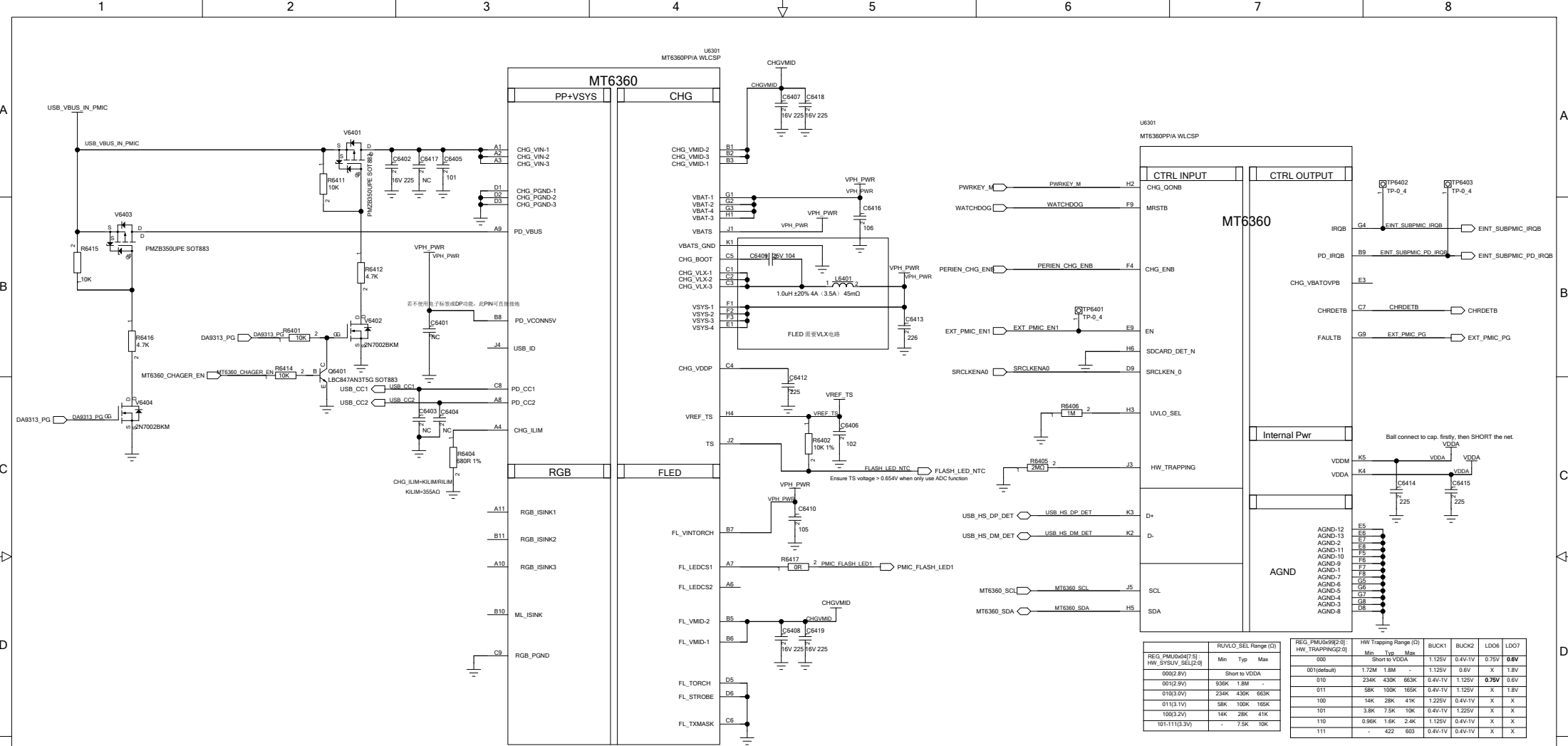
F

MT6315 4-Phase Buck (MD)

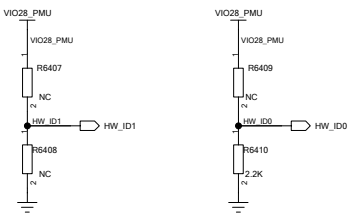


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Board Version and HW ID



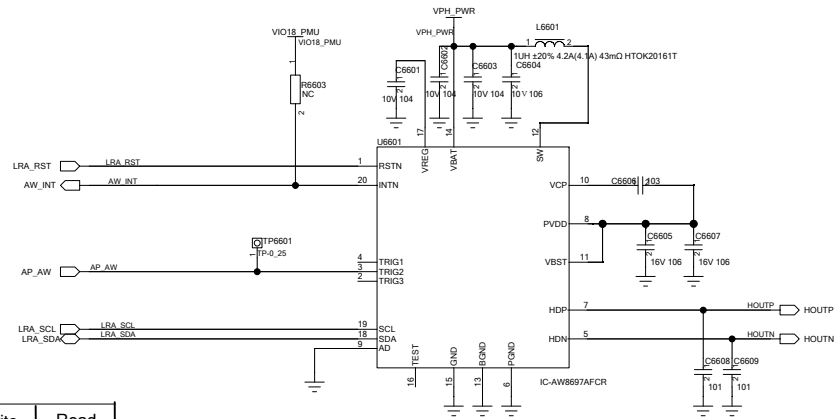
版本名	HW_ID1	HW_ID0
EVB1		
T0-1	HIGH-Z	HIGH-Z
EVT1	HIGH-Z	PU
EVT2	PU	PU
DVT1	PU	PD
DVT2	PD	PD
PVT1	PD	HIGH-Z
MP1	HIGH-Z	PD

1. 开机时软件先设置输入pullup，读取IO状态0；再设置输入pulldown，读取IO状态1；最后设置为输入nopull
2. 上下拉电阻10K以内，不同平台需要确认所选GPIO上下拉电阻阻值，确保外部上下拉能拉高/低

	Sign	Date	Type:
Drawn By			Draw No: Schematic
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LRA Driver

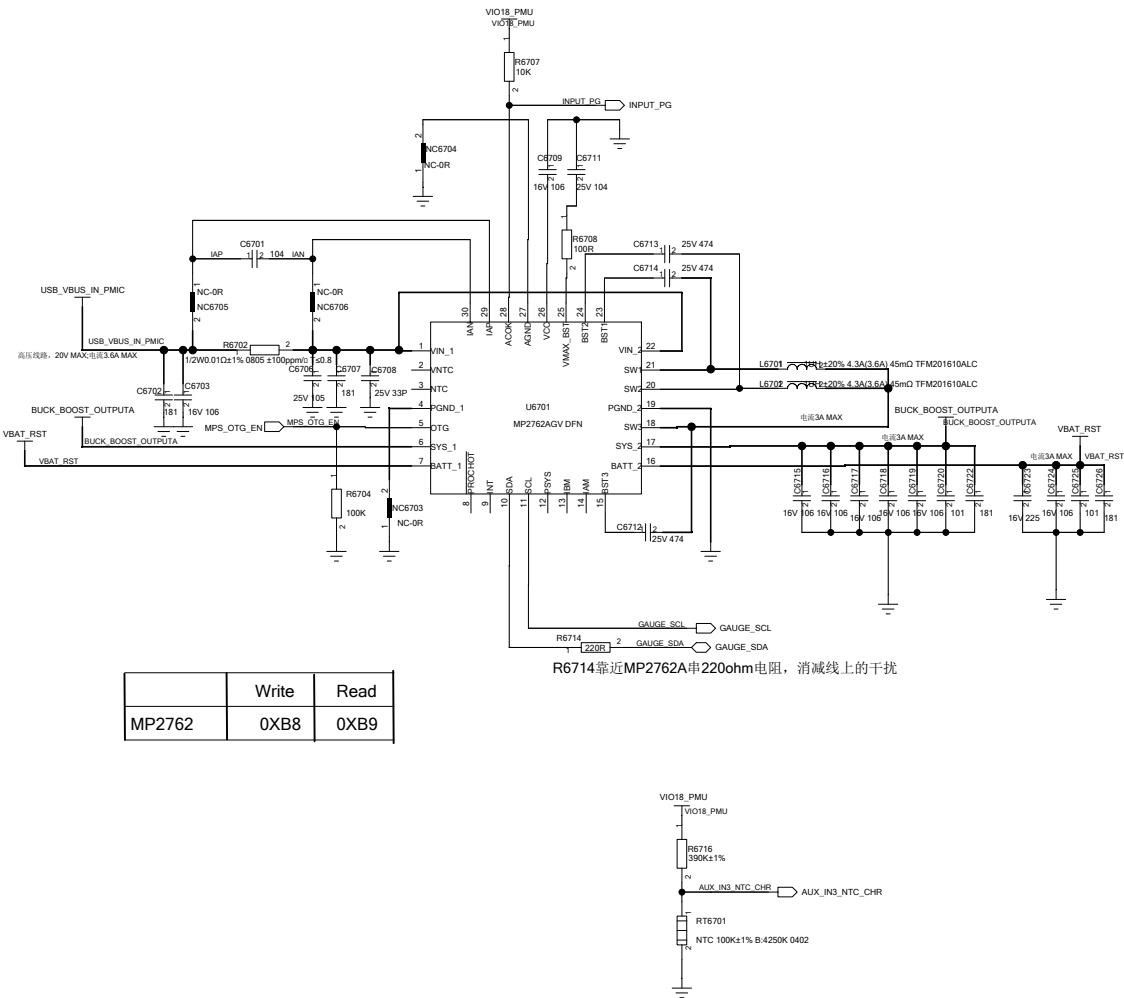


AD	Write	Read
0	0xB4	0XB5
1	0xB6	0XB7

AD=0 0X5A(front 7 bit)
AD=1 0X5B(front 7 bit)

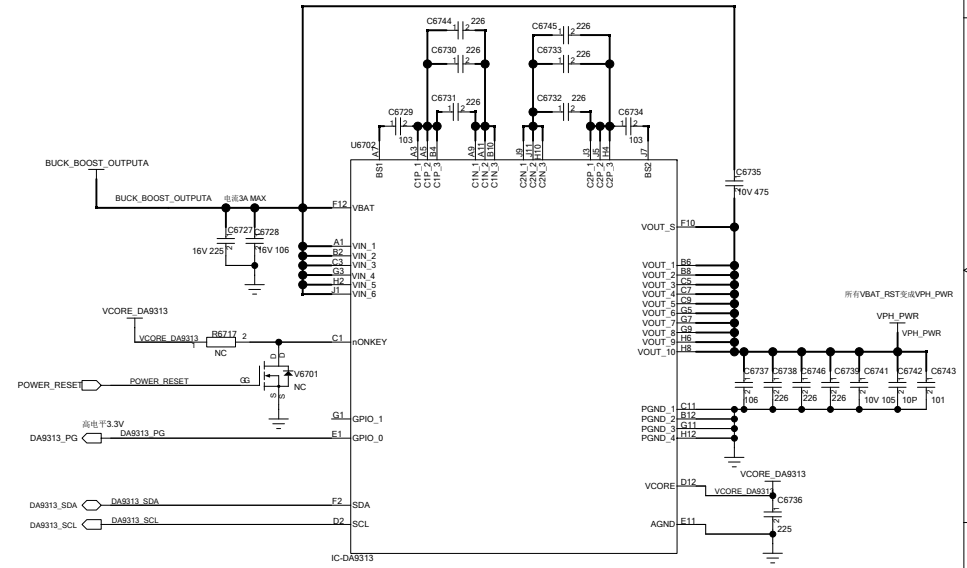
BUCK-BOOST CHARGE

兼容单电芯时全部NC

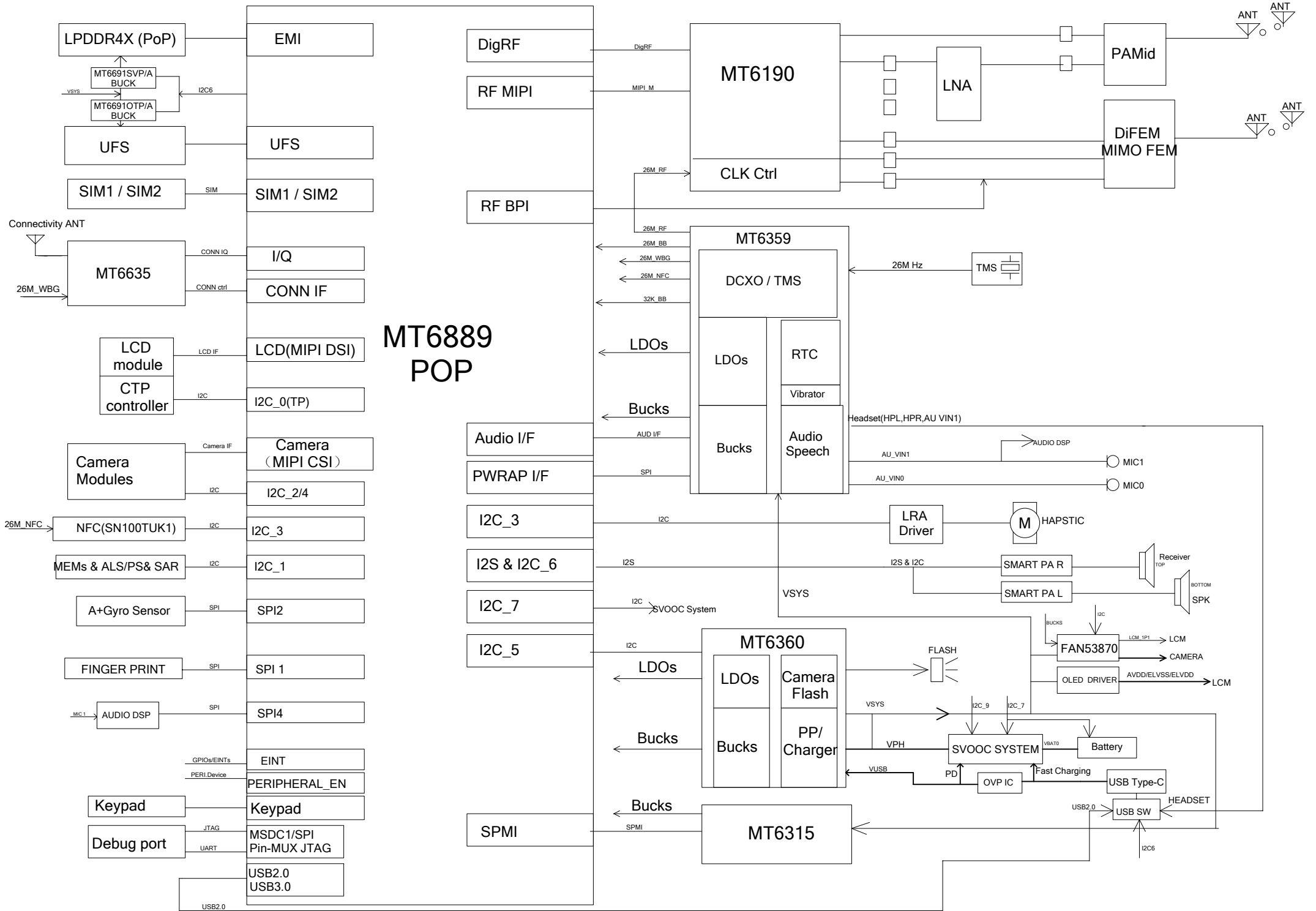


CHARGE PUMP

兼容单电芯时全部NC



MT6889 POP



I2C	AP, SCP, SSPM	Function	I2C/I3C Spec.	I2C Slave Address
I2C-0	AP	CTP	I2C	SDC I2C address: Write:0x90, Read:0x91BOE I2C address:Write:0x96 Read:0x97
I2C-1 (I3C)	SCP		I3C	
		ALPS Sensor		STK33502 I2C address: Write:0x8C, Read:0x8D
		M Sensor		AK09918 I2C address: Write: 0x18, Read: 0x19
		SAR Sensor		SX9324 I2C address: Write: 0x50, Read: 0x51
I2C-2 (I3C)	AP	Rear Camera0 64M	I3C	MAIN CAM 64 Write Read MAIN CAM 48 write read MICRO CAM write read IMX682 SENSOR 0X20 0X21 sensor 0X34 0X35 SENSOR 0X6E 0X6F EEPROM 0XA0 0XA1 EEPROM 0XA0 0XA1 EEPROM 0XA4 0XA5 AF DRIVER 0X18 0X19 DRIVER 0X18 0X19
		Rear Camera 3 2M		
I2C-3	AP	NFC	I2C	NFC I2C address: 0X08 (Write:0x10, Read:0x11)
		LRA Driver		AW8697AFRCR I2C Address: 0x5A (Write:0xB4, Read:0xB5) LM3642 I2C Address: 0x63 (Write:0xC6, Read:0xC7)
I2C-4 (I3C)	AP	Rear Camera 1 8M FF	I3C	FRONT 16M write read FRONT 32M write read MONO CAM 2Mwrite read WIDE CAM write read SENSOR 0X20 0X21 SENSOR 0X20 0X21 SENSOR 0x7A 0x7B SENSOR 0x44 0x45 EEPROM 0XA8 0XA9 EEPROM 0XA8 0XA9 EEPROM 0xA4 0xA5 EEPROM 0xA2 0xA3
		Front Camera0 16M		
		Rear Camera 2 13M LF		
I2C-5	AP	MT6360	I2C	MT6360 PD's I2C address: 0X4E (Write:0X9C, Read:0X9D) MT6360 PMU's I2C address: 0X34 (Write:0X68, Read:0X69)
I2C-6	AP	Smart PA TOP	I2C	T0P Speaker AMP I2C Address: 0x34 (Write:0x68, Read:0x69)
		Smart PA BOT		BOT Speaker AMP I2C Address: 0x35 (Write:0x6A, Read:0x6B)
		MT6691SVP/A		MT6691SVP/A I2C Address: 0x51 (Write:0xA2, Read:0xA3)
		MT6691OTP/A		MT6691OTP/A I2C Address: 0x56 (Write:0xAC, Read:0xAD)
		FSA4480		FSA4480 I2C Address: 0x42 (Write:0x84, Read:0x85)
I2C-7 (I3C)	AP	Fuel Guage	I3C	I2C Address: Write 0xAA Read 0xAB
		Fast Charger MCU		I2C Address: Write 0x0A Read 0x0B
		Charger Pump		I2C Address: Write 0xD0 Read 0xD1
I2C-8 (I3C)	AP	CAM PROWER PM8008	I3C	FAN53870 I2C address: 0X35 (Write:0x6A, Read:0x6B) PM8008 LDO W:0x12 R:0x13 PM8008 Other W:0x10 R:0x11
I2C-9 (I3C)	AP	BOOST BUCK Driver	I3C	MP2762A I2C Address: Write:0xB8, Read:0xB9)
Note : I2C Spec. : Standard mode (100 kbps) and Fast mode (400 kbps), Fast mode Plus (1 Mbps) and High-speed mode (3.4 Mbps)				

