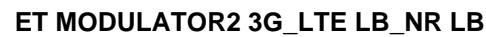
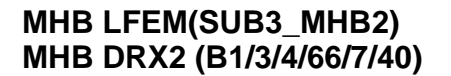
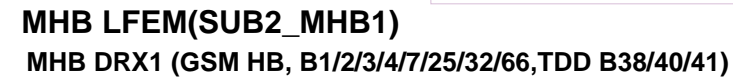
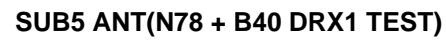
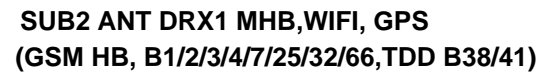


LB LFEM(SUB1_LB) LB DRX1
(GSMLB,B5/8/12/13/17/18/19/20/26/28,N8/28)



RF - ET_Modulators

RF - NR N77/78



UHB_LFEM(PRX2)

NR_LNA_VCC

N78 LPAMID

N78_1T4R

RF2 - SUB ANT

CHANGED BY:	DATE CHANGED:	TIME CHANGED:	QA CHK:	REV:	DRAWING NUMBER:	PAGE:
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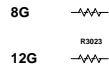
NR VLDO16 NR VLDO2 NR VLDO12 NR VLDO24 NR VLDO16

NR VLDO4 NR VLDO6

NR VLDO14 NR VLDO2

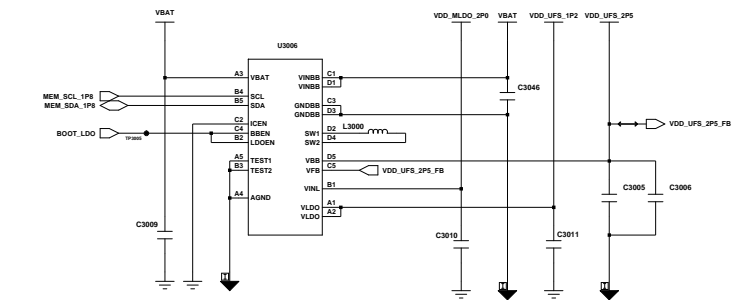
NR VLDO13 NR VLDO4

NR VLDO14 NR VLDO15 NR VLDO16



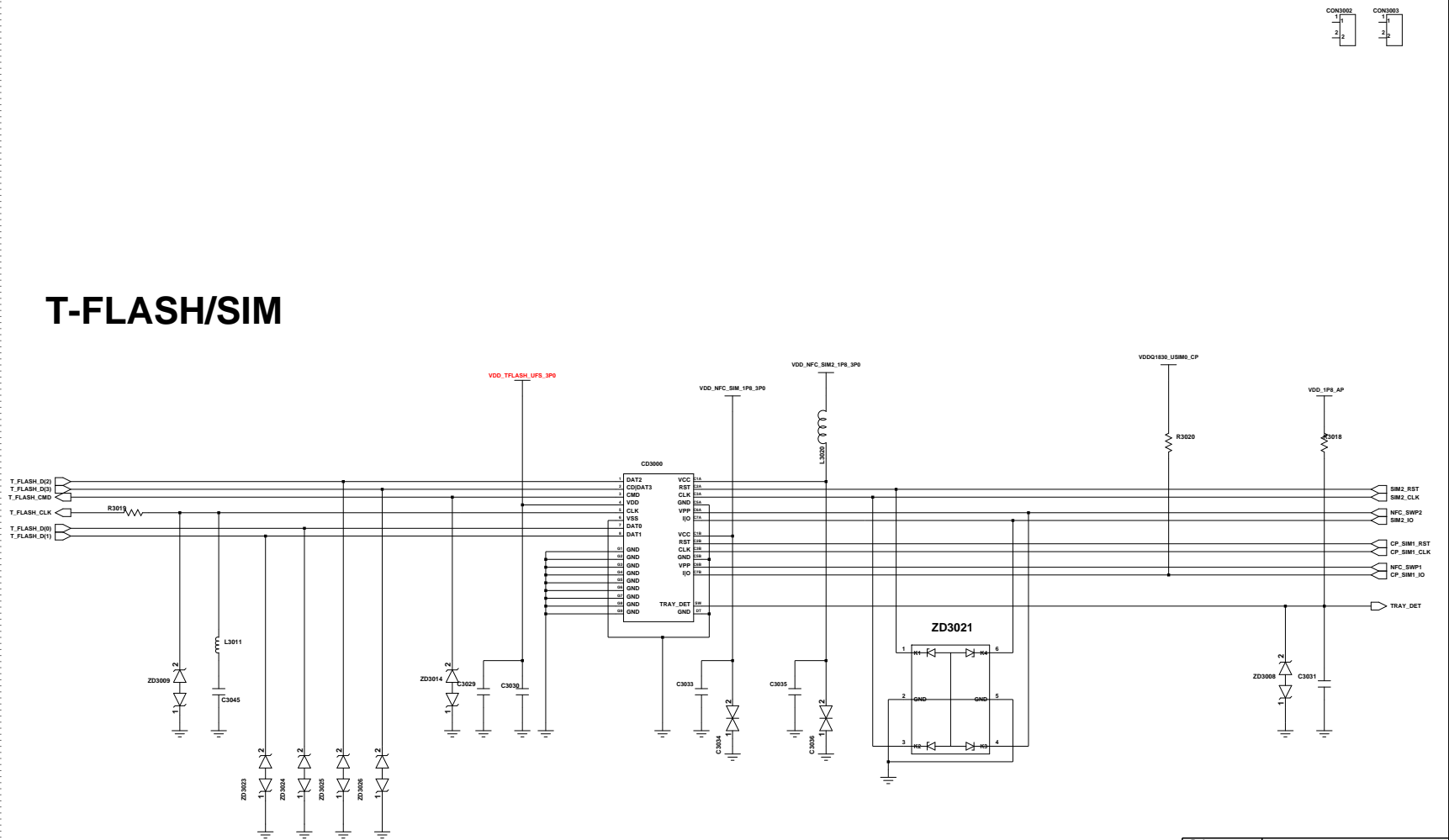
MEMORY

128GB



UFS BUCK BOOSTER

POWERKEY



T-FLASH/SIM

Changed by: user118	Date Changed:	Time Changed:	QA CHK:	REV:	Drawing Number:	Page: 3
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BT/WIFI



6 AXIS SENSOR (GYRO,ACCEL)

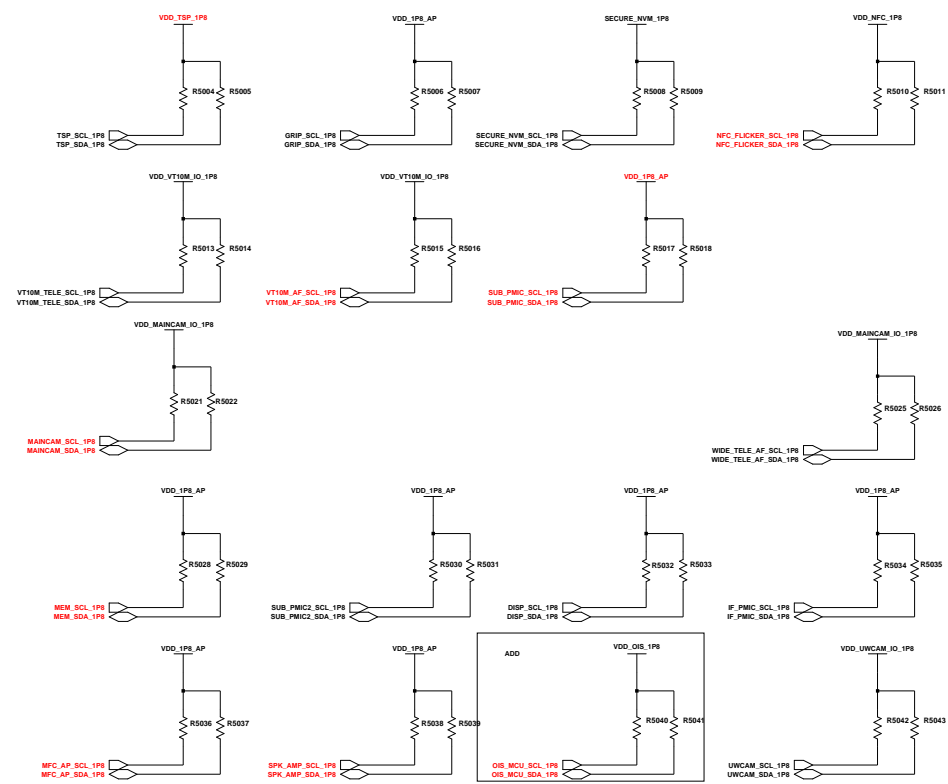
BAROMETER SENSOR



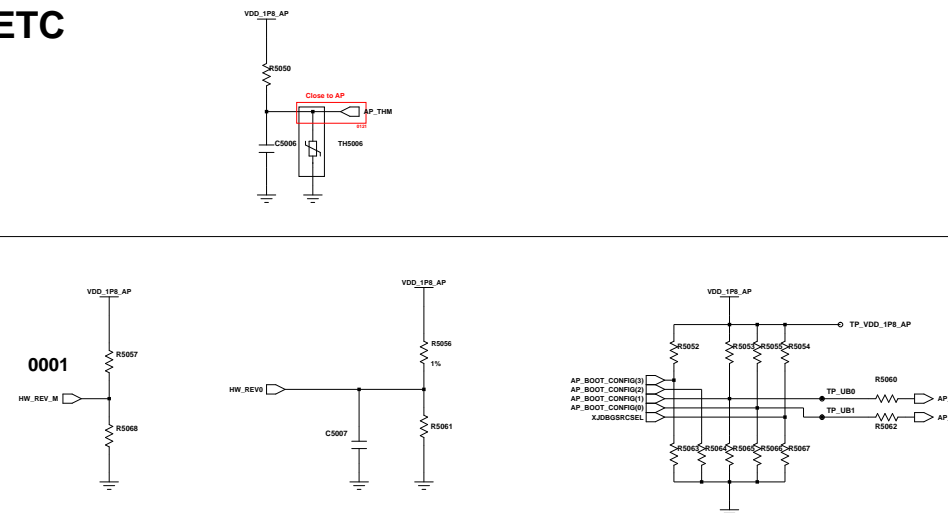
MAGNETIC SENSOR



H/W I2C

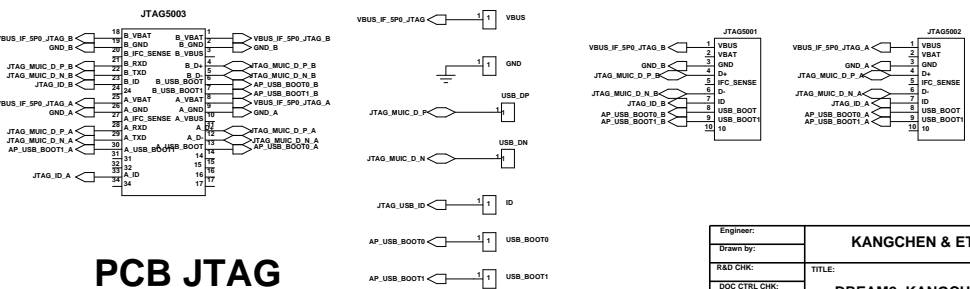


ETC



OM[2:0]	1st / 2nd
000	UFS_CH0 / USB
001	USB
010	SD CARD / USB
011	UFS_CH0 / SD CARD

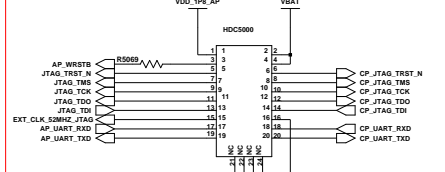
H/W Revision Setting



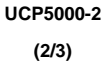
Engineer:	<div>KANGCHEN & ETC</div> <div>TITLE: <div>Size:</div></div> <div>DREAM2_KANGCHEN</div>		
Drawn by:			
R&D CHK:			
DOC CTRL CHK:			
MFG ENGR CHK:			
QA CHK:	REV	Drawing Number	6 Page

PCB JTAG

JTAG CONNECTOR



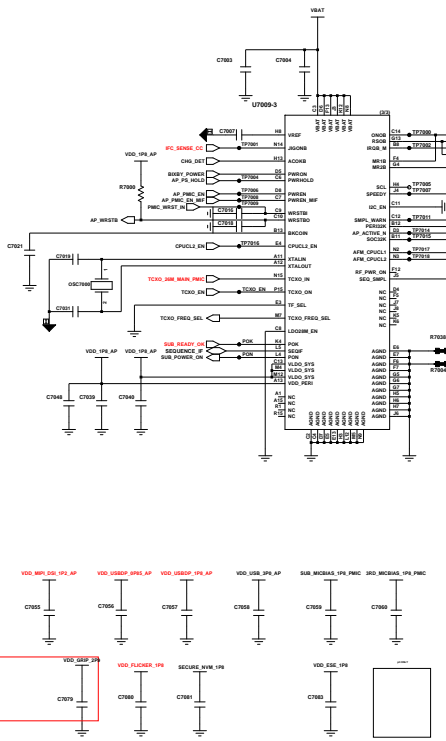
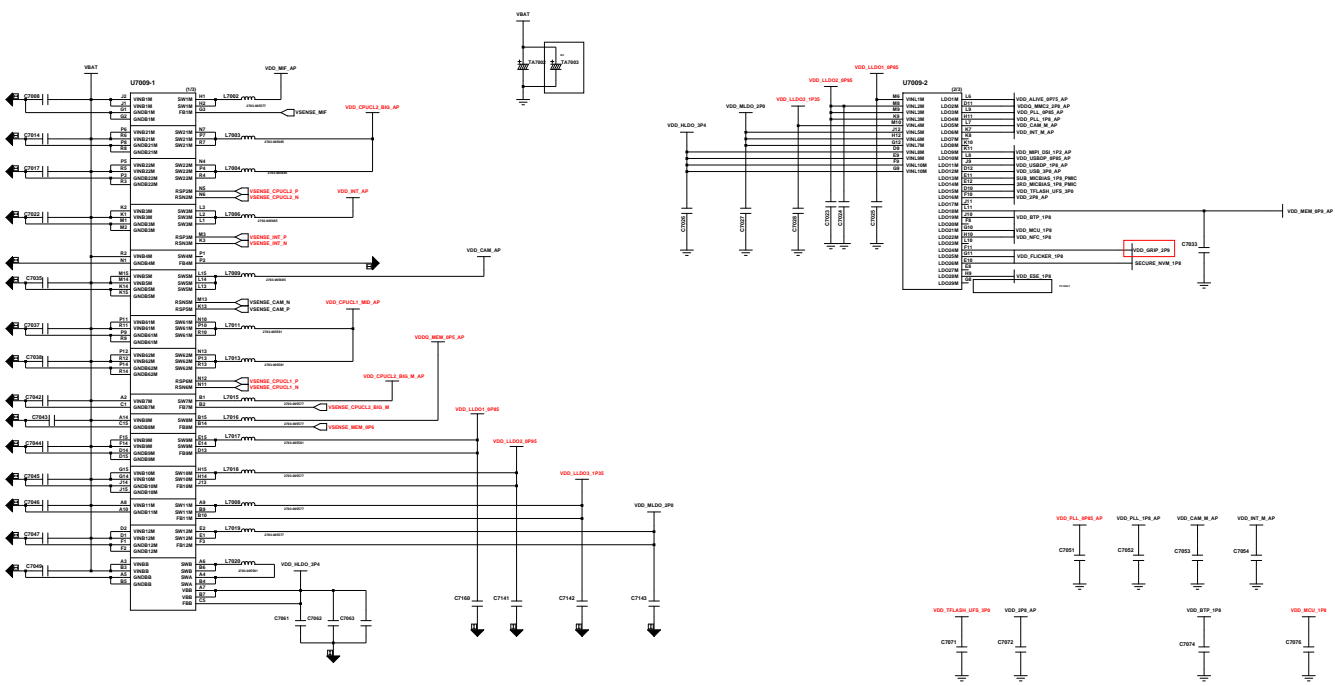
FOR JIG



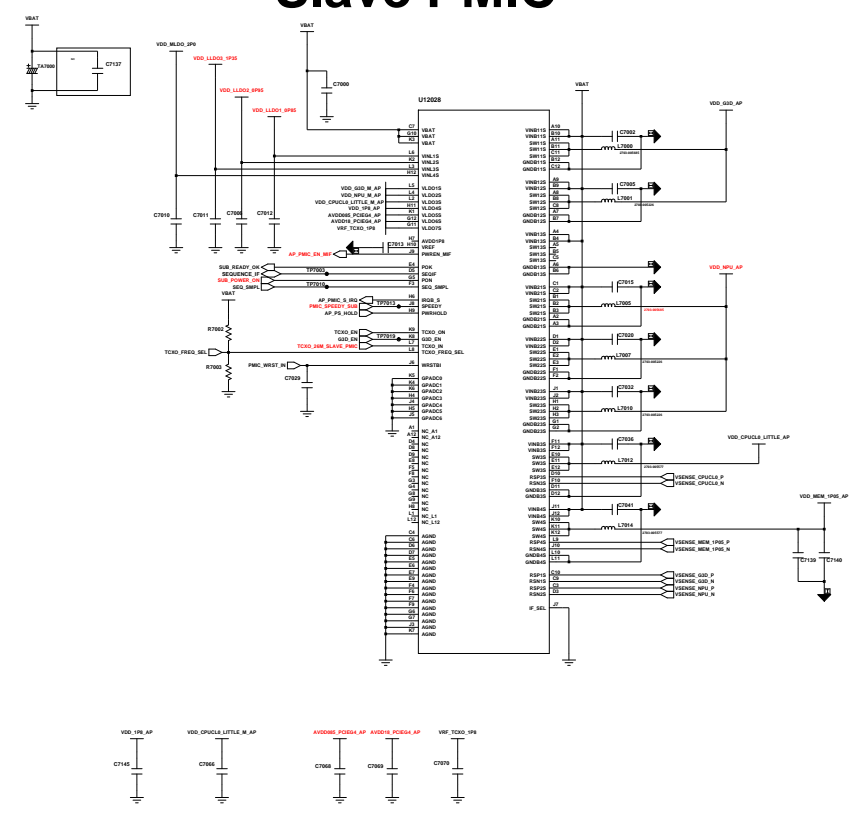
Engineer: user52	KANGCHEN POWER / GND	
Drawn by: user52		
R&D CHK:	TITLE:	Size:
DOC CTRL CHK:	DREAM2_KANGCHEN	
MFG ENGR CHK:		

AP Master PMIC

SYM1 -> SYM2 (07/09)

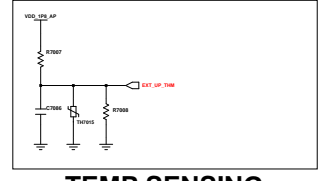
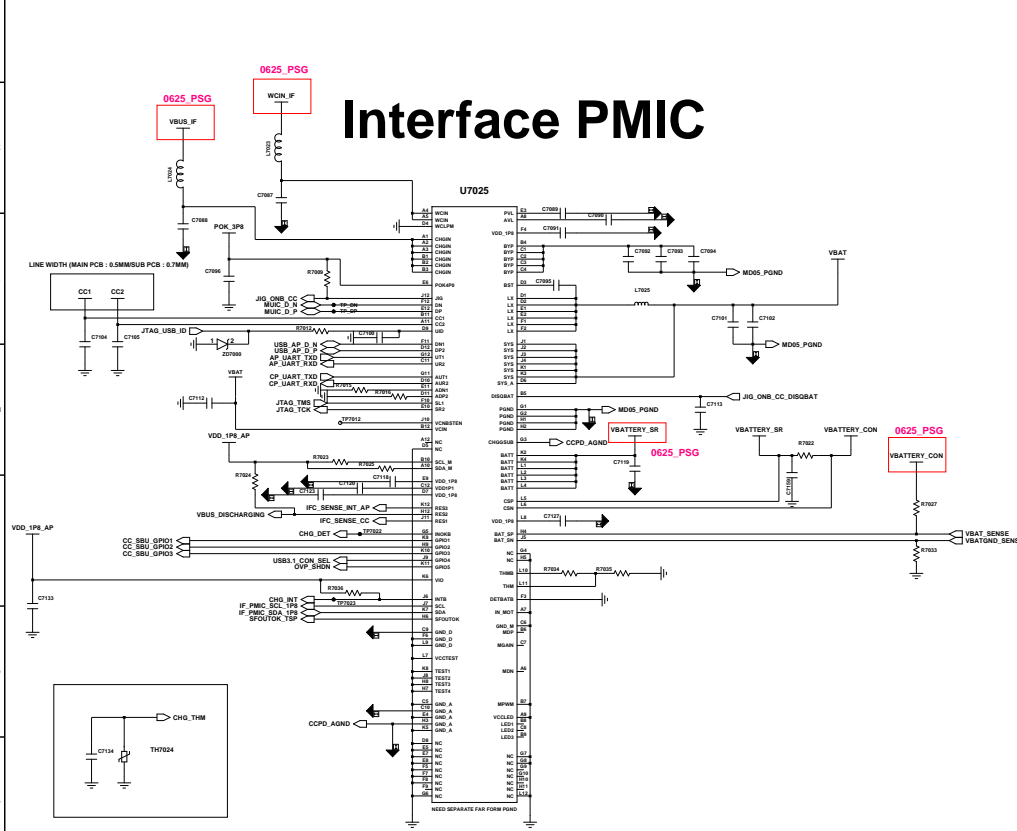


Slave PMIC

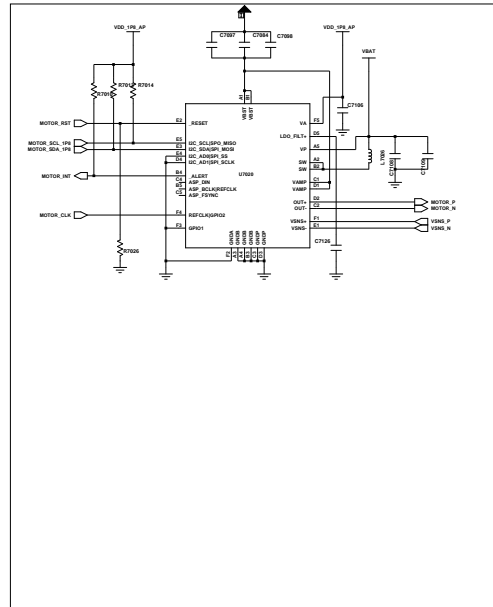


VCONN_BOOSTER

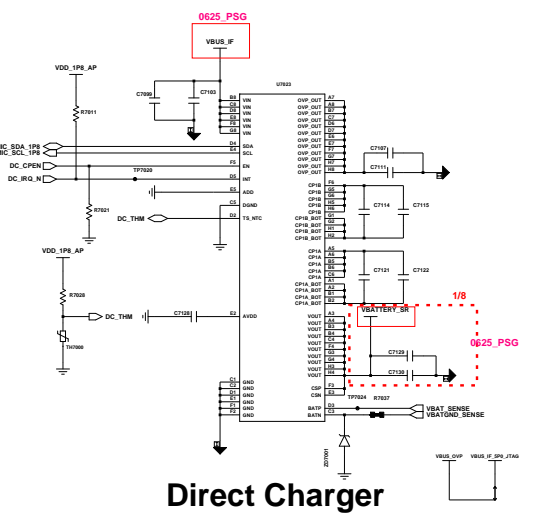
Interface PMIC



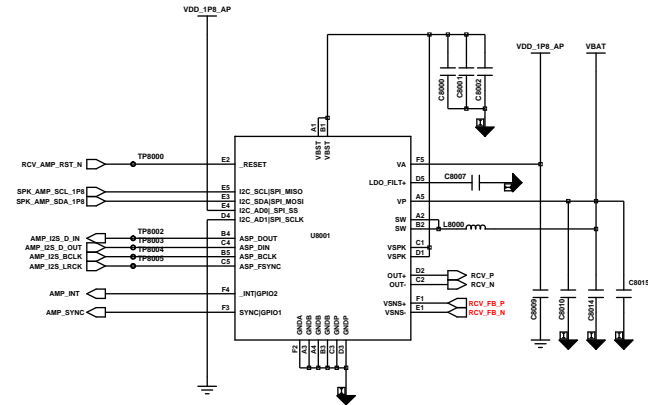
HAPTIC DRIVER



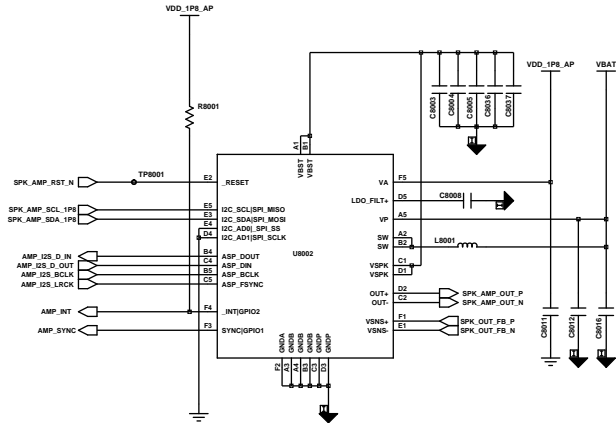
Direct Charger



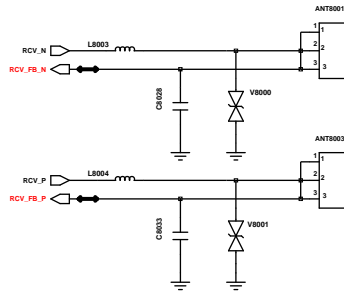
AUDIO



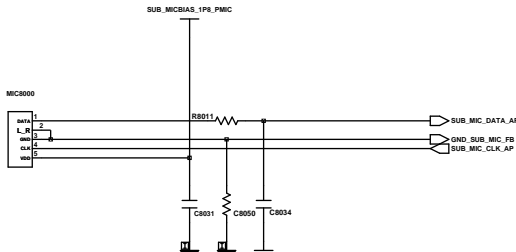
RCV AMP



SPK AMP



RCV CONTACT

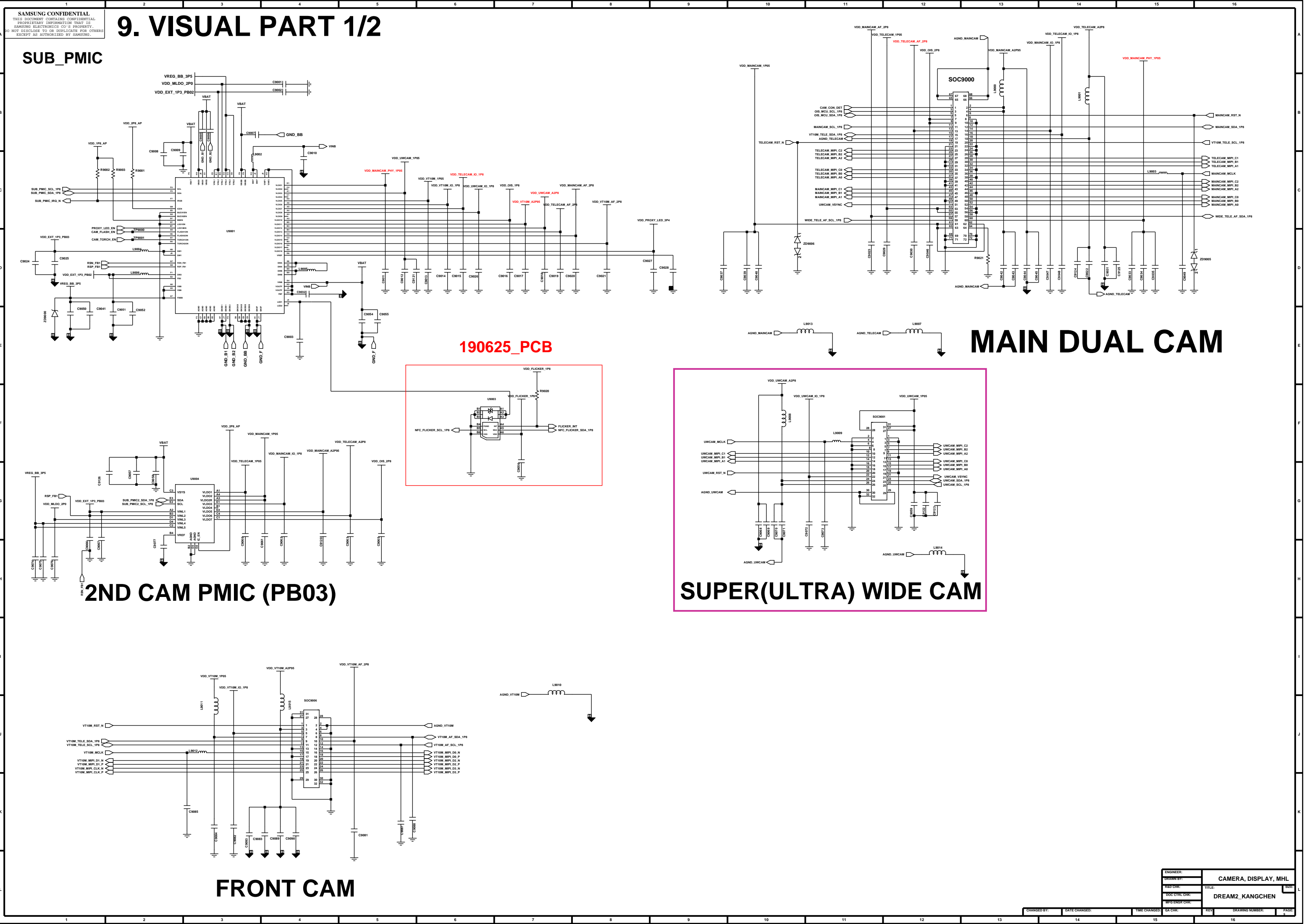


2RD MIC

AUDIO CODEC less

P1 ONLY

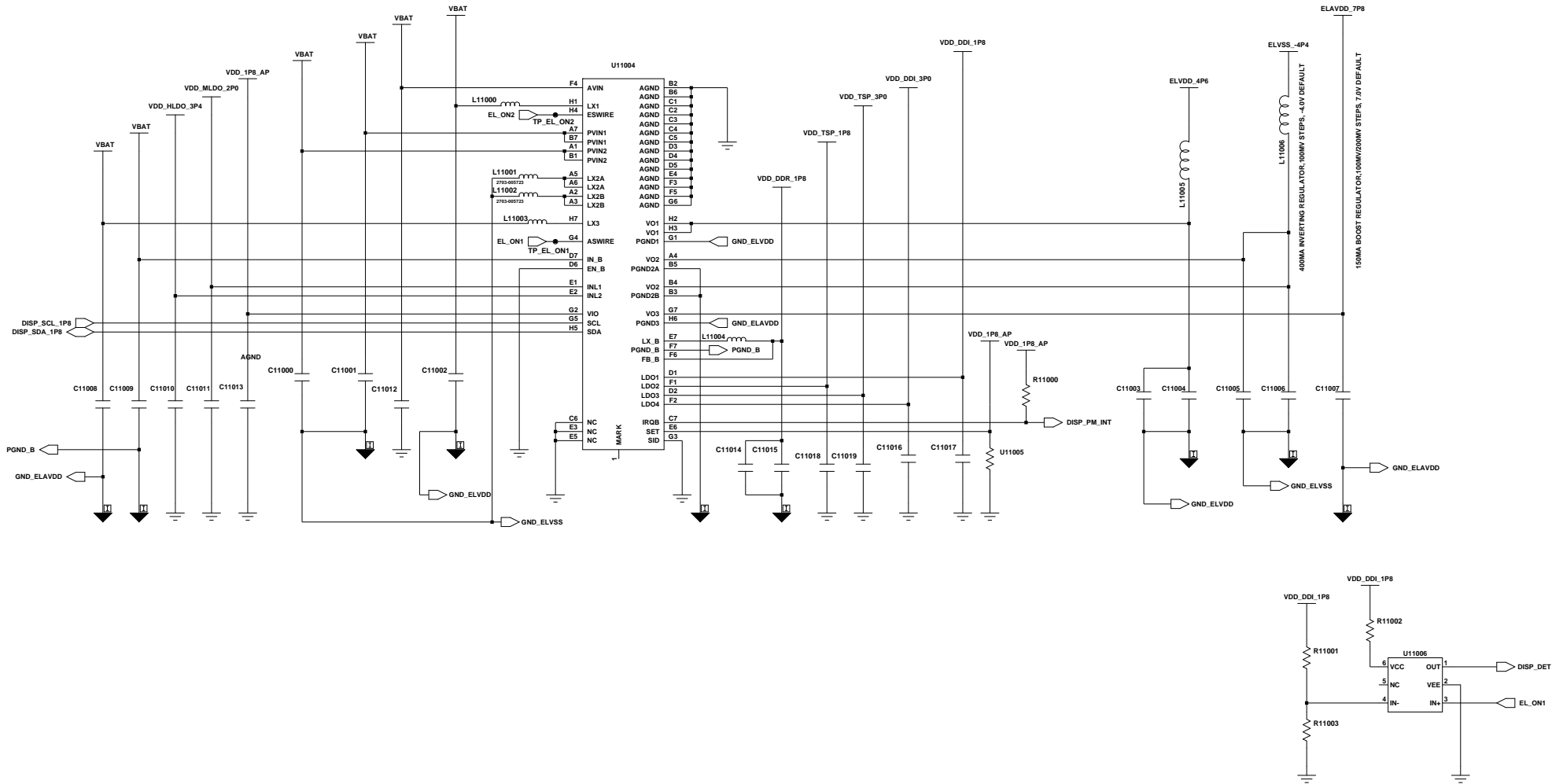
SUB MIC & LED_Flicker

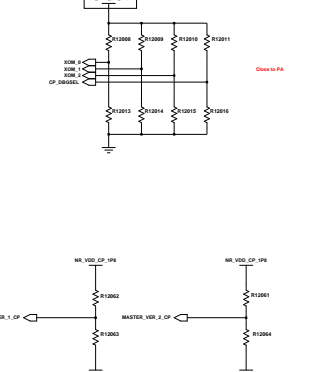
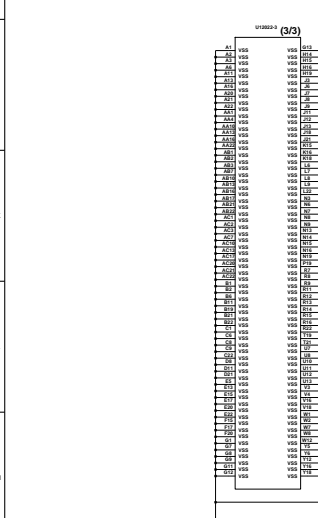


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DISPLAY

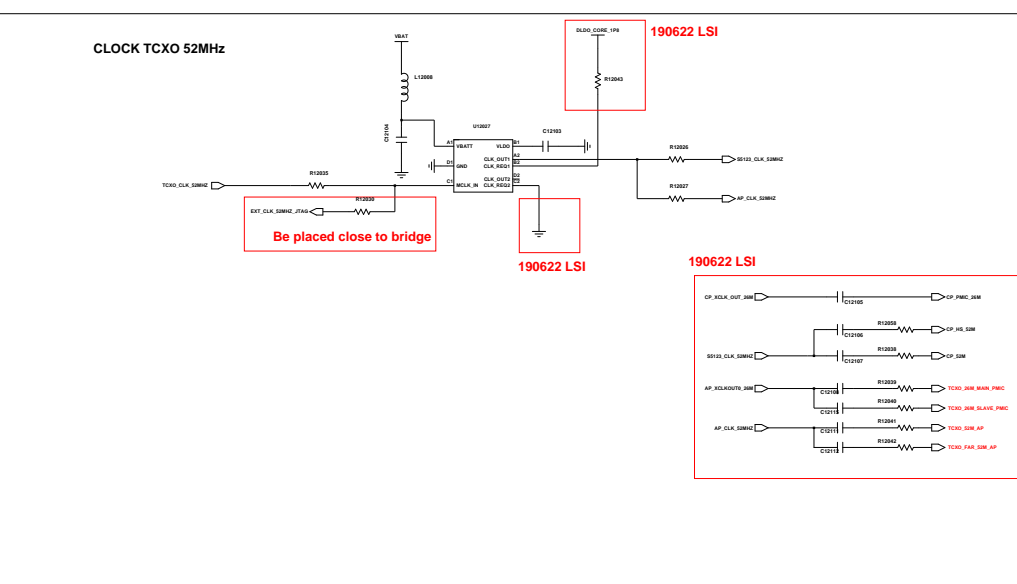
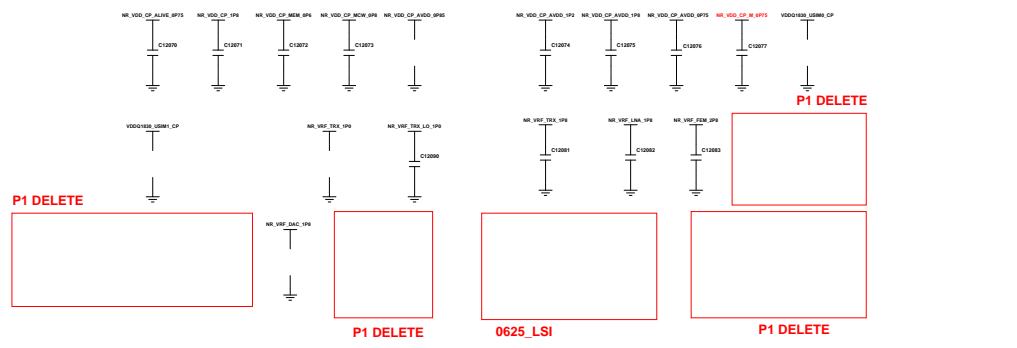
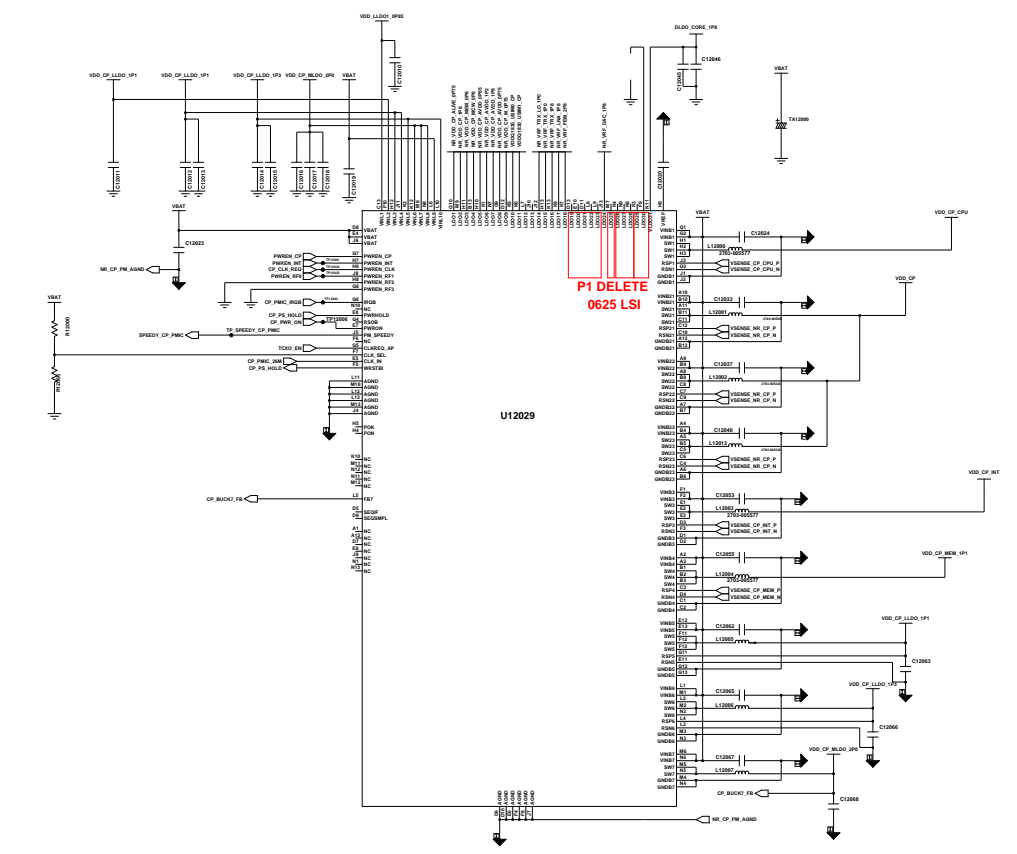
DISPLAY PMIC



**HWID**

R12007	R12012	
NC	0	DV1 (CP EVT0.1)
430	30	DV1 (CP EVT1)
390	62	DV2 (CP EVT0.1)
360	91	DV2 (CP EVT1)
330	120	DVR (CP EVT0.1)
300	150	DVR (CP EVT1)
270	180	PV1 (CP EVT1)
240	220	PV1 REV0.3A_(S\$800)
200	240	PV2 REV0.4
180	270	PVR REV0.5

CP PMIC



KEY-LESS



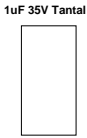
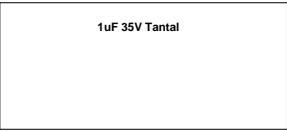
P3 Only

60GHz (802.11ay)_Module

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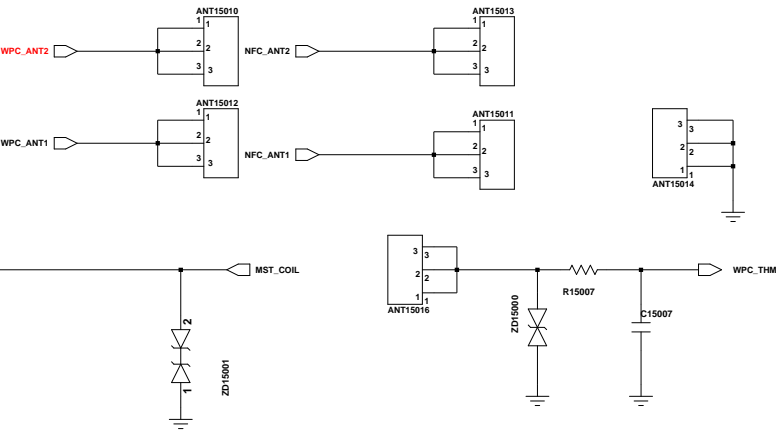
CHARGER

AUX SW



OVP LS

WIRELESS CHARGER IC



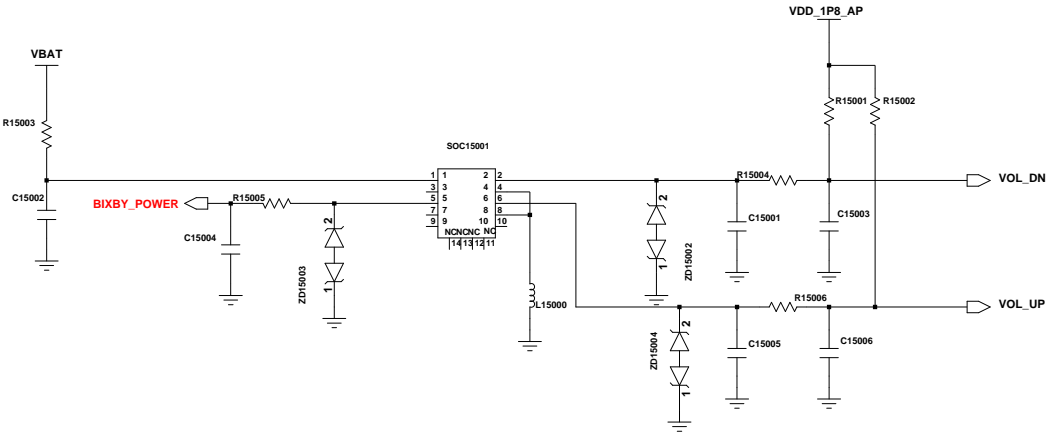
MST SWITCH

MST,NFC,WPC Conn

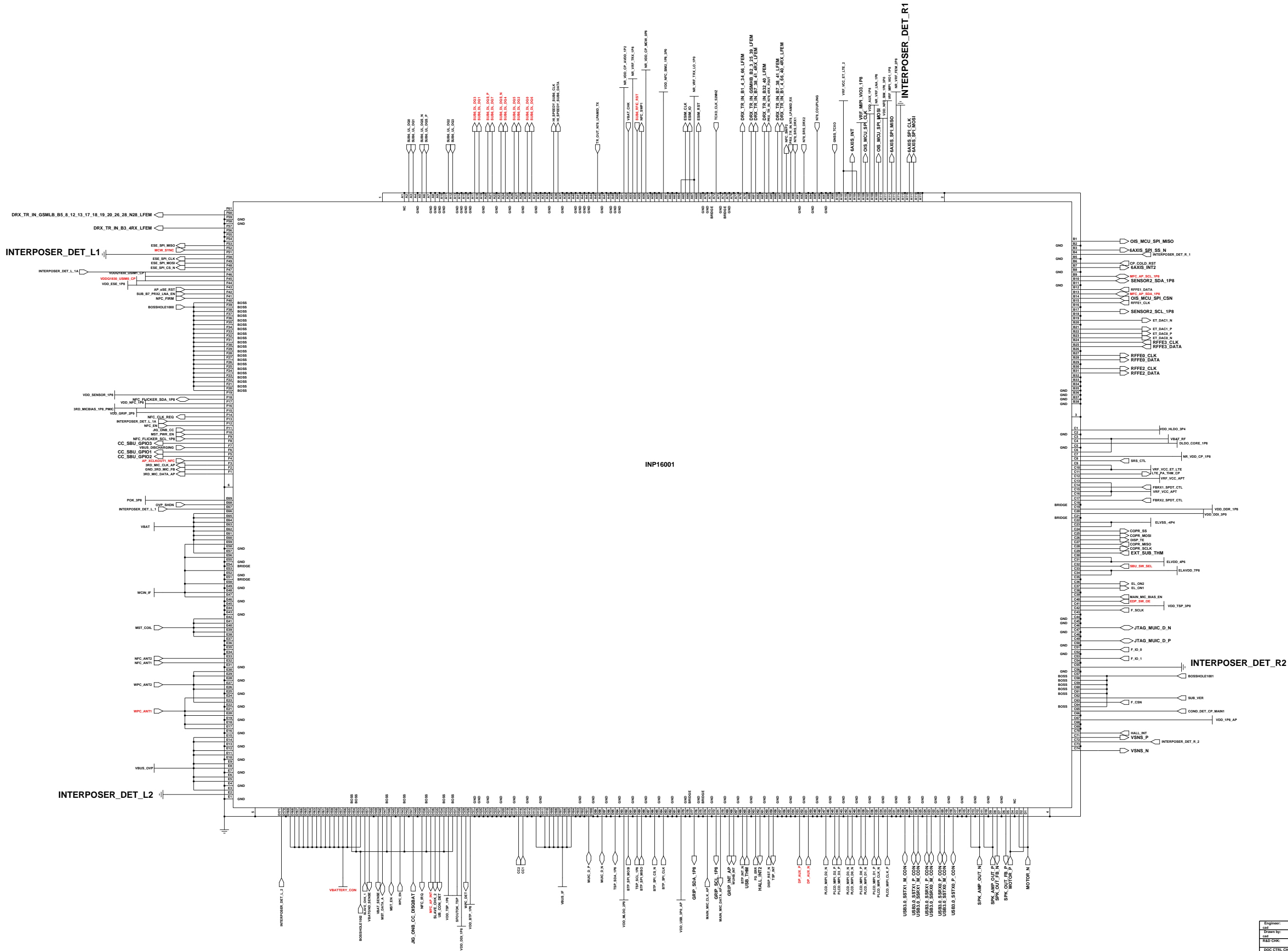
CHECK PIN MAP




BATTERY CONNECTOR

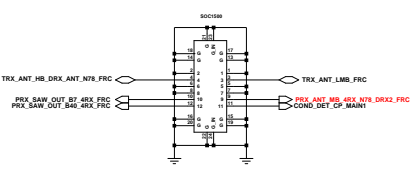


QLING COMMON GND : A8, A9, A10, A13, A14, A15, A16, A19, A22, A25, A28, A31, A34, A37, A40, A41, A42, A43, A46, A48, A50, A69, A70, A71, A73, A74, A75



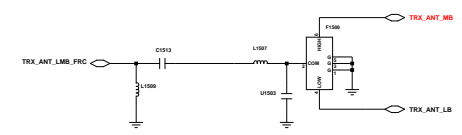
Engineer: cad			
Drawn by: cad			
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DOC CTRL CHK:	P1_S_REV0.0_0622_HW	16 11 12 A	
MFG ENGR CHK:			
QA CHK:	REV:	Drawing Number:	Page:

RF1

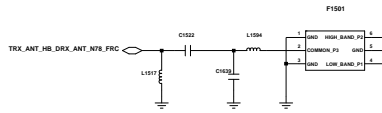


RF Connector

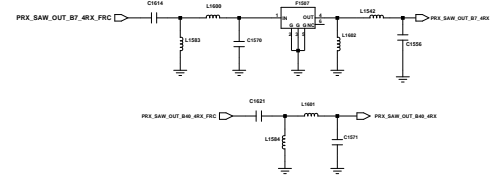
MAIN1 ANT(LB/MB)
(GSM LB/HB, FDD B1/2/3/4/5/8/12/13/17/18/19/20/25/26/28/32/66,TDD B34/39)



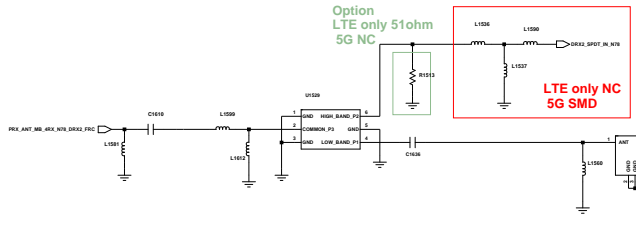
MAIN2 ANT(HB)
(FDD B7, TDD 38/40/41 N78_DRX1)



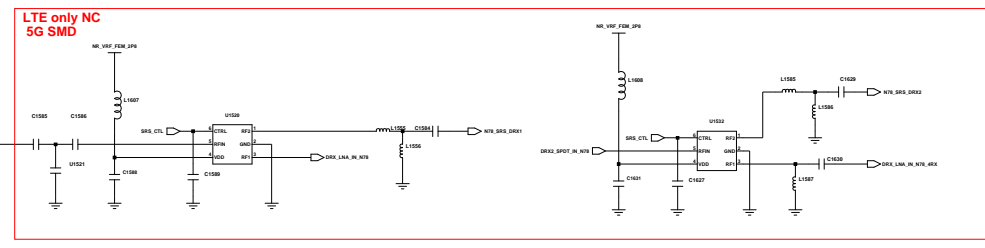
MAIN4 ANT (B7_4RX, N40_4RX)



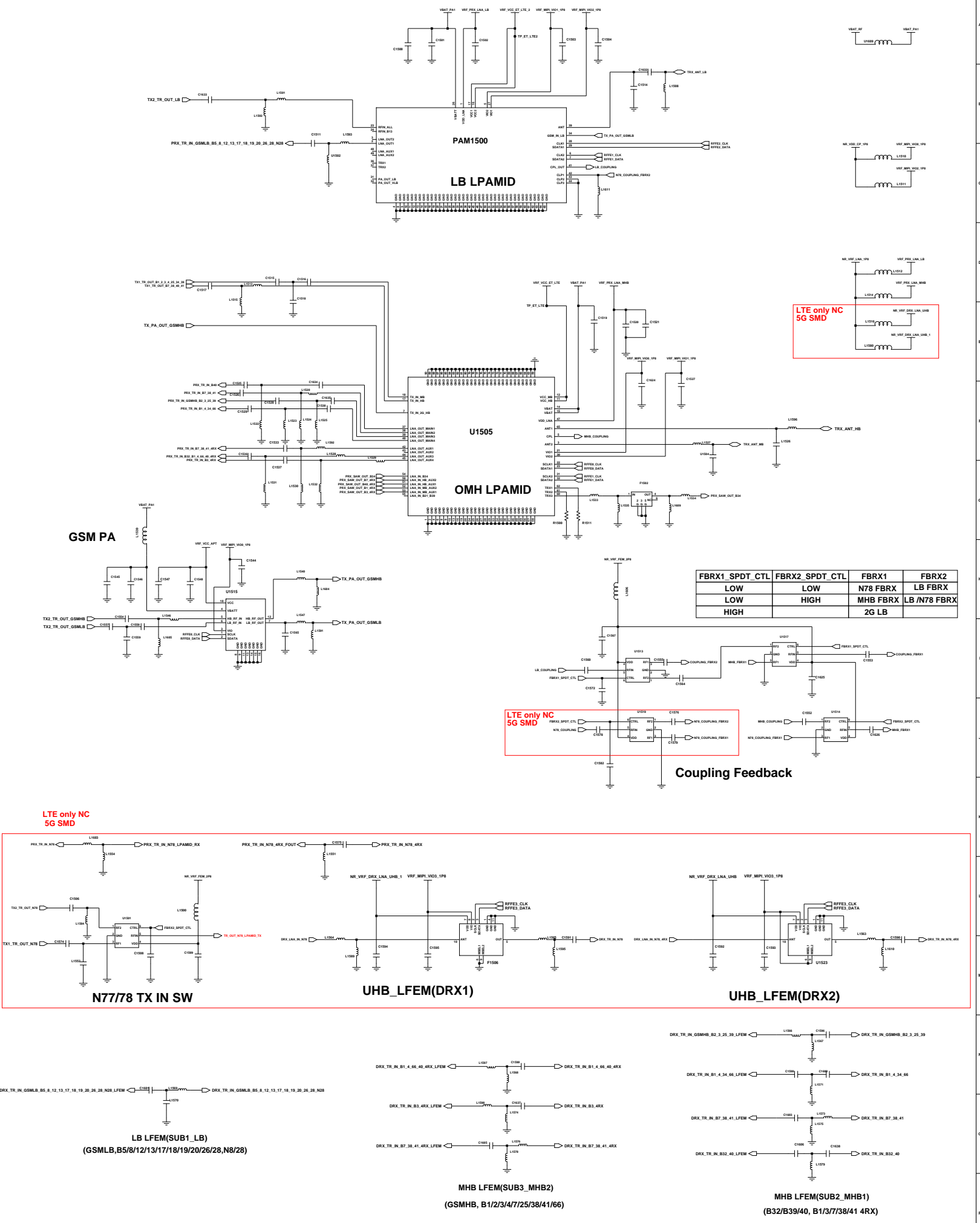
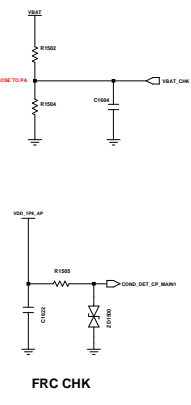
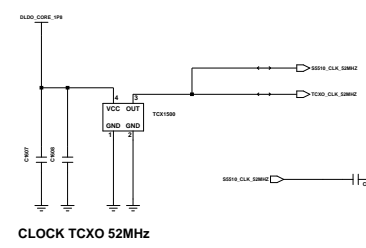
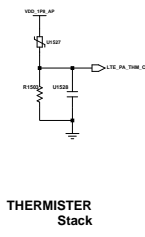
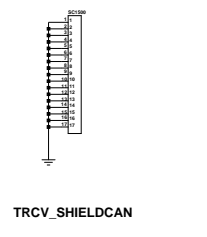
MAIN3 ANT (B1/3_4RX, N78_DRX2)



NR DRX SRS Switch

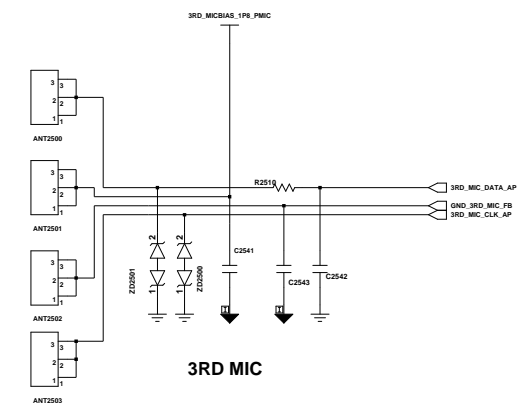
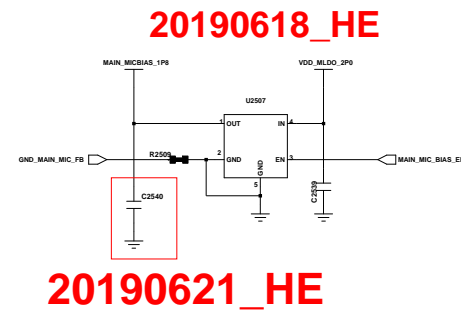
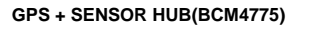


RF1 - ETC





AUDIO



ETC



		SLAVE_CHK_1		SLAVE_CHK_2		M/M
		R2515	R2513	R2519	R2518	0 ohm
-	-	0	100K open	0	100K open	
B	5G Global / LTE+FR1	0	100K open	1	open 100K	R2512
F	LTE	1	open 100K	0	100K open	R2516

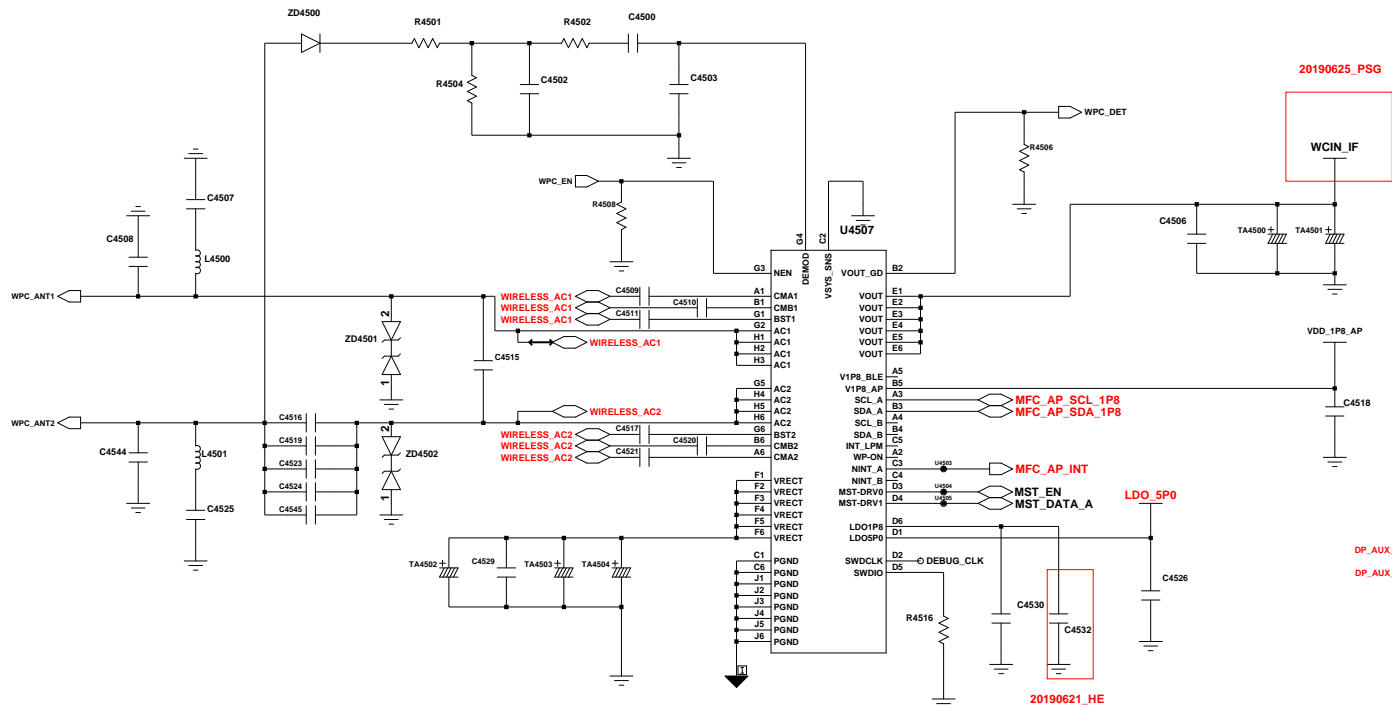
DISPLAY



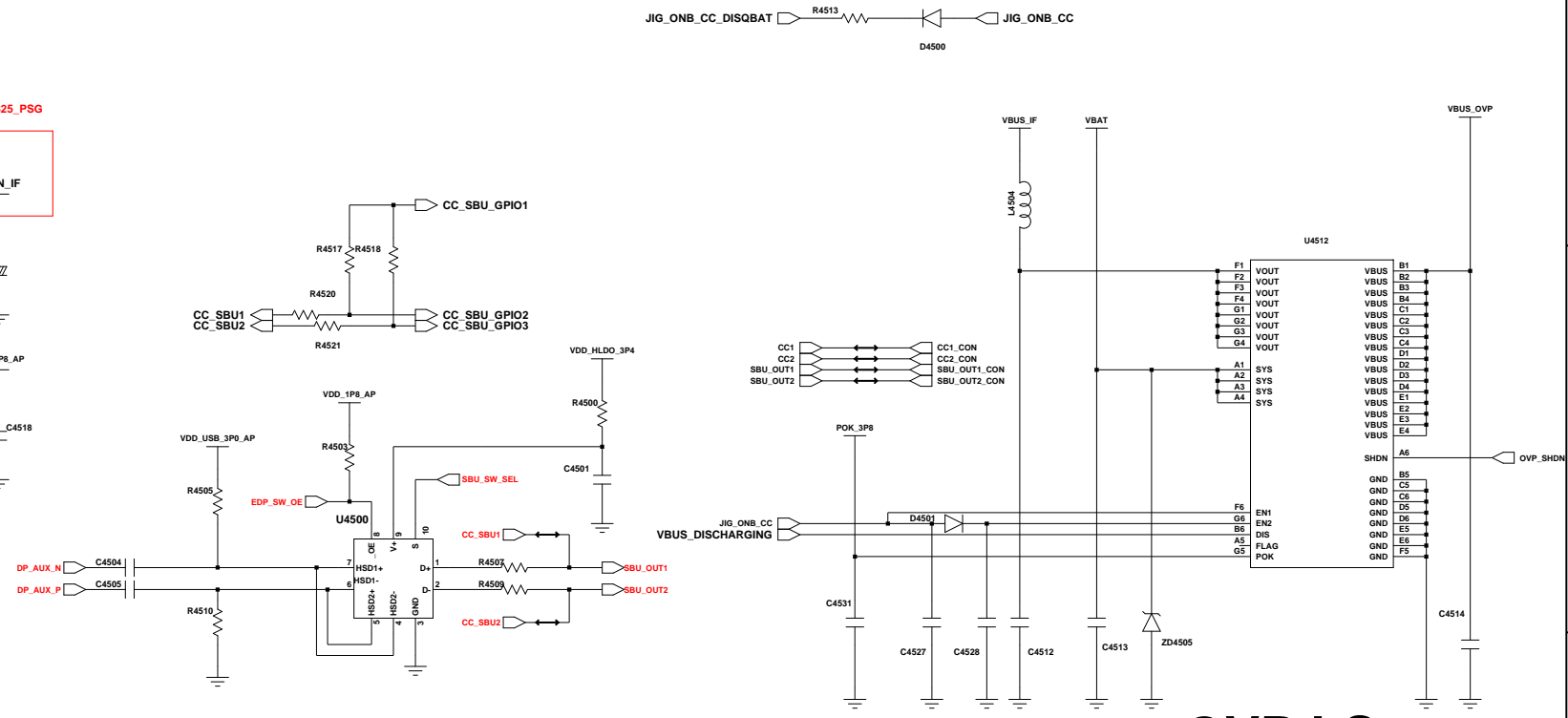
SENSORS



CHARGER



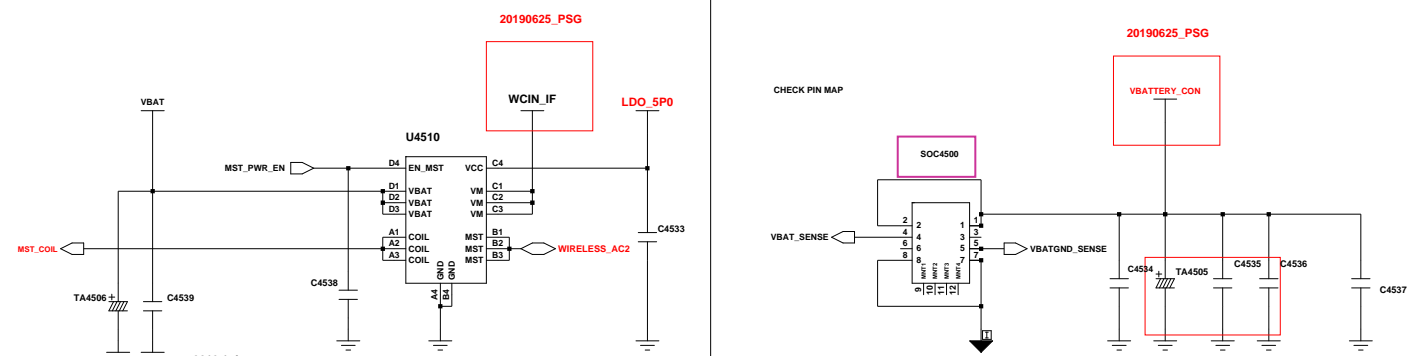
WIRELESS CHARGER IC



AUX SW

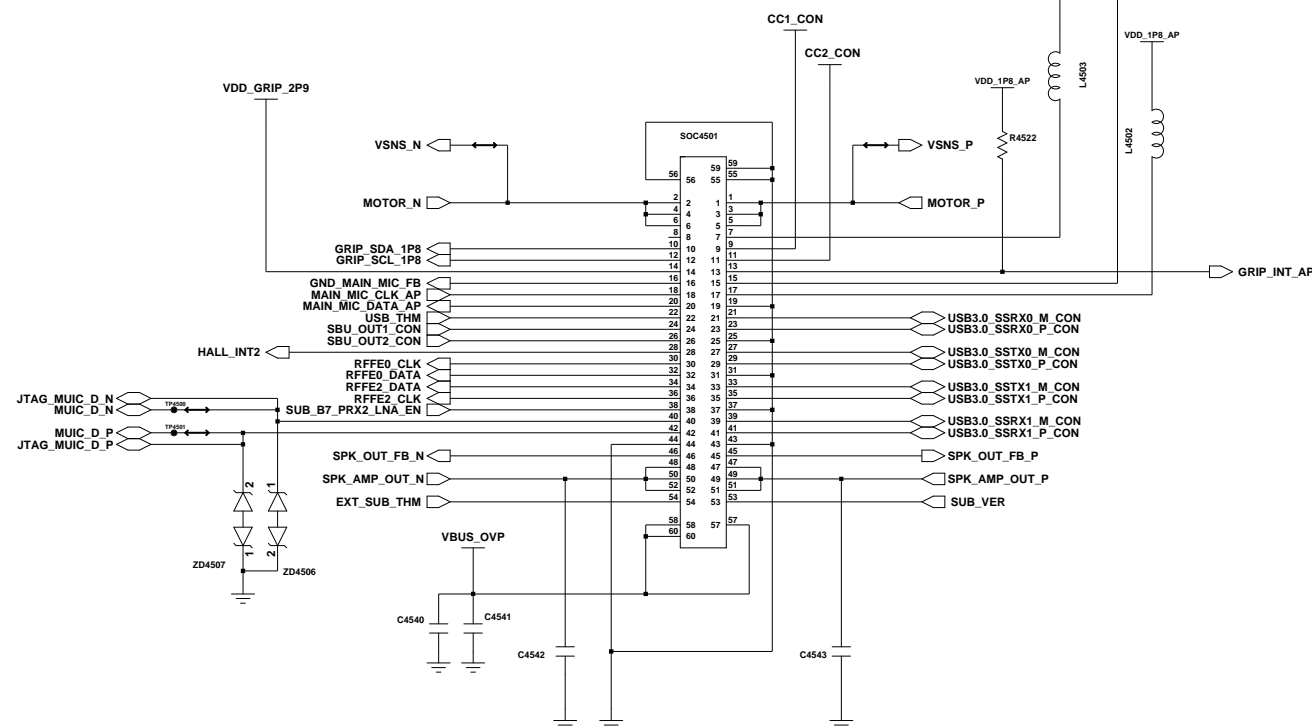
OVP LS

Lower SUB IF Conn.

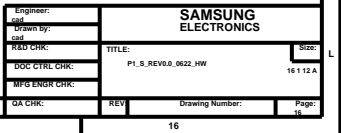


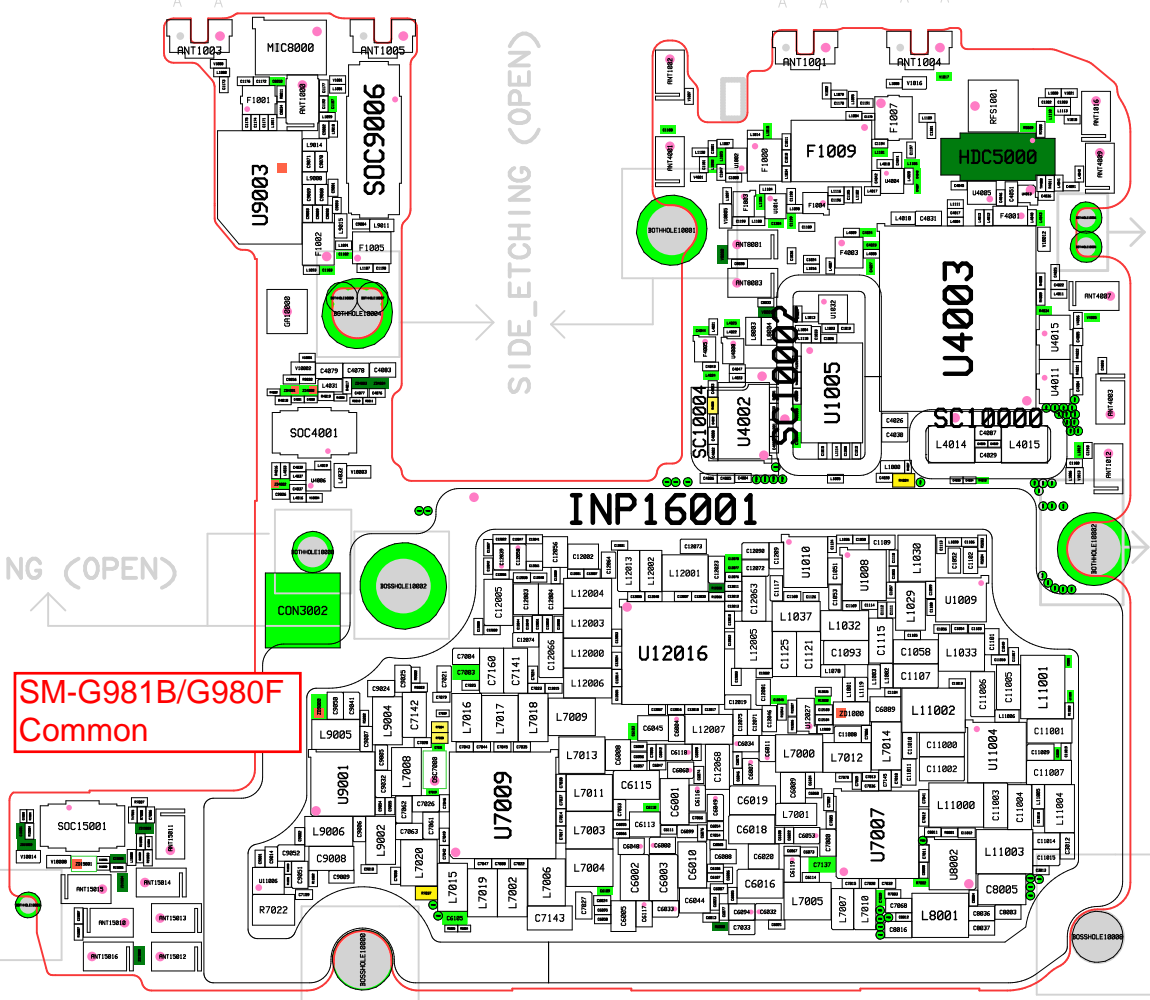
BATTERY CONNECTOR

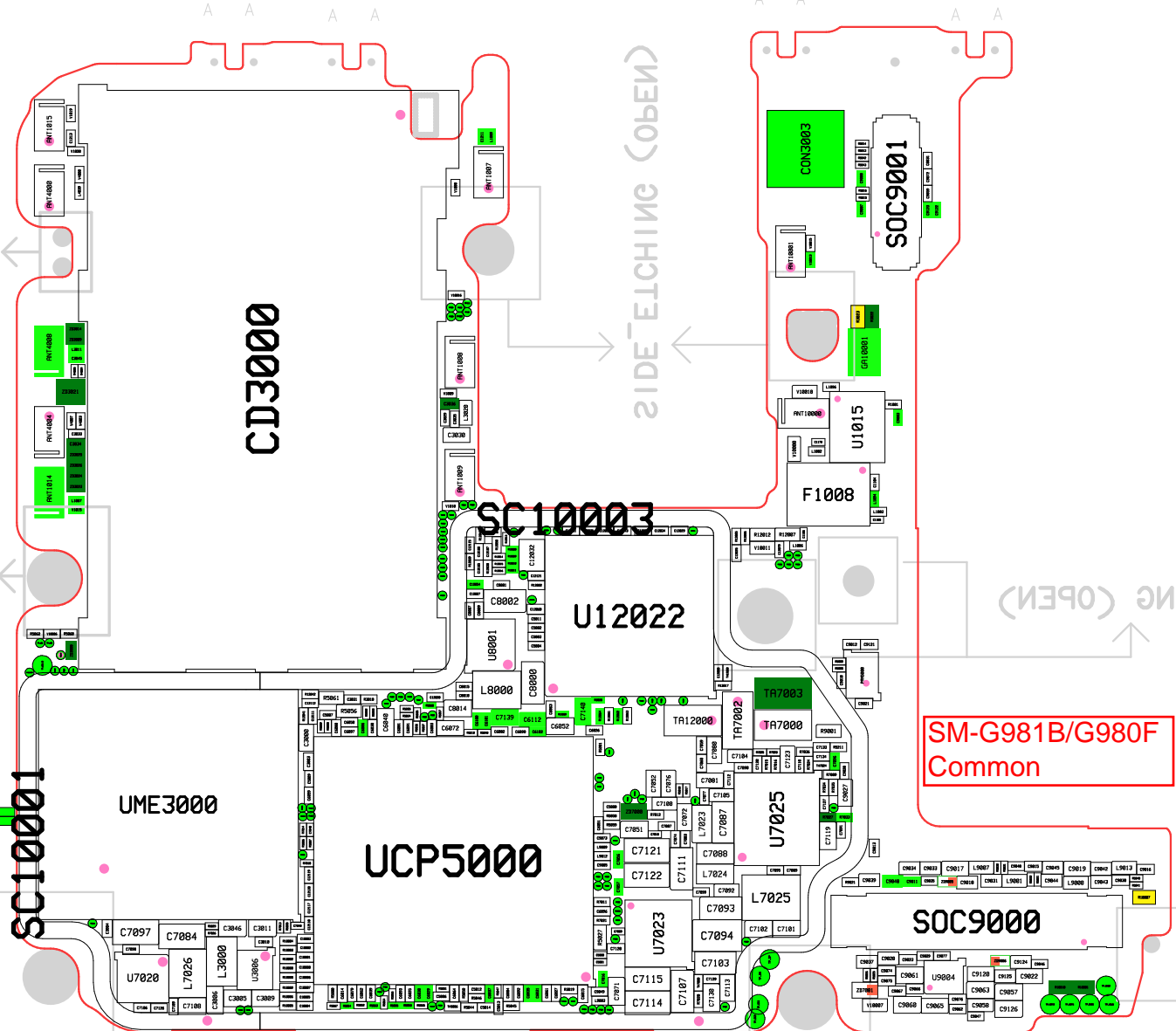
MST SWITCH

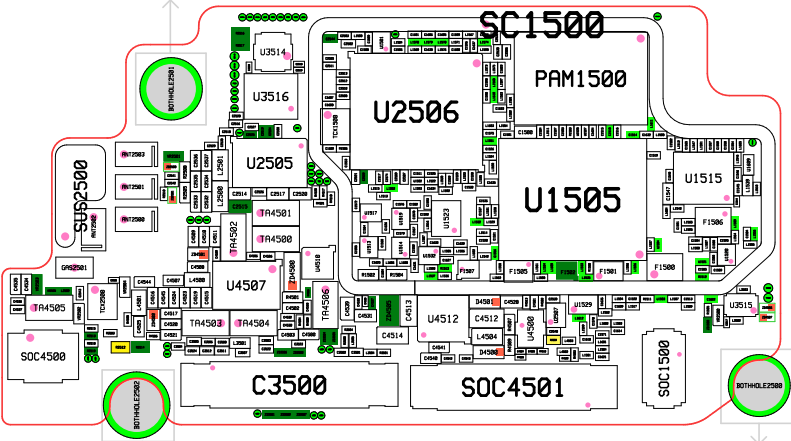


INP5500









INP5500

CON2501

