

SM-S918U_S_AP_REV0.7

SHEET01 : RF (1/2)

SHEET02 : RF (2/2), RF PMIC #1

SHEET03 : BT/WIFI, UWB

SHEET04 : NFC IC, MFC IC, SENSOR, MOTOR DRIVER IC, AUX S

SHEET05 : AP(1/2), UFS, NVM

SHEET06 : AP(2/2), Charger block

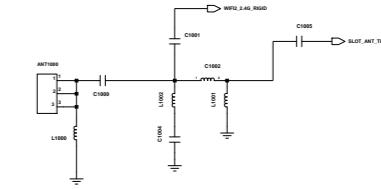
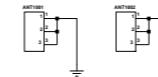
SHEET07 : AP PMIC set, Clock Generator

SHEET08 : SPK AMP, MIC

SHEET09 : CAM PMIC, CAM Conn., CAM MCU, DISPLAY PMIC, D

SHEET10 : 5G IFIC, mmW connector, RF PMIC #2

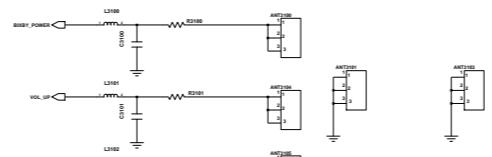
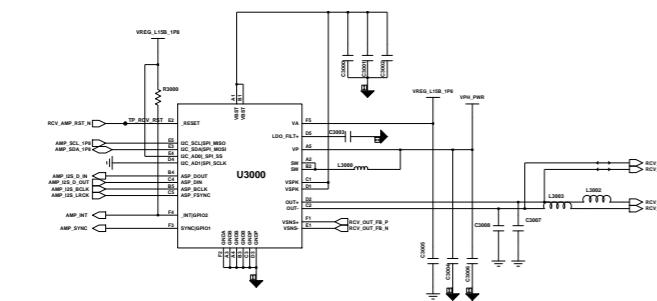
Engineer:	
Drawn by:	
REQ CHK:	
DOC CTRL CHK:	
MPG ENGR CHK:	



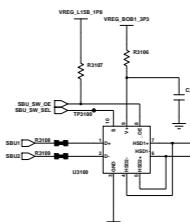
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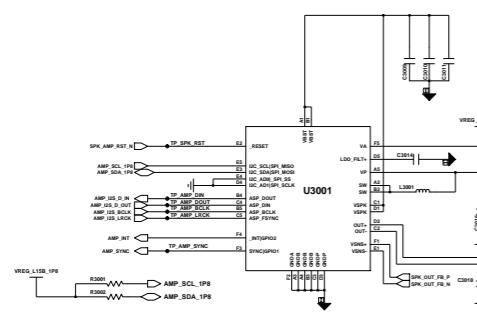
AUDIO



Vol + Power key

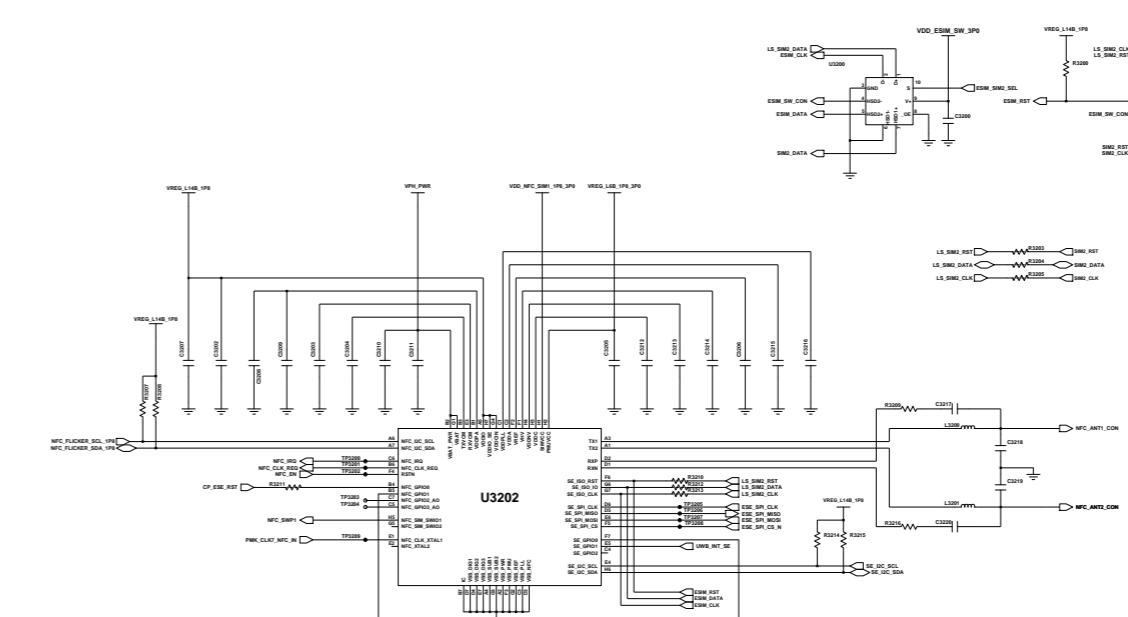
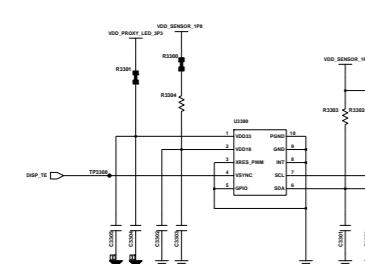


AUX SW



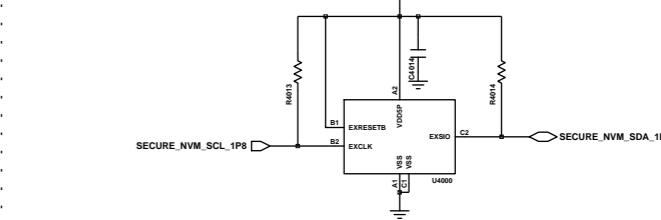
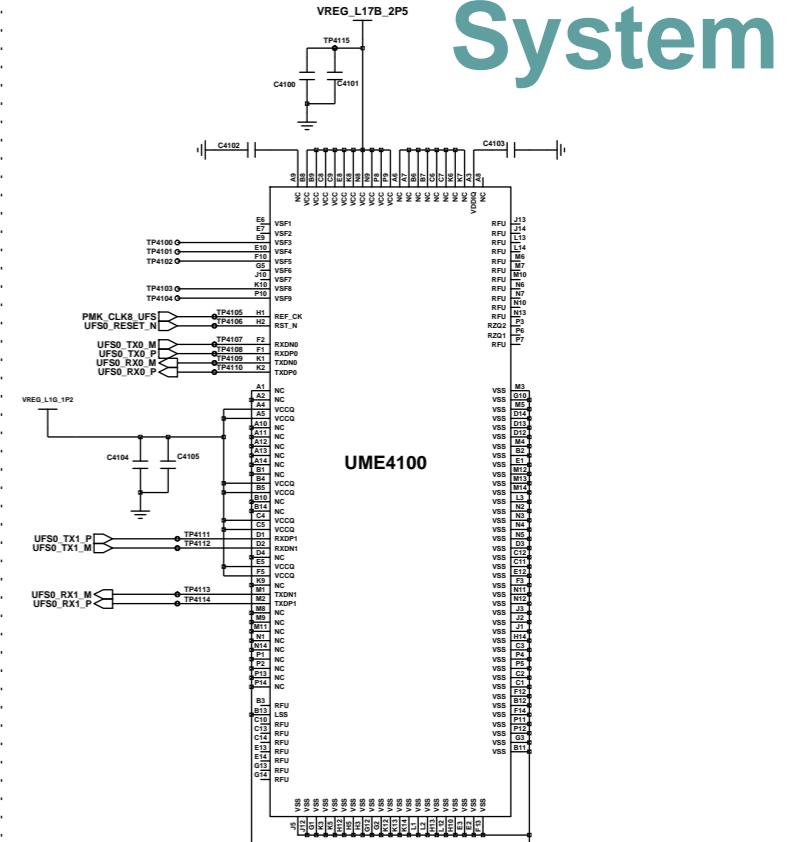
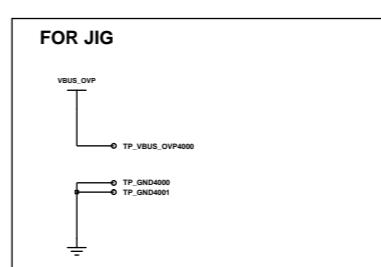
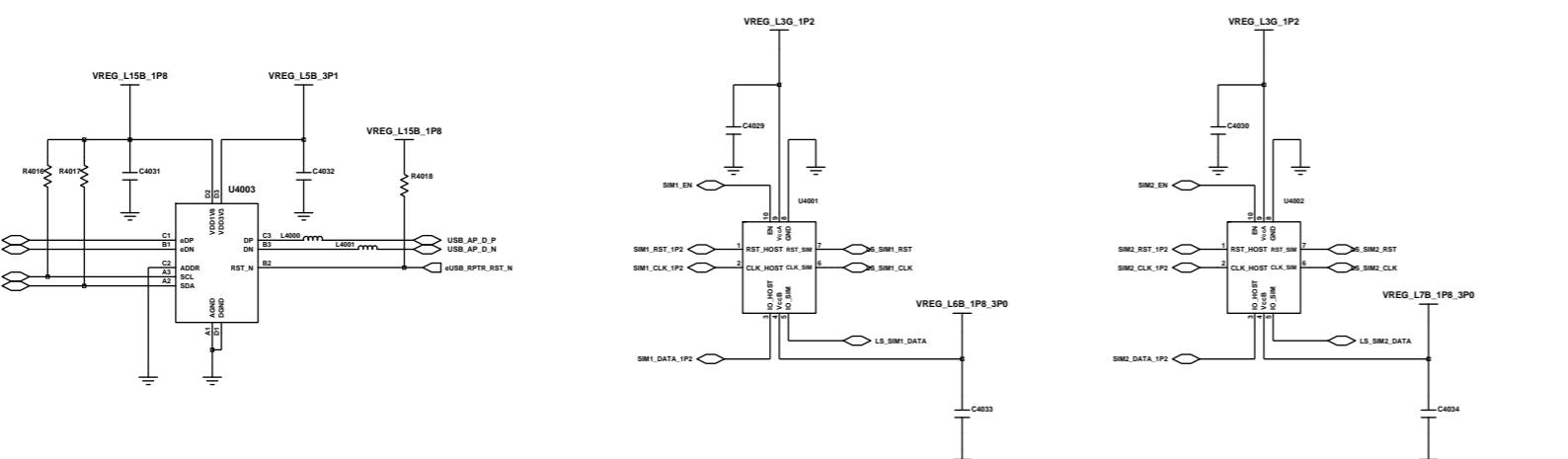
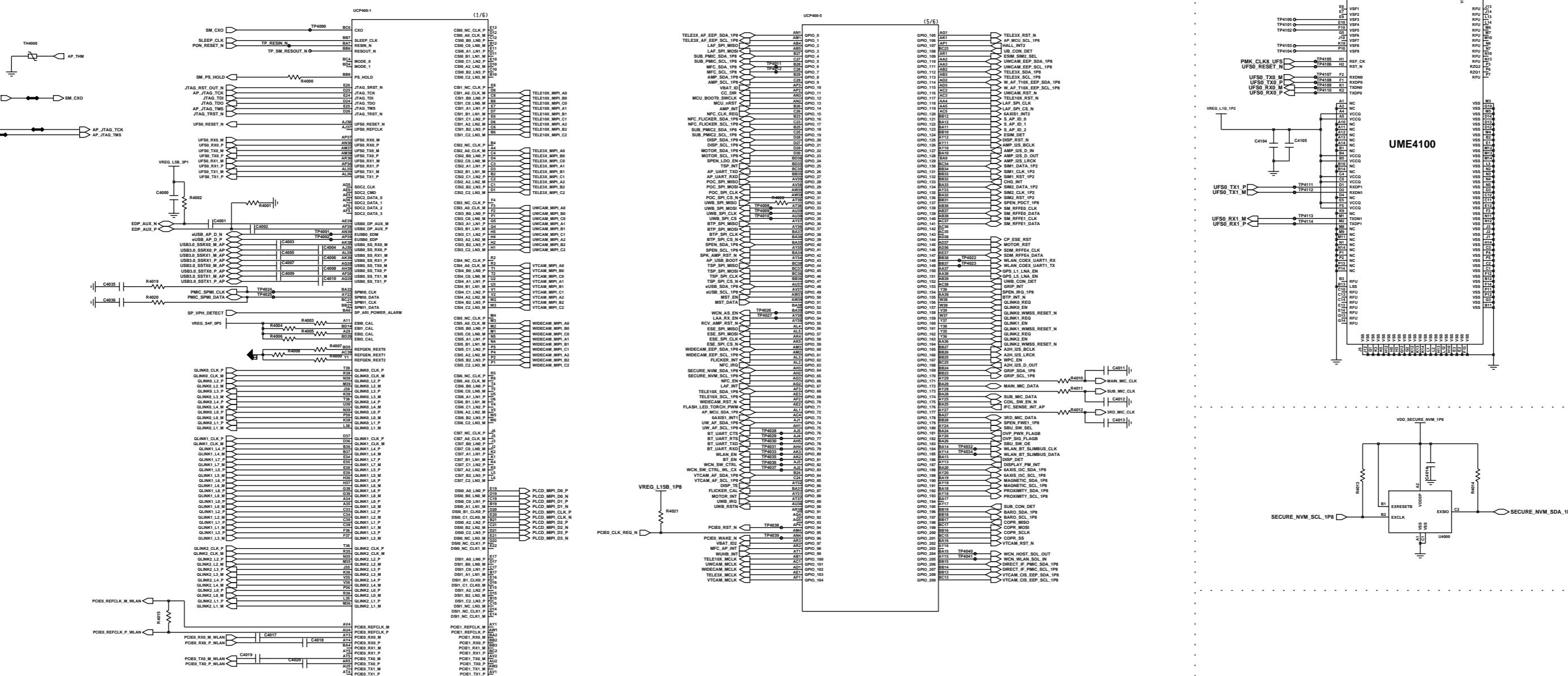
Power

Visual



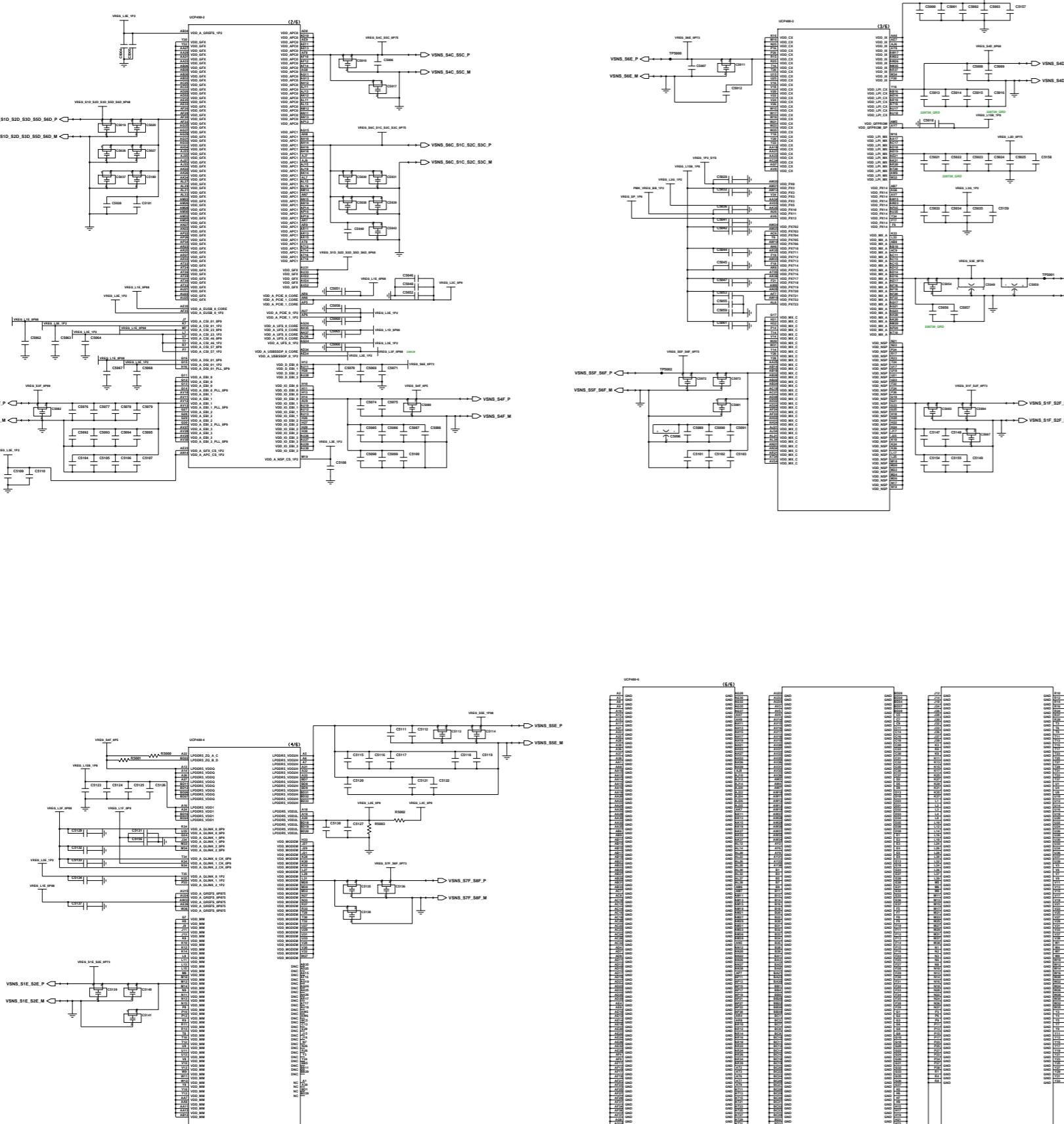
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System

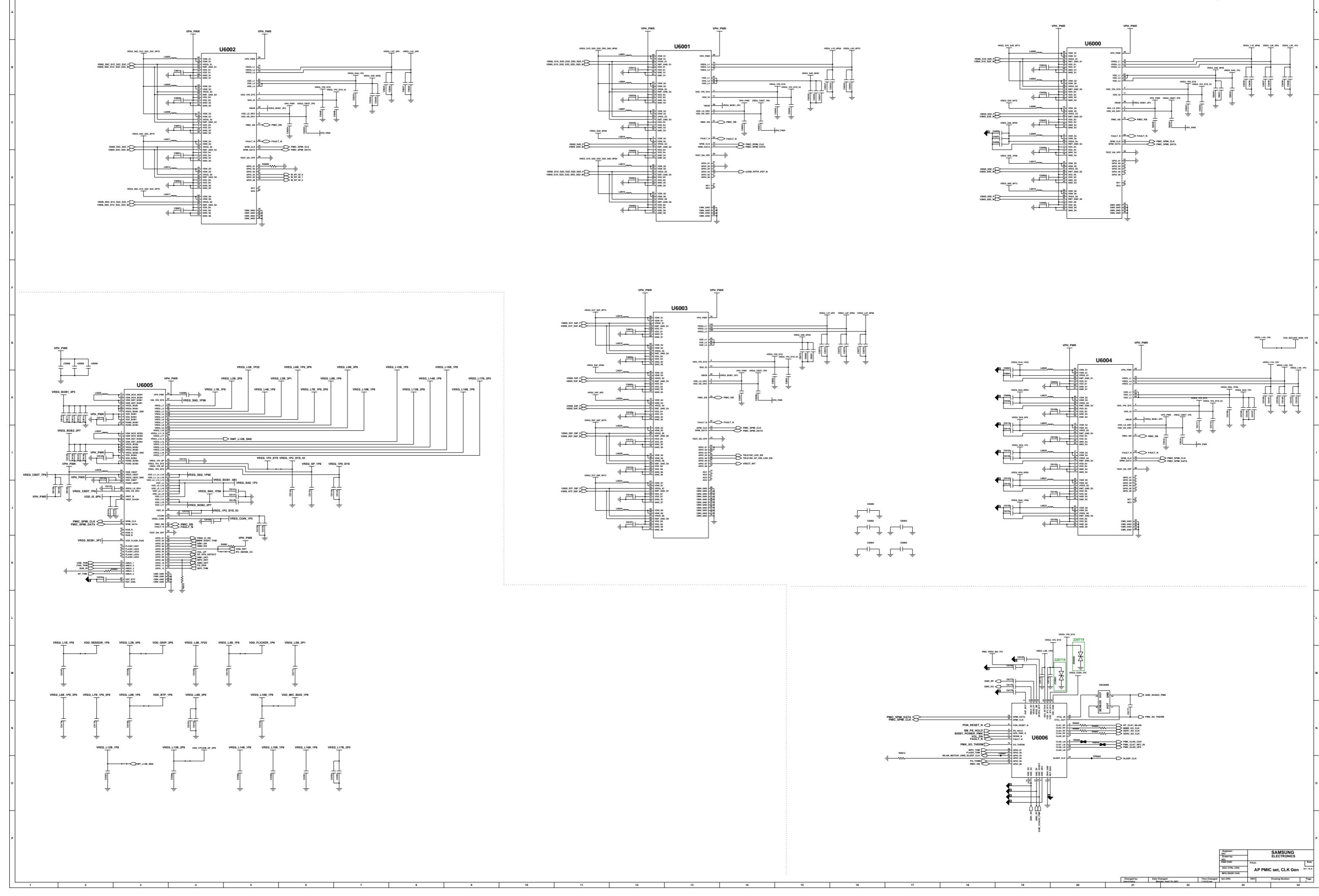


Engineer: SEC	SAMSUNG ELECTRONICS	
Drawn by: SEC		
R&D CHK:	TITLE:	
DOC CTRL CHK:	AP (1/2), UFS, NVM	
MFG ENGR CHK:		
	16112	

Power Charger block

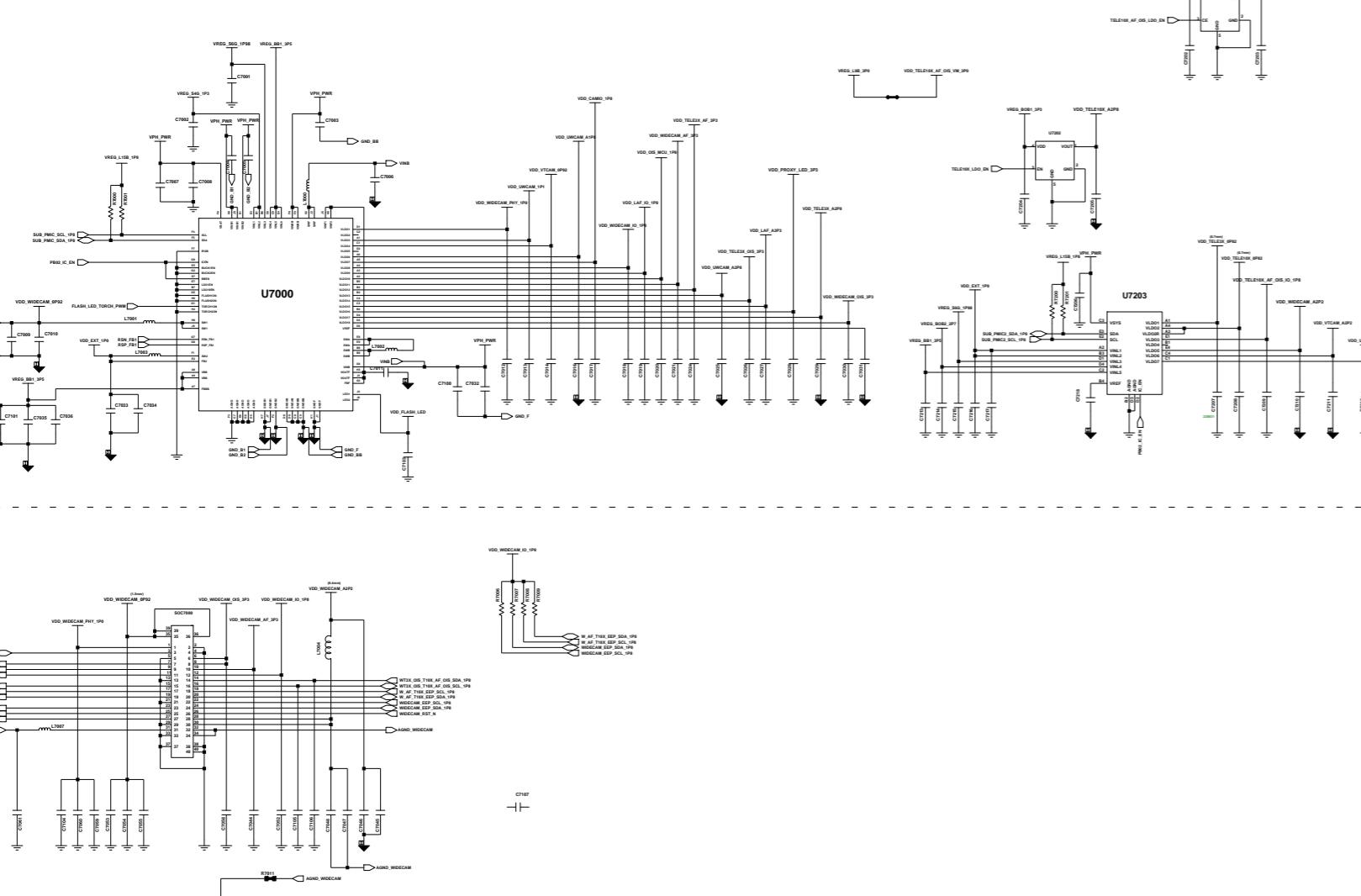


Power System

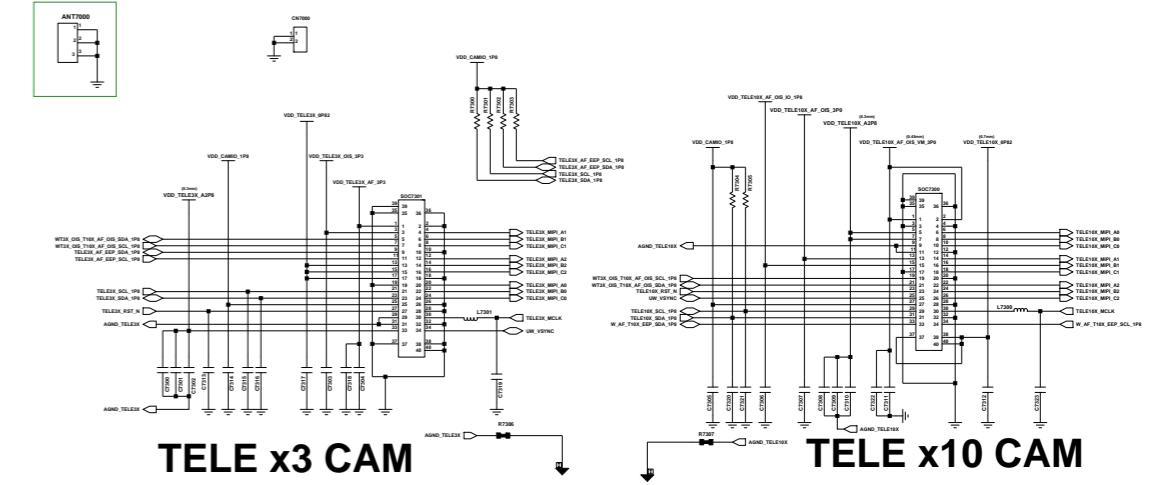


CAMERA

Visual



Main Wide CAM

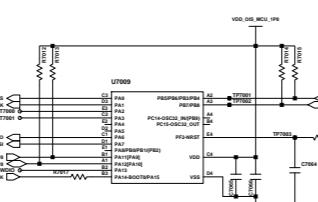


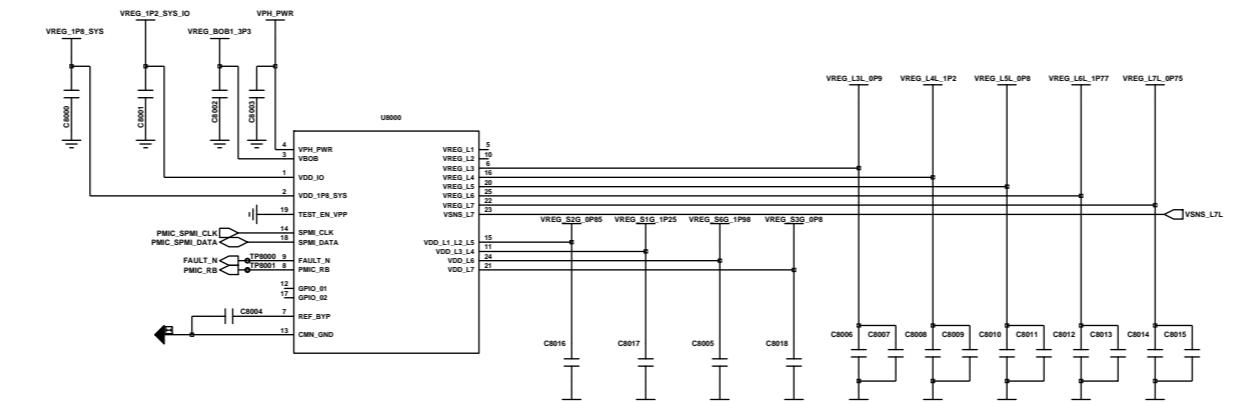
AGNO

TELE x10 CAM



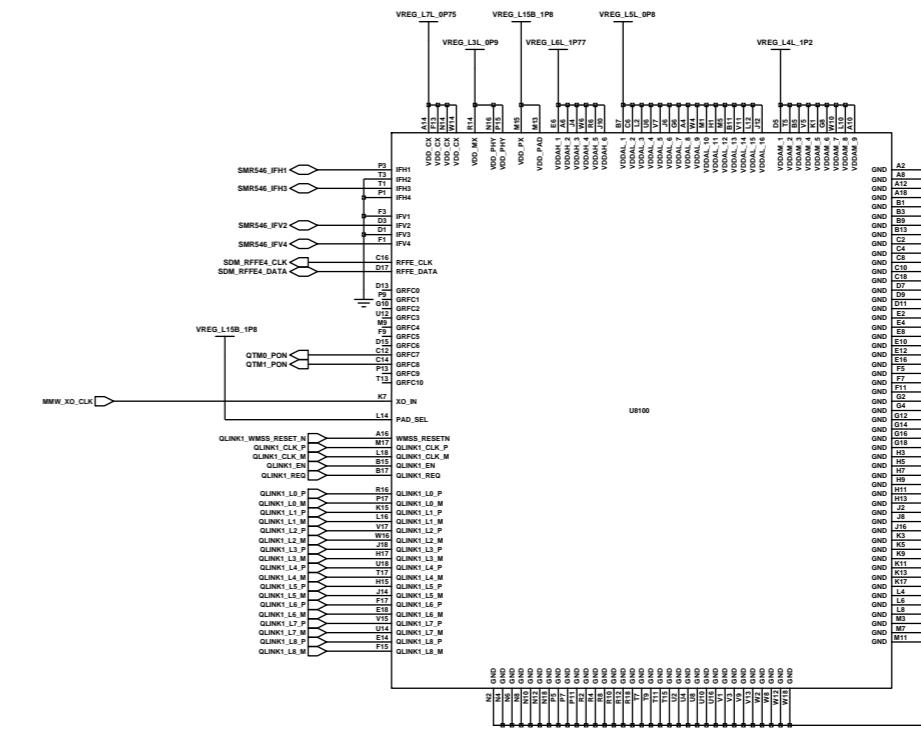
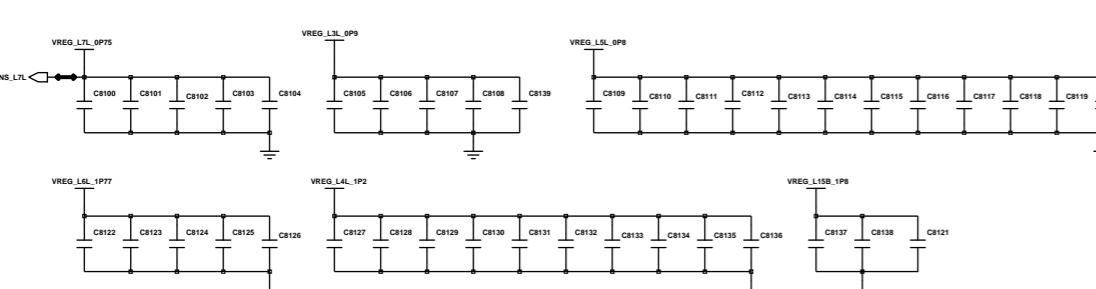
CAM MC



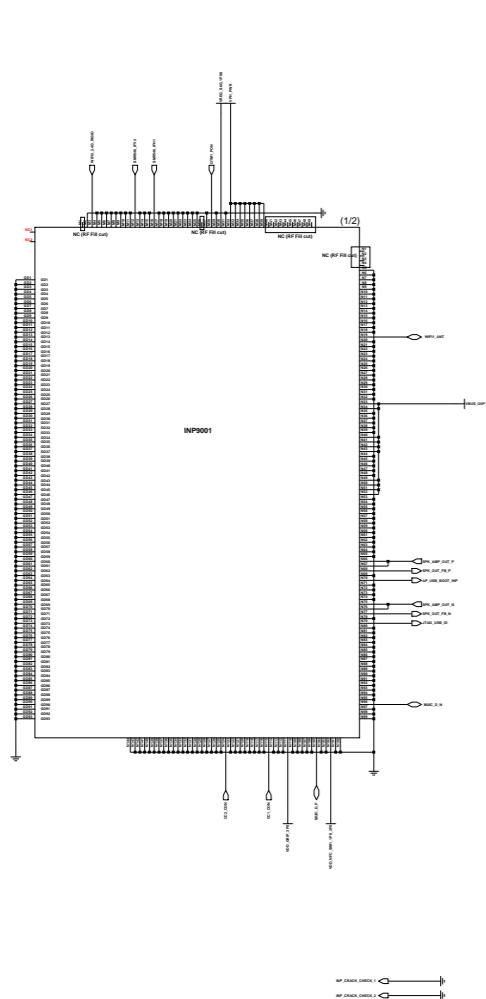


5G IFIO

Radiation

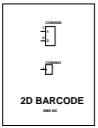


INTERPOSER



S_AP_ID	ID_0	ID_1	ID_2	NA_mmW
1	L	L	H	NA CAN_Sub6
2	L	H	L	CHN_Sub6
3	L	H	H	N/A
4	H	L	L	JPN_mmW
5	H	L	H	KOR_Sub6
6	H	H	L	EUR_Sub6
7	H	H	H	Global_Sub6
				NA

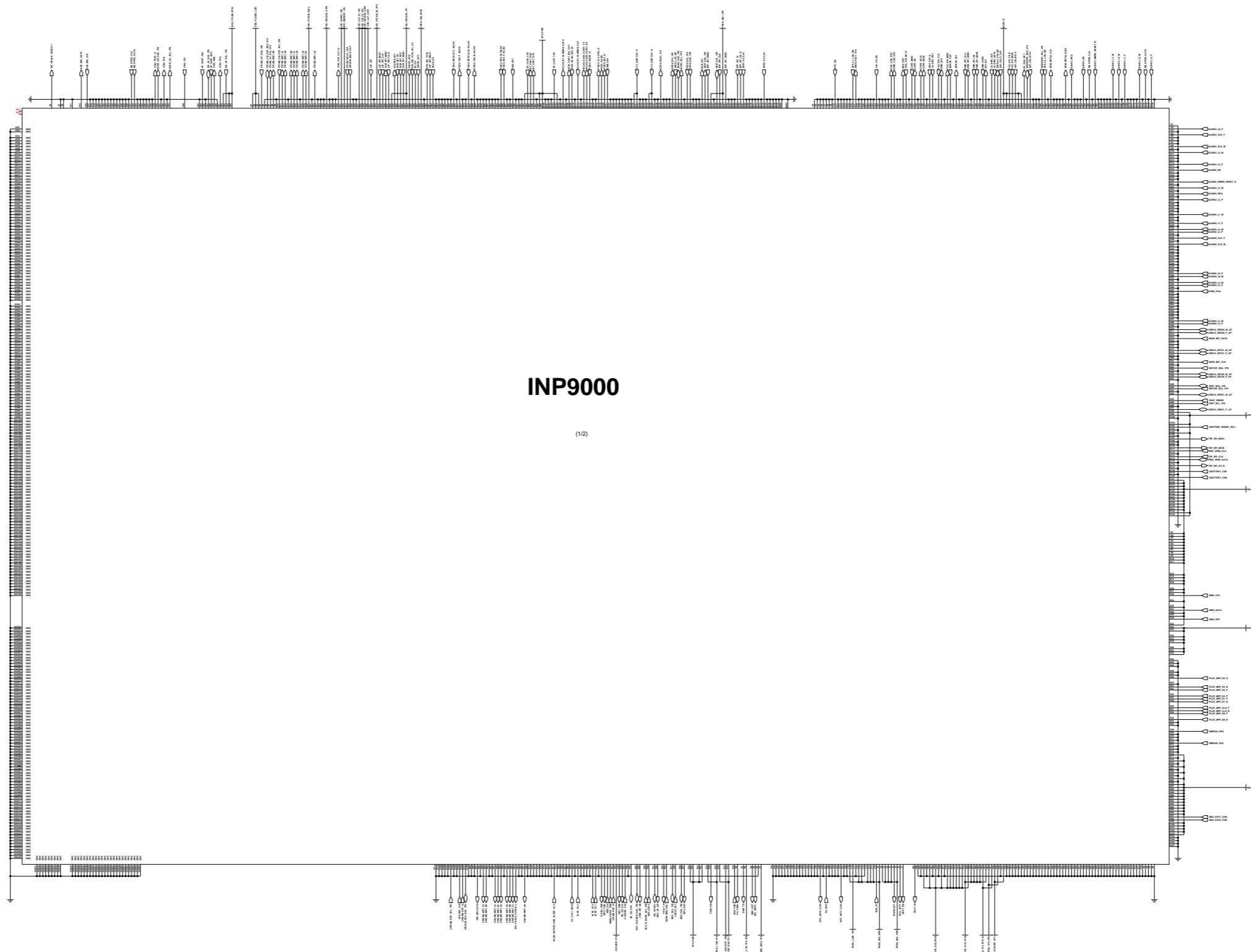
SHIELDCAN

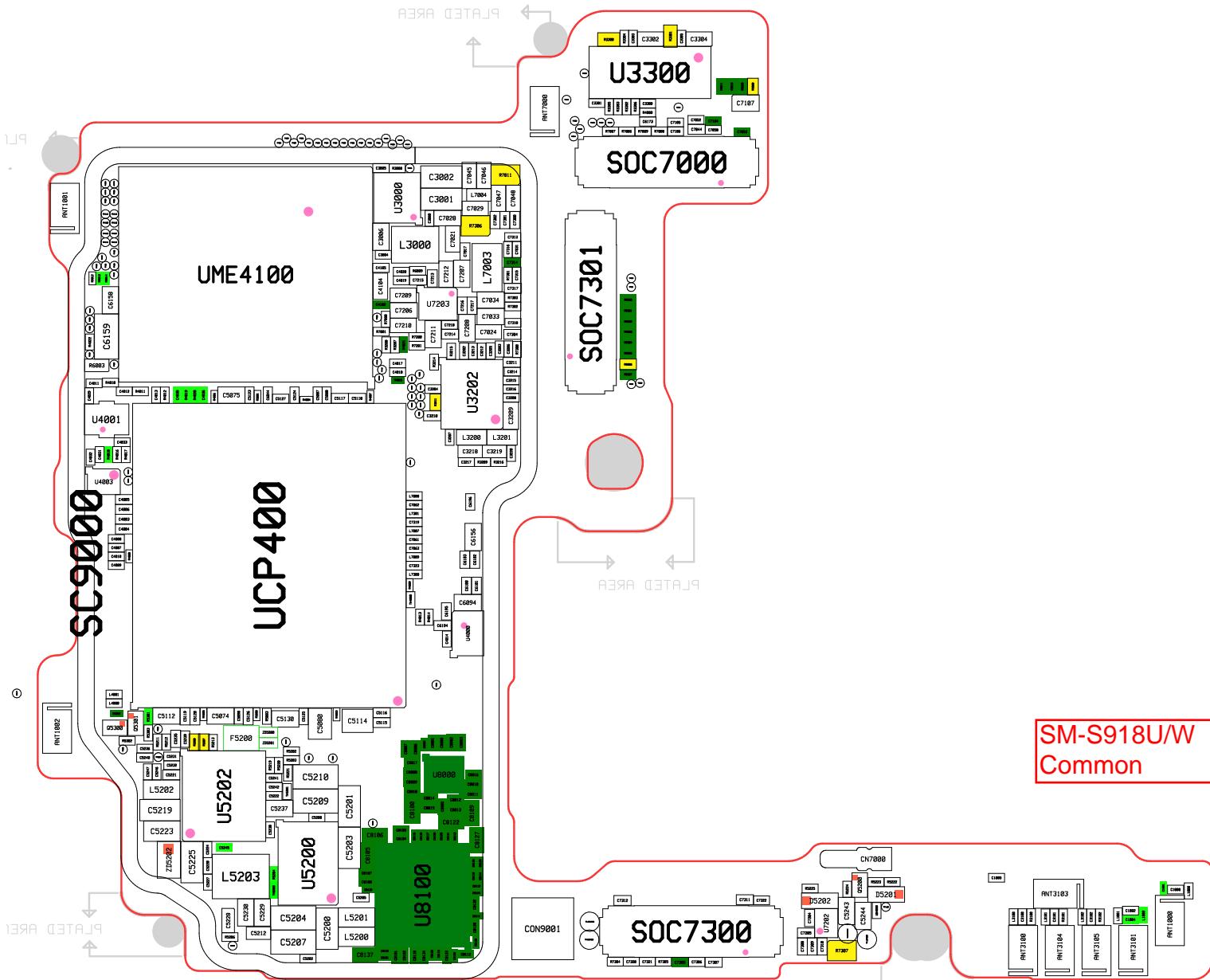


8/256
12/256
12/512
12/1T

NA (U)
CAN (W)
CHN (O)
GLB (E)
JPN (D)
KOR (N)
EUR (B)
N/A

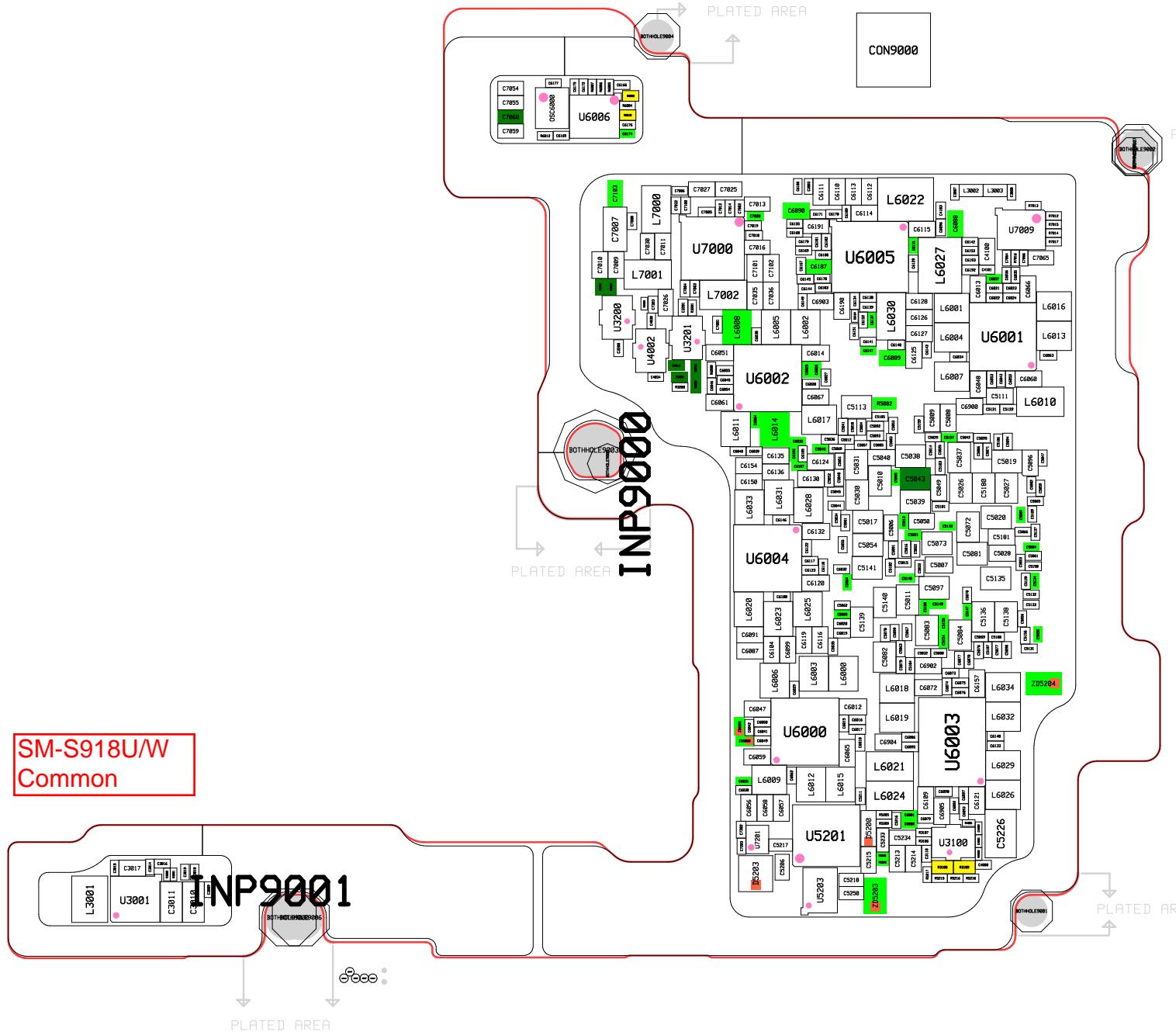
BOSS HOLE





SM-S918U/W
Common





SM-S918U_M_RF

2022.10.25

SHEET01 : RF (1/2)

SHEET02 : RF (2/2), RF PMIC #1

SHEET03 : BT/WIFI, UWB

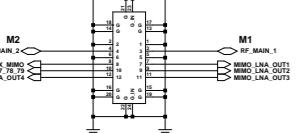
SHEET04 : Battery Conn, W/C Conn, MFC, Sensor

SHEET05 : IF SUB Conn, MOTOR DRV IC, RCV Conn, MIC

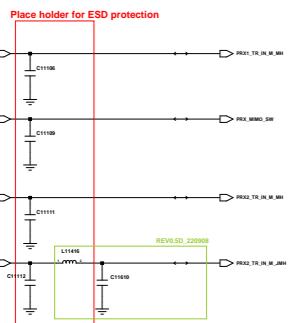
SHEET06 : DISPLAY PMIC, DISPLAY Conn, DIGITIZER, CAM Conn

SHEET07 : INTERPOSER

Engineer:	
Drawn by:	
REQ CHK:	
DOC CTRL CHK:	
MFG ENGR CHK:	

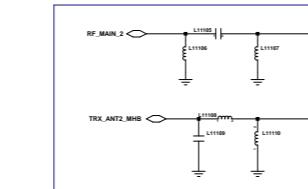


MAIN FRC



MAIN1 ANT (LB, MB, JPN B21)

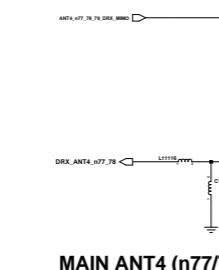
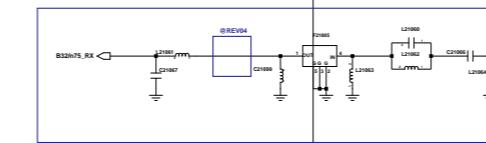
MAIN2 ANT (HB_n41)



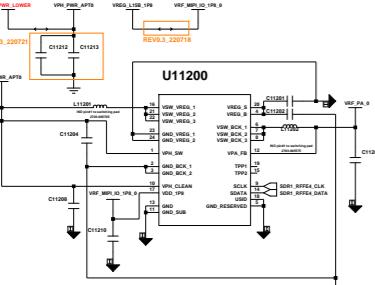
MHB MIMO_LNA OUT



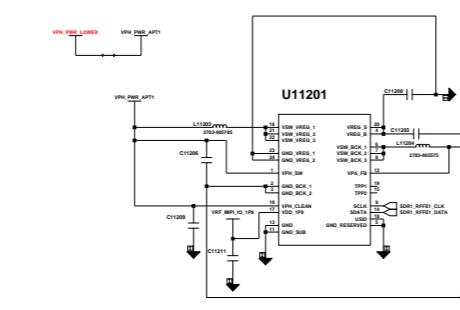
MAIN ANT3 (B48_n77/78/79)



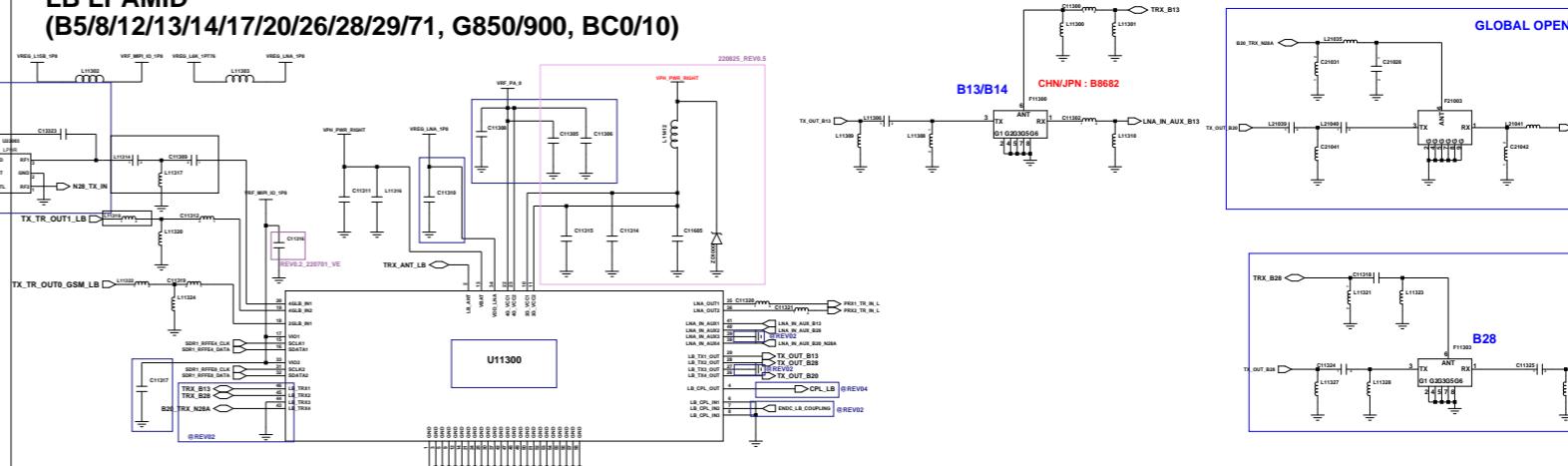
ET Modulator(QET6105#0)



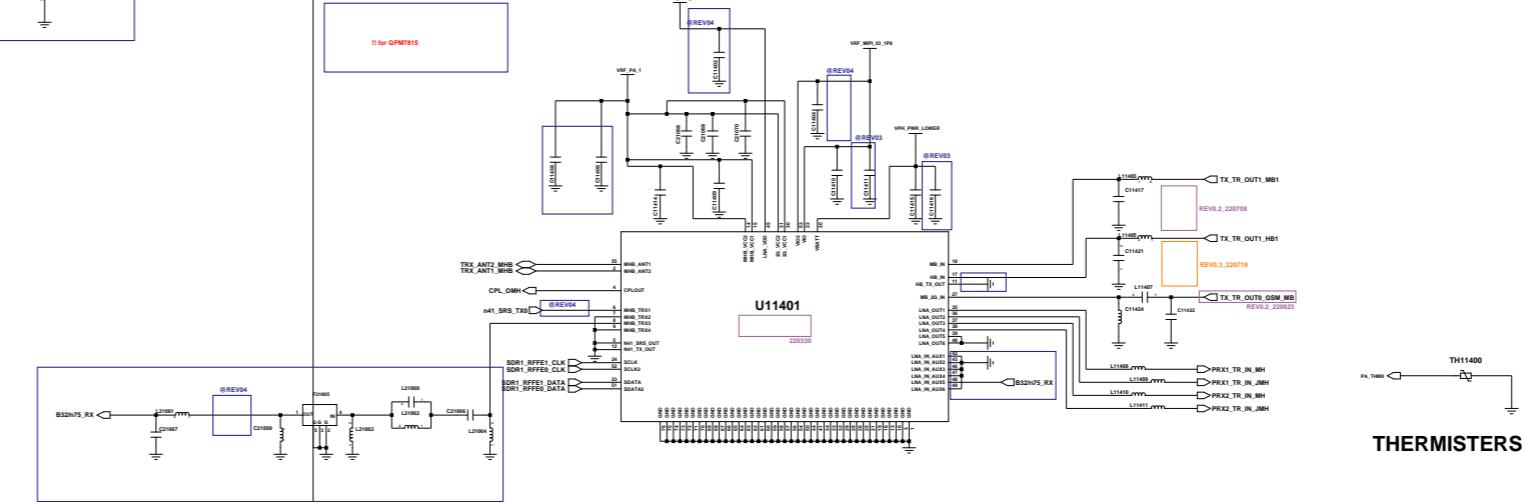
ET Modulator(QET6105#1)



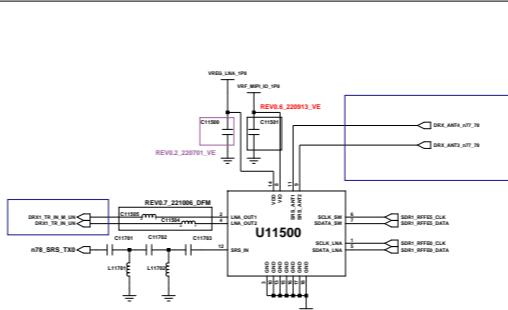
LB LPAMID (B5/8/12/13/14/17/20/26/28/29/71, G850/900, BC0/10)



OMH LPAMID (B1/2/3/4/7/25/30/34/38/39/40/41/66, G1800/1900, BC1)

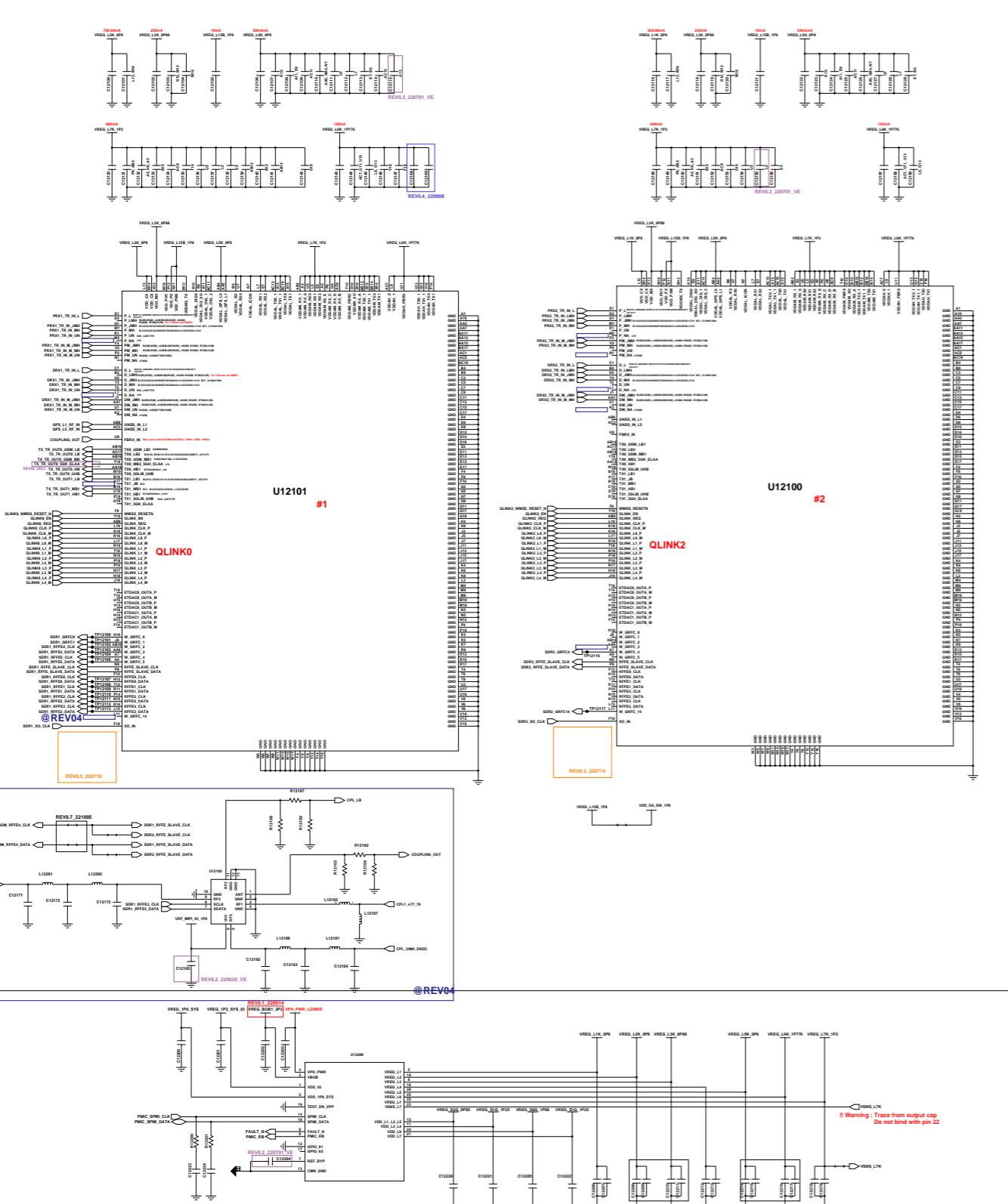


THERMISTERS

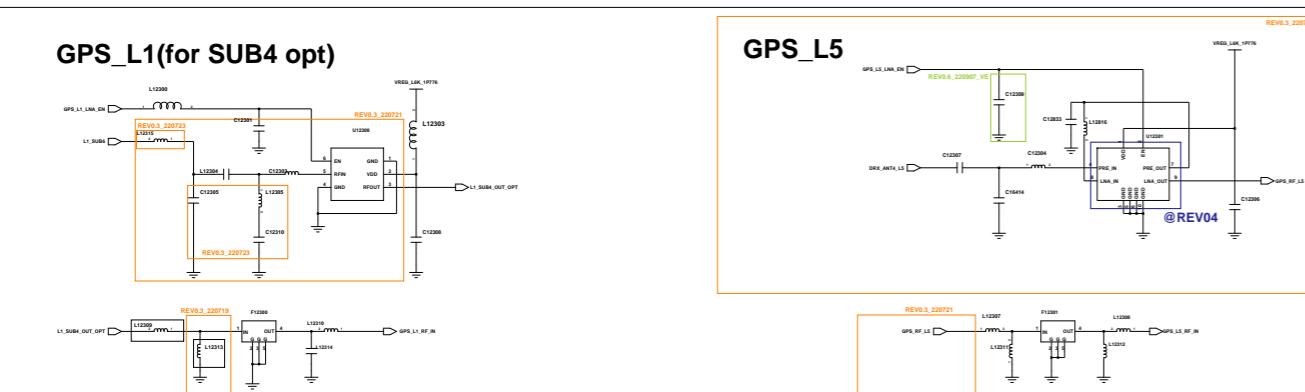


Engineer:	SEC
Drawn by:	SEC
Reviewed by:	SEC
Approved by:	SEC
DOC CTRL:	SEC
WIF CTRL:	SEC
WIF ISSUE:	SEC
RF (1/2)	SEC

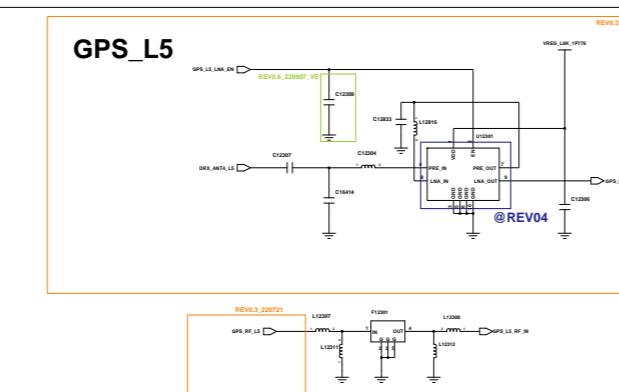
TRANSCEIVER(Dual SDR735)



GPS_L1(for SUB4 opt)

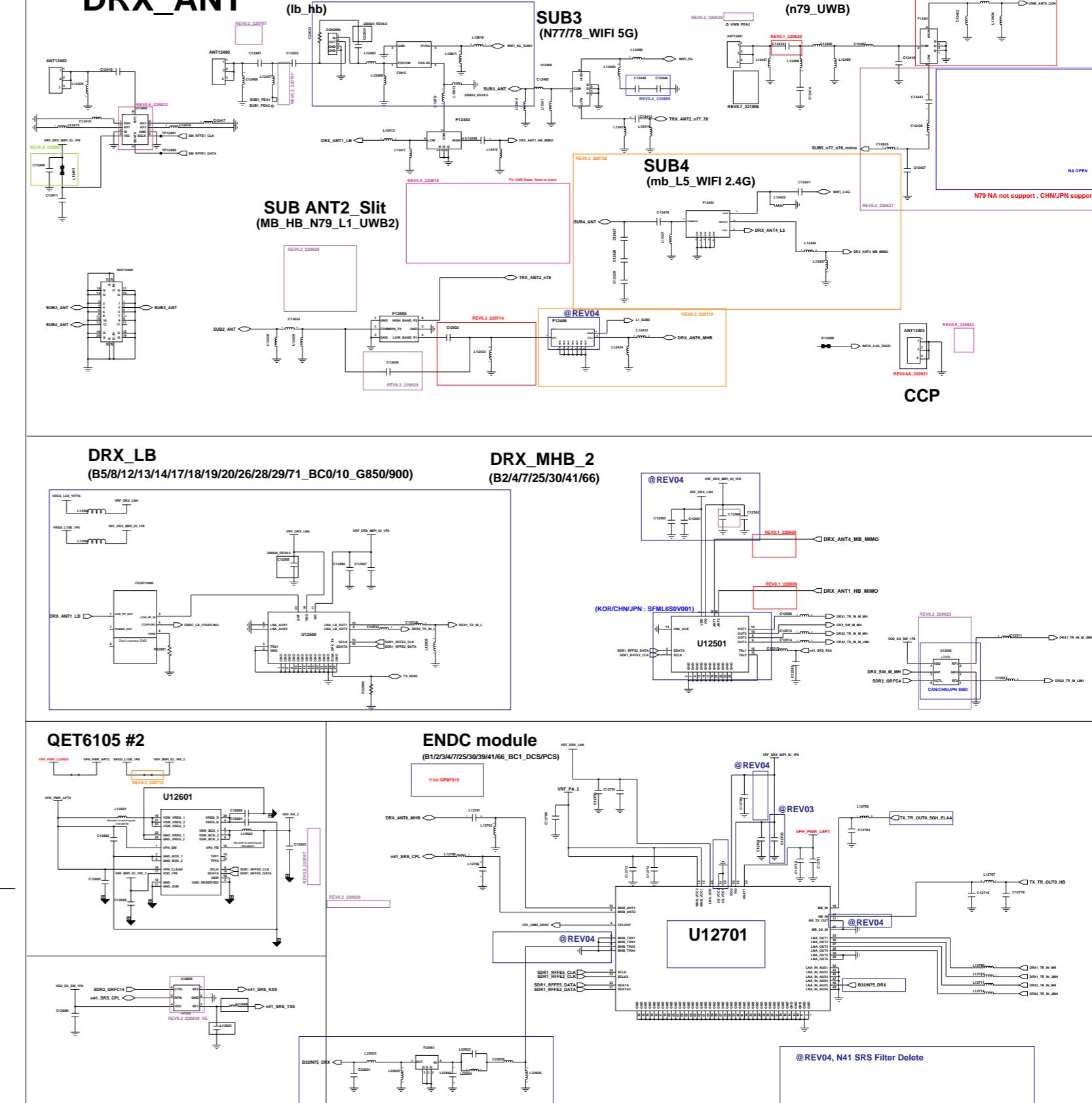


GPS_



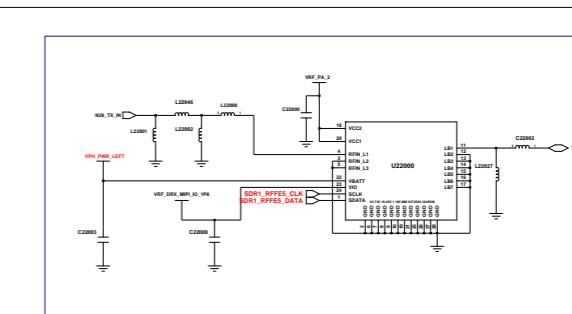
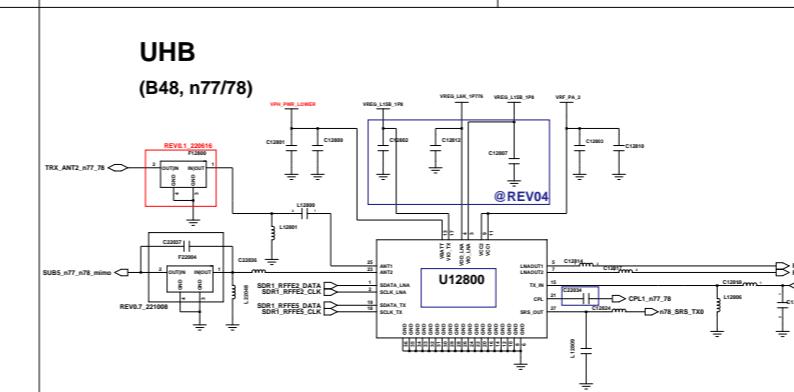
DRX_AN

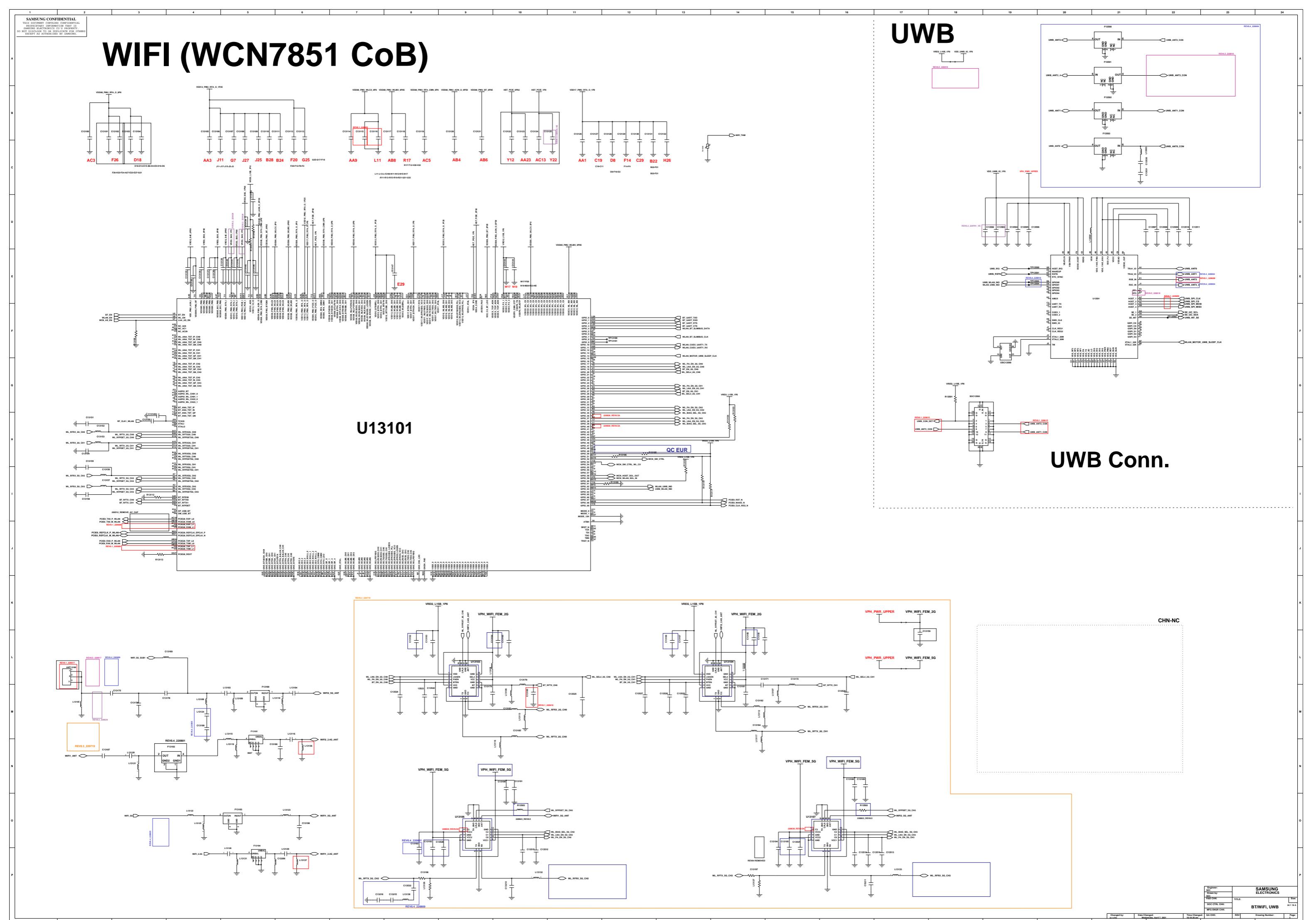
T SUB ANT1_Fram (lb hb)



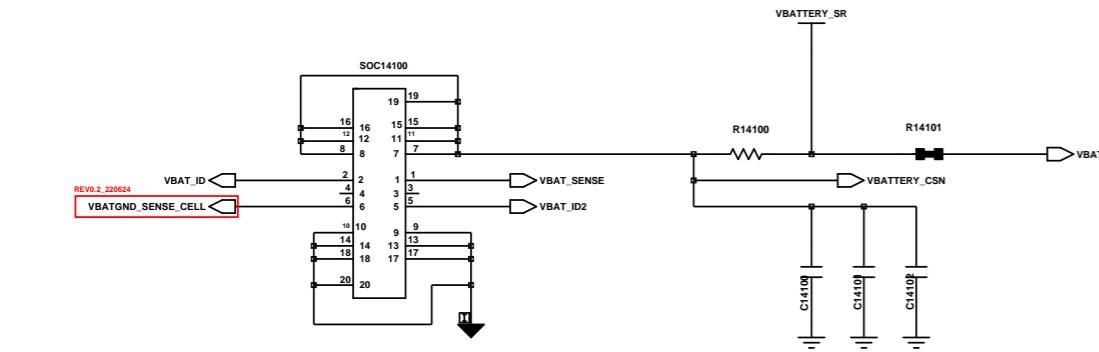
11

(B48, n77)

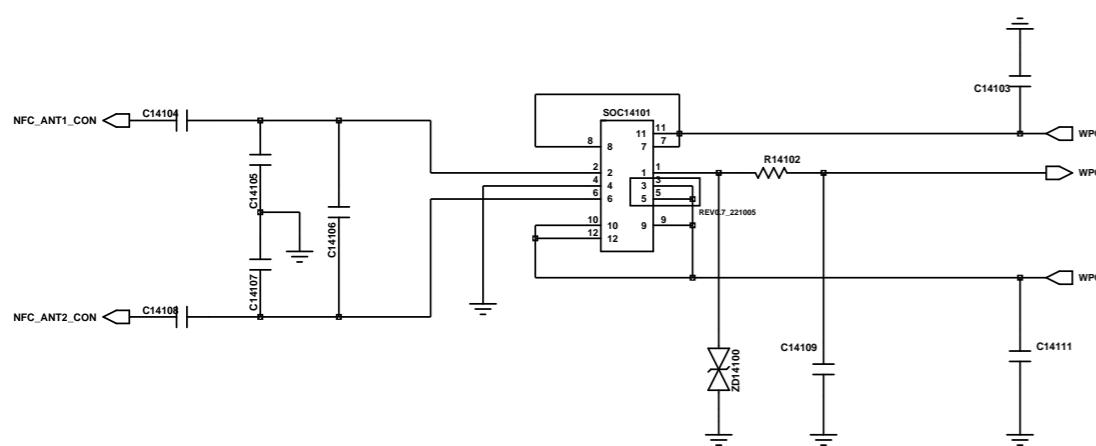




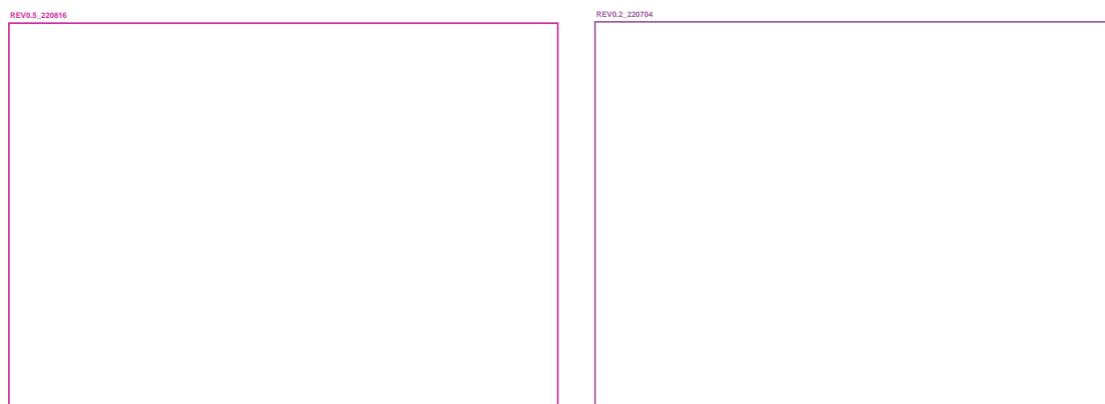
Power



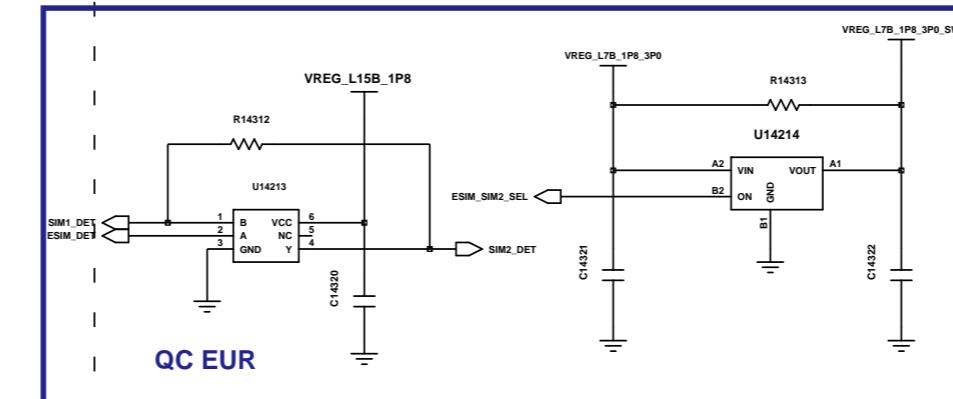
Battery Connector



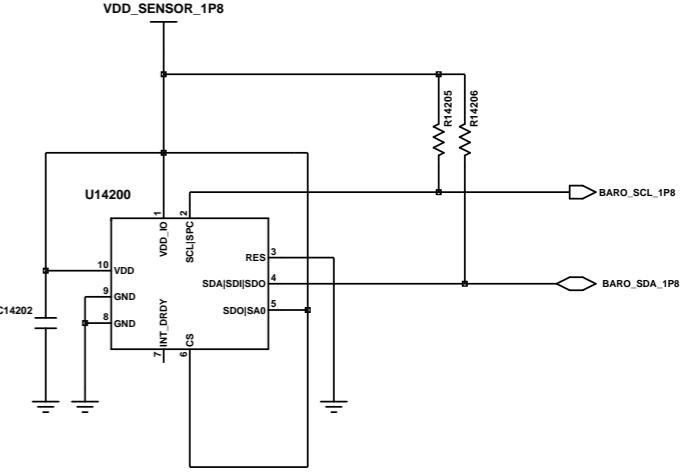
W/C CONNECTOR



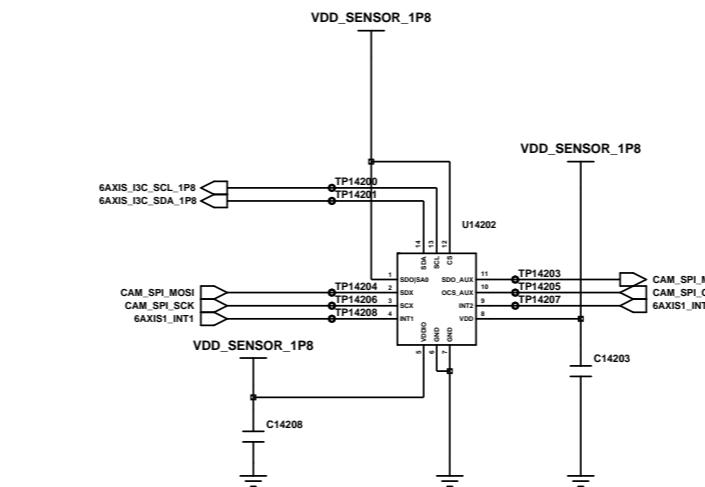
MST SW



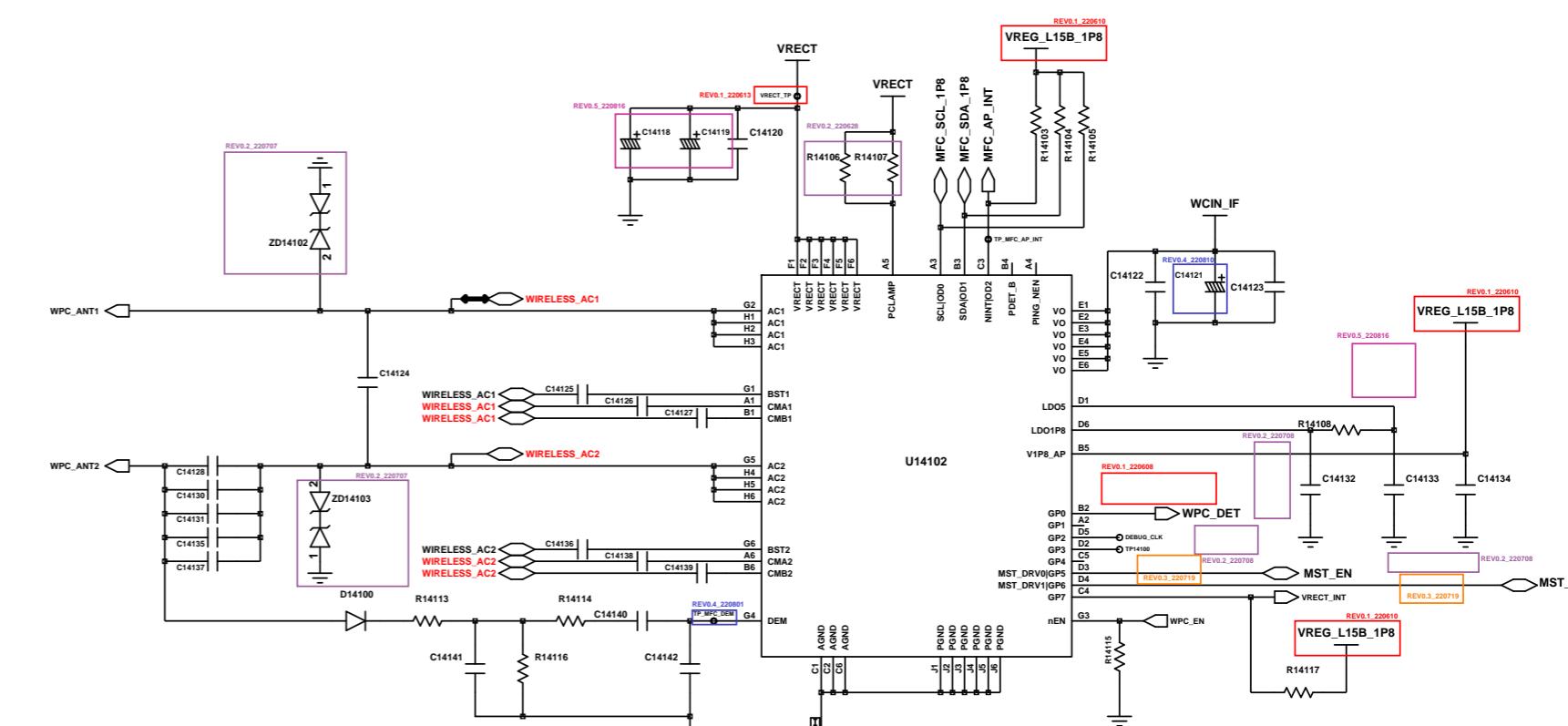
QC EUR



Barometer SENSOR



6 Axis SENSOR (GYRO,ACCEL)

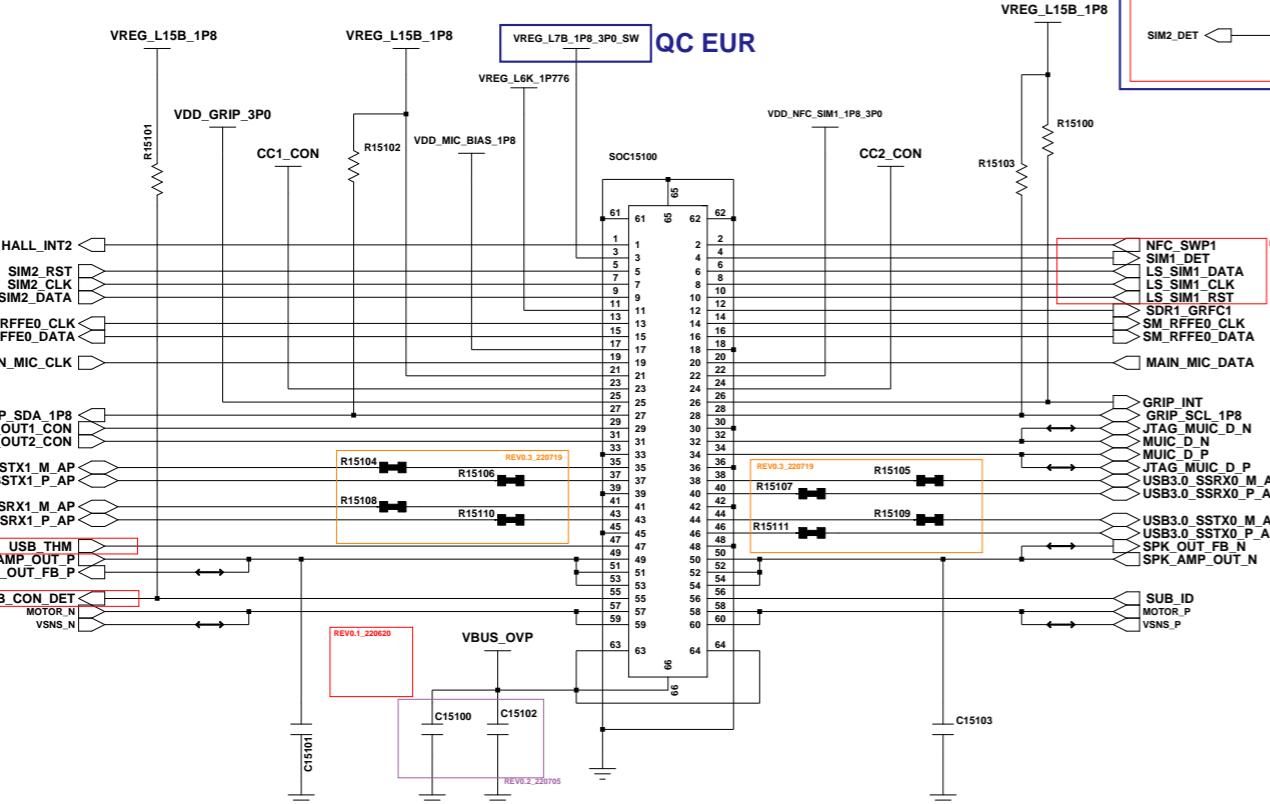


MFC

Engineer: SEC	SAMSUNG ELECTRONICS
Drawn by: SEC	
R&D CHK: DOC CTRL CHK:	TITLE: DM1_R00_M_RF_220511_START
MFG ENGR CHK:	Size: 1218A
Changed by: kit00.kim	
Date Changed: Tuesday, May 10, 2022	
Time Changed: 5:59:13 pm	
QA CHK: REV	
Drawing Number: Page: 4	

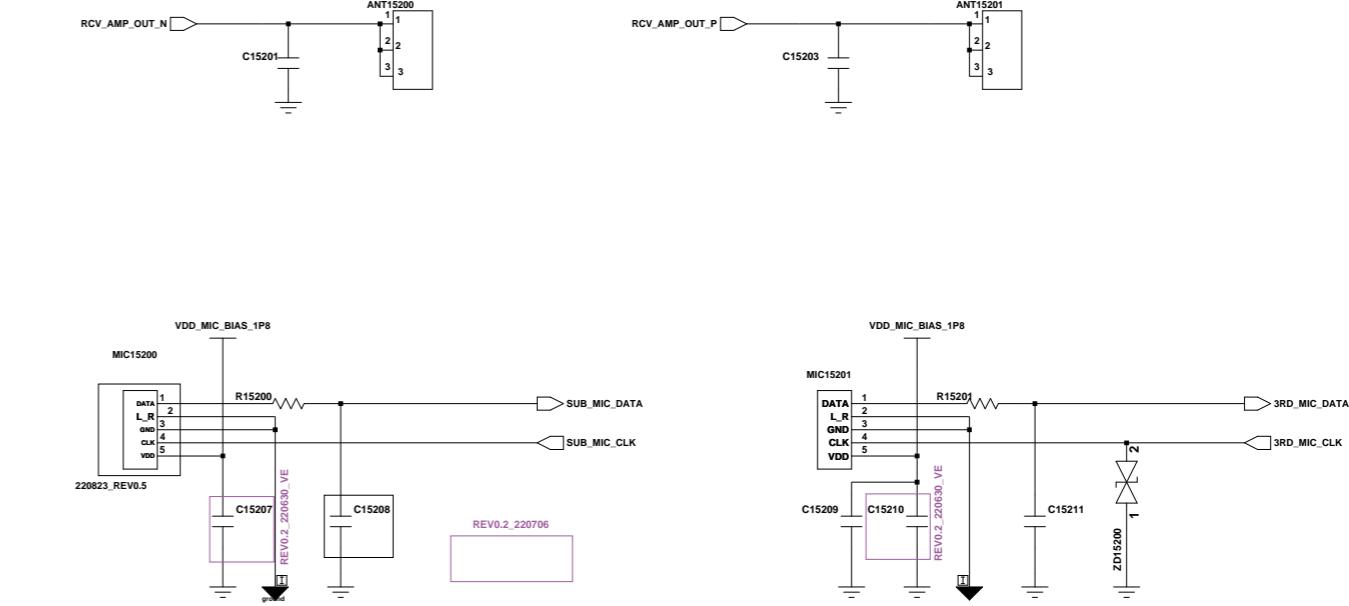
System

AUDIO



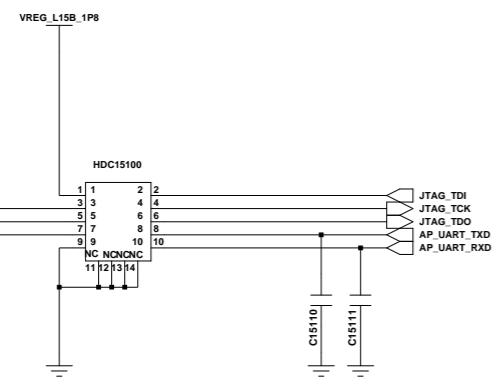
IF SUB PBA Conn

RCV Conn.



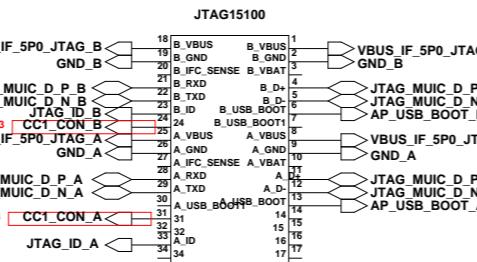
2nd MIC

3rd MIC

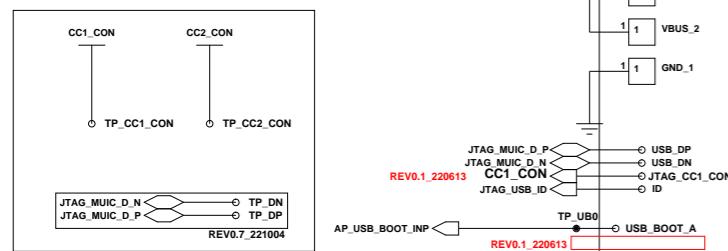


JTAG Conn

**CAUTION: PLACEMENT
Near to TP_AP_USB_BOOT**



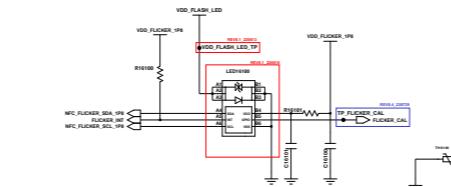
PCB array JTAG



MOTOR DRV IC

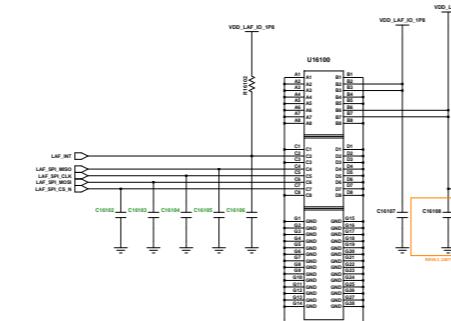
Engineer:	SAMSUNG ELECTRONICS	
Drawn by:	SEC	
R&D CHK:	SEC	
DOC CTRL CHK:	TITLE: AUDIO	
MFG ENGR CHK:	Size: 1218A	

CAMERA



Flash & Flicker sense

CAM PMIC (PB02)

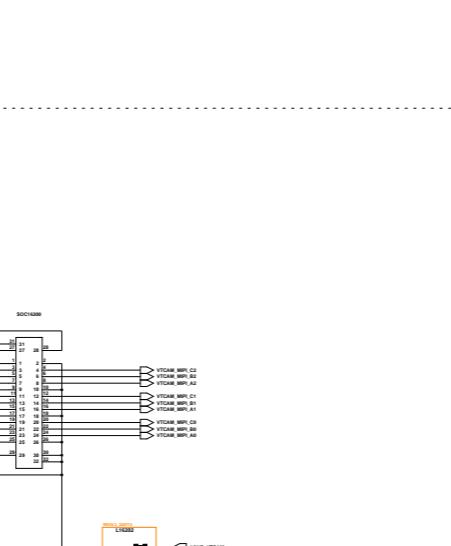


RANGING sens

Wide CAM



Tele CA



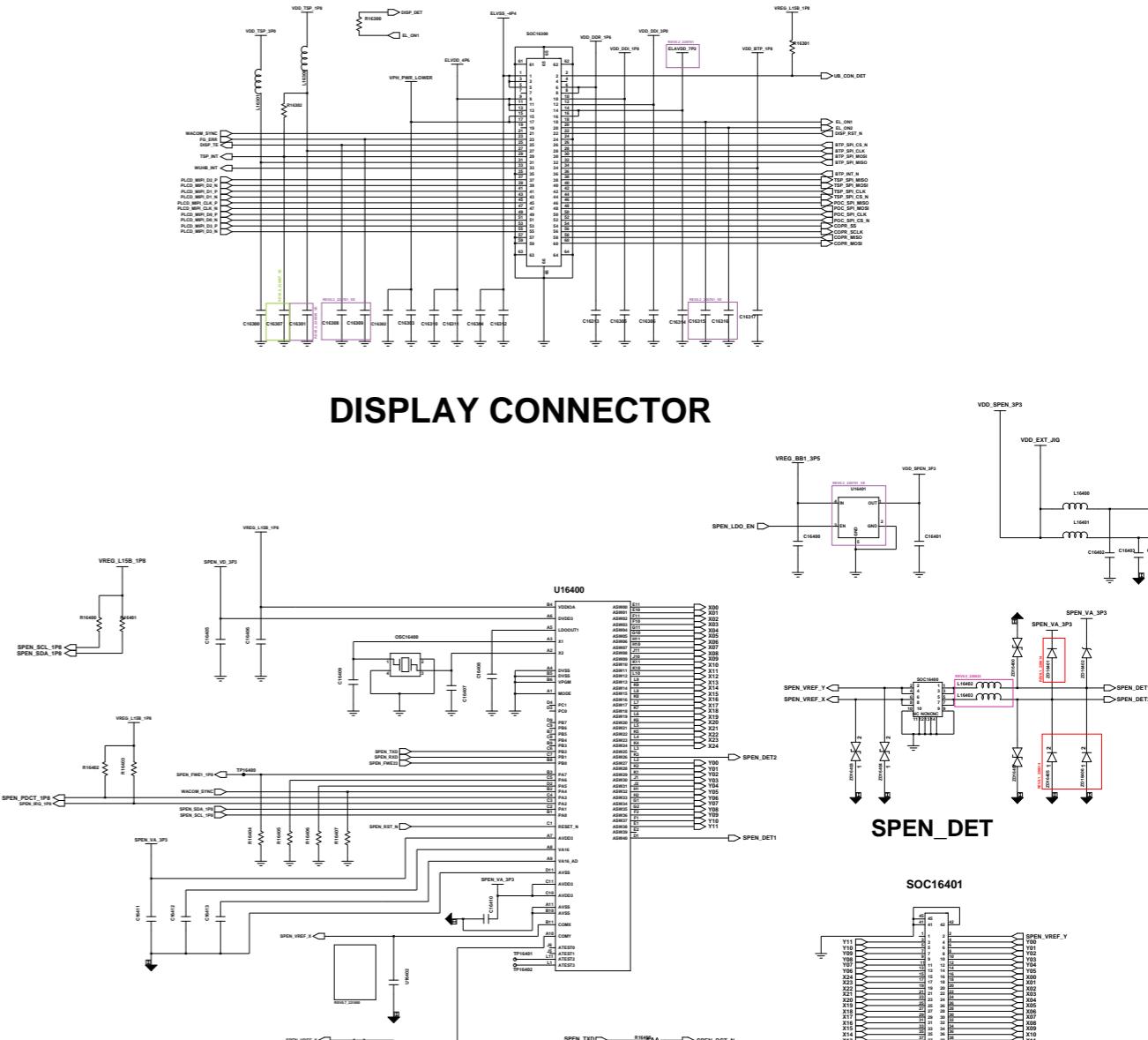
ULTRA WIDE CAR



CAM MC



DIGITIZER(WEZ02)

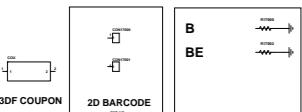
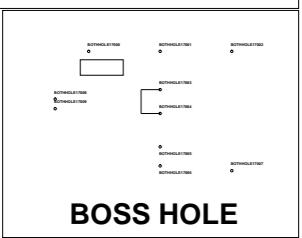
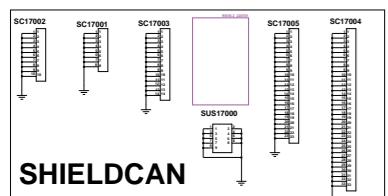
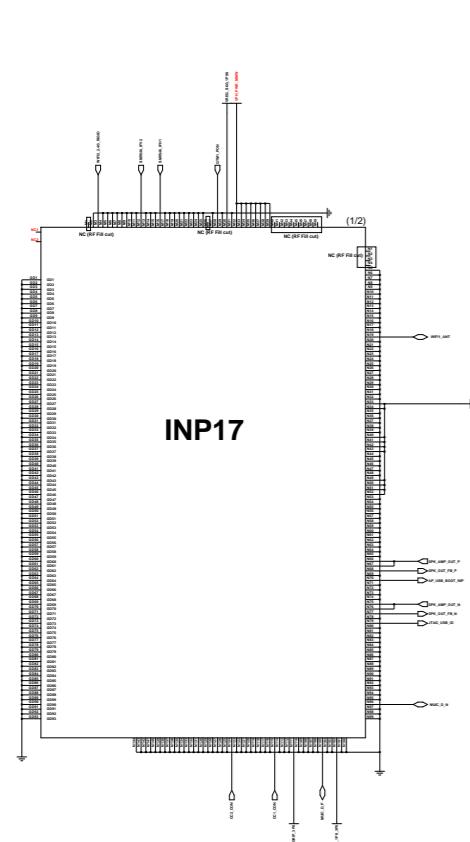


DIGITIZER TP



DIGITIZER CONN

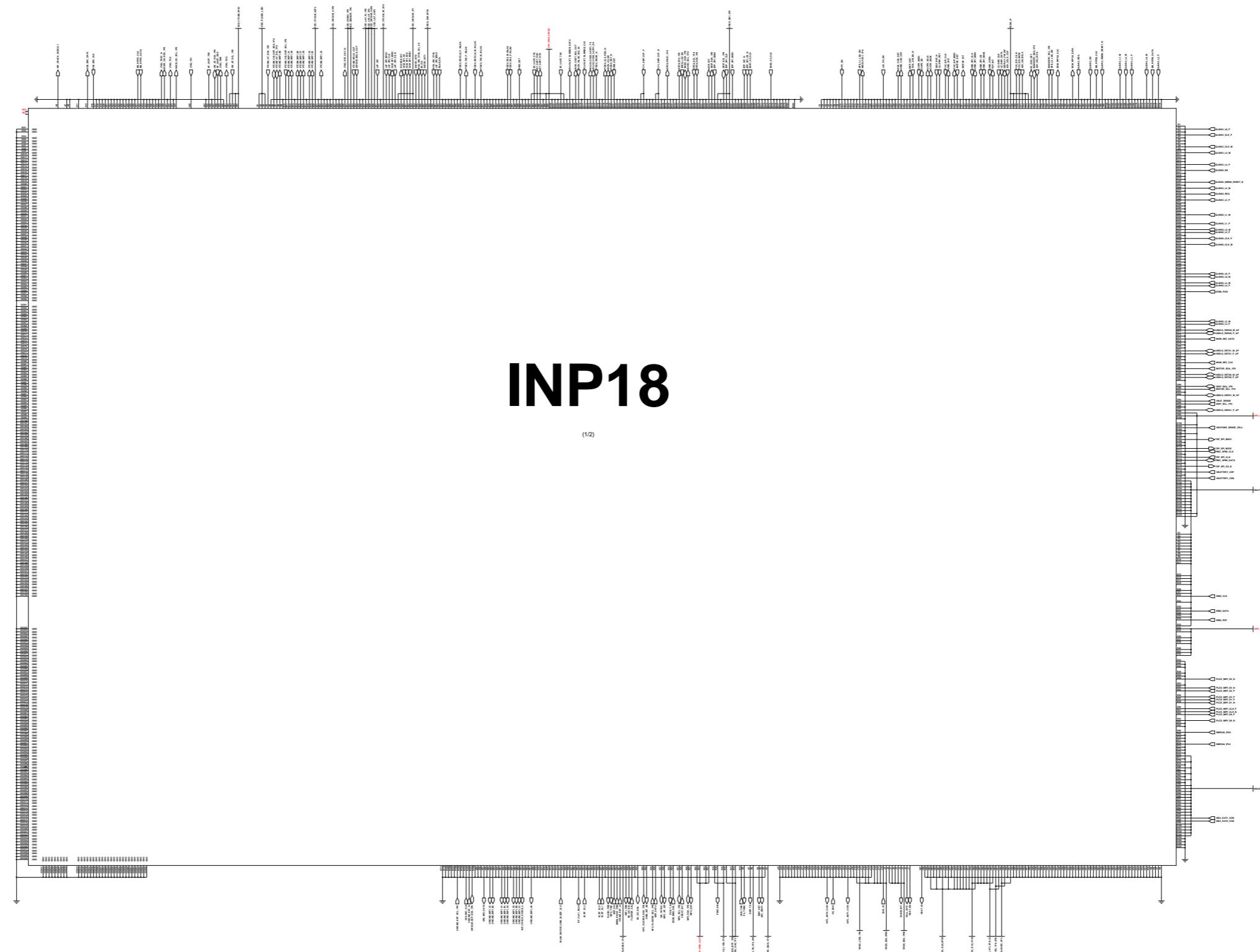
INTERPOSER Design



M_RF_PBA table

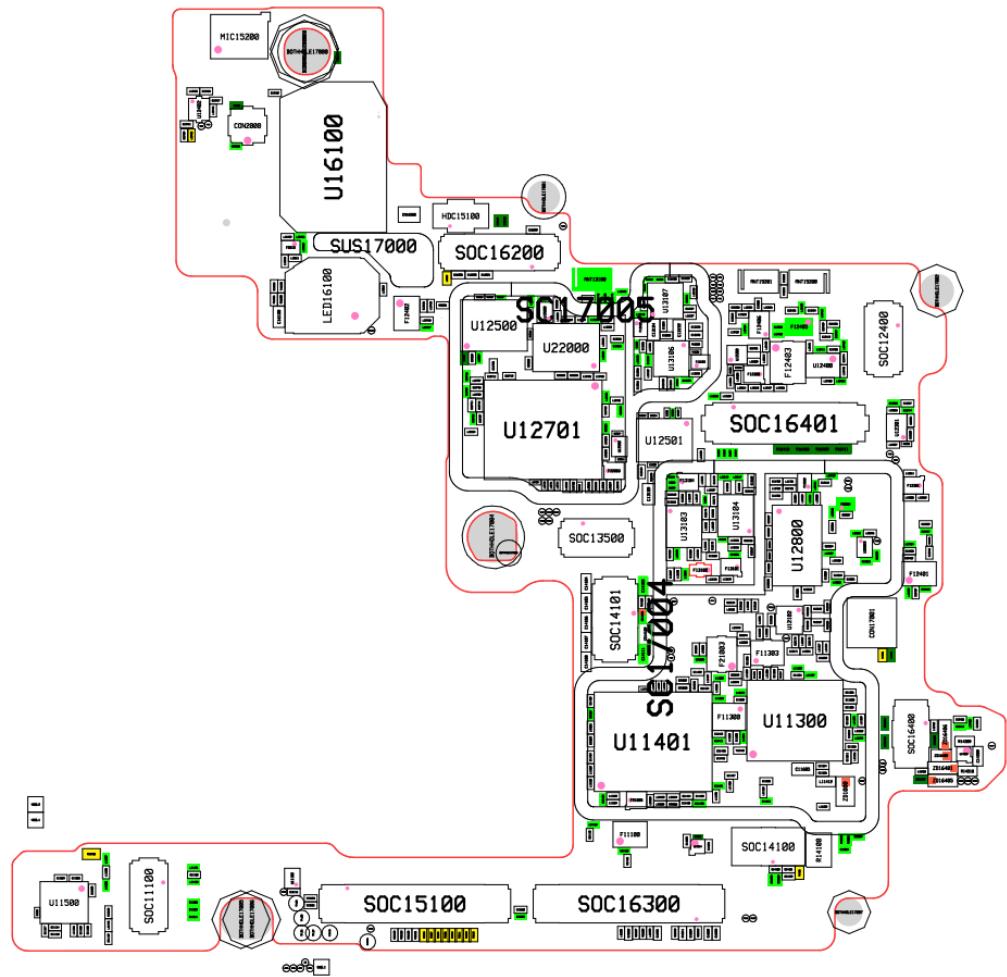
ID	ID	D	I	D	S	DM3_M_RF
0	L	L	L	N/A	mmW	
1	L	L	H	N/A	CAN_Sube	
2	L	H	L		CHN_Sube	
3	H	L	L		JPN_mmW	
4	H	L	L		JPN_mmW	
5	H	L	H		KOR_Sube	
6	H	H	L		EUR_Sube	
7	H	H	H		N/A	

M_RF_ID

RF CHECK CHECK_1
RF CHECK CHECK_2

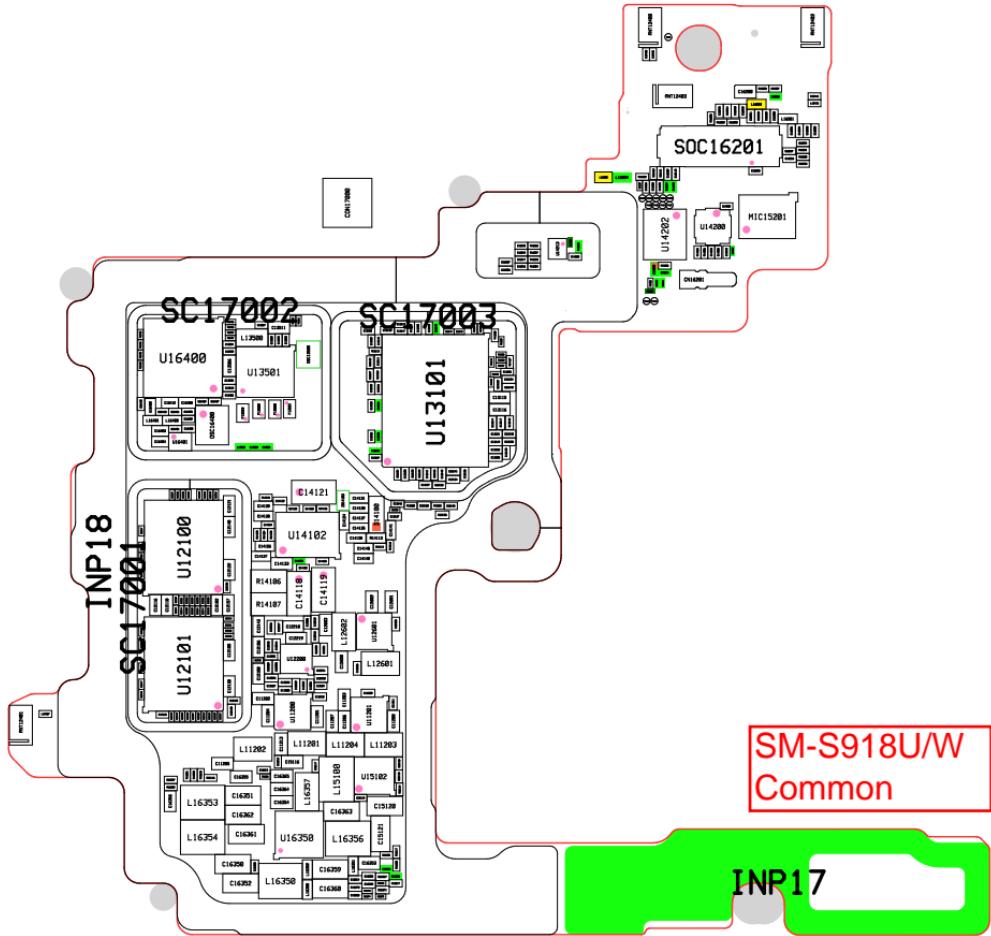
INP18

(1/2)



SM-S918U/W

Common



SM-S918U/W
Common

INP17

JTAG15100

DM3 EUR IFSUB PBA

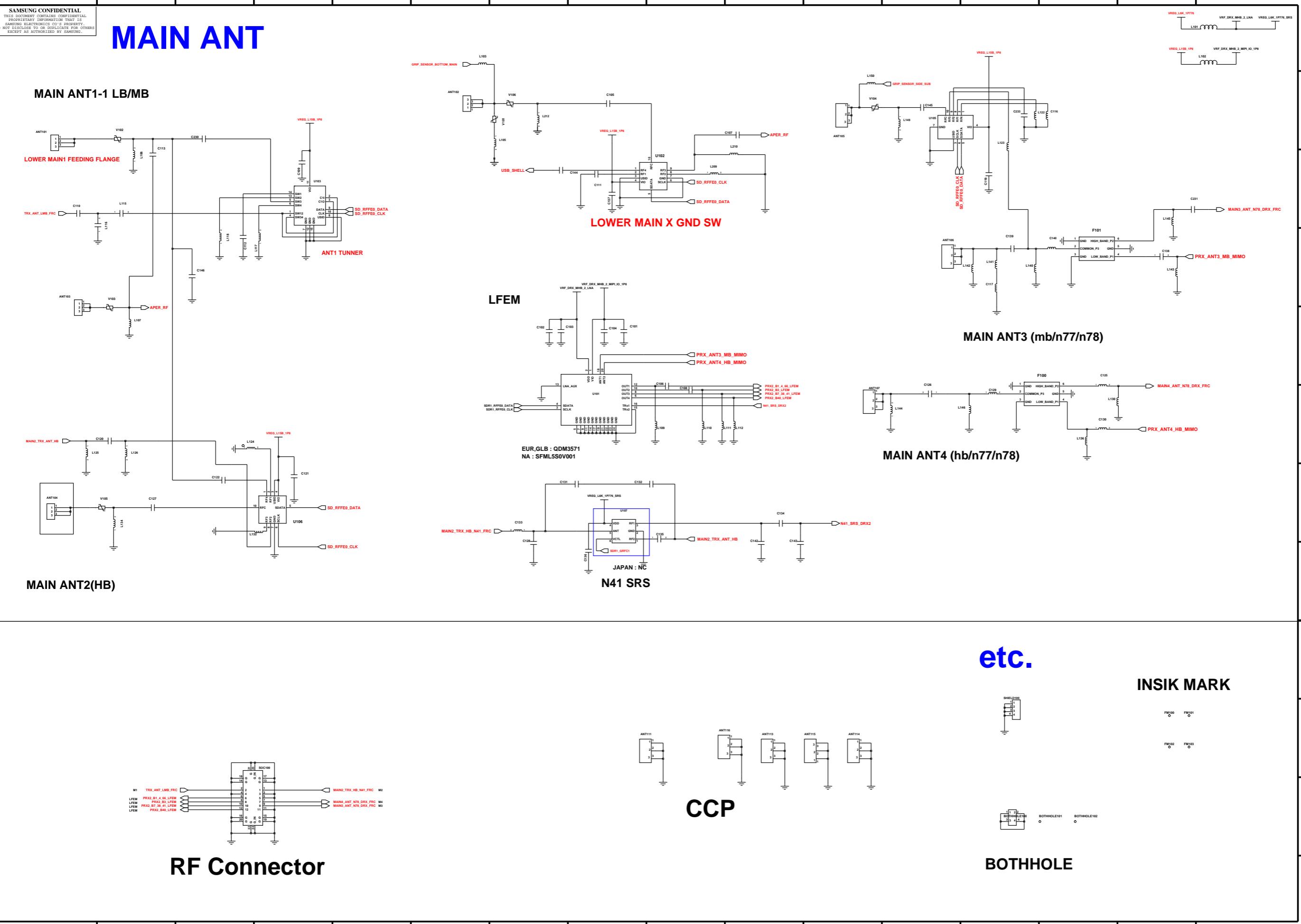
2022. 09. 27

sheet01 : ANT

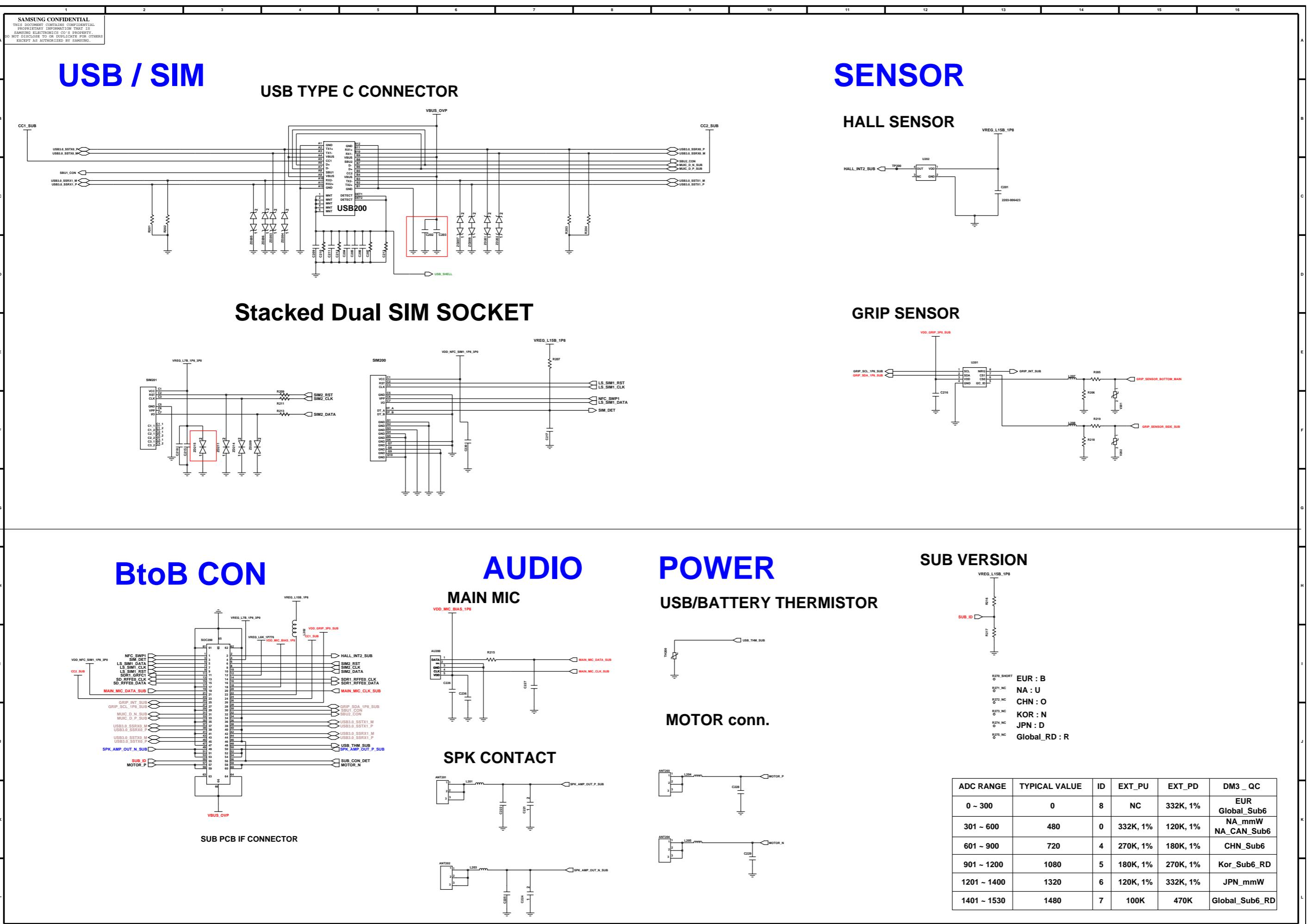
sheet02 : LOGIC

Engineer:	
Drawn by:	
R&D CHK:	TITLE:
DOC CTRL CHK:	Size:
MFG ENGR CHK:	L

LOWER USB PBA



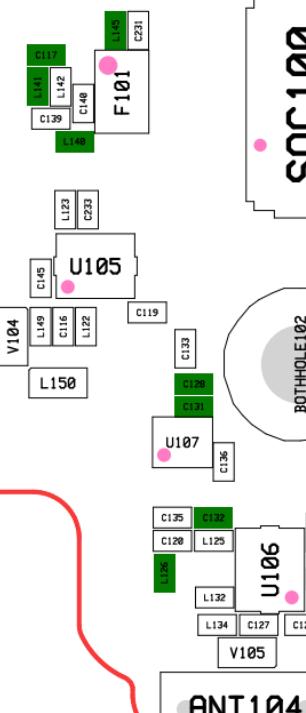
LOWER USB PBA



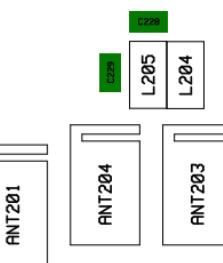
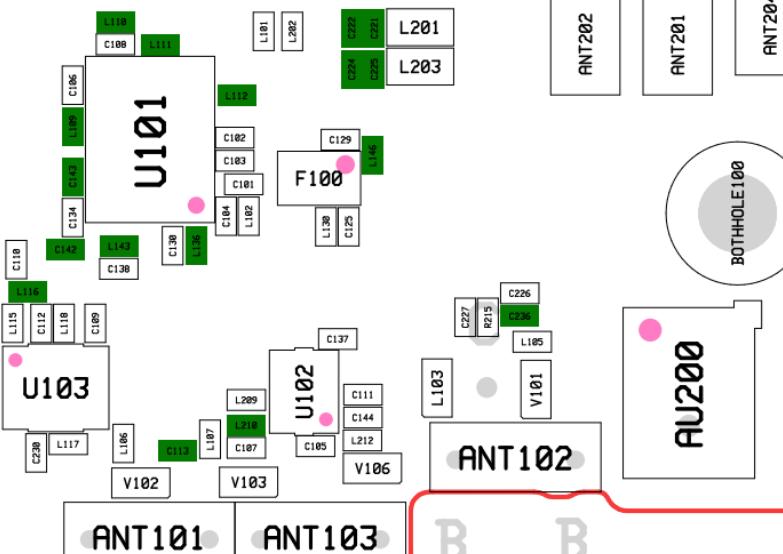
SM-S918U/W
Common

REF1,NC
REF2,NC
REF3,NC
REF4,NC
REF5,NC

PRB0



SOC200



B B

B B B B B

B B B

E E D D F F

SM-S918U/W
Common

USB200

SHIELD100

ANT113

U202

C201

SIM200

SIM201

TP000
P000

ANT116

C210

C211

C212

C208

C209

C207

C213

C206

C205

C204

C203

ZD201

ZD202

ZD204

ZD203

ZD206

ZD205

ZD207

ZD208

R202

R201

ZD209

ZD200

TP000
P000

C215

C214

C217

C216

C218

C219

R205

R206

L207

L208

R218

R219

V201

V202

ANT111

ANT115

U201

ANT106

ANT105

R211

ZD204

R217

C215

R287

C217

R213

ZD205

ZD206

ANT114

C216

ANT115

U201

ANT106

ANT105