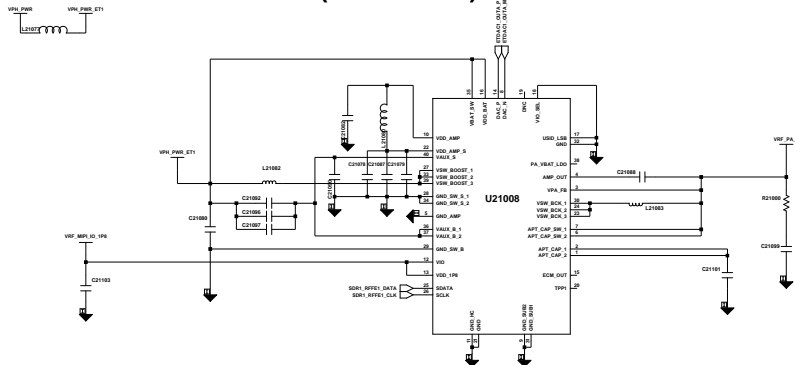
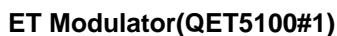
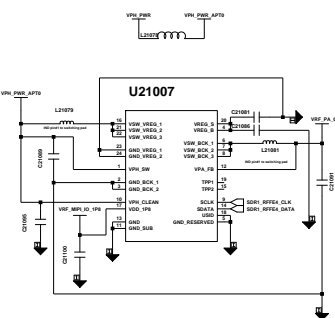
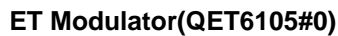
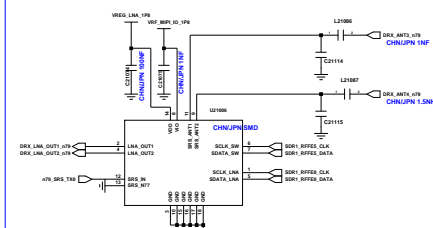
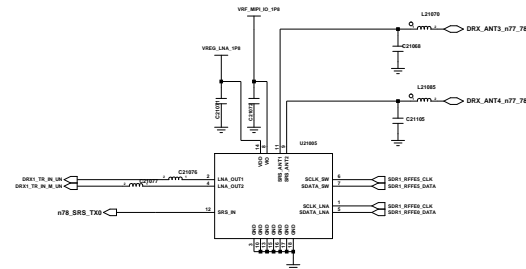
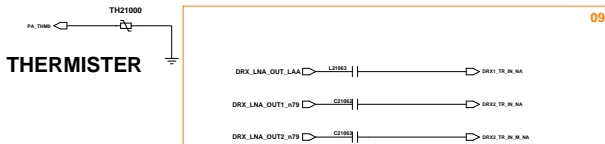
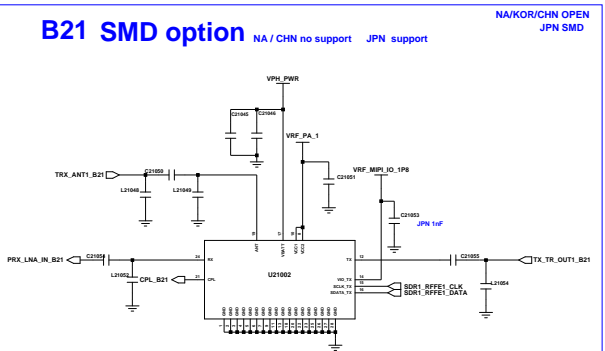
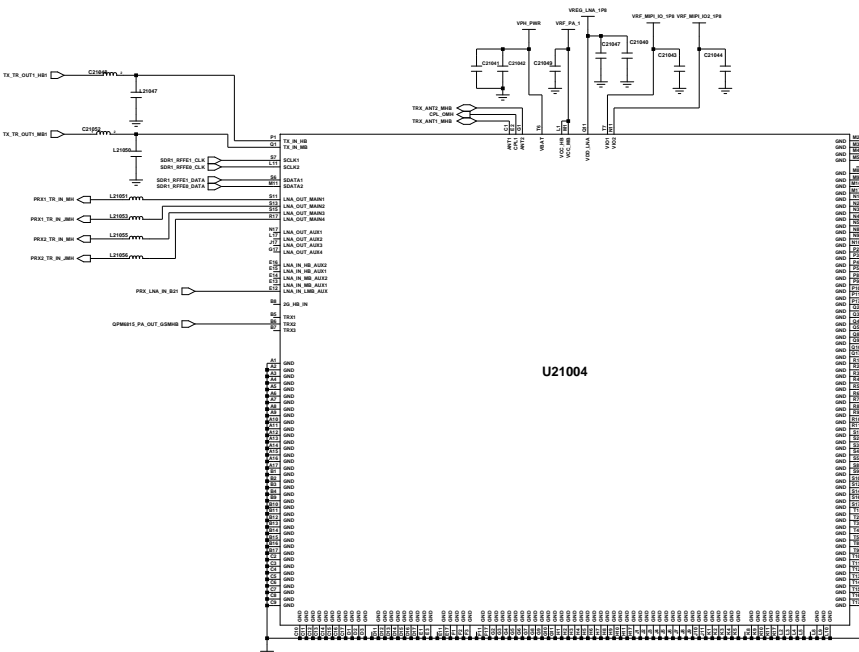
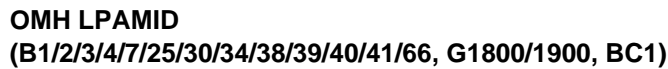
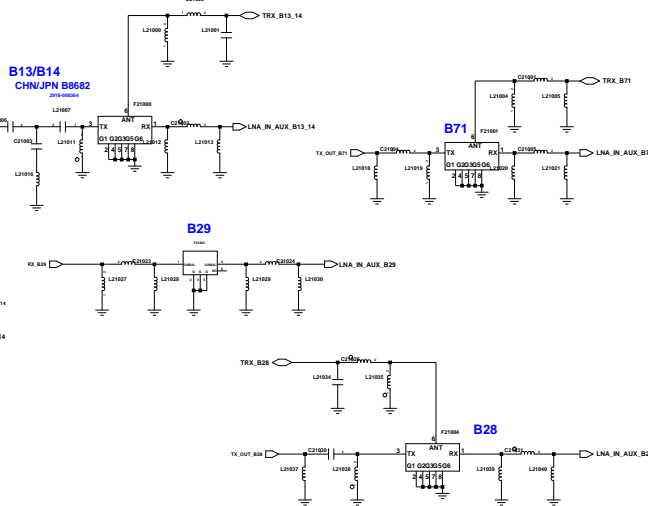
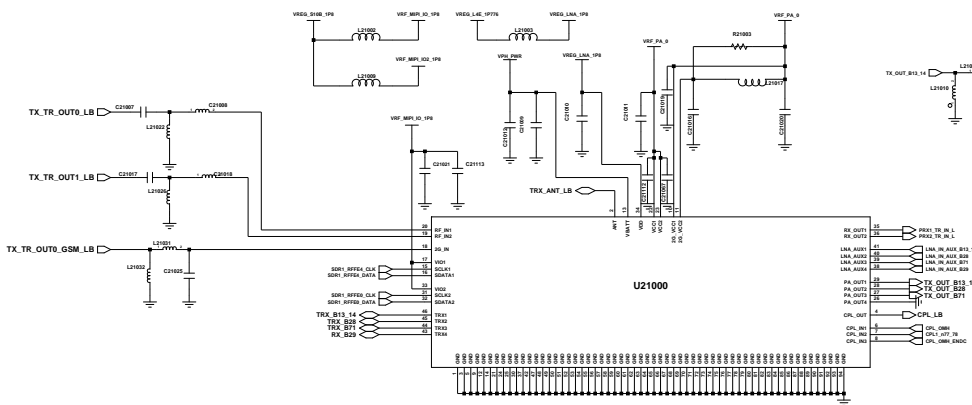
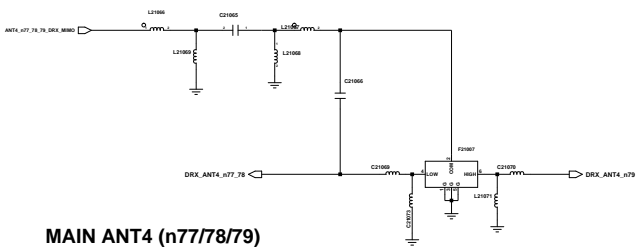
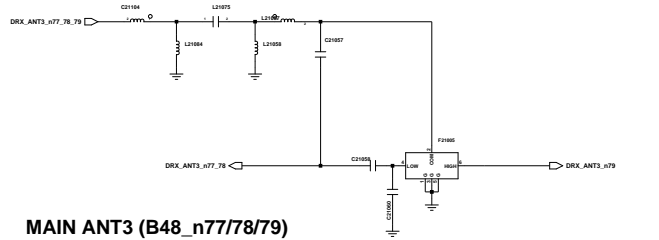
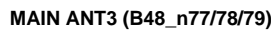
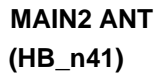
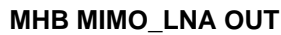


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<div><div>SM-S908U MASTER RF REV0.7B</div></div>																B										
<div><div>11 2021</div></div>																C										
																D										
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1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16											
													Changed by: sznd		Date Changed:		Time Changed:		QA CHK:		REV		Drawing Number:		Page: 6	
													Engineer:		Drawn by:		RED CHK:		DOC CTRL CHK:		MASTER CHK:		TITLE: U_SMB350		Size: 6	

SM-S908U MASTER RF REV0.7B

11 2021

Engineer:		
Drawn by:		
RDG CHK:	TITLE:	Size:
DOC CTRL CHK:	U_S908U50	
MFG ENGR CHK:		
QA CHK:	REV:	Drawing Number:
		Page:



2G_PA DELETE



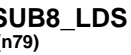
DRX_MHB_2
(B2/4/7/25/30/41/66)



(B1/2/3/4/7/25/30/39/41/66_BC1_DCS/PCS)

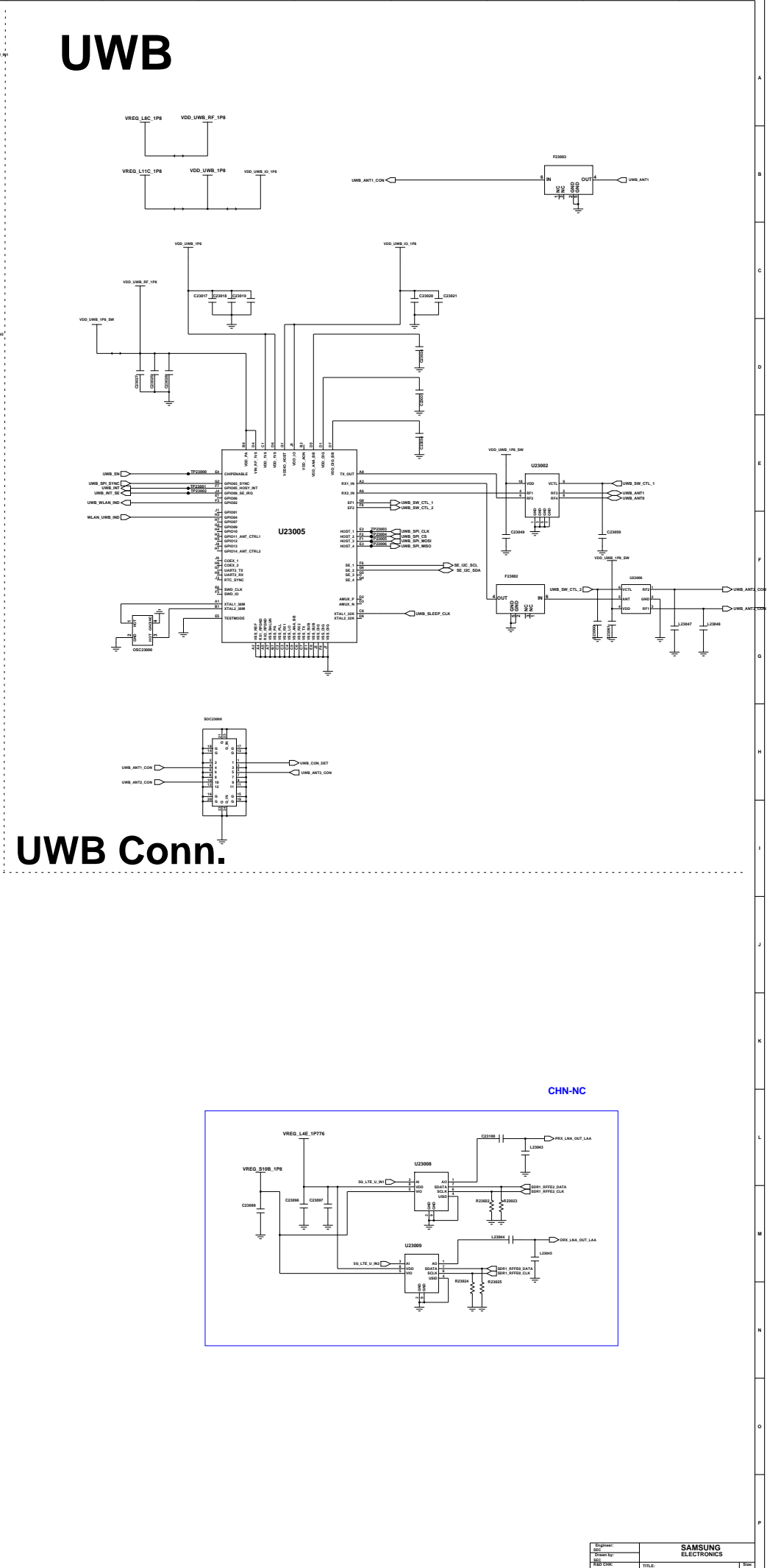


(B48, n77/78)



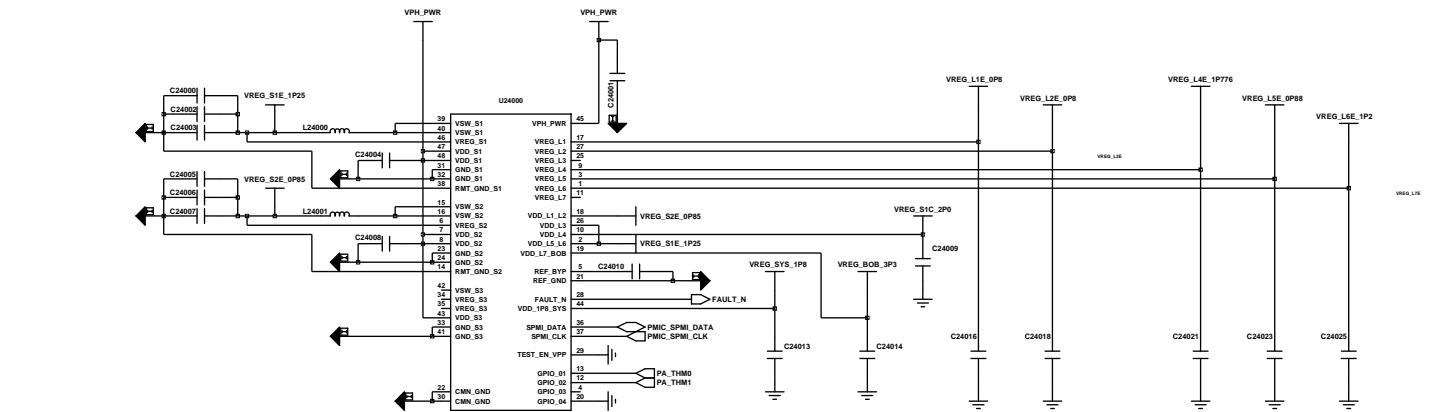
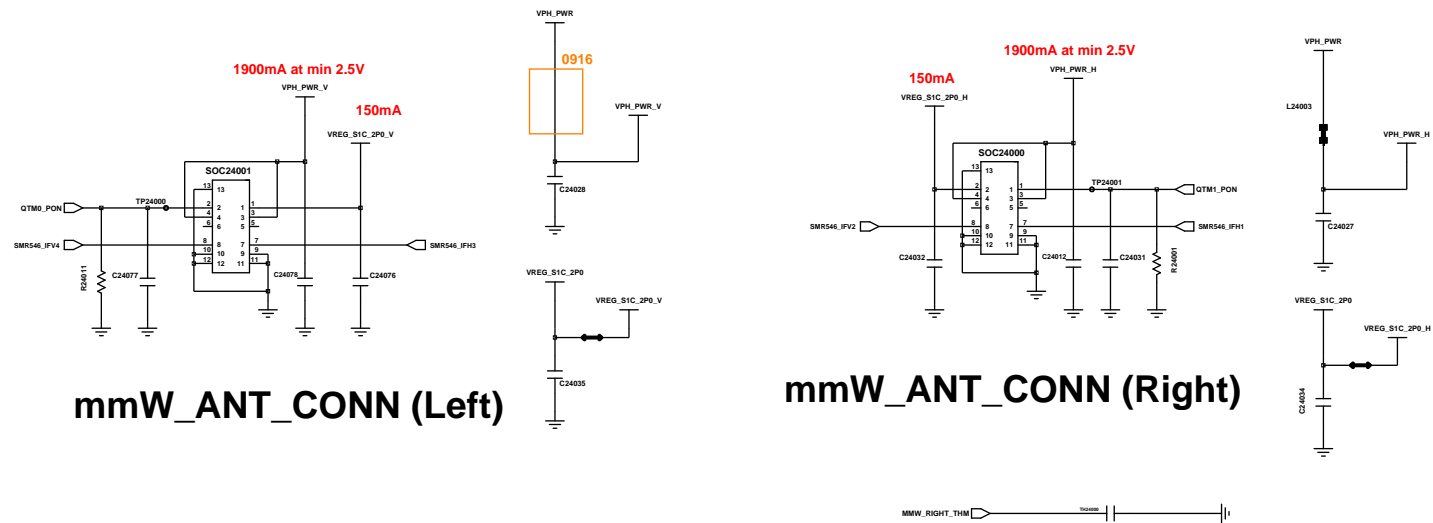
SUB4_Slit

(WIFI1 2.4G/5G, GPS L5)

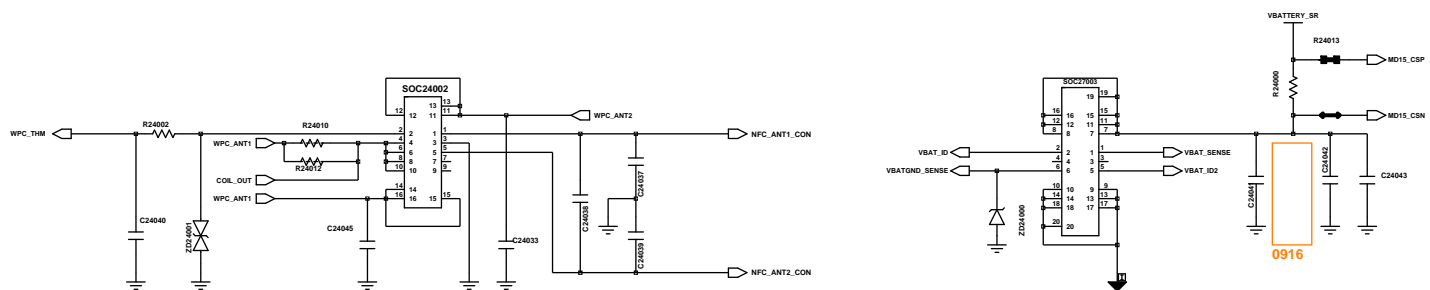


The circuit diagram illustrates a 16-bit parallel adder implemented using two 74181 ALUs and two 74183 4-bit parallel carry look-ahead generators. The ALUs are configured to perform the addition of two 16-bit numbers, A and B, using two's complement representation. The carry-in is set to 1, and the carry propagation is managed by the 74183 carry look-ahead generators. The final 16-bit sum is output as S15, S14, ..., S0. The diagram also shows the internal logic of the 74181 ALU, including the 4-bit parallel carry look-ahead generator (U23008) and the 4-bit parallel carry look-ahead generator (U23009).

Power



PMR735A

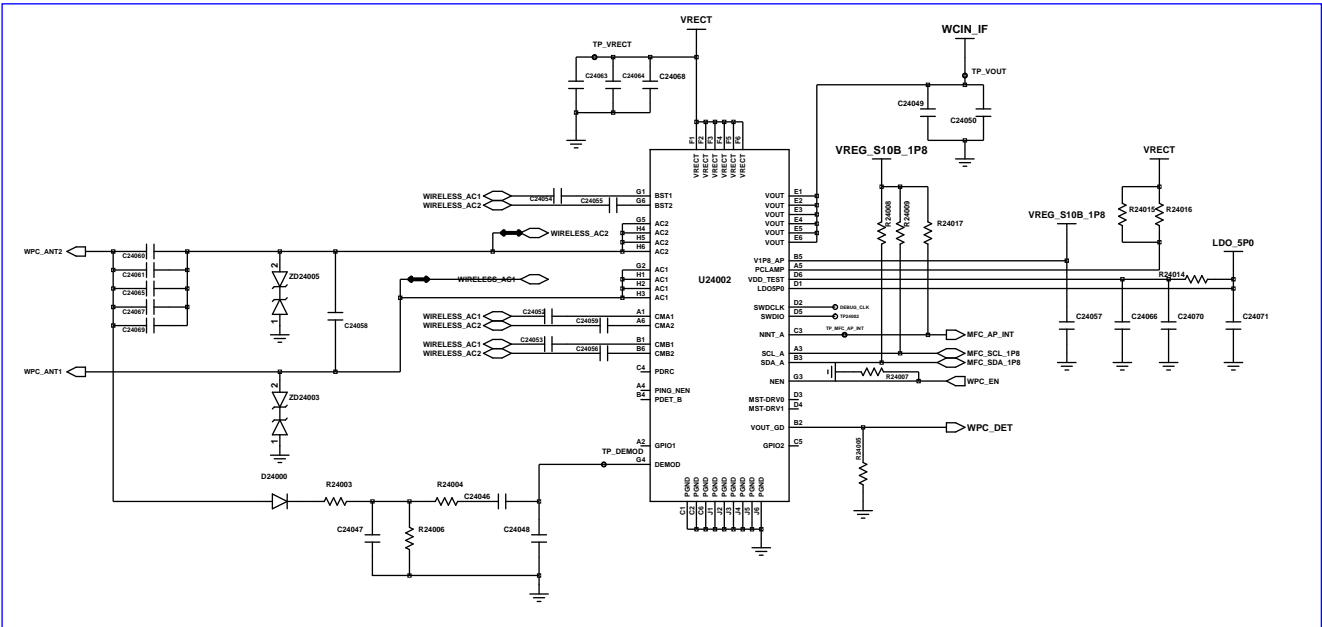
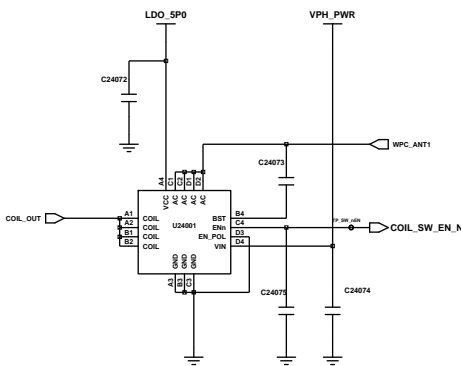


W/C CONNECTOR


Battery Connector

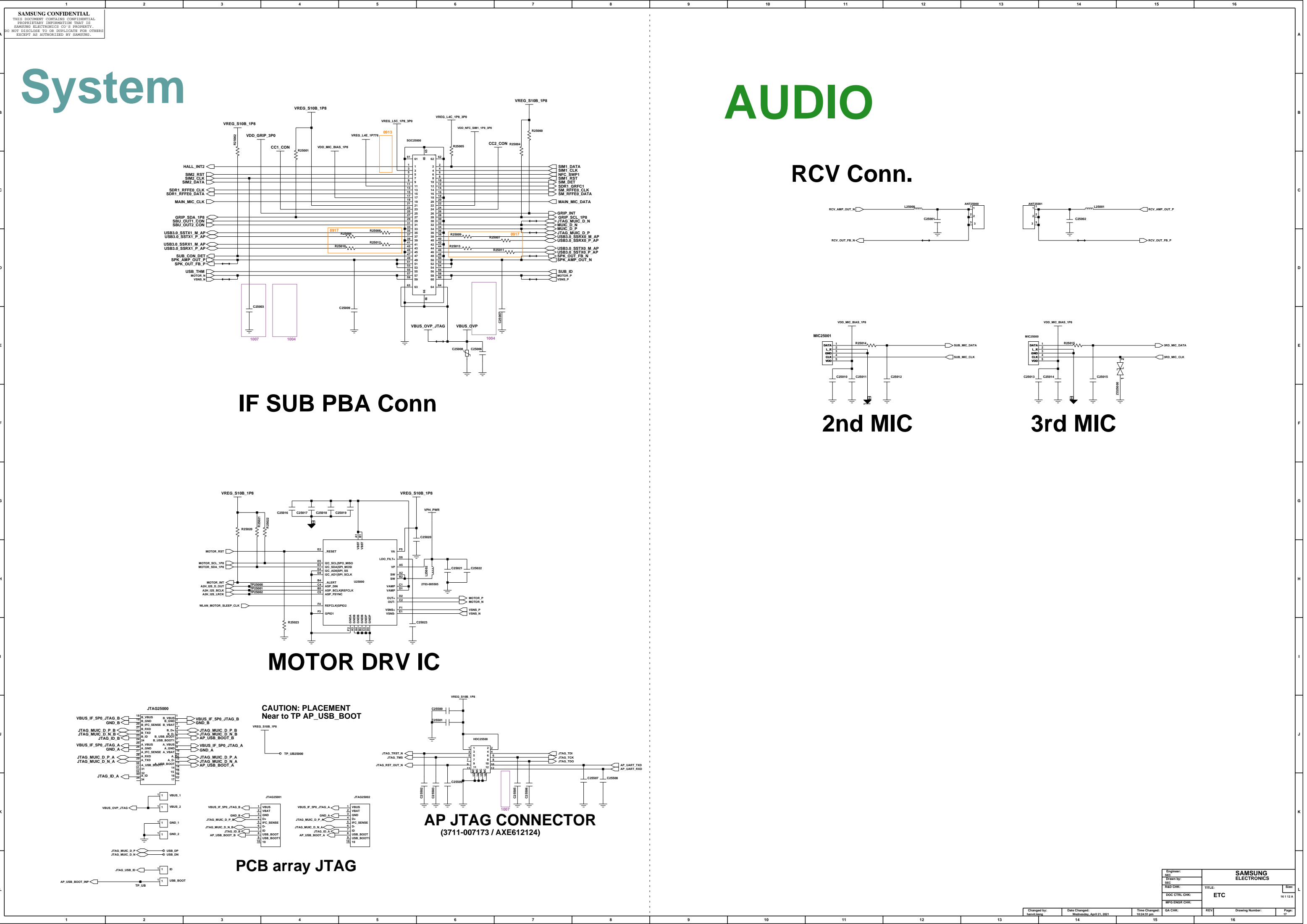
Power

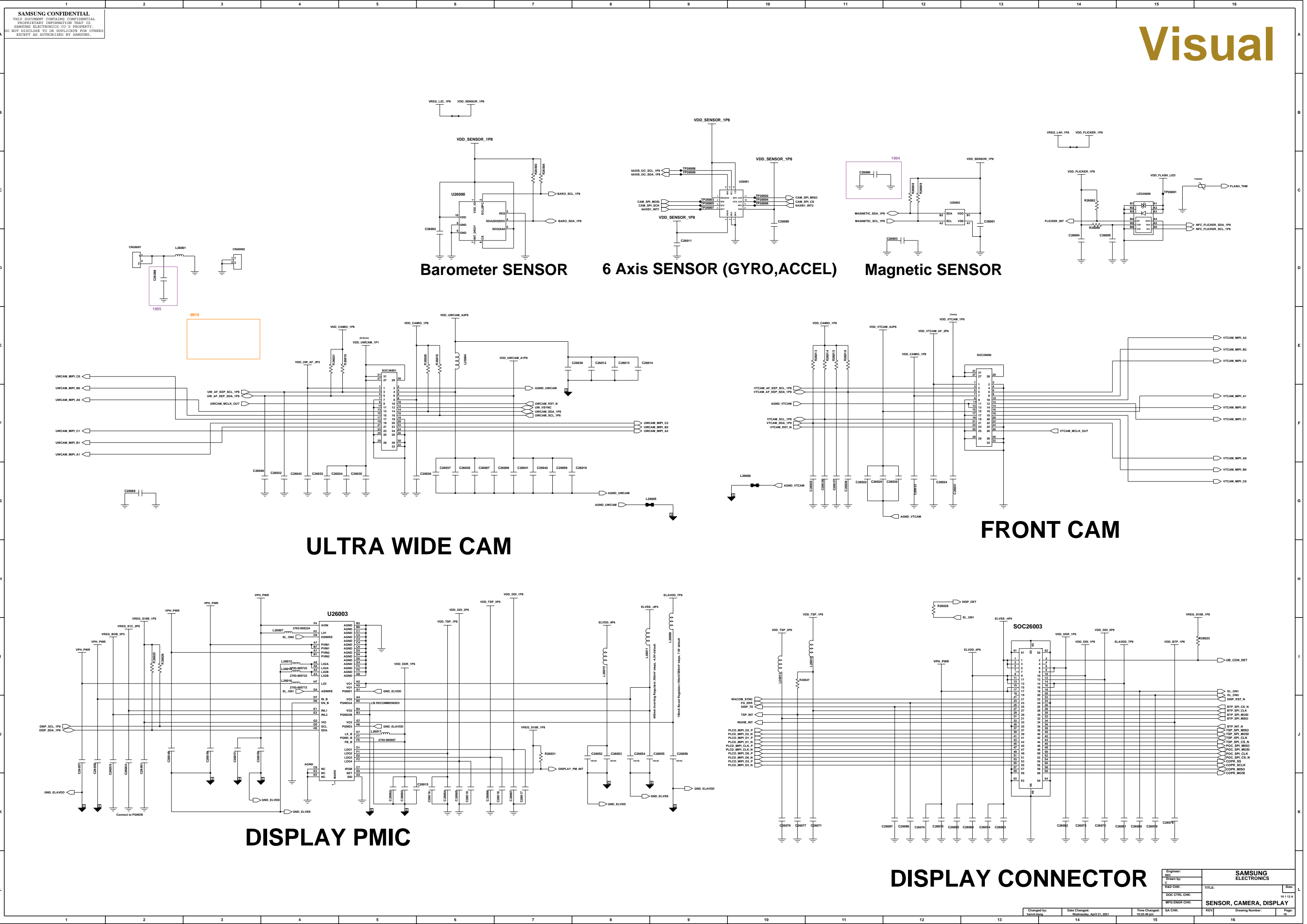
IDT->LSI



MFC

Engineer: SEC			
Drawn by: SEC			
R&D CHK:	TITLE:	Size:	
DOC CTRL CHK:	ETC	16 112 A	
MFG ENGR CHK:			
QA CHK:	REV	Drawing Number:	Page:





Visual

Barometer SENSOR

6 Axis SENSOR (GYRO,ACCEL)

Magnetic SENSOR

FRONT CAM

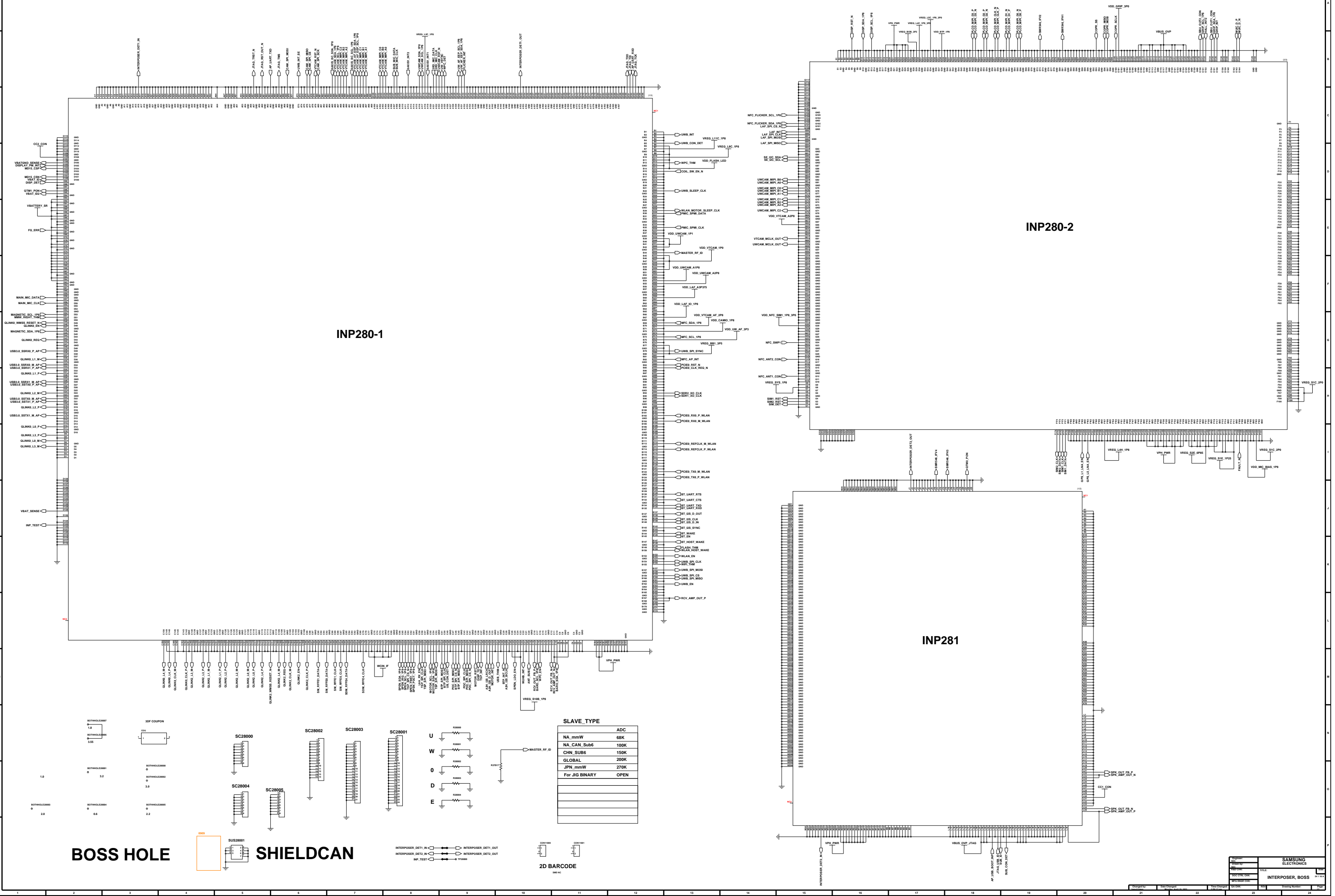
ULTRA WIDE CAM

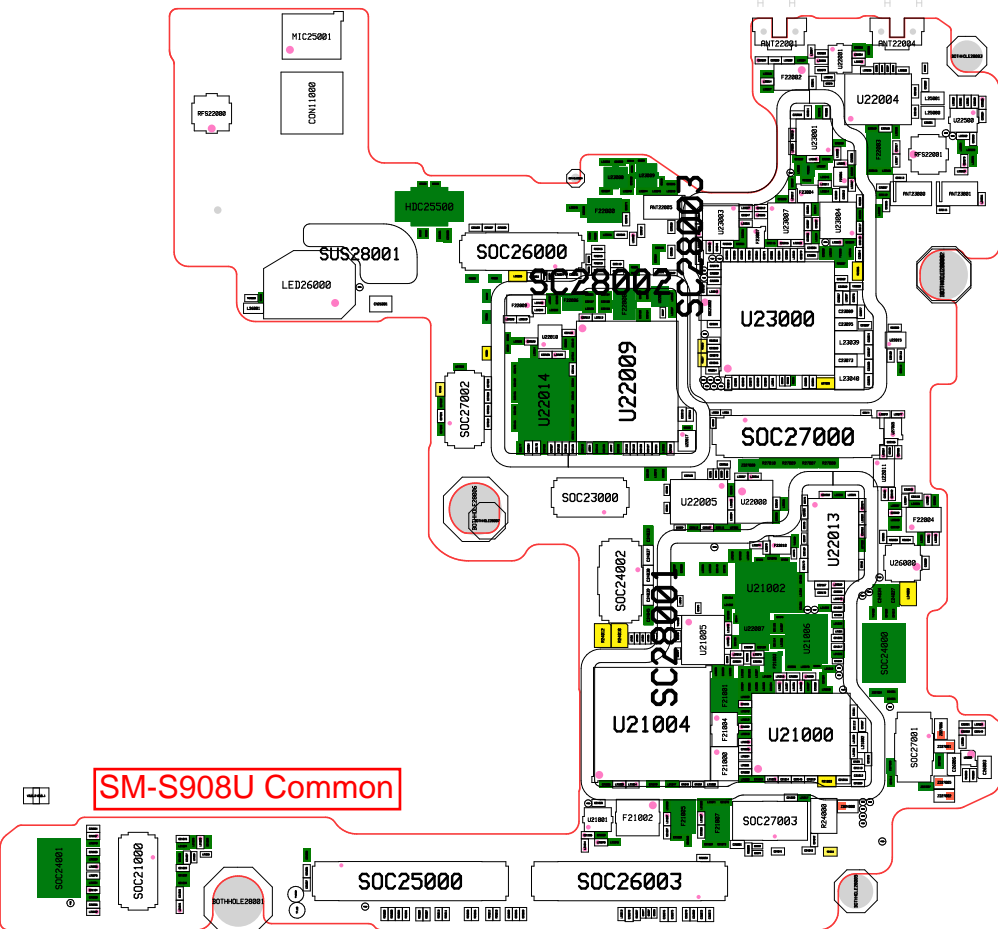
DISPLAY PMIC

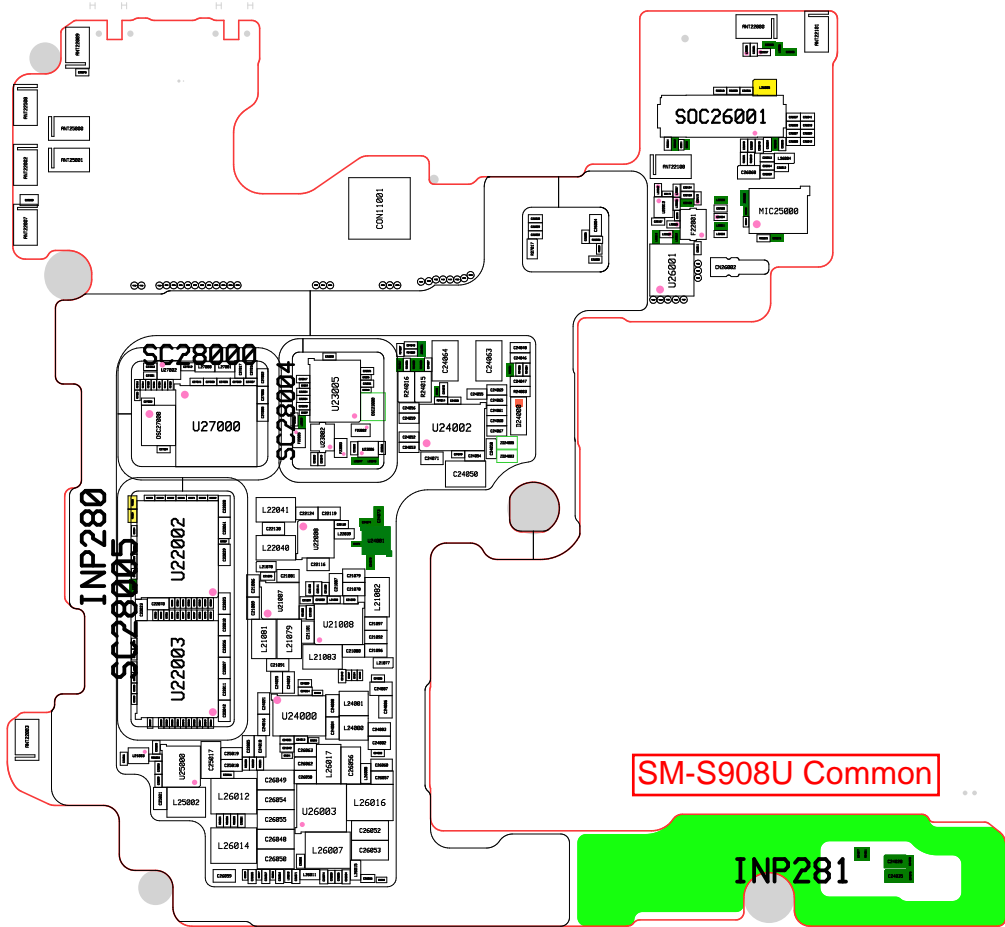
DISPLAY CONNECTOR

INTERPOSER

Design







	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
A	<div>SAMSUNG CONFIDENTIAL THIS DOCUMENT CONTAINS CONFIDENTIAL PROPRIETARY INFORMATION THAT IS SAMSUNG ELECTRONICS CO.'S PROPERTY. DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS EXCEPT AS AUTHORIZED BY SAMSUNG.</div>															
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K																
L																
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16

B0 QCOM Secondary_AP Rev0.7A

2021.10.

- SHEET01 : RF (1/2)
- SHEET02 : RF (2/2)
- SHEET03 : BT/WIFI, UWB
- SHEET04 : NFC IC, SENSOR, AUX SW, SIM SW
- SHEET05 : AP(1/2), UFS, NVM
- SHEET06 : AP(2/2)
- SHEET07 : AP PMIC, IF PMIC, Direct Charger, OVP IC, SIDE KEY
- SHEET08 : SPK AMP, MIC
- SHEET09 : CAM PMIC, CAM Conn., CAM MCU
- SHEET10 : RF PMIC, IF IC
- SHEET11 : INTERPOSER

Engineer:			
Drawn by:			
RED CHK:	TITLE:		Size:
DOC CTRL CHK:	U_SMB250		
MFG ENGR CHK:			
QA CHK:	REV:	Drawing Number:	Page:

MAIN FRC

MAIN1 ANT
(LB,MB,JPN B21)

MAIN2 ANT
(HB_n41)

MHB MIMO_LNA OUT

MAIN ANT3 (B48_n77/78/79)

THERMISTERS

MAIN ANT4 (n77/78/79)

on MASTER PCB

ET Modulator(QET6105#0)

ET Modulator(QET5100#1)

on MASTER PCB

LB LPAMID
(B5/8/12/13/14/17/20/26/28/29/71, G850/900, BC0/10)

on MASTER PCB

OMH LPAMID
(B1/2/3/4/7/25/30/34/38/39/40/41/66, G1800/1900, BC1)

on MASTER PCB

on MASTER PCB

2G_PA

on MASTER PCB

on MASTER PCB

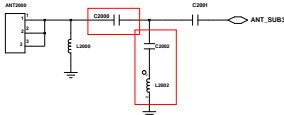
GPS_L5

on MASTER PCB

DRX_ANT

on MASTER PCB

SUB ANT2
(N77,N78)



SUB3
(UWB)

SUB4
(WIFI1 2.4G/5G, GPS L5)

SUB ANT5
(L1,MB,HB,N79)



CCP

DRX_LB
(B5/8/12/13/14/17/18/19/20/26/28/29/71_BC0/10_G850/900)

DRX_MHB_2
(B2/4/7/25/30/41/66)

on MASTER PCB

QET6105 #2

ENDC module

on MASTER PCB

UHB
(B48, n77/78)

UHB
(n79)

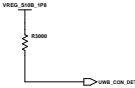
on MASTER PCB

WIFI (BCM4389 CoB)

on MASTER PCB

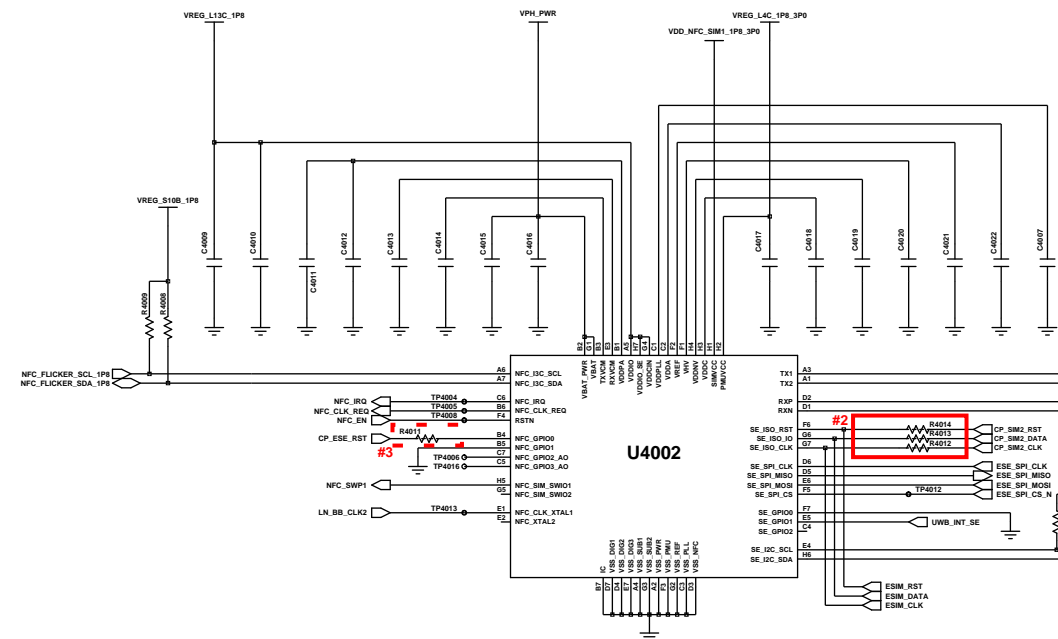
UWB

on MASTER PCB

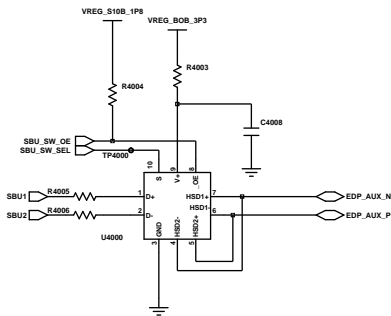
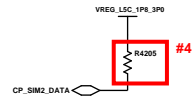
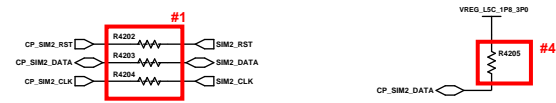
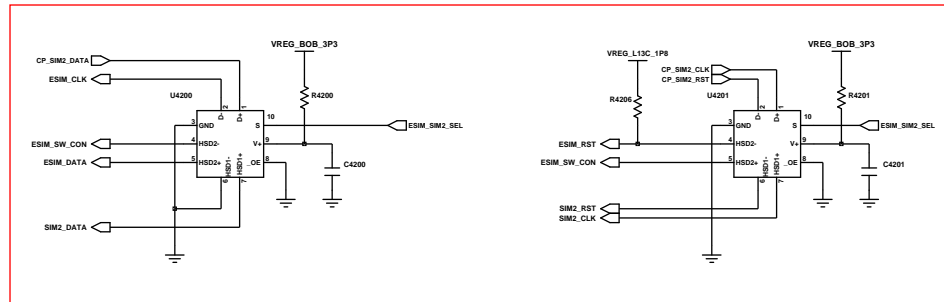


UWB Conn.

Power



		NFC IC	#1	#2	#3	#4		Switch IC	ESIM_SIM2_SEL
NA	Nano + eSIM	SN220U	OPEN	SHORT	SHORT	20k		NC	
EUR/Global	2-Nano + eSIM	SN220U	OPEN	OPEN	SHORT	20k		SMD	HSD1(H)_SIM2 HSD2(L)_eSIM
CHN	2-Nano	SN220T	SHORT	OPEN	OPEN	20k		NC	
JPN	Nano	SN220F	OPEN	OPEN	OPEN	OPEN		NC	



Power



MST Switch

Battery Connector



SENSORS

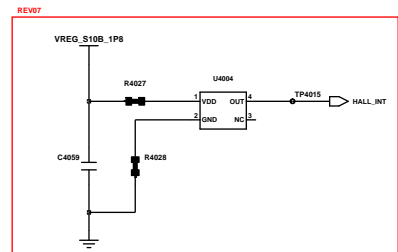
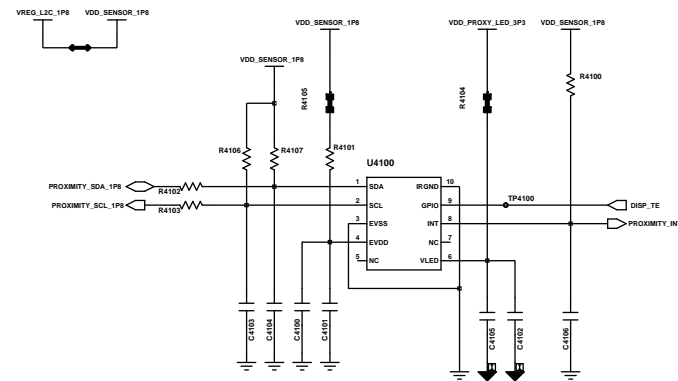


Visual

Barometer SENSOR

6 Axis SENSOR (GYRO,ACCEL)

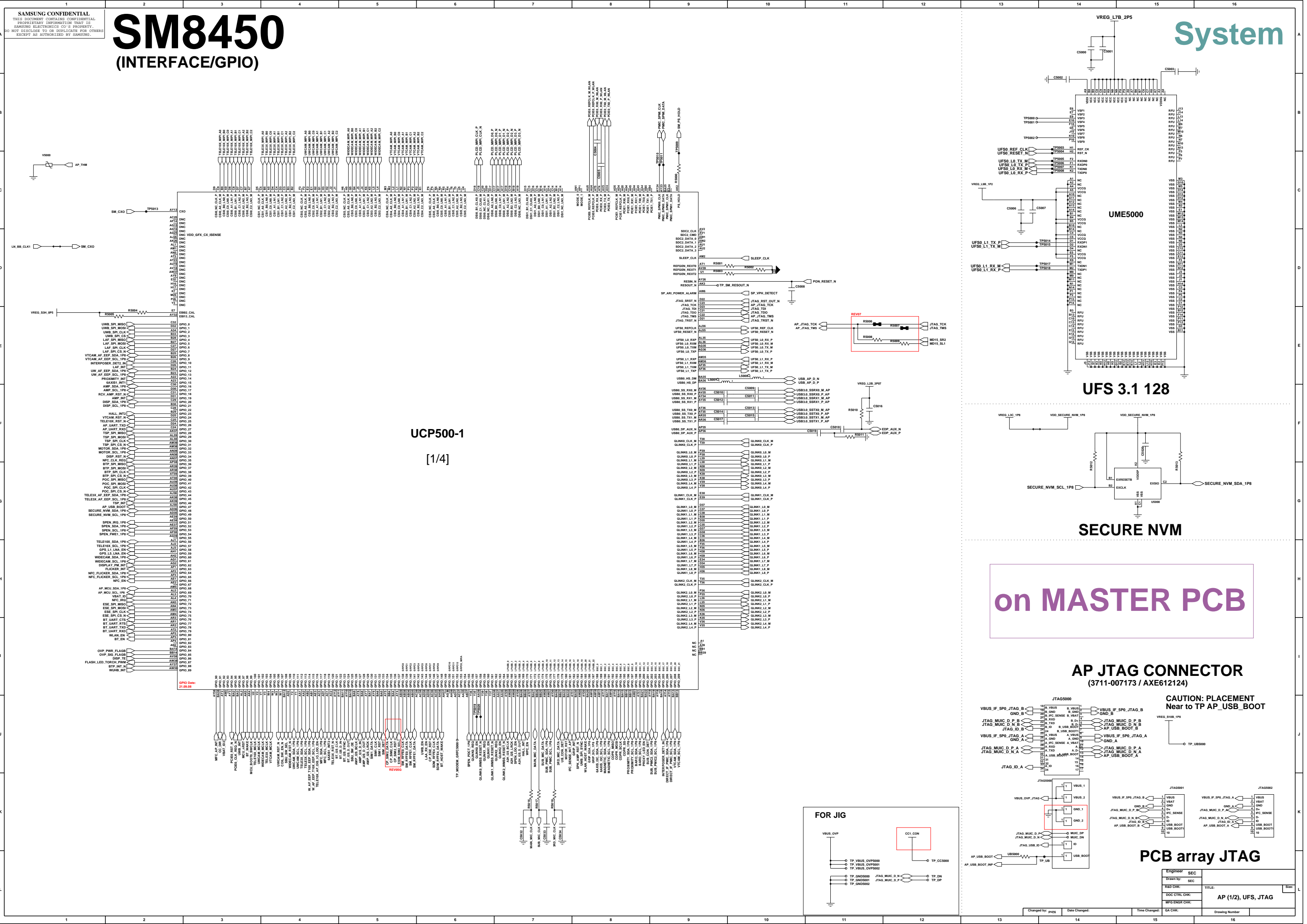
Magnetic SENSOR



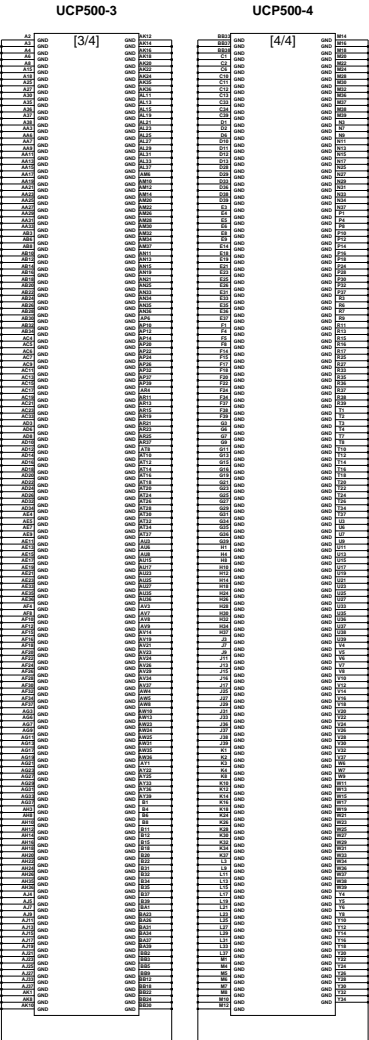
COVER_DET HALL_IC










Grip Sensor (Upper side)

Proximeter/Ambient Light Sensor



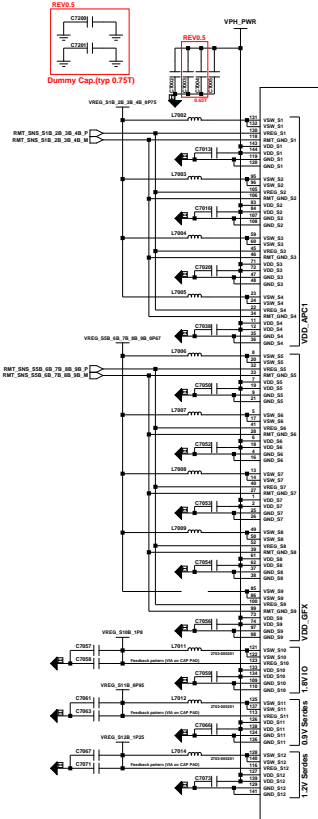
Power



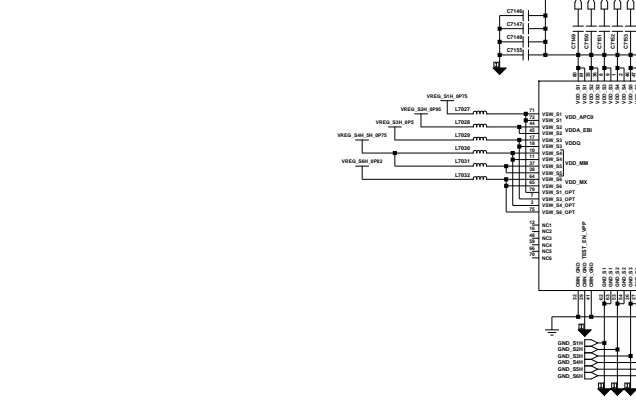
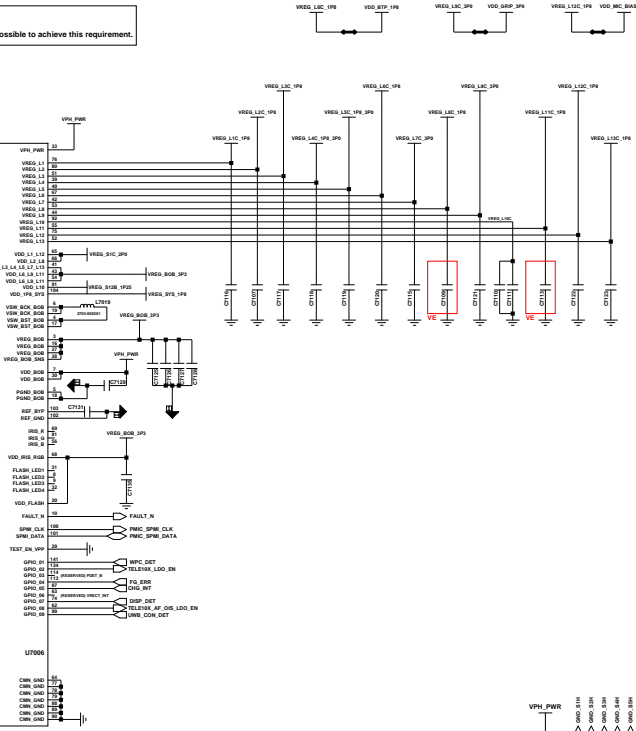
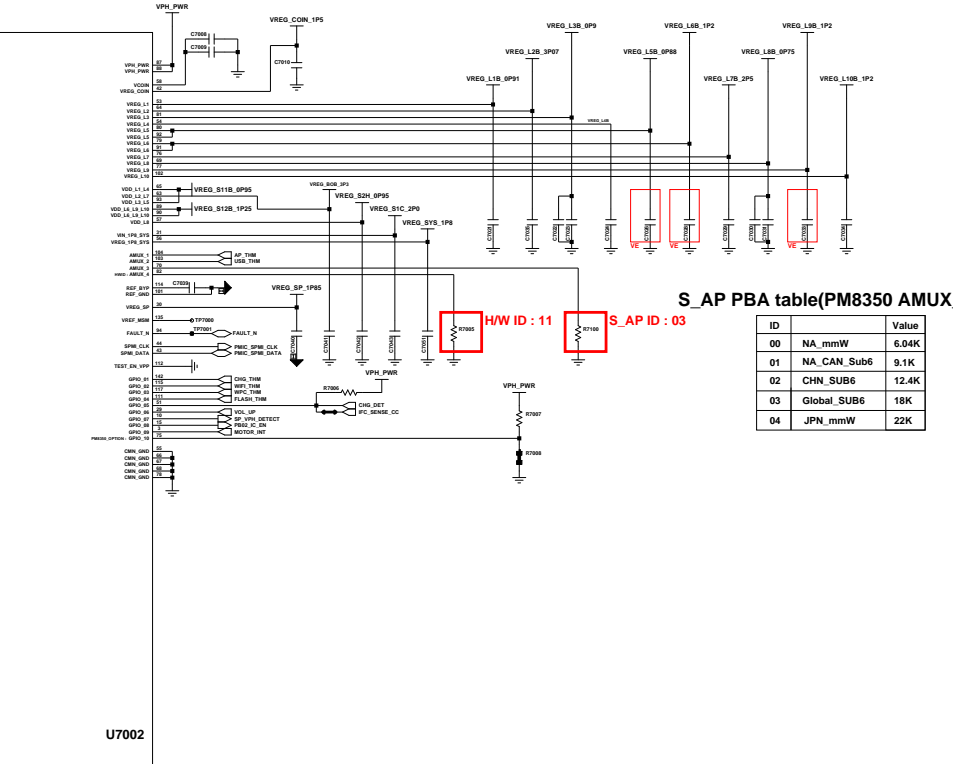
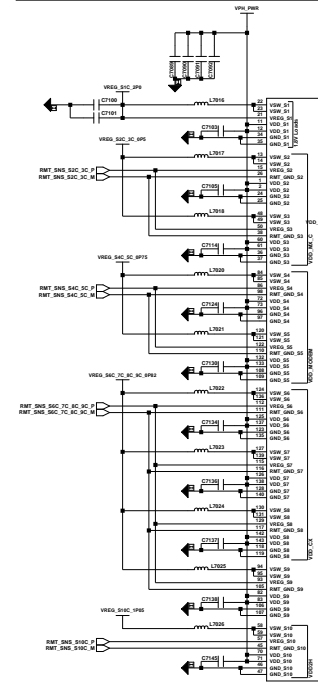
UFS 128G		NA (U)	
256G		CAN (W)	
512G		CHN (0)	
1T		JPN (D)	
		Global(E)	

AP PMIC set & CLK Generator

!! CAUTION !!
Place Cin 10uF Bulk Caps at corner of PM8350 and as close to PM8350 as possible to achieve this requirement.



!! CAUTION !!
Place Cin 10uF Bulk Caps at corner of PM8350C and as close to PM8350C as possible to achieve this requirement.



S AP PBA table(PM8350 AMUX_3)

ID	Value
00	NA_mmW 6.04K
01	NA_CAN_Sub6 9.1K
02	CHN_SUB6 12.4K
03	Global_SUB6 18K
04	JPN_mmW 22K

H/W ID table(PM8350 AMUX_4)

HW ID	BOARD REVISION	Value	Mech
00	Next P SW bring-up A-Type	6.04K	
01	B0 QCOM REV0.0	9.1K	
02	SM-S908U REV0.1	12.4K	
03		18K	
04	SM-S908U REV0.2	22K	
05	SM-S908U REV0.3	30K	
06		39K	
07	SM-S908U REV0.4	47K	
08	SM-S908U REV0.5	56K	
09	SM-S908U REV0.5G	68K	
10	SM-S908U REV0.6 / 0.6A	82K	
11	SM-S908U REV0.7	100K	
12	SM-S908U REV0.7	120K	
13		150K	
14		180K	
15		200K	
16		240K	
17		280K	
18		330K	
19		430K	
20		560K	
21		750K	
22		1000K	
23		1500K	
24		3000K	

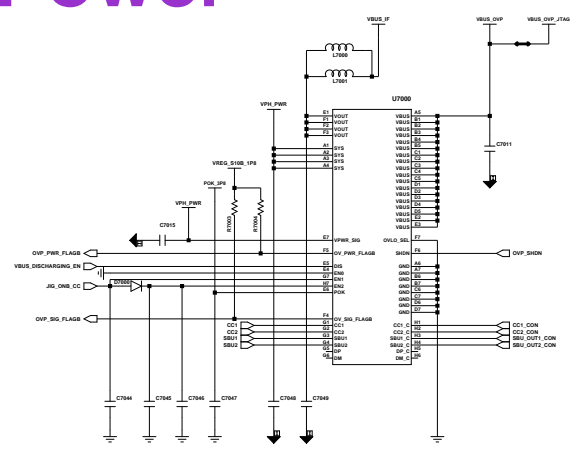
Option pin state (PM83450; 76.8MHz XO)

OPTION	OPTION	OPTION	OPTION
1	0	LOW	LOW
2	0	LOW	LOW
3	0	LOW	LOW
4	0	LOW	LOW
5	0	LOW	LOW
6	0	LOW	LOW
7	0	LOW	LOW
8	0	LOW	LOW
9	0	LOW	LOW
10	0	LOW	LOW
11	0	LOW	LOW
12	0	LOW	LOW
13	0	LOW	LOW
14	0	LOW	LOW
15	0	LOW	LOW
16	0	LOW	LOW
17	0	LOW	LOW
18	0	LOW	LOW
19	0	LOW	LOW
20	0	LOW	LOW
21	0	LOW	LOW
22	0	LOW	LOW
23	0	LOW	LOW
24	0	LOW	LOW

Placeholder for 1nF

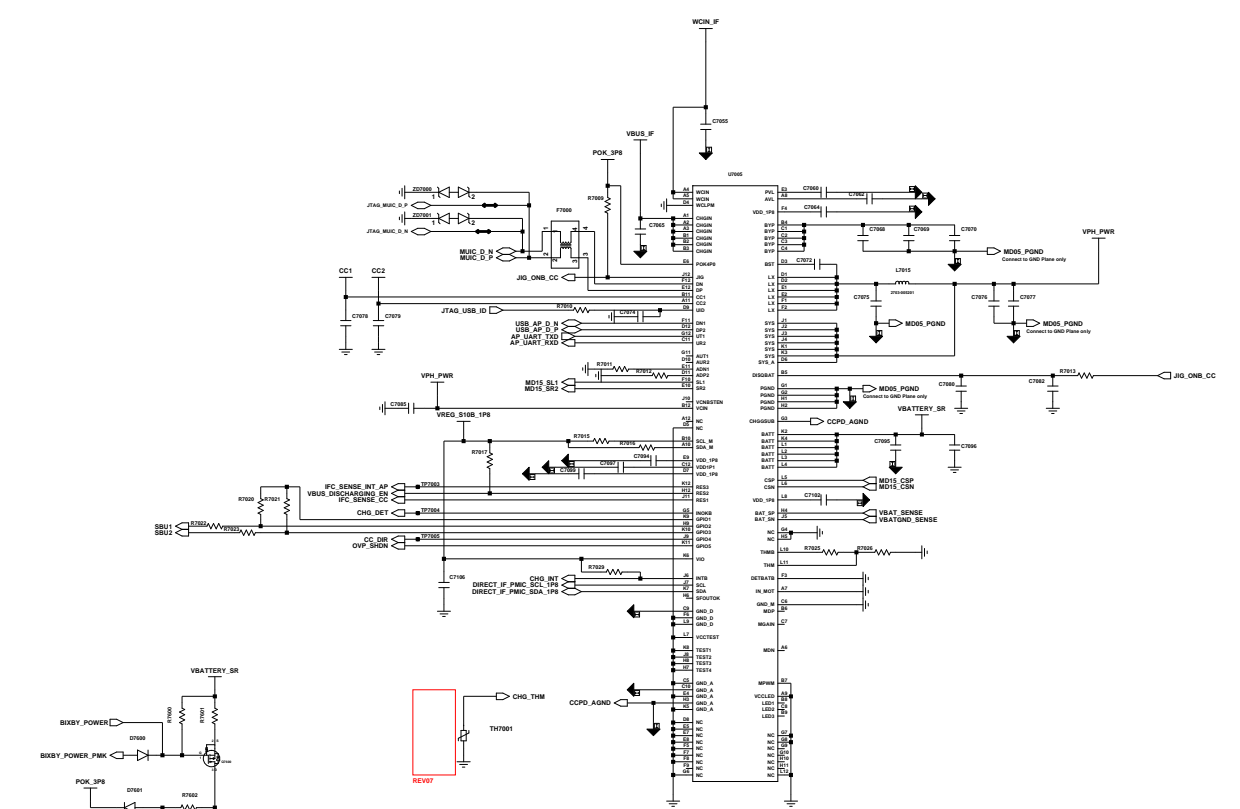
Power

Power System



OVP LS

Direct Charging



IF PMIC

Power Key

Vol key

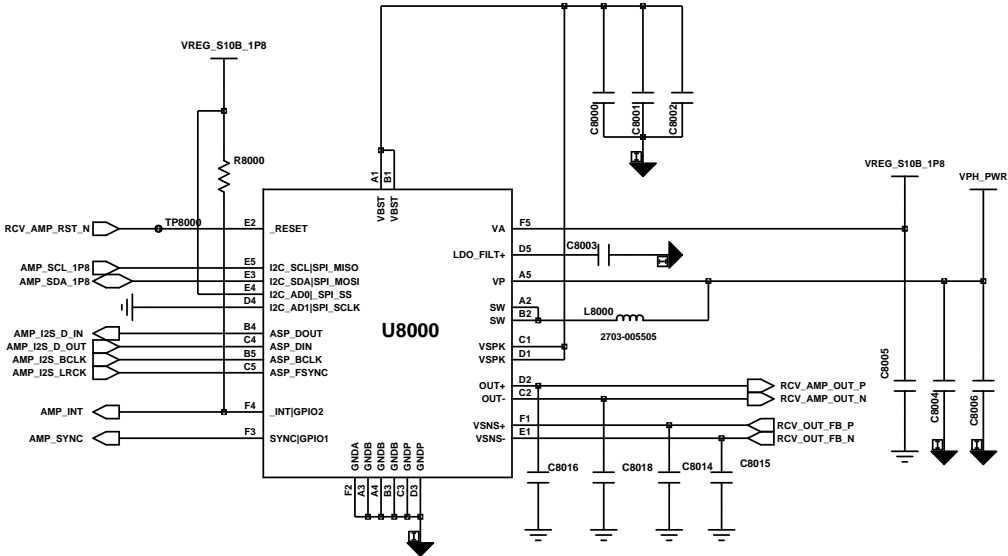
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AUDIO PART

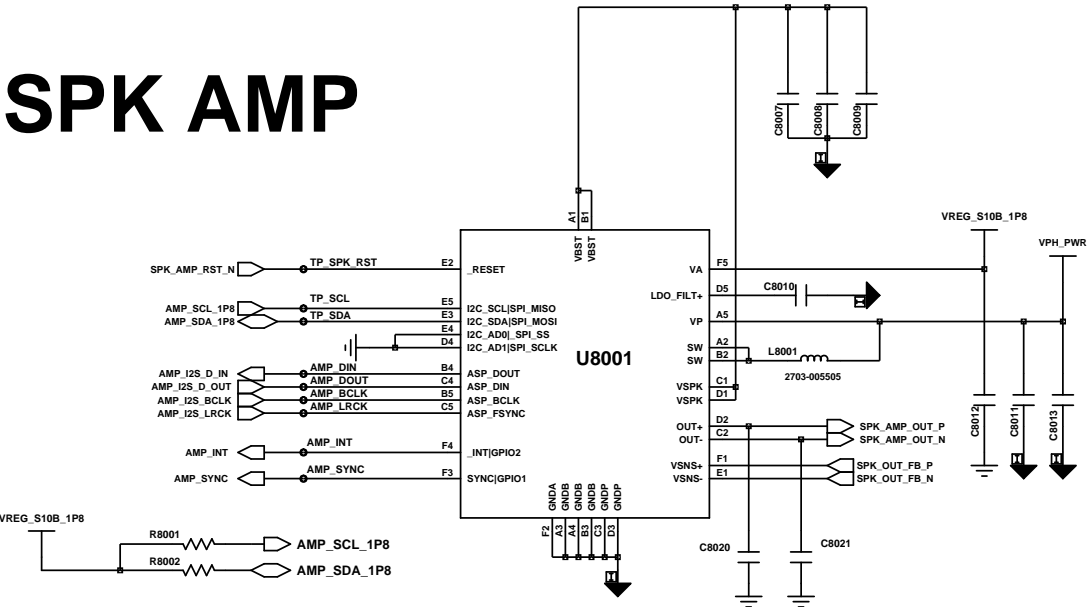
RCV Conn.

on MASTER PCB

RCV AMP



SPK AMP



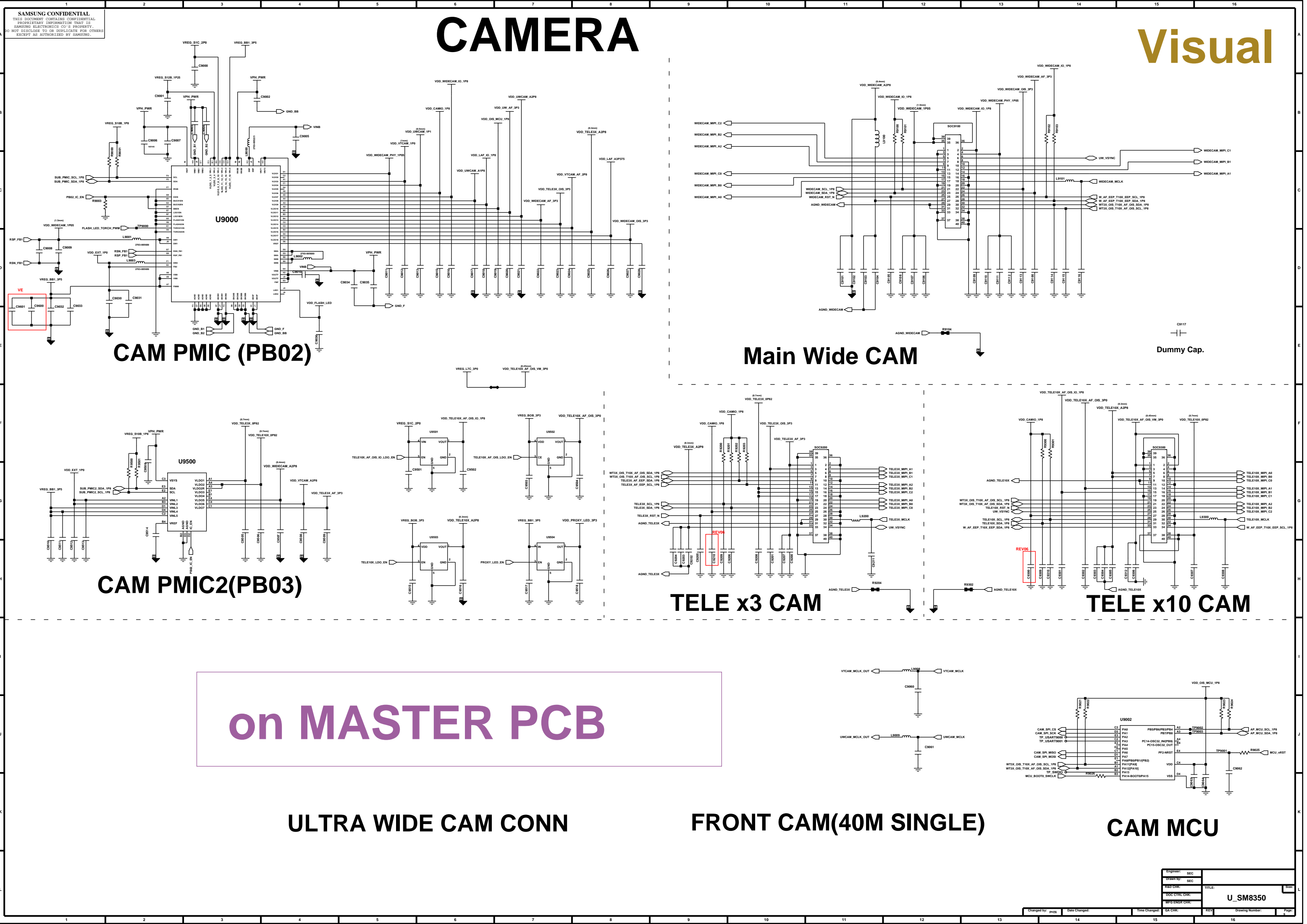
AUDIO

on MASTER PCB

2nd MIC

3rd MIC

Engineer:	SEC	SAMSUNG ELECTRONICS	
Drawn by:	SEC		
R&D CHK:		TITLE:	Size:
DOC CYRL CHK:		Audio	1218A
MFG ENGR CHK:			
QA CHK:	REV	Drawing Number:	Page:



NC

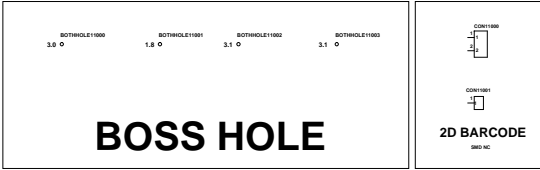


Radiation



mmW_ANT_CONN (Right)

Changed by: puz6	Date Changed:	Time Changed:	QA CHK:	REV:	Drawing Number:	Page: 13
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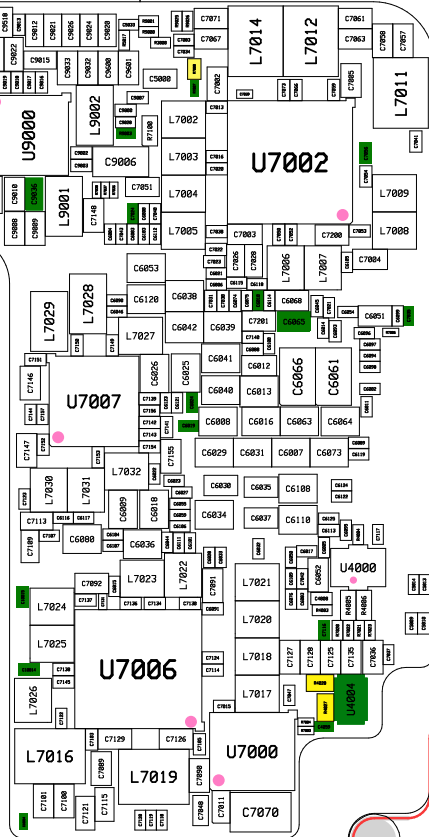


P1100

PLATED AREA

A schematic diagram of a circuit board section. It features a large component labeled U8001 with a pink dot on its left side. To its left is a component labeled L8001. Above U8001 are components C8006, C8007, and C8008. To the right of U8001 are components C8009 and C8008. To the left of L8001 is component C8003. The components are interconnected by lines representing the circuit board traces.

INP 11001



HOOTHOLE11881

