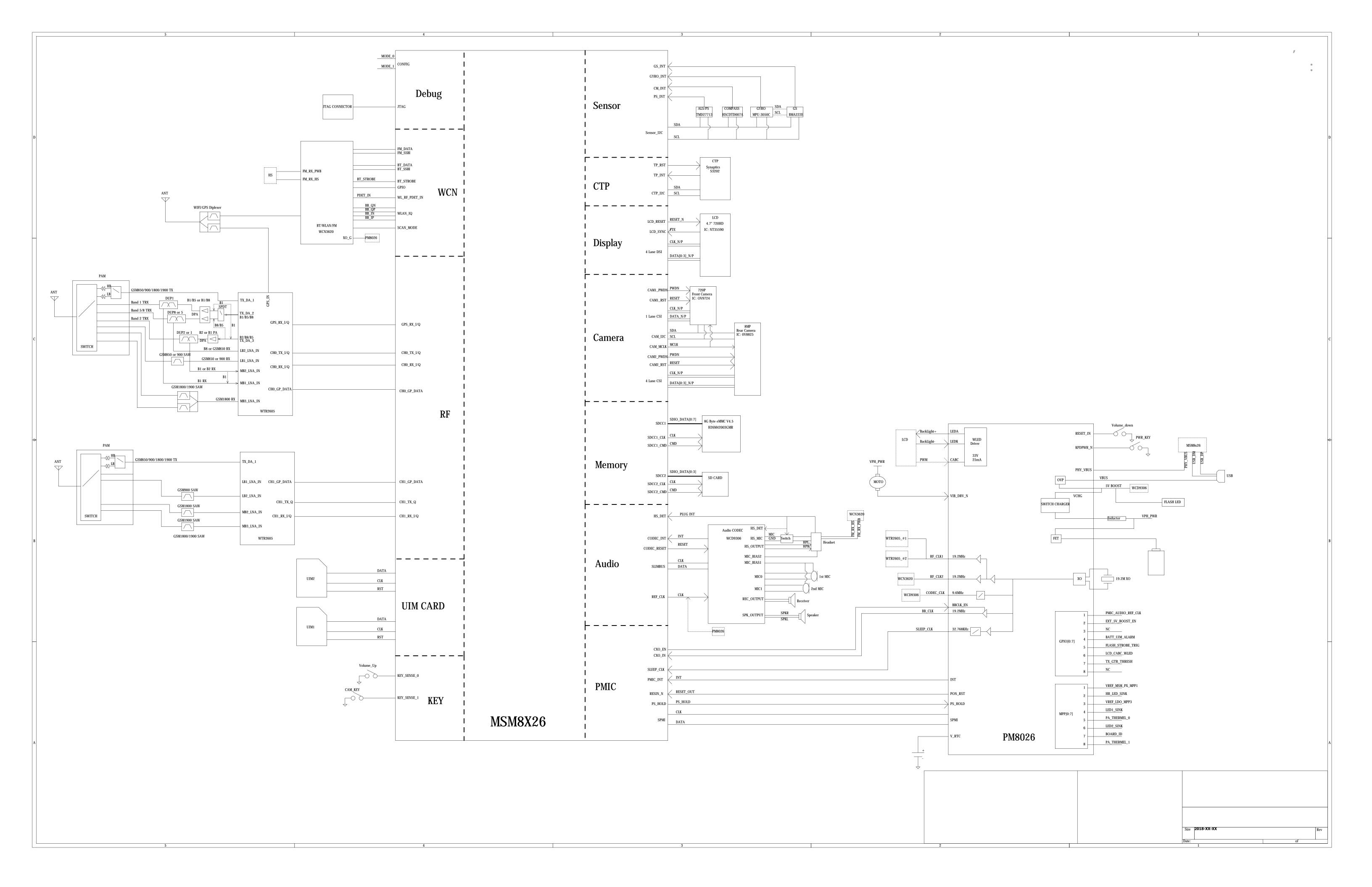
| # | Content | # | Content |
|----|----------------------------|----|------------------------------|
| 1 | Contents Table | 21 | WTR2605_Power_Secondary path |
| 2 | Reversion history | 22 | WTR2605_Secondary path |
| } | Block Diagram | 23 | WIFI/BT |
| | GPIO Map | 24 | GPS/APT/XO distribution |
| | MSM8x26_Control_1 | 25 | Audio |
| | MSM8x26_Control_2 | 26 | Sensors |
| | MSM8x26_GPIO | 27 | Memory |
| | MSM8x26_Power_1 | 28 | MultiMedia |
| | MSM8x26_Power_2 | 29 | Interface |
|) | MSM8x26_GND | 30 | Change List |
| | PM8226_PWR | | |
|), | PM8226_Control_1 | | |
| 2 | PM8226_Control_2 | | |
| Į | PRX Matching_Primary path | | |
| , | WTR2605_Power_Primary path | | |
| 3 | WTR2605_Primary path | | |
| 7 | WTR2605_TXM_Primary path | | |
| 3 | UMTS B1&B8_Primary path | | |
| | UMTS B2_Primary path | | |
|) | WTR2605_GSM_Secondary path | | |
| | | | |
| | | | |

Revision History

| SCHEMATIC MCN | HW VERSION | VERSION | DESCRIPTION OF CHANGE | |
|---------------|------------|---------|---|--|
| | | A | EVT2.0 Initial release | |
| I DOO NGOOO | | В | Please refer to Item 1 in "Change List " page | |
| LD20-NC003 | EVT2.0 | С | Please refer to Item 2 to Item 4 in "Change List " page | |
| | | D | Please refer to Item 5 in "Change List " page | |
| | | E | Please refer to Item 6 to Item 8 in "Change List " page | |
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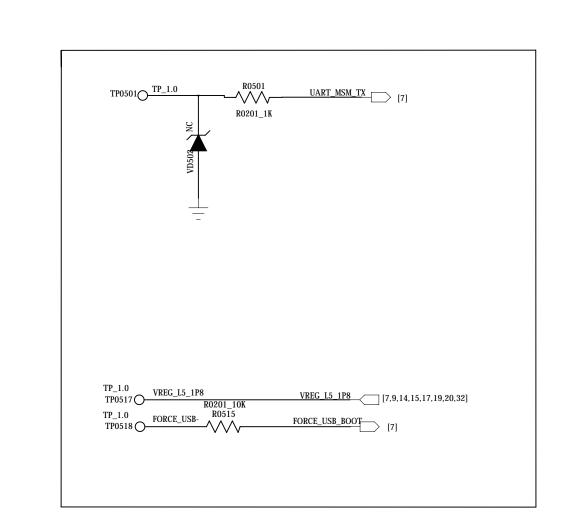


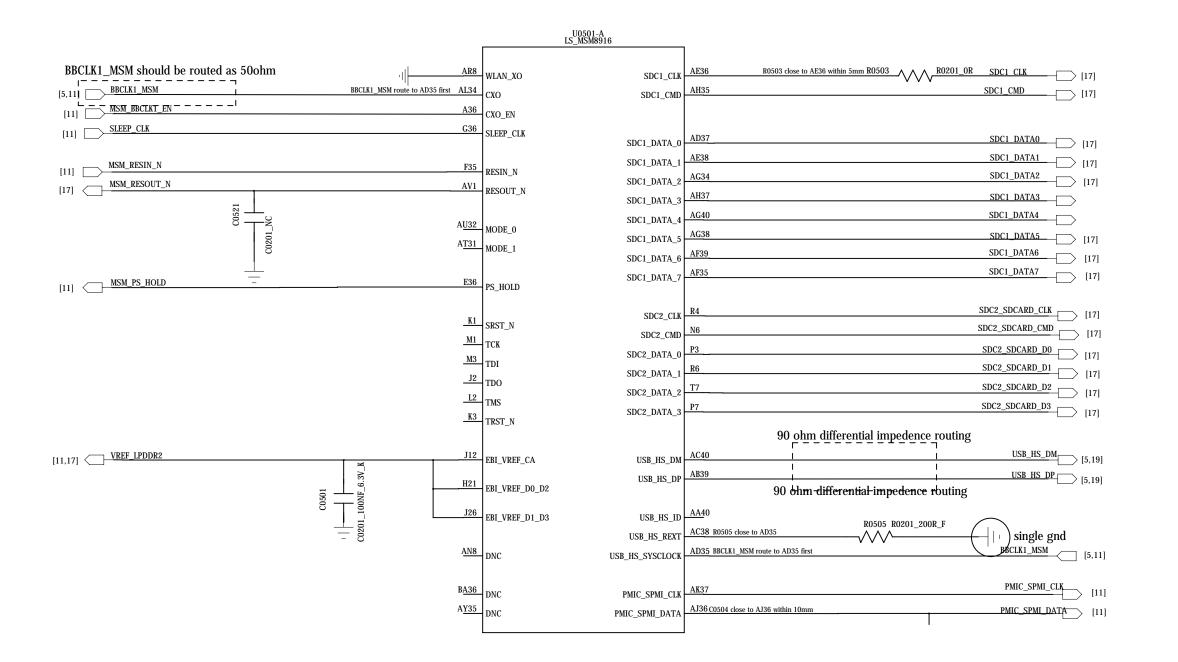
| | MSM8x | 26 GPIO (| Configuration For QRD8x26 | 3 | |
|---------|-------------------------------|-----------|----------------------------------|----------|---------------------|
| GPIO_0 | NC | GPIO_41 | WCSS_WLAN_DATA1 | GPIO_82 | CHO_PA_RANGE1 |
| GPIO_1 | NC | GPIO_42 | WCSS_WLAN_DATA0 | GPIO_83 | CH0_TXM_CTL1_SW1 |
| GPIO_2 | NC | GPIO_43 | WCSS_WLAN_SET | GPIO_84 | CHO_TXM_CTL2_SW0 |
| GPIO_3 | NC | GPIO_44 | WCSS_WLAN_CLK | GPIO_85 | CHO_GSM_PA_ON_HB |
| GPIO_4 | NC | GPIO_45 | WCSS_FM_SSBI | GPIO_86 | CHO_GSM_PA_ON_LB |
| GPIO_5 | NC | GPIO_46 | WCSS_FM_SDI | GPIO_87 | TX_GTR_THRESH |
| GPIO_6 | I2C2_SDA_SENS | GPIO_47 | WCSS_BT_CTL | GPIO_88 | NC |
| GPIO_7 | I2C2_SCL_SENS | GPIO_48 | WCSS_BT_DAT_STB | GPIO_89 | NC |
| GPIO_8 | UART3_TX | GPIO_49 | NC | GPIO_90 | NC |
| GPIO_9 | UART3_RX | GPIO_50 | NC | GPIO_91 | NC |
| GPIO_10 | I2C6_SDA_NFC | GPIO_51 | NC | GPIO_92 | NC |
| GPIO_11 | I2C6_SCL_NFC | GPIO_52 | NC | GPIO_93 | NC |
| GPIO_12 | NC | GPIO_53 | UIM2_DATA | GPIO_94 | NC |
| GPIO_13 | MSM_HS_DET | GPIO_54 | UIM2_CLK | GPIO_95 | CHO_GSM_TX_PHASE_D0 |
| GPIO_14 | NC | GPIO_55 | UIM2_RST | GPIO_96 | CHO_GSM_TX_PHASE_D1 |
| GPIO_15 | CAM_1V8_EN | GPIO_56 | NC | GPIO_97 | NC |
| GPIO_16 | CTP_RST_N | GPIO_57 | UIM1_DATA | GPIO_98 | NC NC |
| GPIO_17 | CTP_INT | GPIO_58 | UIM1_CLK | GPIO_99 | NC NC |
| GPIO_18 | I2C5_SDA_CTP | GPIO_59 | UIM1_RST | GPIO_100 | NC |
| GPIO_19 | I2C5_SCL_CTP | GPIO_60 | LCD_ID | GPIO_101 | CHO_SSBI_PRX |
| GPIO_20 | NFC_DISABLE_N | GPIO_61 | BATT_UIM_ALARM | GPIO_102 | CH0_SSBI_TX_GPS |
| GPIO_21 | NFC_INT_N | GPIO_62 | NC | GPIO_103 | NC |
| GPIO_22 | SPI_CS_EHT | GPIO_63 | ACCL_INT1_N | GPIO_104 | NC |
| GPIO_23 | NC | GPIO_64 | NC | GPIO_105 | GPS_ELNA_EN |
| GPIO_24 | DSI_LCD_TE | GPIO_65 | ALSP_INT_N | GPIO_106 | VOLUME+ |
| GPIO_25 | DSI_LCD_RST | GPIO_66 | MAG_INT_N | GPIO_107 | VOLUME- |
| GPIO_26 | CAM_MCLKO | GPIO_67 | MSM_CODEC_REF_CLK | GPIO_108 | NC |
| GPIO_27 | NC | GPIO_68 | CEDEC_INT1_N | GPIO_109 | NC |
| GPIO_28 | CAM2_RST_N | GPIO_69 | NC | GPIO_110 | NC |
| GPIO_29 | I2CO_SDA_CAM | GPIO_70 | CODEC_SLIMBUS_CLK | GPIO_111 | NC |
| GPIO_30 | I2CO_SCL_CAM | GPIO_71 | CODEC_SLIMBUS_DATA | GPIO_112 | NC |
| GPIO_31 | NC | GPIO_72 | CODEC_RST_N | GPIO_113 | FORCE_USB_BOOT |
| GPIO_32 | FLASH_STROBE_TRIG | GPIO_73 | FORT_CAM_ID | GPIO_114 | NC NC |
| GPIO_33 | NC | GPIO_74 | BACK_CAM_ID | GPIO_115 | NC NC |
| GPIO_34 | NC | GPIO_75 | CHO_PA_ON4_B1 | GPIO_116 | |
| GPIO_35 | CAM2_STANDBY | GPIO_76 | NC | GPIO_117 | NC NC |
| GPIO_36 | CAM1_STANDBY | GPIO_77 | WTRO_GSM_TDS_SEL | GPIO_118 | NC |
| GPIO_37 | CAM1_RST_N | GPIO_78 | CHO_PA_ON1_B8 | GPIO_119 | NC |
| GPIO_38 | NC | GPIO_79 | CHO_TXM_CTLO_SPDT | GPIO_120 | NC |
| GPIO_39 | WCSS_BT_SSBI | GPIO_80 | CHO_TAM_CTLO_SLDT CHO_WTR_RF_ON | G110_120 | |
| GPIO_40 | WCSS_BT_SSBT WCSS_WLAN_DATA2 | GPIO_80 | CHO_PA_RANGEO (BOOT_CONFIG_0) | | |

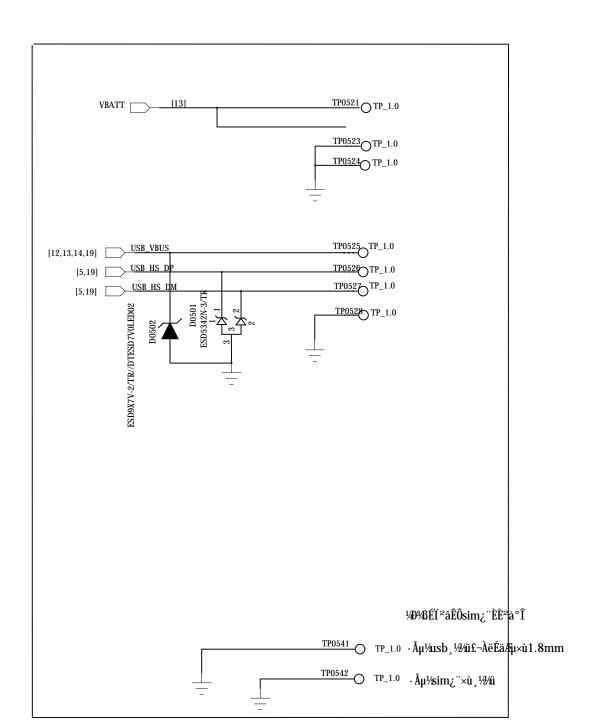
| PM80 | PM8026 GPIO/MPP Configuration For QRD8x26 | | | | |
|--------|---|-------|------------|--|--|
| GPIO_1 | AUDIO_REF_CLK (DIVCLK1) | MPP_1 | VREF_PX | | |
| GPIO_2 | EXT_5VBOOST_EN | MPP_2 | HR_LED_SNK | | |
| GPIO_3 | BBCLK2_EN | MPP_3 | VREF_LDO | | |
| GPIO_4 | BATT_UICC_ALARM | MPP_4 | LED1_SINK | | |
| GPIO_5 | FLASHNOW | MPP_5 | PA2_THERM | | |
| GPIO_6 | CABC_WLED | MPP_6 | LED2_SINK | | |
| GPIO_7 | TX_GTR_THRESH | MPP_7 | HWID | | |
| GPIO_8 | SYS_BOOST_FORCE_BYP | MPP_8 | PA1_THERM | | |

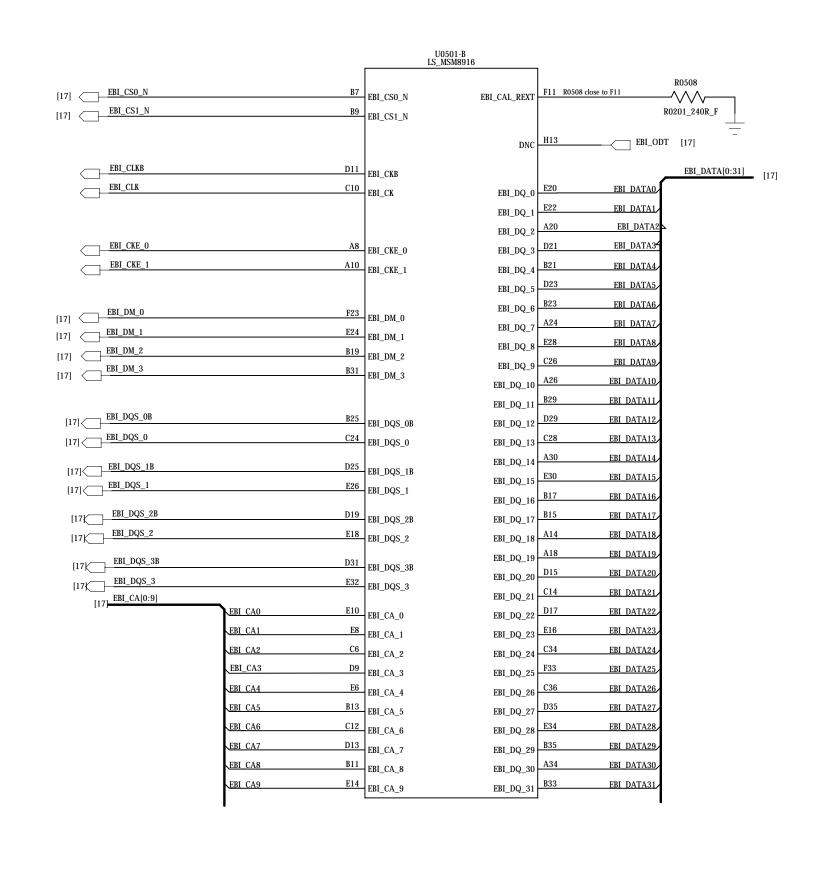
| I2C Address List For QRD8x26 | | | | | |
|------------------------------|---------------|------------|---------------|--------------|--|
| I2C GPIO | Device | MPN | Write Address | Read Address | |
| | Accelerometer | BMA222E | 0x31 | 0x30 | |
| CDIO[0.7] | Magnetometer | HSCDTD007A | 0x19 | 0x18 | |
| GPIO[6:7] | Gyroscope | MPU-3050C | 0xD1 | 0xD0 | |
| | ALS/Proximity | TMD27713T | 0x73 | 0x72 | |
| GPIO[18:19] | Touch Screen | S3202 | 0x41 | 0x40 | |
| CDIO[00.90] | Front Camera | OV9724 | 0x41 | 0x40 | |
| GPIO[29:30] | Rear Camera | OV8825 | 0x6D | 0x6C | |

| | Interrupt List For QRD8x26 | | | | | |
|----------|----------------------------|--------------------------------|-------------------------|--|--|--|
| GPIO | 2018-XX-XX | Description | State | | | |
| GPIO_11 | MSM_HS_DET | Headset Plug In Detection | Active Low | | | |
| GPIO_17 | CTP_INT | Interrupt From CTP Sensor | Defualt Low /Open Drain | | | |
| GPIO_38 | SD_CARD_DET_N | SD Card Insert Detection | Insert Low/Remove High | | | |
| GPIO_49 | ACCL_INT2_N | Interrupt2 From Accelerometer | Default High/Push-Pull | | | |
| GPIO_63 | ACCL_INT1_N | Interrupt1 From Accelerometer | Default High/Push-Pull | | | |
| GPIO_64 | GYRO_INT_N | Interrupt From Gyroscope | Default High/Push-Pull | | | |
| GPIO_65 | ALSP_INT_N | Interrupt From ALS/Proximity | Active Low /Open Drain | | | |
| GPIO_66 | MAG_INT_N | Interrupt From Magnetometer | Active High /Push-pull | | | |
| GPIO_68 | CODEC_INT1_N | Interrupt From CODEC | PA1_THERM | | | |
| GPIO_115 | ETH_INT_N | Interrupt From Ethernet Driver | Active Low /Push-pull | | | |







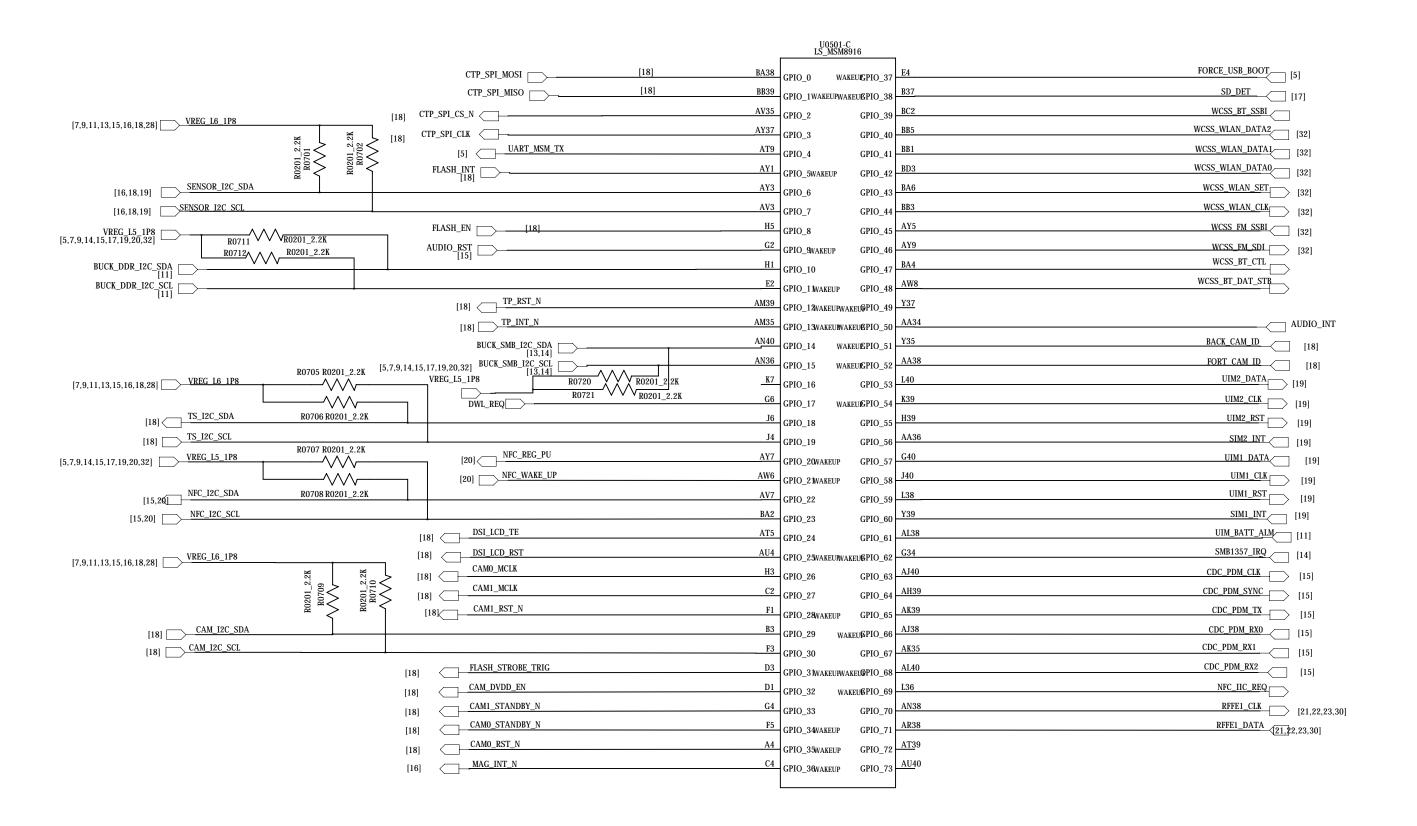


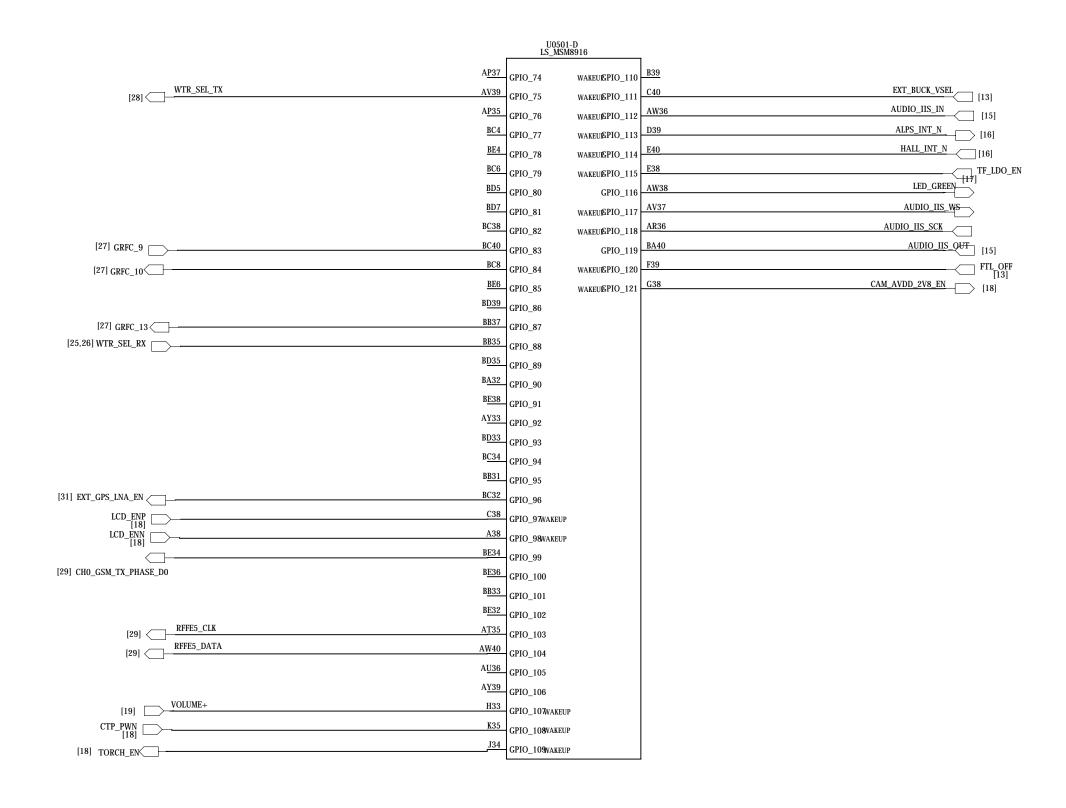
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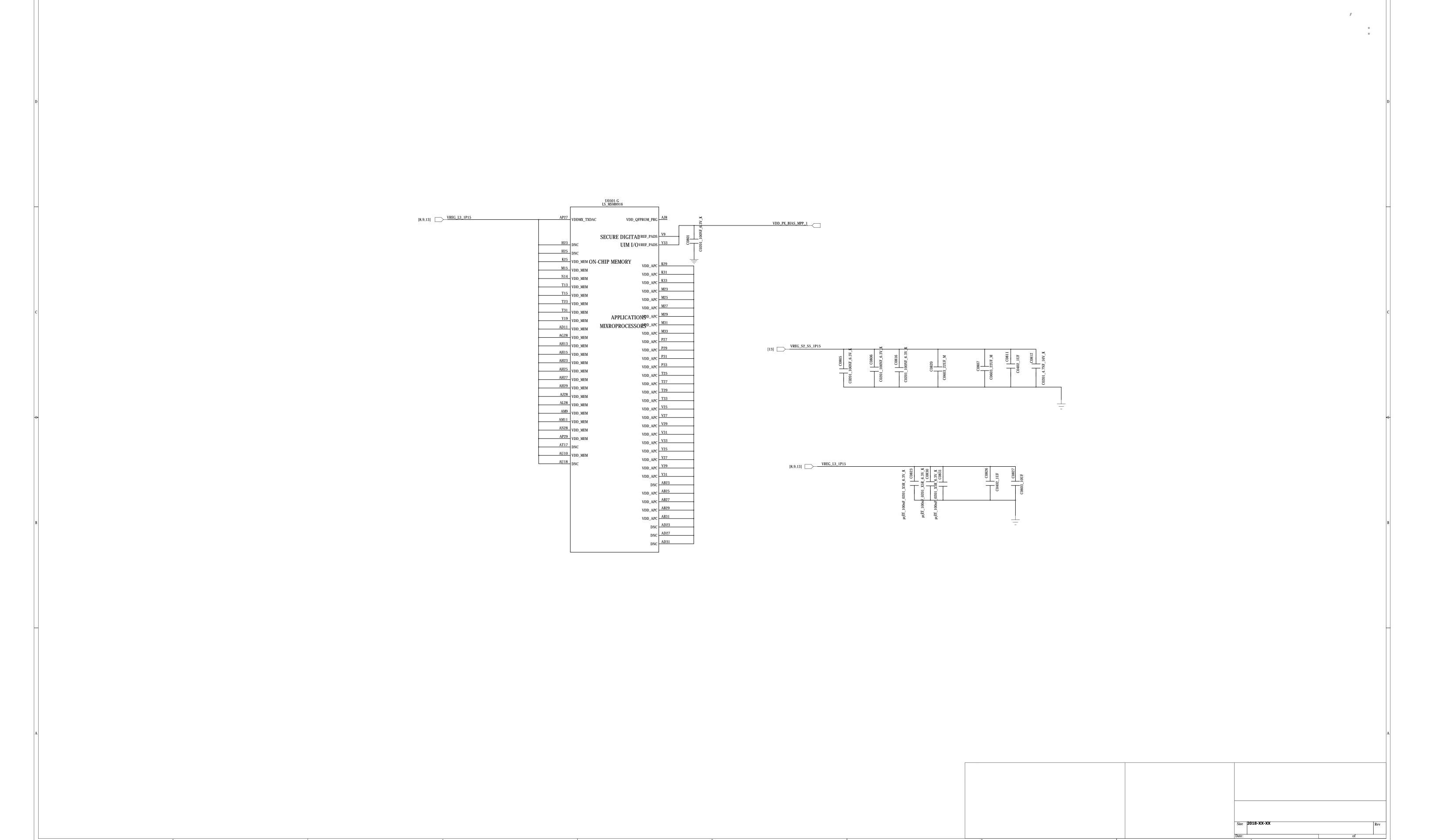
MIPI DSI signal: 100 ohm differential impedence routing U0501-E LS_MSM8916 [18] MIPI_CSIO_DO_N MIPI_DSIO_CLK_N [18] MIPI_DSIO_CLK_N MIPI DSIO CLK P [18] [18] MIPI CSIO DO P MIPI_DSIO_CLK_P AG2 MIPI_DSIO_LNO_N AF1 MIPI DSI0 D0 N [18] MIPI_CSIO_CLK_N [18] MIPI_CSIO_CLK_P MIPI DSIO DO P [18] MIPI_DSIO_LNO_P AE2 MIPI CSIO-MAIN CAM [18] MIPI_CSIO_D1_N MIPI DSI0 D1 N [18] MIPI_CSIO_LN1_N MIPI_DSIO_LN1_N AF3 MIPI DSI0-LCD MIPI_DSI0_D1_P [18] [18] MIPI CSIO D1 P MIPI_DSIO_LN1_P AE4 MIPI_CSIO_LN1_P MIPI DSIO D2 N [18] MIPI_DSIO_LN2_N AH MIPI DSI0 D2 P [18] MIPI_DSIO_LN2_P AG4 MIPI_CSIO_LN2_P MIPI_DSI0_D3_N [18] [18] MIPI CSIO D3 N AA6 MIPI_CSIO_LN3_N MIPI_DSI0_LN3_N AL2 [18] MIPI CSIO D3 P MIPI_DSI0_D3_P [18] /5 MIPI_CSIO_LN3_P MIPI_DSIO_LN3_P AK [18] MIPI_CSI1_D0_N MIPI CSI1-SUB CAM [18] MIPI CSI1 CLK N [18] MIPI CSI1 D1 P MIPI CSI signal: 100 ohm differential impedence routing WTR0 Primary RX r-----[29] WTRO_PRXBB_I_P BB21 BBRX_IP_CH0 BE20 BBRX_QM_CHO [29] WTRO PRXBB Q P BC20 BBRX_QP_CH0 WTRO_TXBB_I_M [29] TX_DACO_IM BA26 AY19 BBRX_IM_CH1 WTR0 TXBB I P [29] WTR0_TXBB_Q_M [29] ______ WTRO Diversity RX <u>WTR0 TXBB Q P</u> [29] BBRX_QM_CH1 TX_DACO_QP BD25 [29] WTRO_DRXBB_Q_P VREG_L7_1P8 [9,13,21,22,23,30,32] TX_DACO_IREF AU26 BB17 BBRX_QP_CH1 TX_DAC0_VREF AR26 VREF_DAC_MPP_3 [11] WTR1 Primary RX TX_DAC1_QM AY23 R0607 R0201_3.92K_F [32] WCSS BB IN WTRO_GPSBB_I_R [32] WCSS BB IP WTRO_GPSBB_Q_P

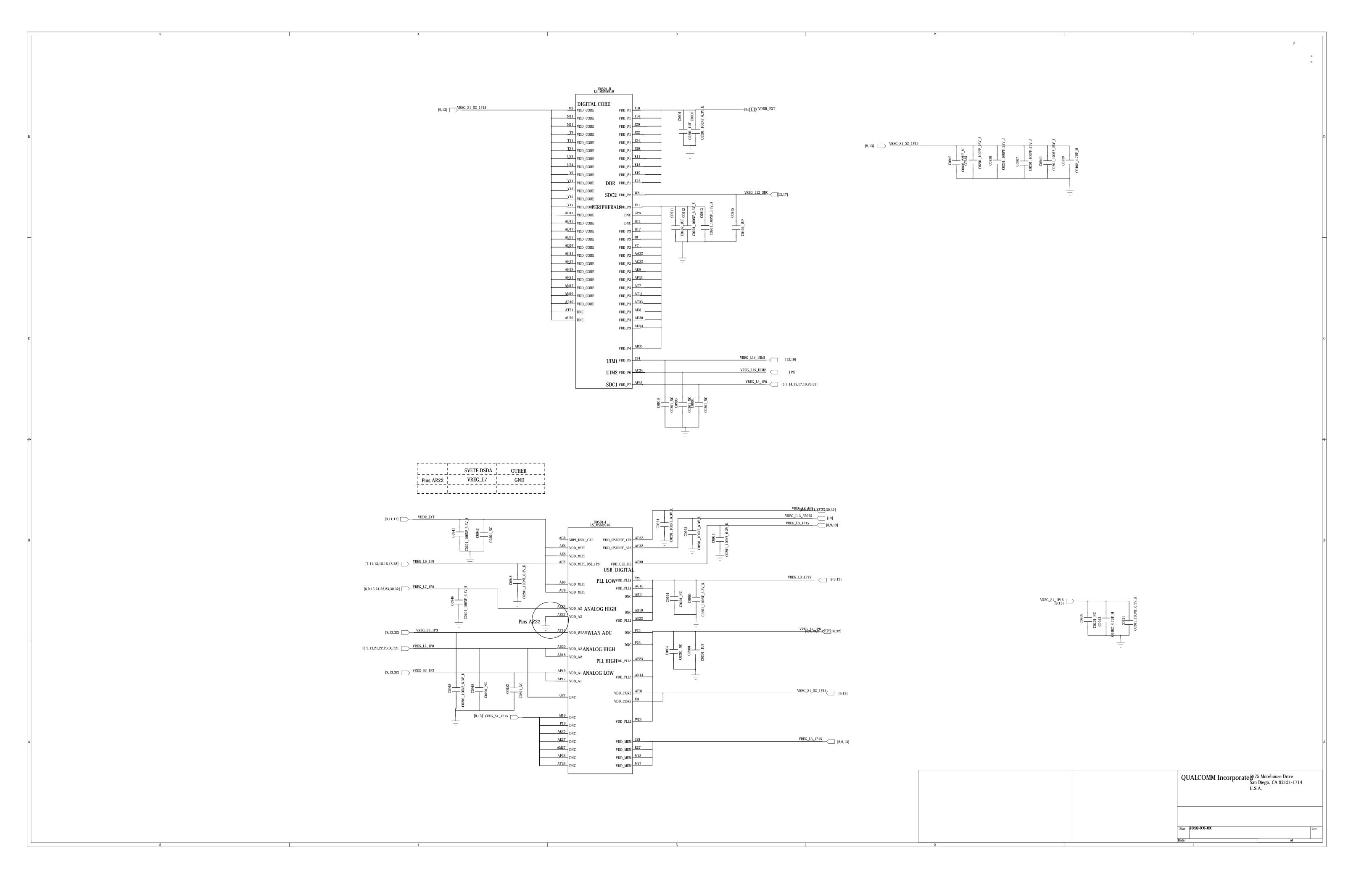


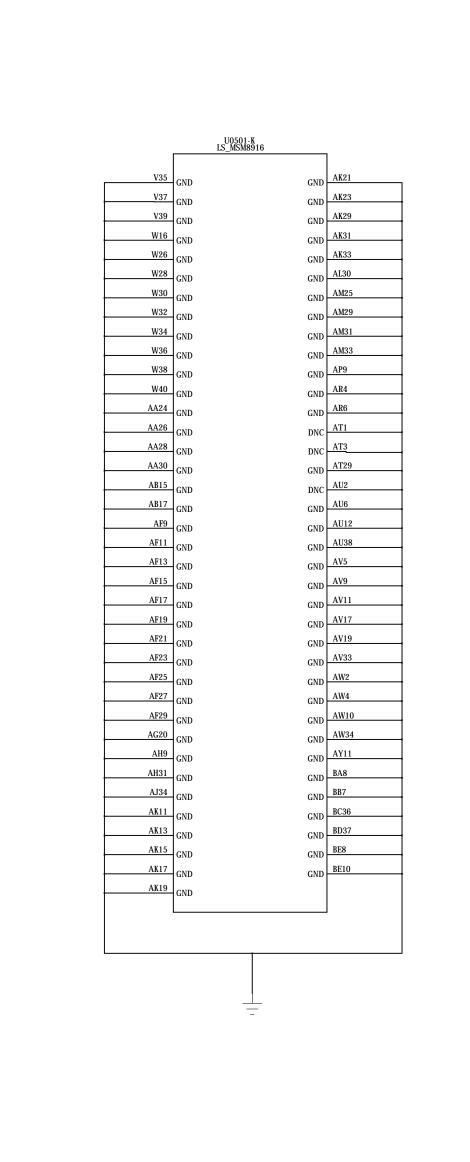


| | Configuration GI | PIO Table | |
|---------|------------------|---------------|--|
| GPIO_80 | WDOG_DISABLE | BOOT_CONFIG_0 | |
| GPIO_81 | BOOT_SEL_1 | BOOT_CONFIG_1 | |
| GPIO_82 | BOOT_SEL_2 | BOOT_CONFIG_2 | |
| GPIO_83 | BOOT_SEL_3 | BOOT_CONFIG_3 | |
| GPIO_84 | | BOOT_CONFIG_4 | |
| GPIO_86 | | BOOT_CONFIG_5 | |
| GPIO_87 | | BOOT_CONFIG_6 | |
| GPIO_88 | | BOOT_CONFIG_7 | |
| GPIO_90 | | BOOT_CONFIG_8 | |
| GPIO_37 | FORCE_USB_BOOT | | |

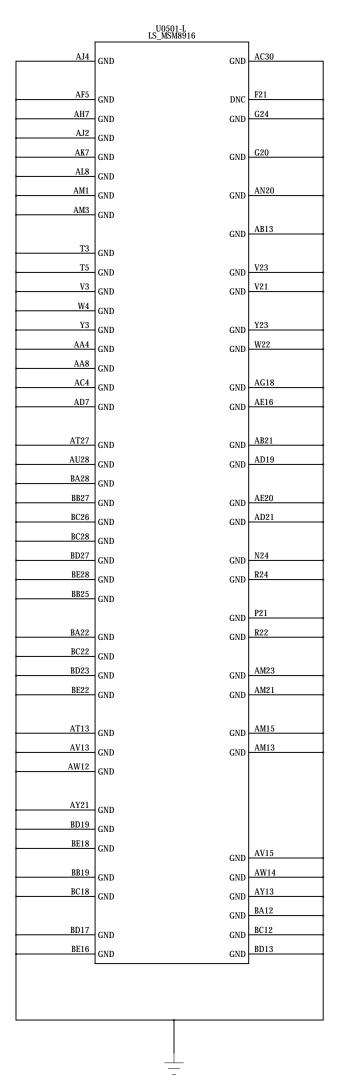
| BOOT_SEL | BOOT MODE |
|-----------------|-----------------|
| 0b000 (Default) | eMMC->SDC2->USB |
| 0b001 | SDC2->eMMC->USB |
| 0b010 | eMMC->USB |
| 0b011 | USB-HS only |
| | |
| | |

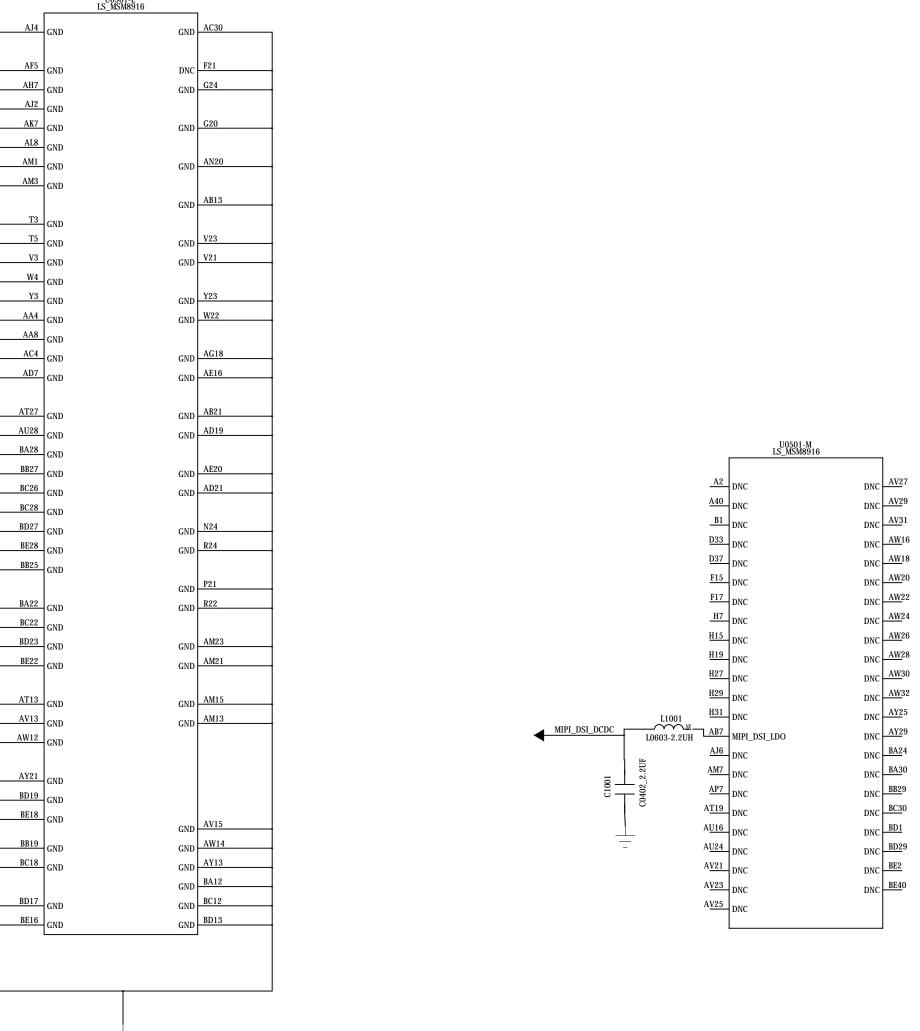




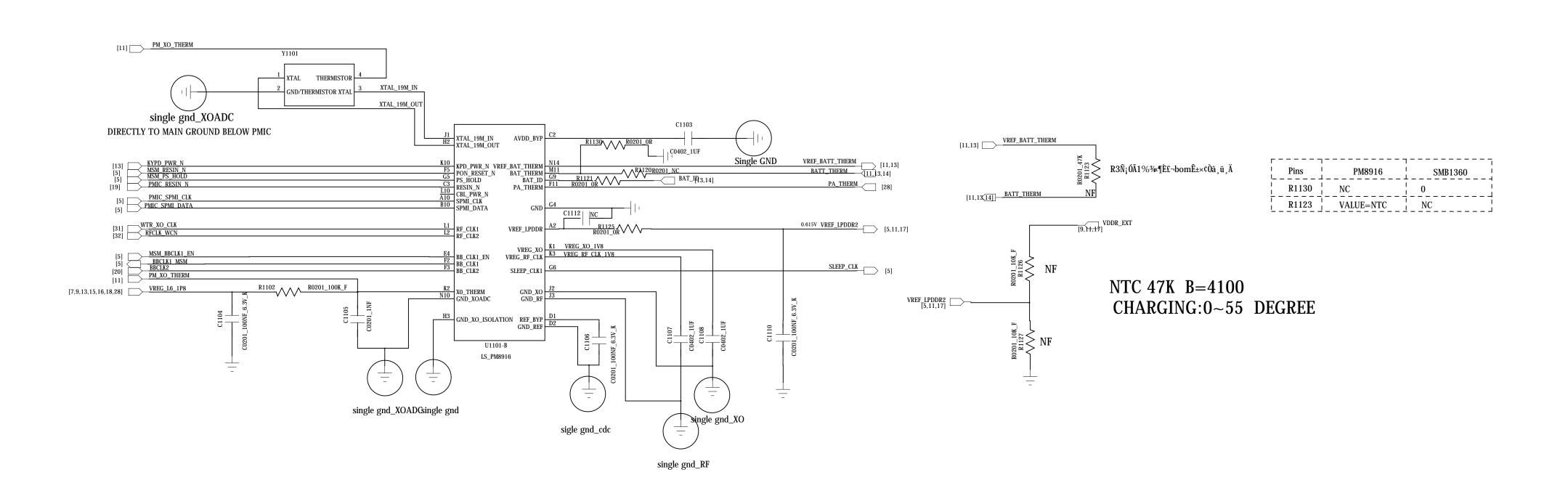


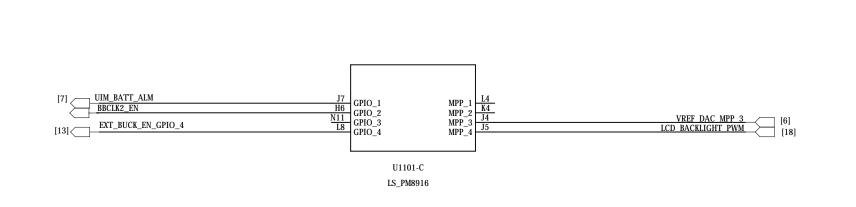
| A6 | LS_MSM | 18916 | was |
|------------|------------|------------|------------|
| A 1 A | GND | GND | M37 |
| A12 A16 | GND | GND | M39 |
| A10 | GND | GND | N2 N4 |
| A28 | GND | GND | N8 |
| A32 | GND | GND | N22 |
| В5 | GND | GND | N26 |
| C8 | GND | GND | N28 |
| C18 | GND GND | GND GND | N30 |
| C20 | GND | GND | N32 |
| C32 | GND | GND | N34 |
| D5 | GND | GND | N36 |
| D7 | GND | GND | N38 |
| D27 | GND | GND | N40 |
| E12 | GND | GND | P5 |
| F7 | GND | GND | P9 |
| F19 | GND | GND | P11 |
| F25 | GND | GND | P13 |
| F27 | GND | GND | P15 |
| F29 | GND | GND | P17 |
| F37 | GND | GND | P35 |
| G8 | GND | GND | P37 |
| G10 | GND | GND | P39 |
| G12 | GND | GND | R8 |
| G14 | GND | GND | R26 |
| G16 | GND | GND | R28 |
| G18 | GND | GND | R30 |
| G28 | GND | GND | R32 |
| G30 G32 | GND | GND | R34 |
| H35 | GND | GND | R36 R38 |
| Н37 | GND | GND | R40 |
| J16 | GND | GND | T17 |
| J18 | GND | GND | T19 |
| J32 | GND | GND | T35 |
| J36 | GND | GND | T37 |
| J38 | GND | GND | T39 |
| К5 | GND GND | GND GND | U26 |
| К9 | GND | GND | U28 |
| K17 | GND | GND | U30 |
| K21 | GND | GND | U32 |
| <u>K37</u> | GND | GND | U34 |
| L4 | GND | GND | U36 |
| L6 | GND | GND | U38 |
| | GND | GND | U40 |
| L8 | GND | GND | V11 |
| | UND | | V13 |
| L8 | GND | GND | |
| L8 L30 | | GND GND | V15 |
| L30 L32 | GND | | |

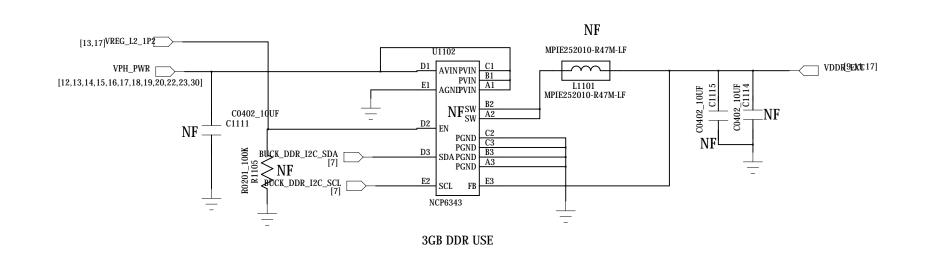




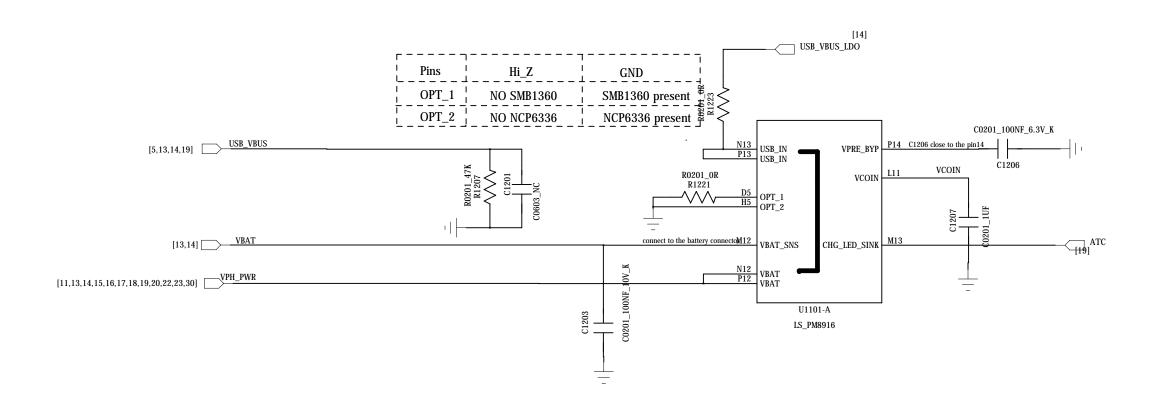
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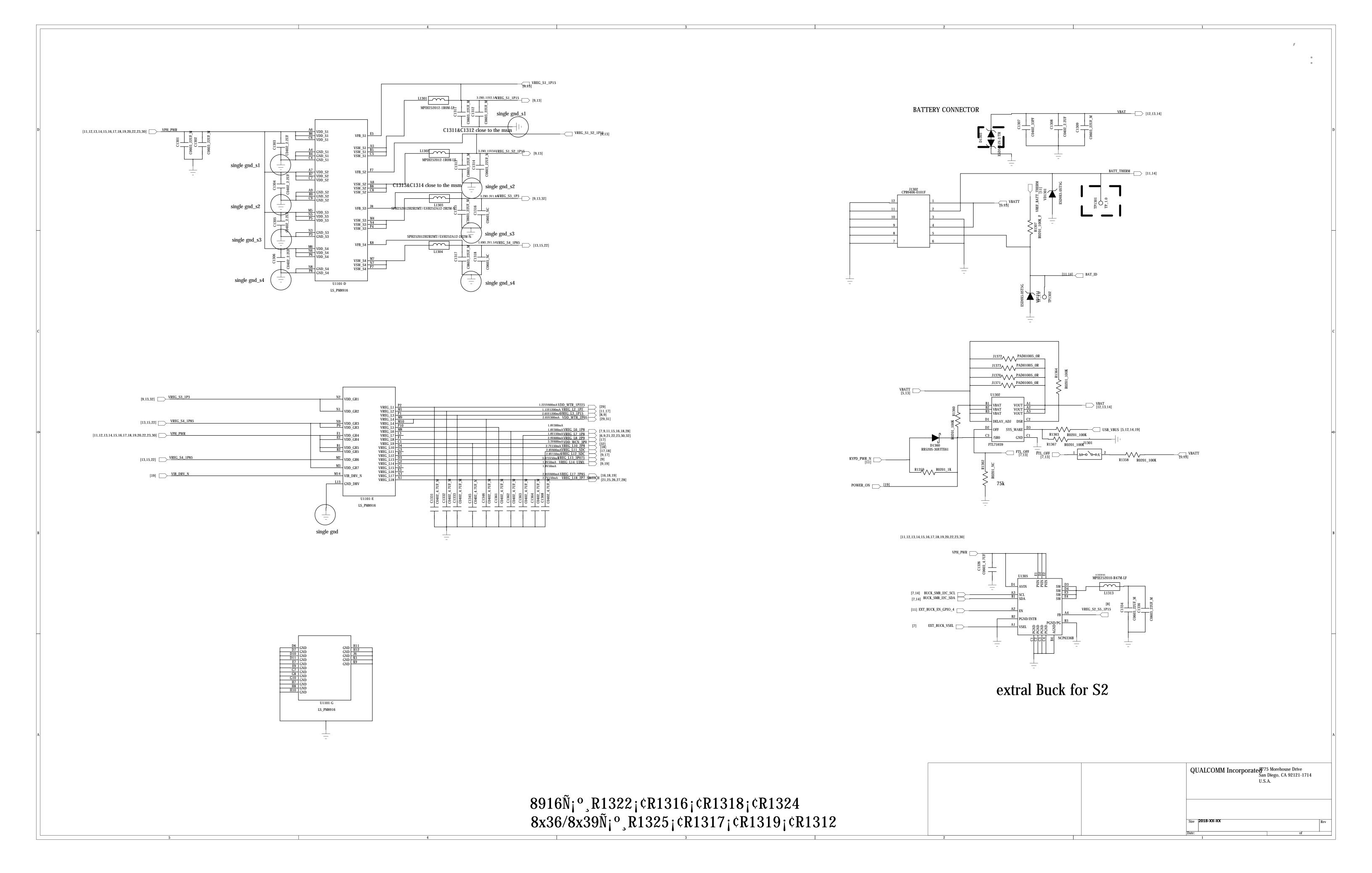


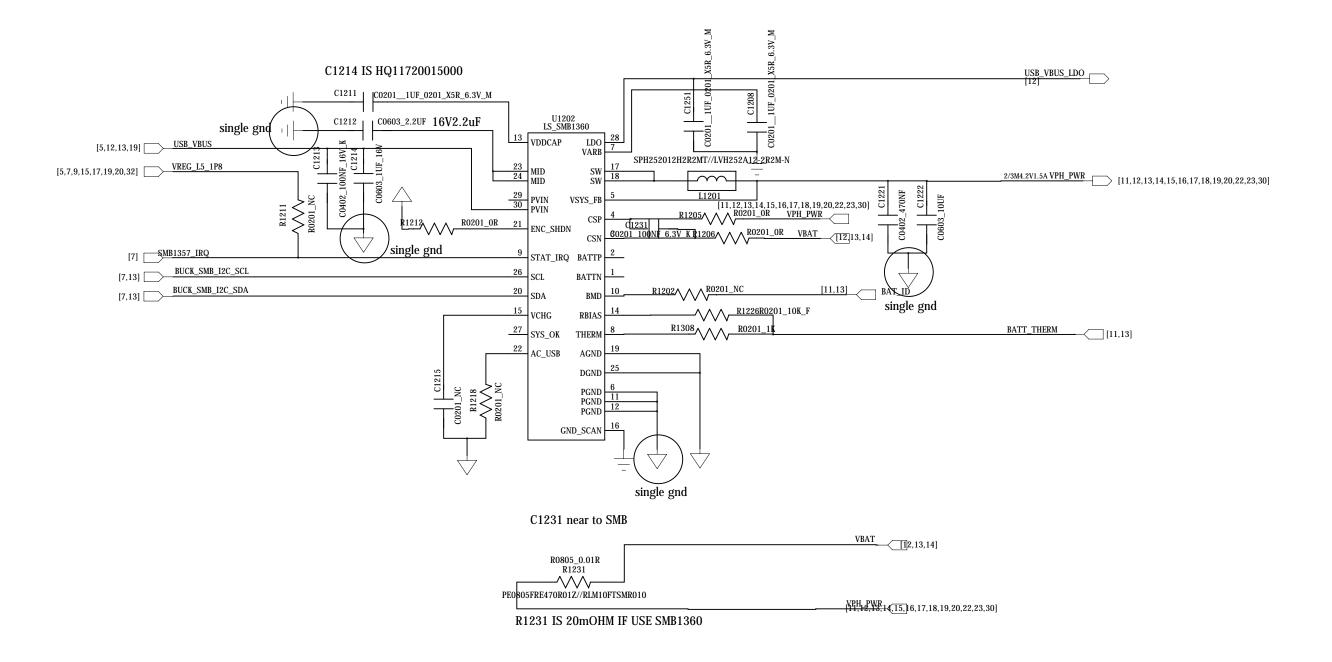
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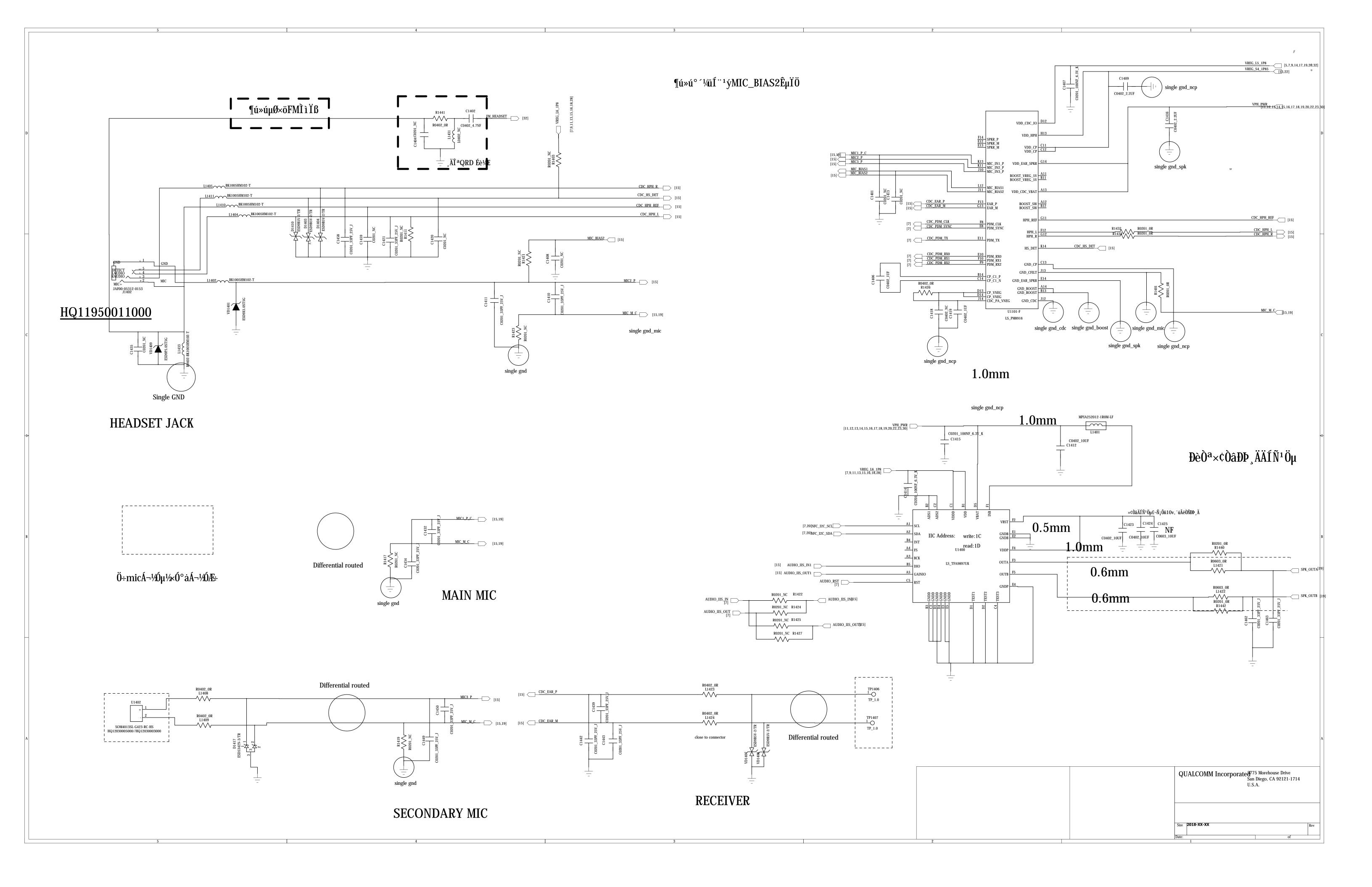
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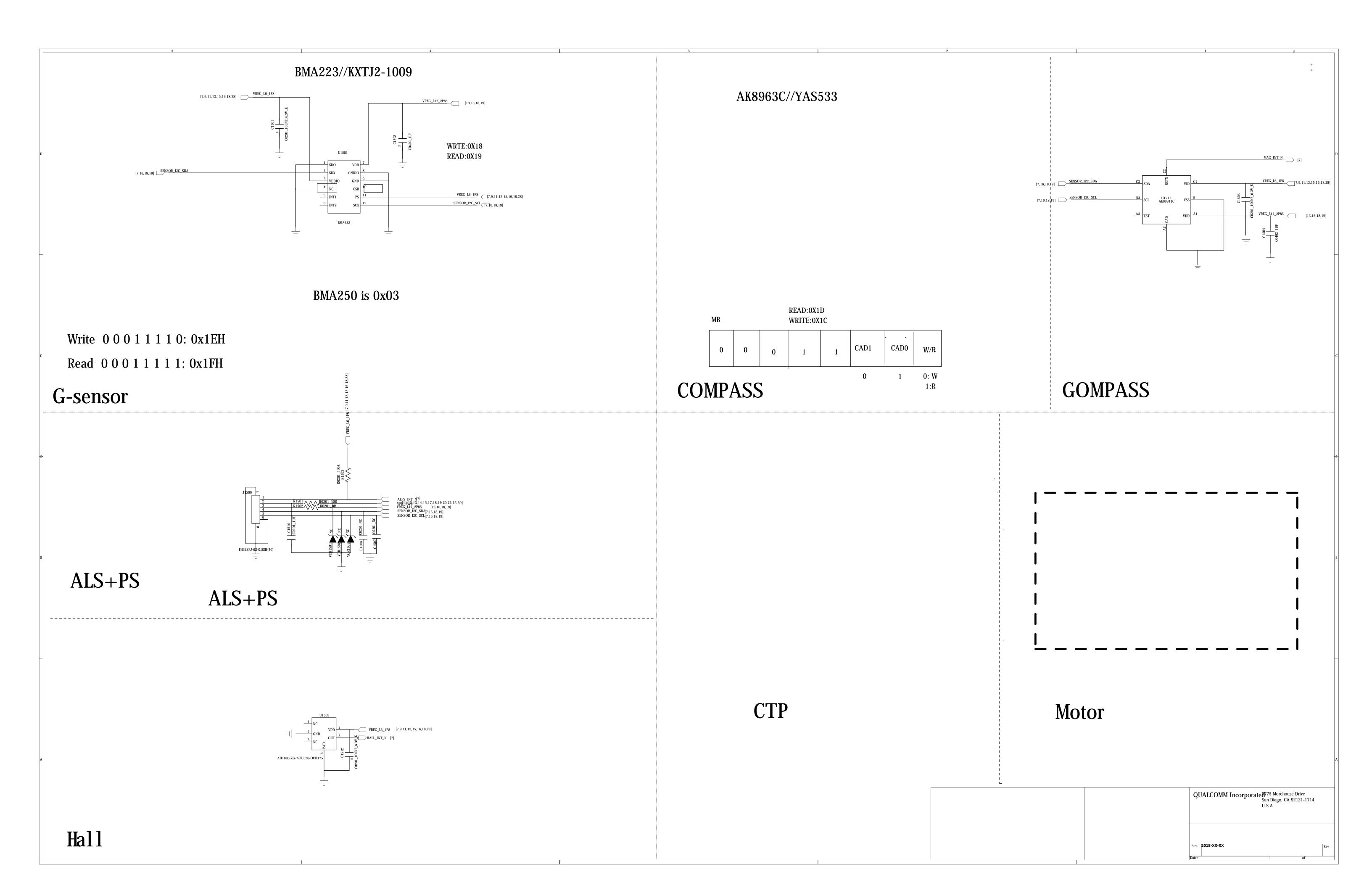


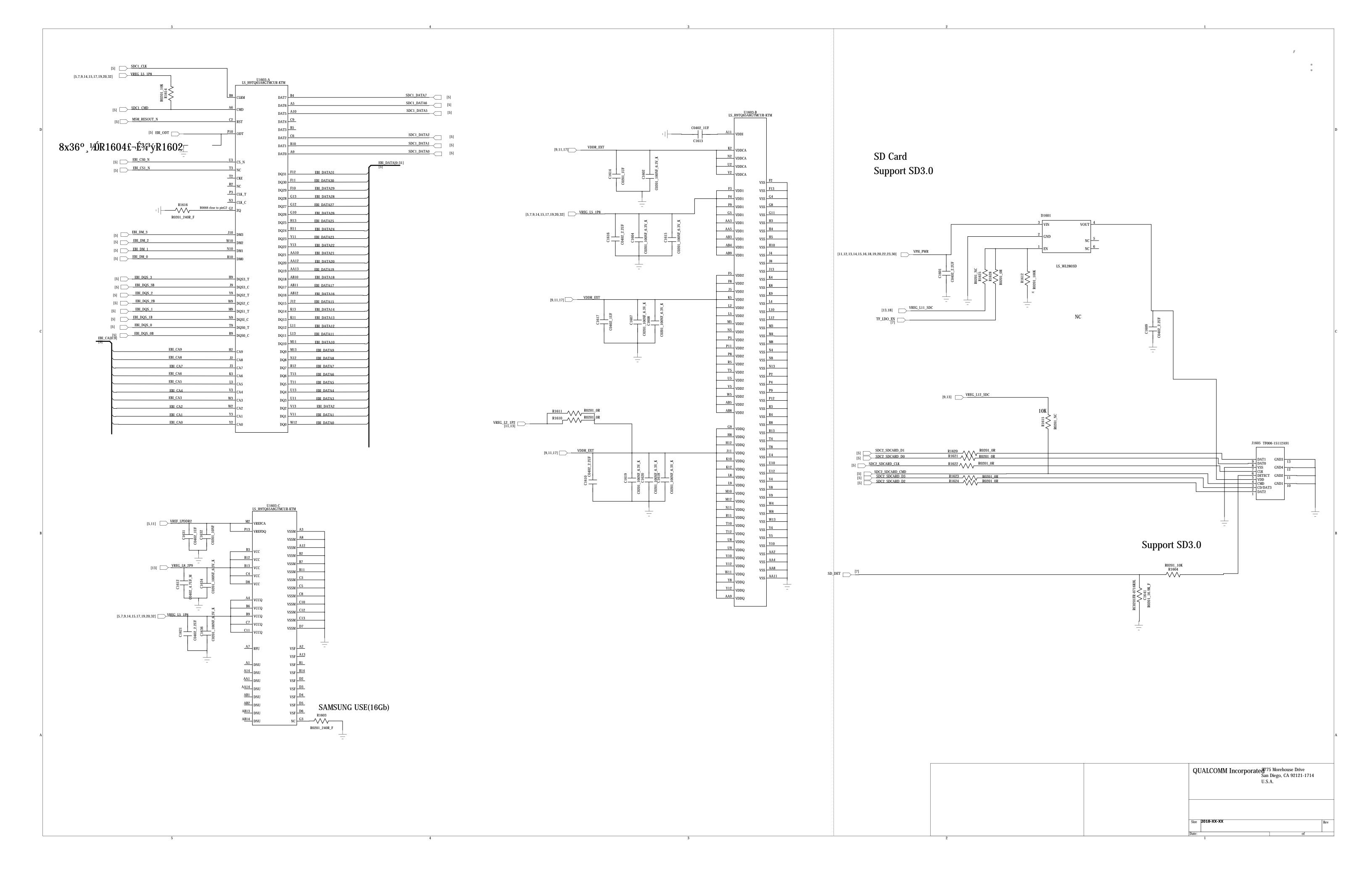


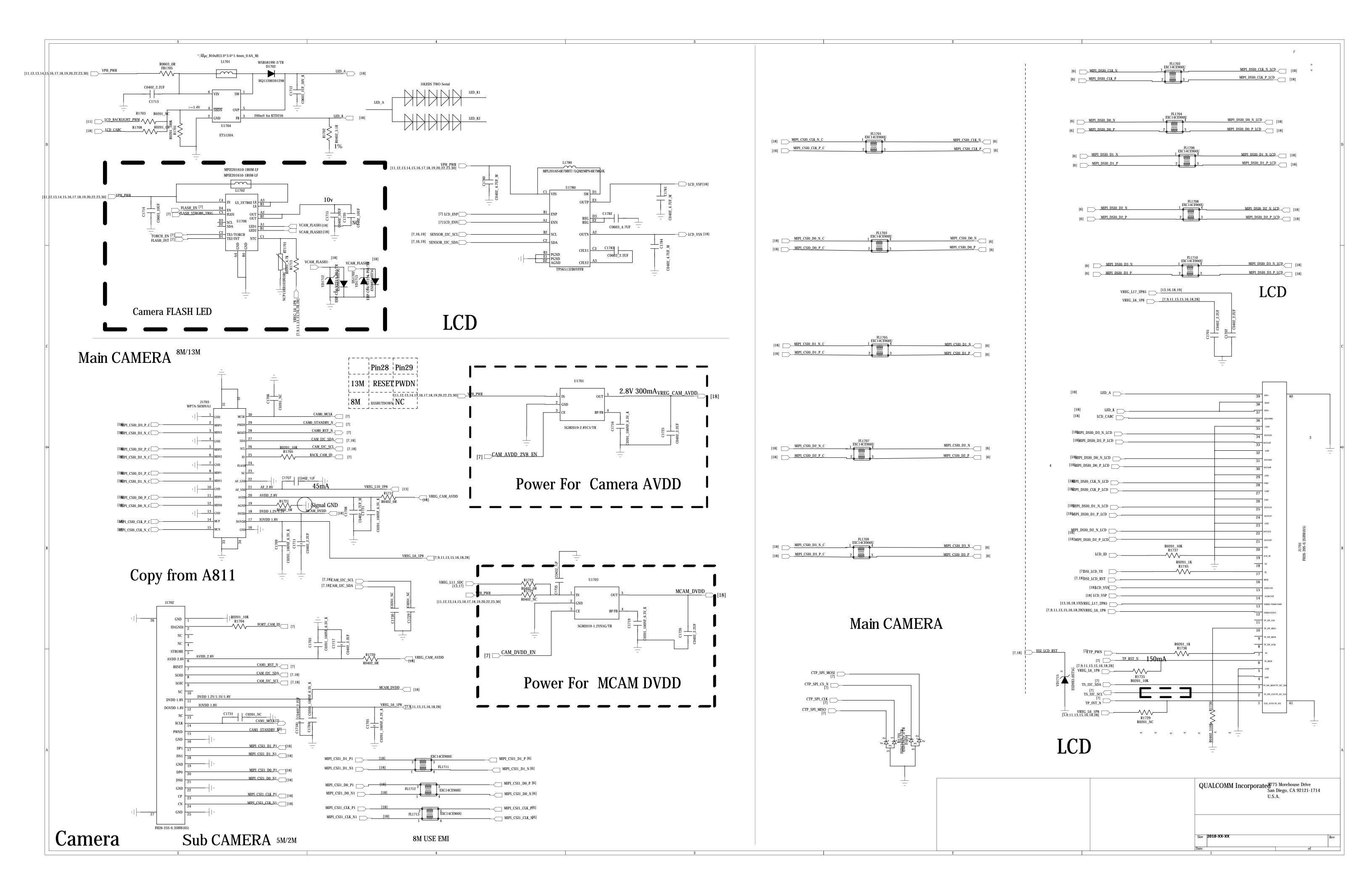
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U.S.A.

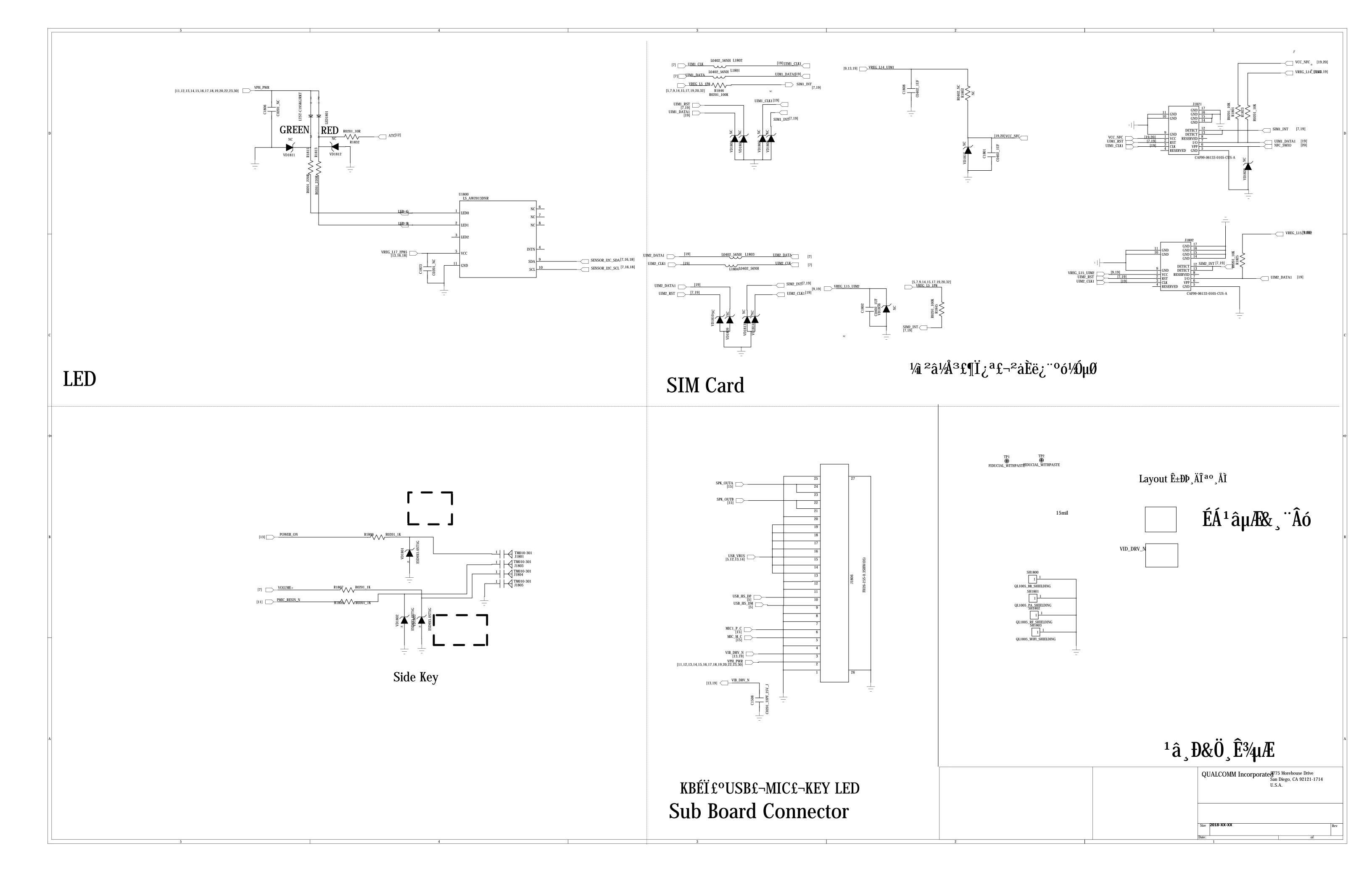
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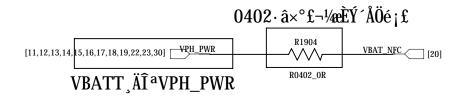


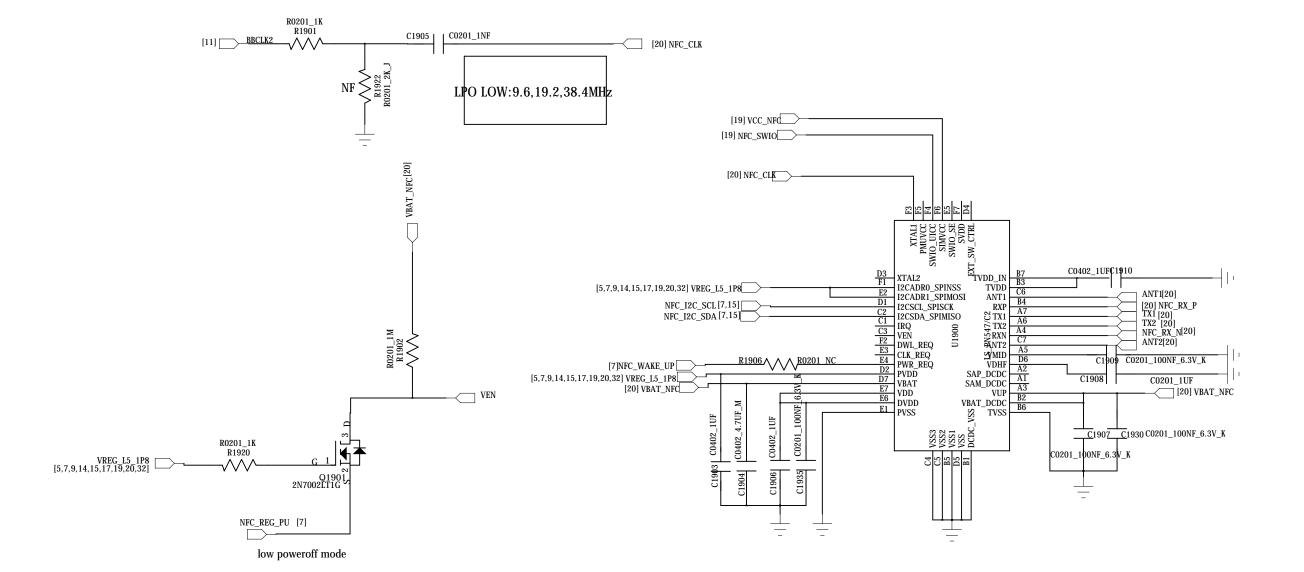










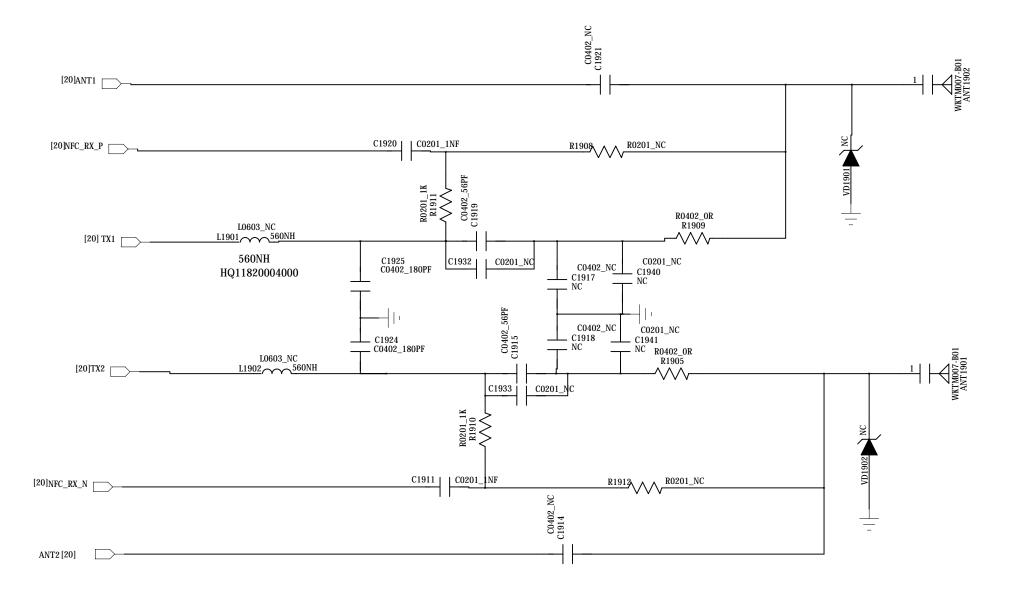


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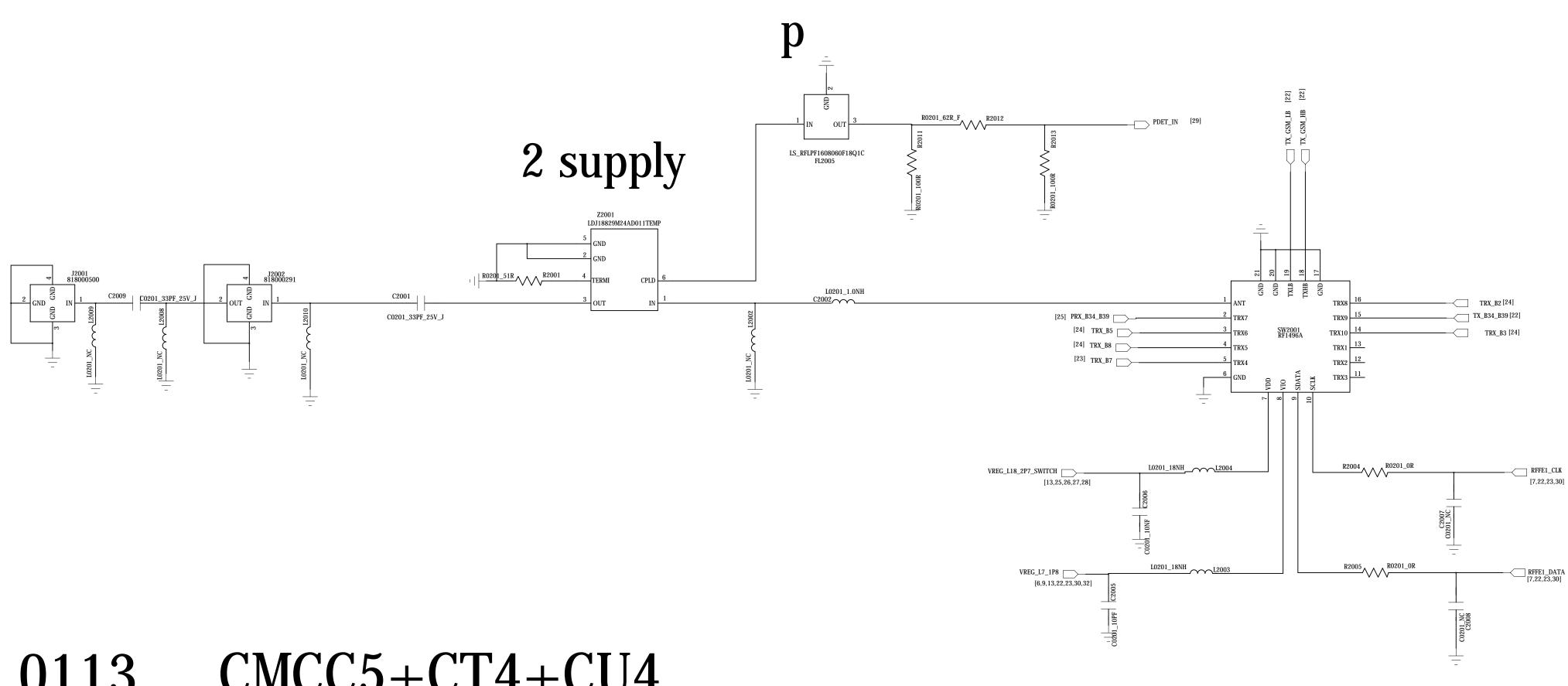
| VBAT | PVDD | VEN | MODE |
|------|------|-----|------------------|
| OFF | OFF | X | POWER OFF |
| ON | OFF | ON | LOW POWER(150uA) |
| ON | X | OFF | POWER OFF |
| ON | ON | ON | FULL POWER |

RF



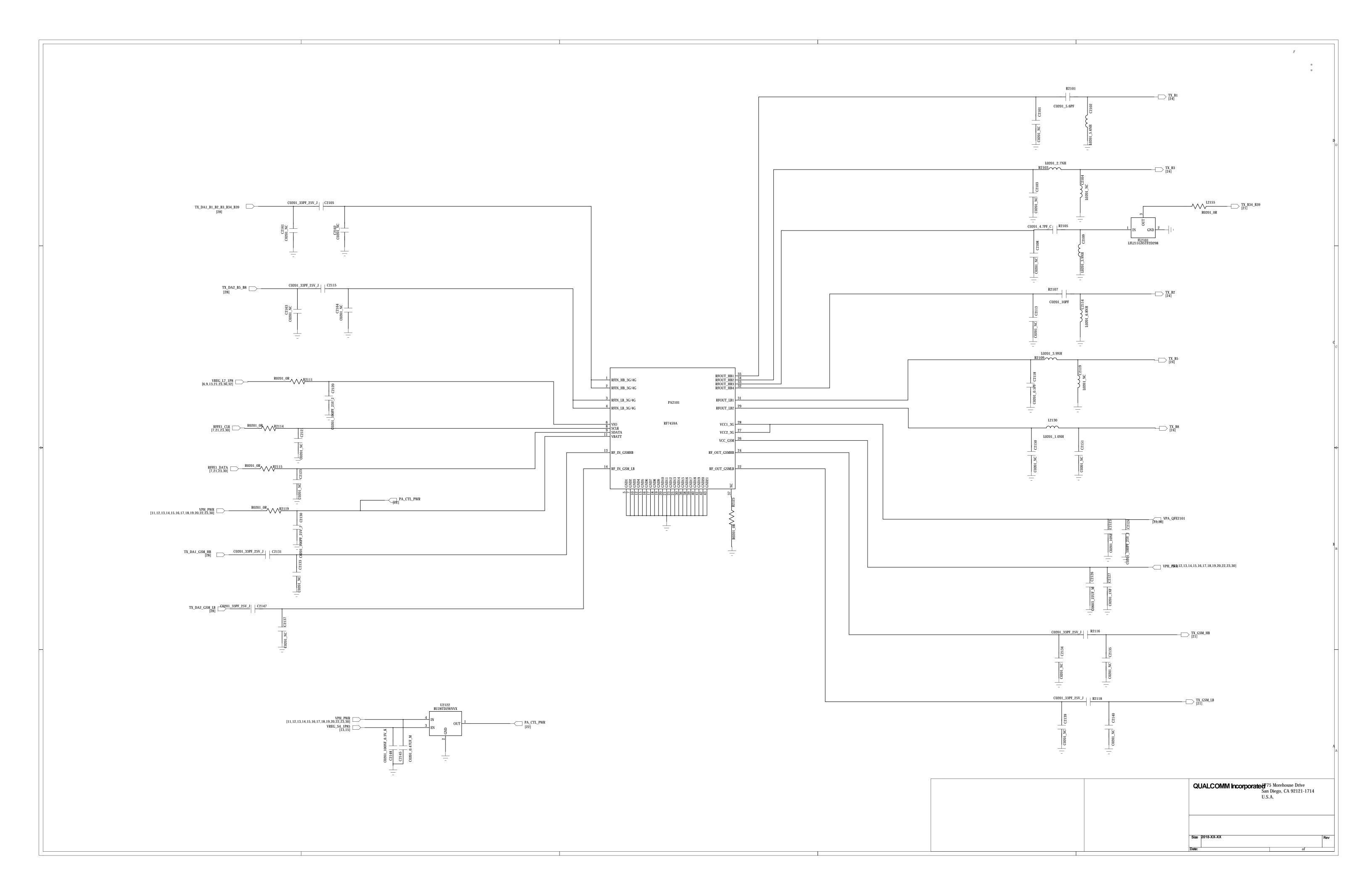
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PRIMARY_ANTENNA_SW

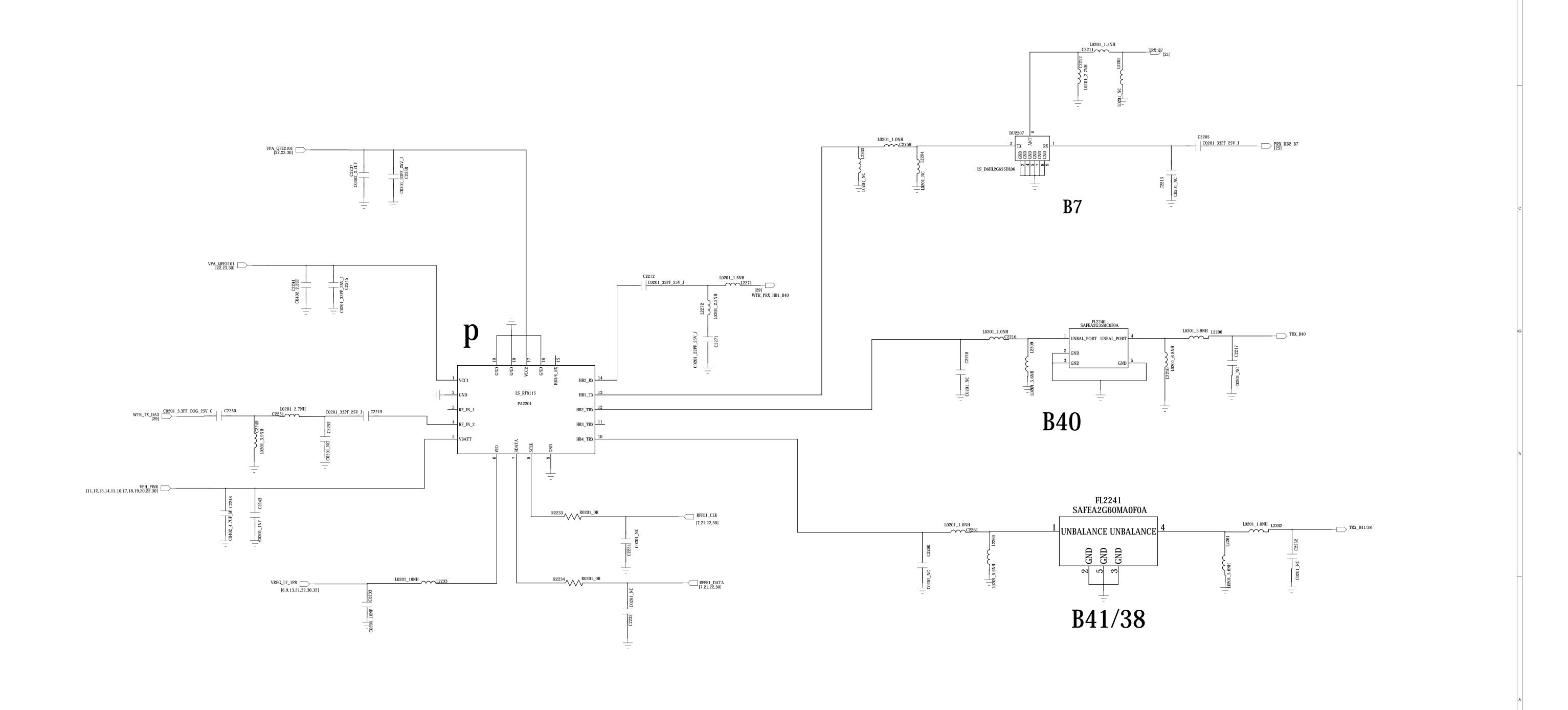


0113 CMCC5+CT4+CU4

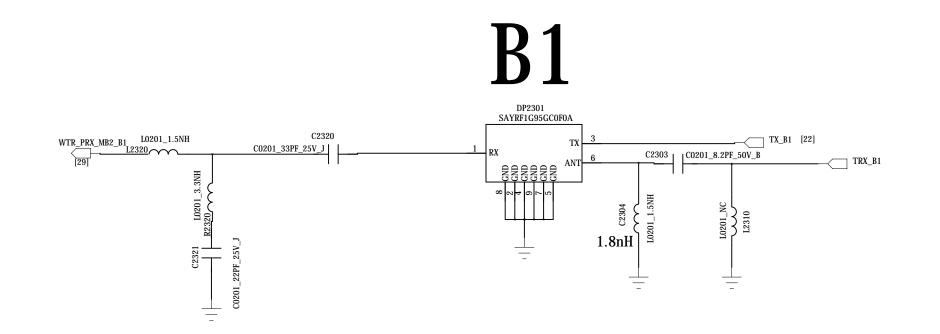
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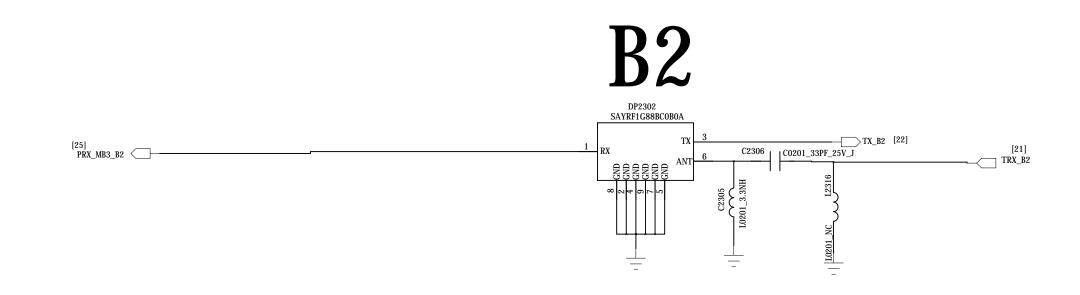


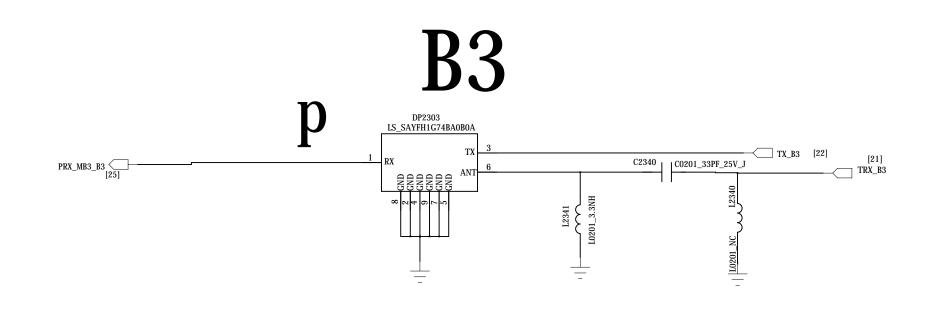
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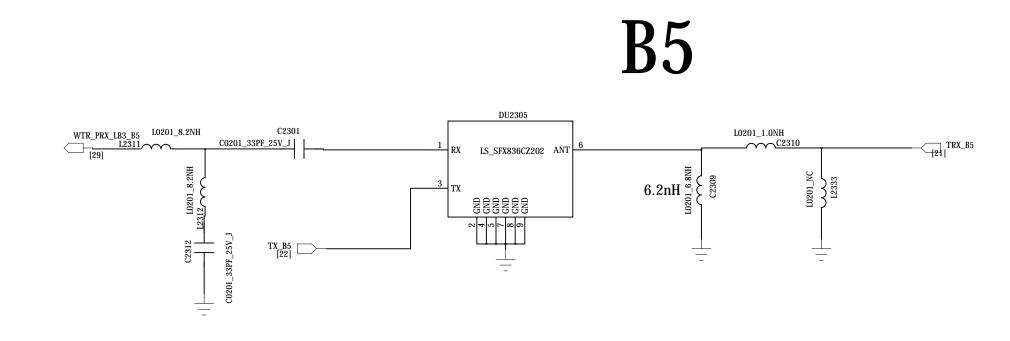


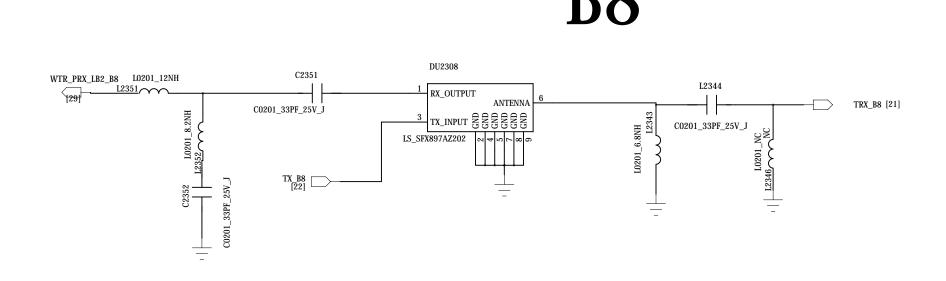
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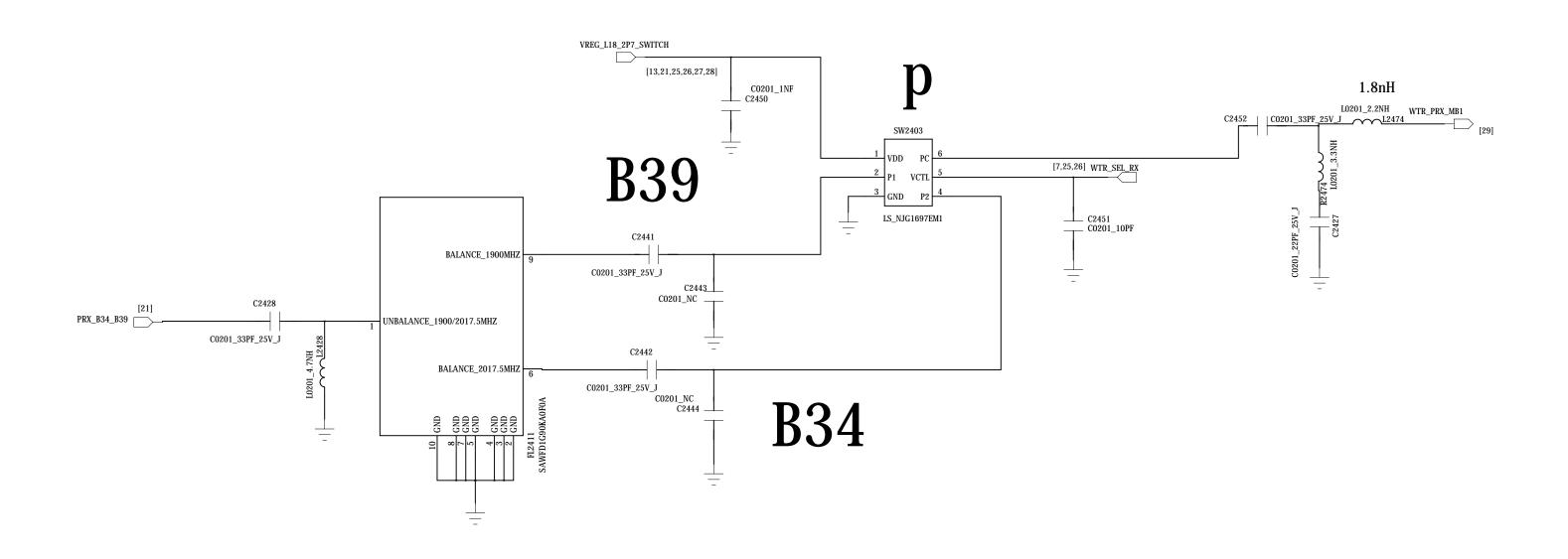




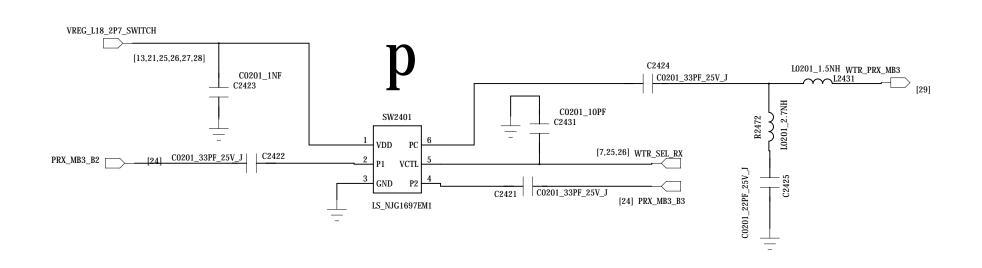




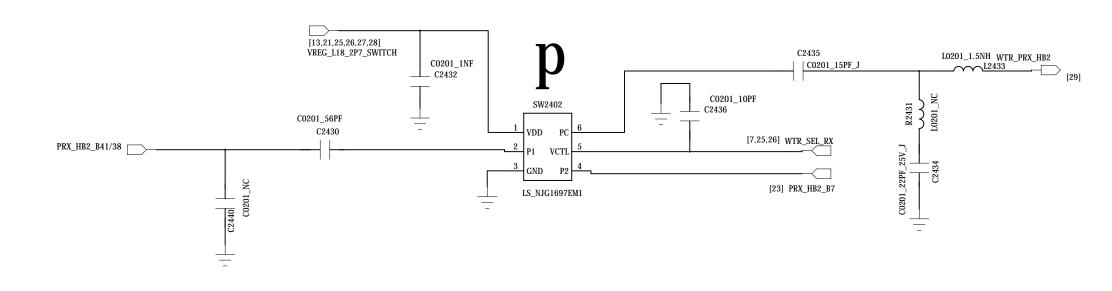
PRX_B34/39

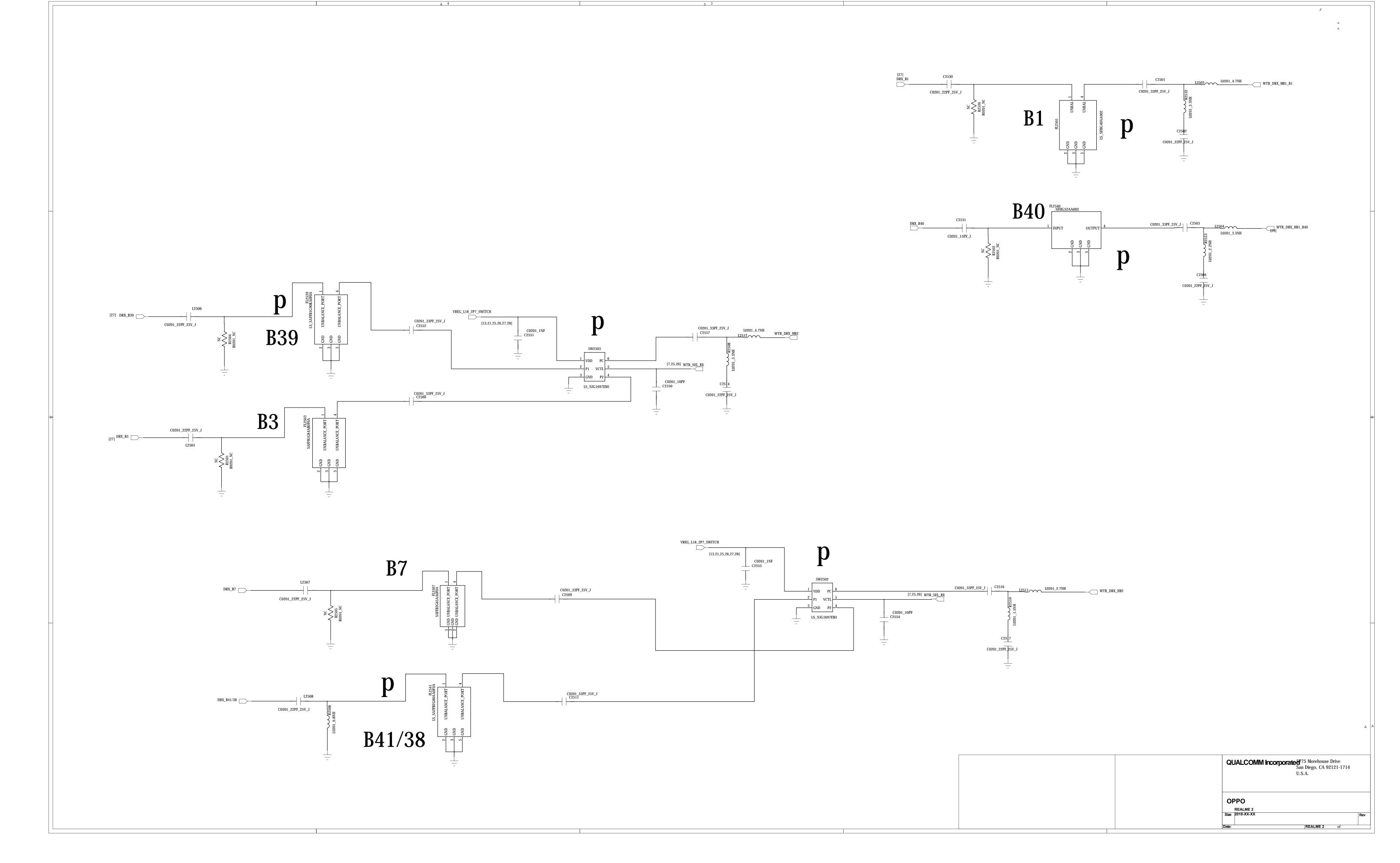


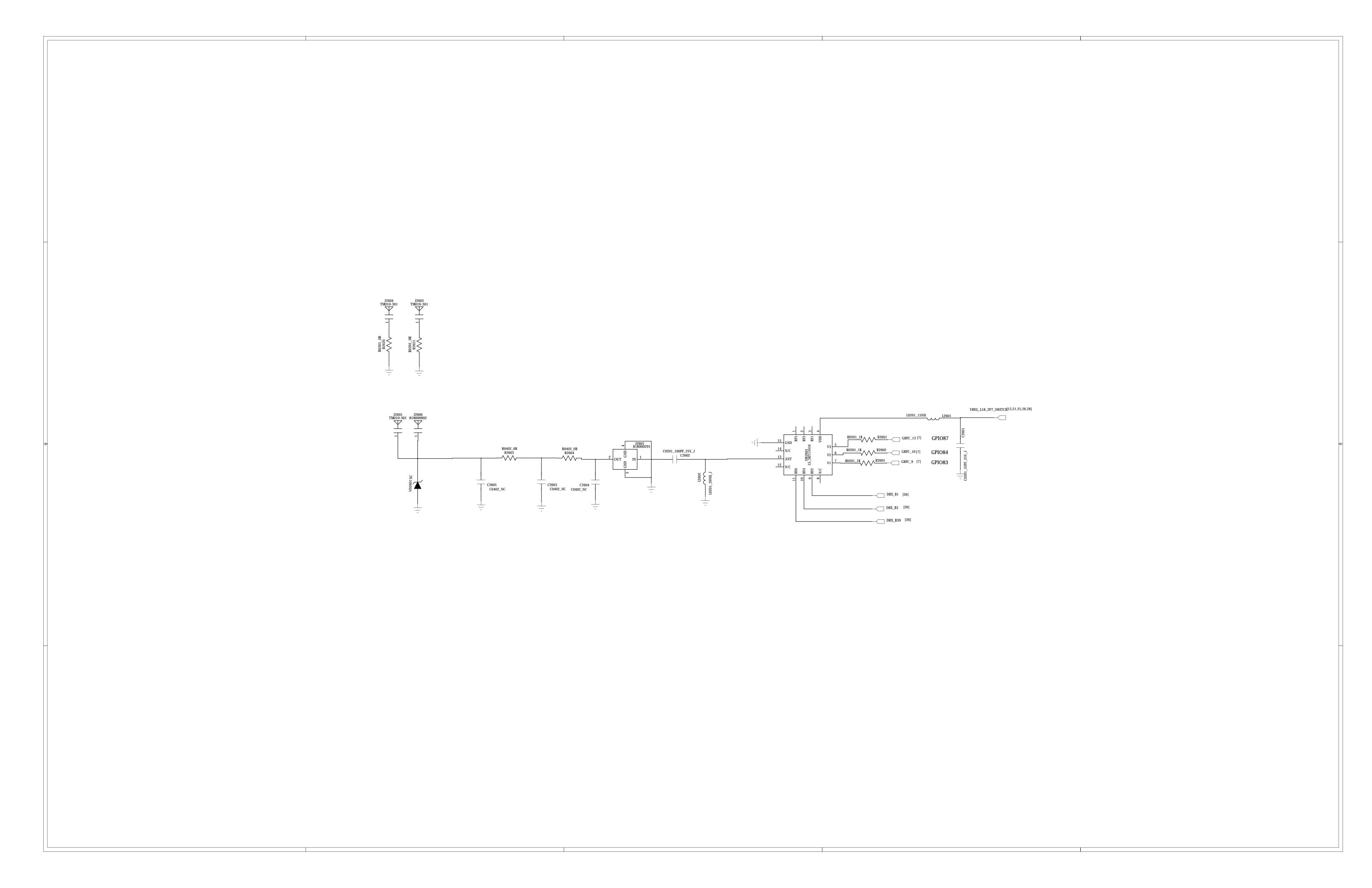
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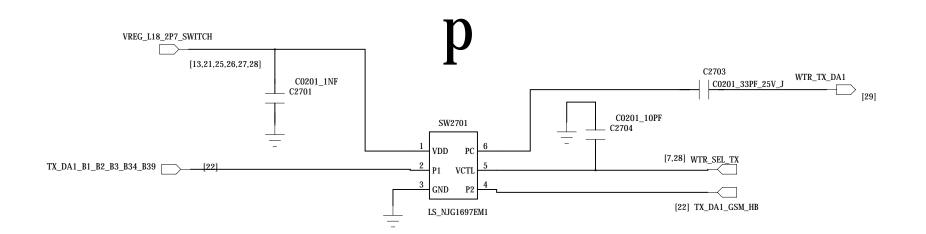
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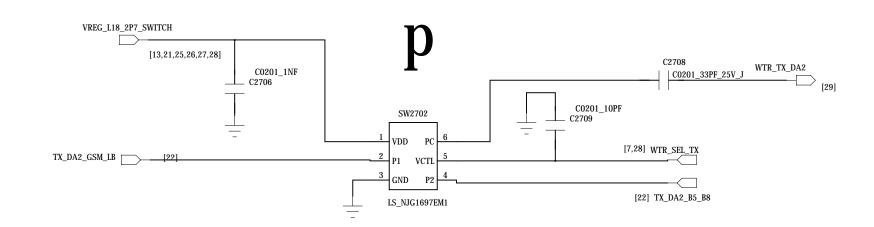




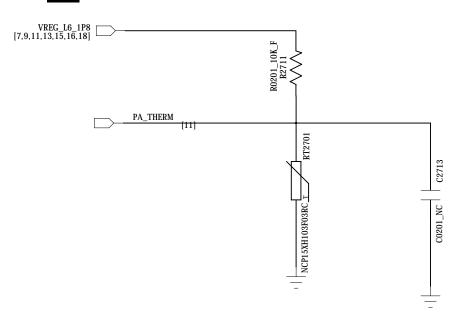


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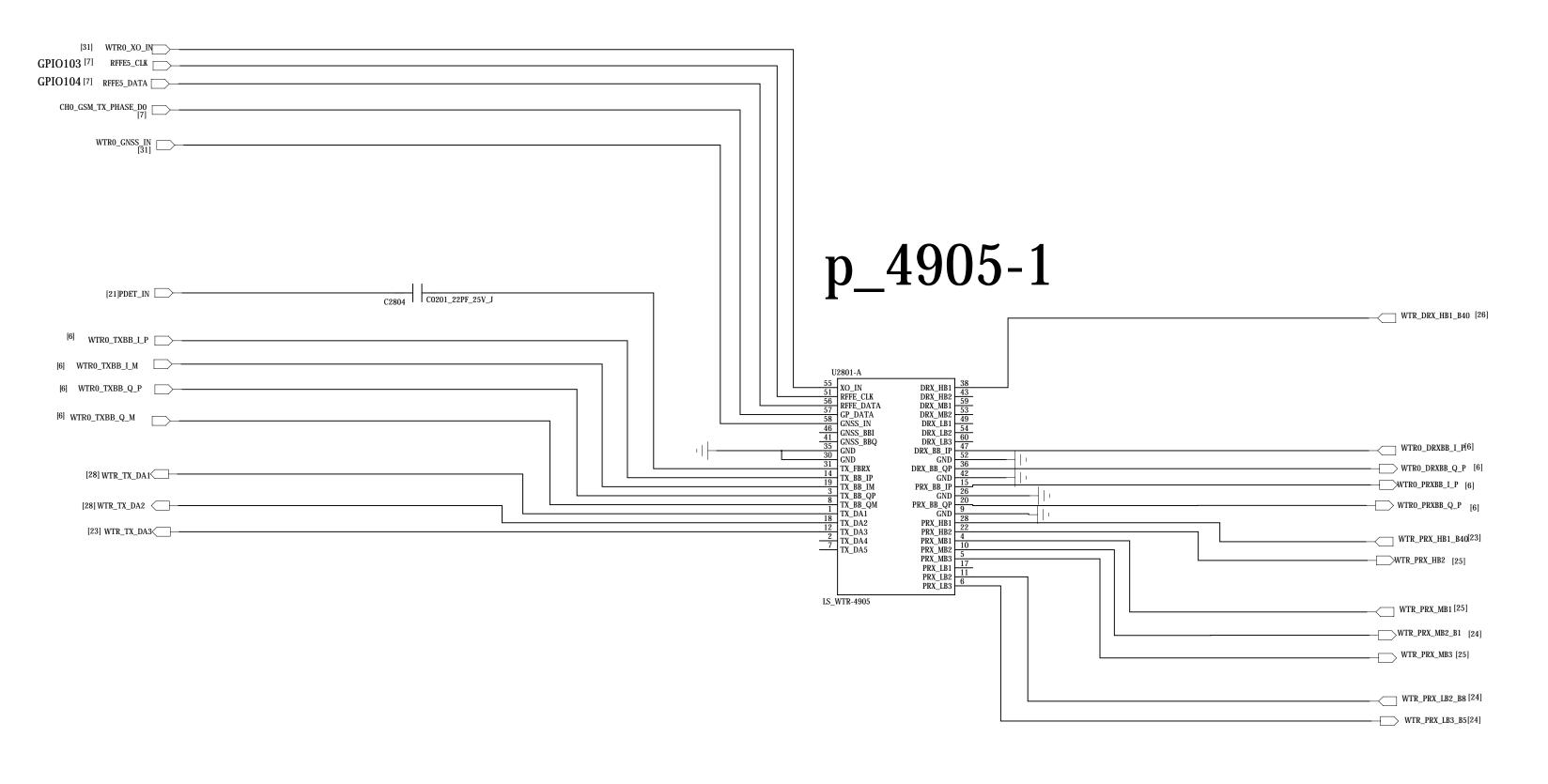


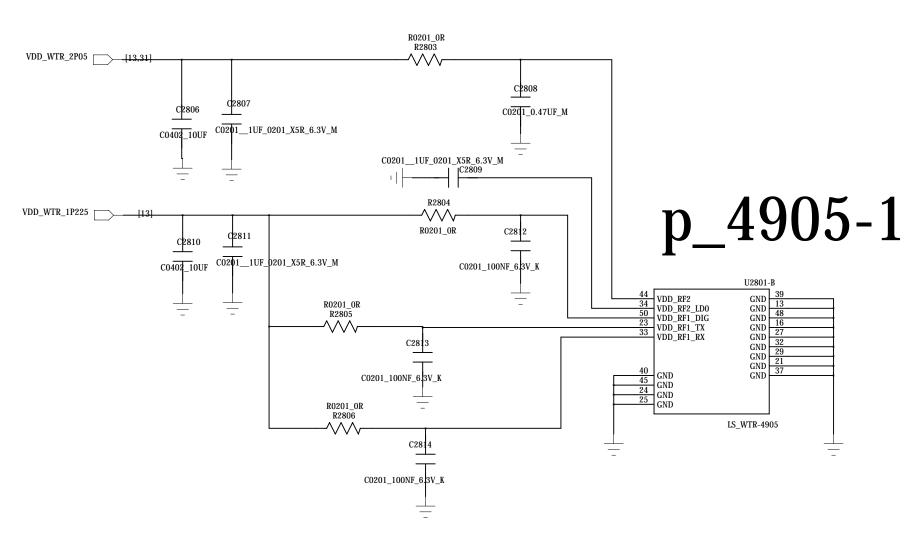


PA_THERMAL

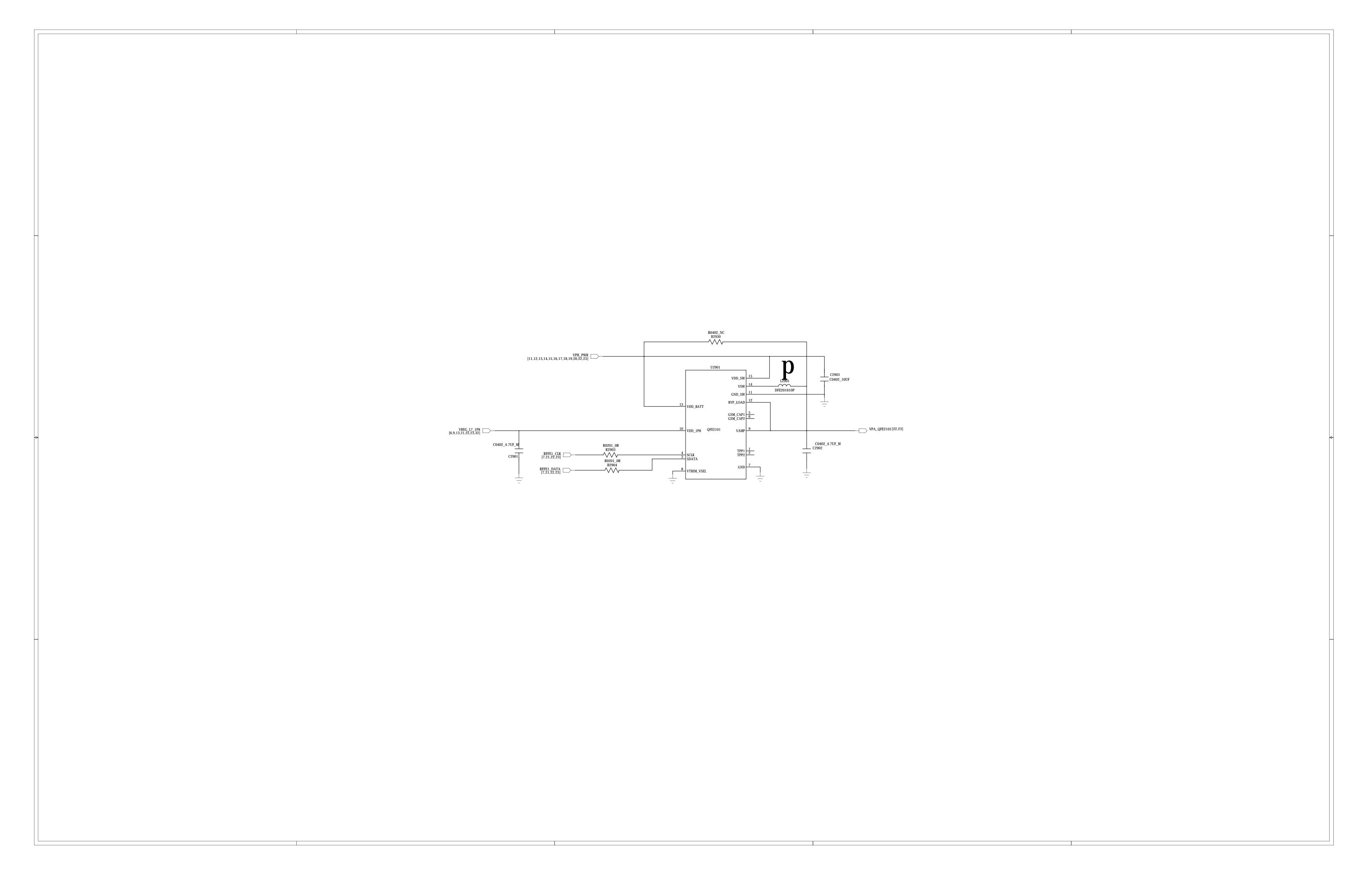


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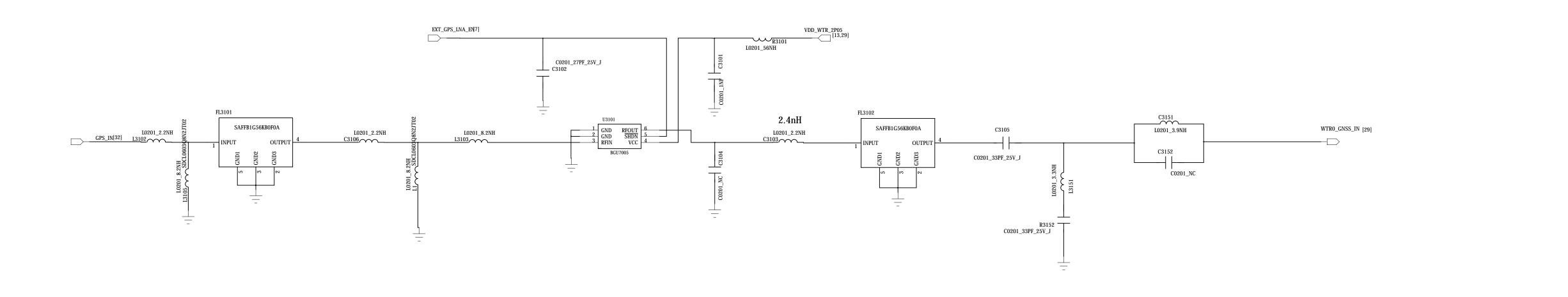




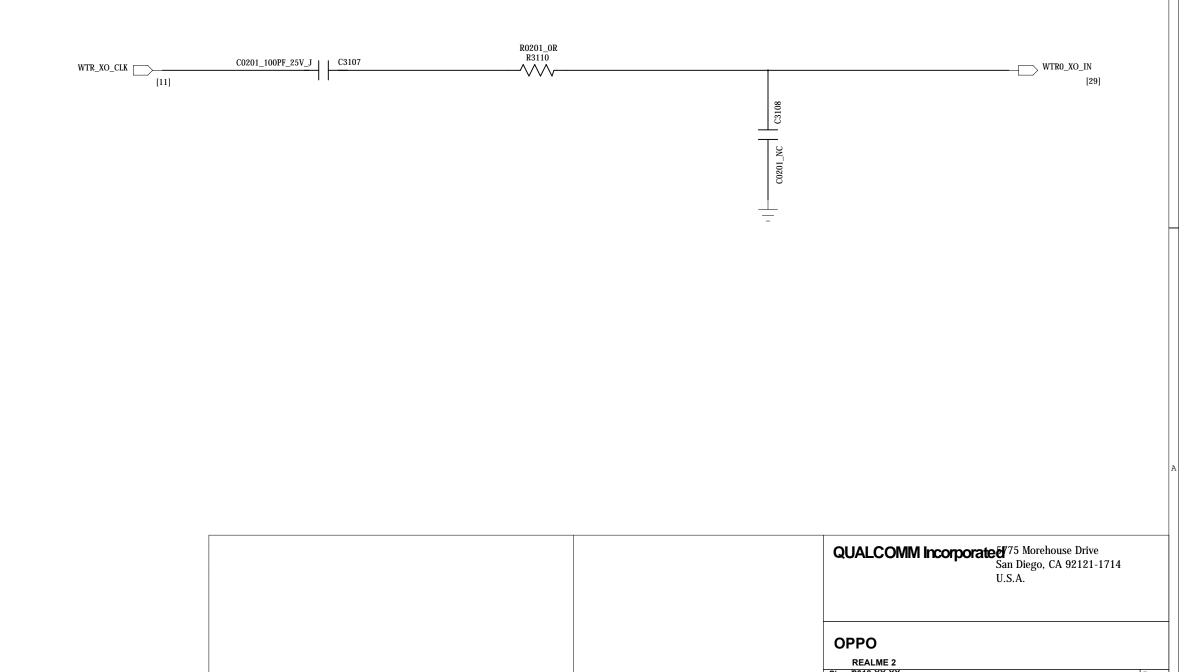
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