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SM-S908B_GW Ya UjV\$F 9 J \$"†

21.11.28

- sheet01 : INTERPOSER
- sheet02 : mmW / AUDIO / PROXY
- sheet03 : UFS / HALL IC / USIM / HALL IC
- sheet04 :
- sheet05 modAP / System Clock
- sheet06 : modAP Power_decoupling capacitor
- sheet07 : Power management IC(modAP / Peri) & IFPMIC
- sheet08 :
- sheet09 : CAMERA
- sheet10 : ANT / SC / KEY / AUX SW / BOSS

Engineer:	
Drawn by:	
Rev. Chk:	
Rev. Enter Chk:	
TITLE:	

Change By: Date Changed: Time Changed: QA Chk: REV: Drawing Number: Page:

SM-S908B_GW Ya UjV\$F 9 J \$"+

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Engineer:	
Drawn by:	
RED CHK:	
APG ENGR CHK:	
TITLE:	Sheet

MASTER

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INTERPOSER

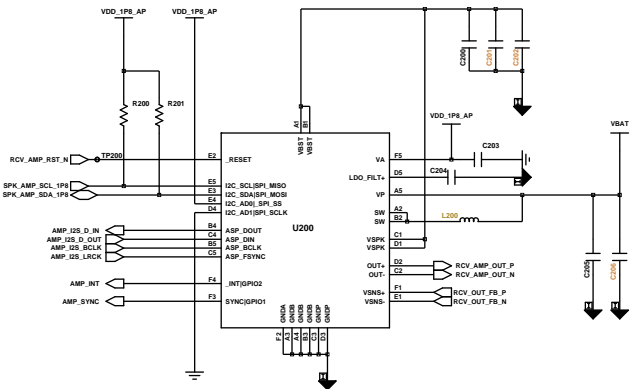


MASTER

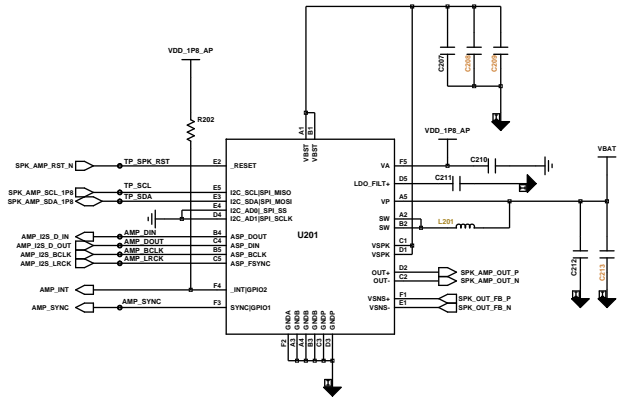
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MMW IFIC

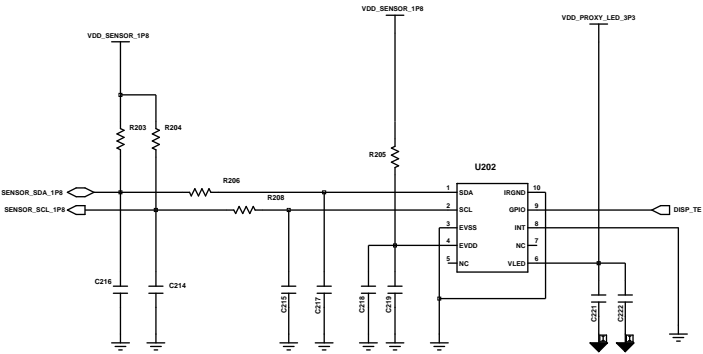
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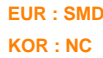
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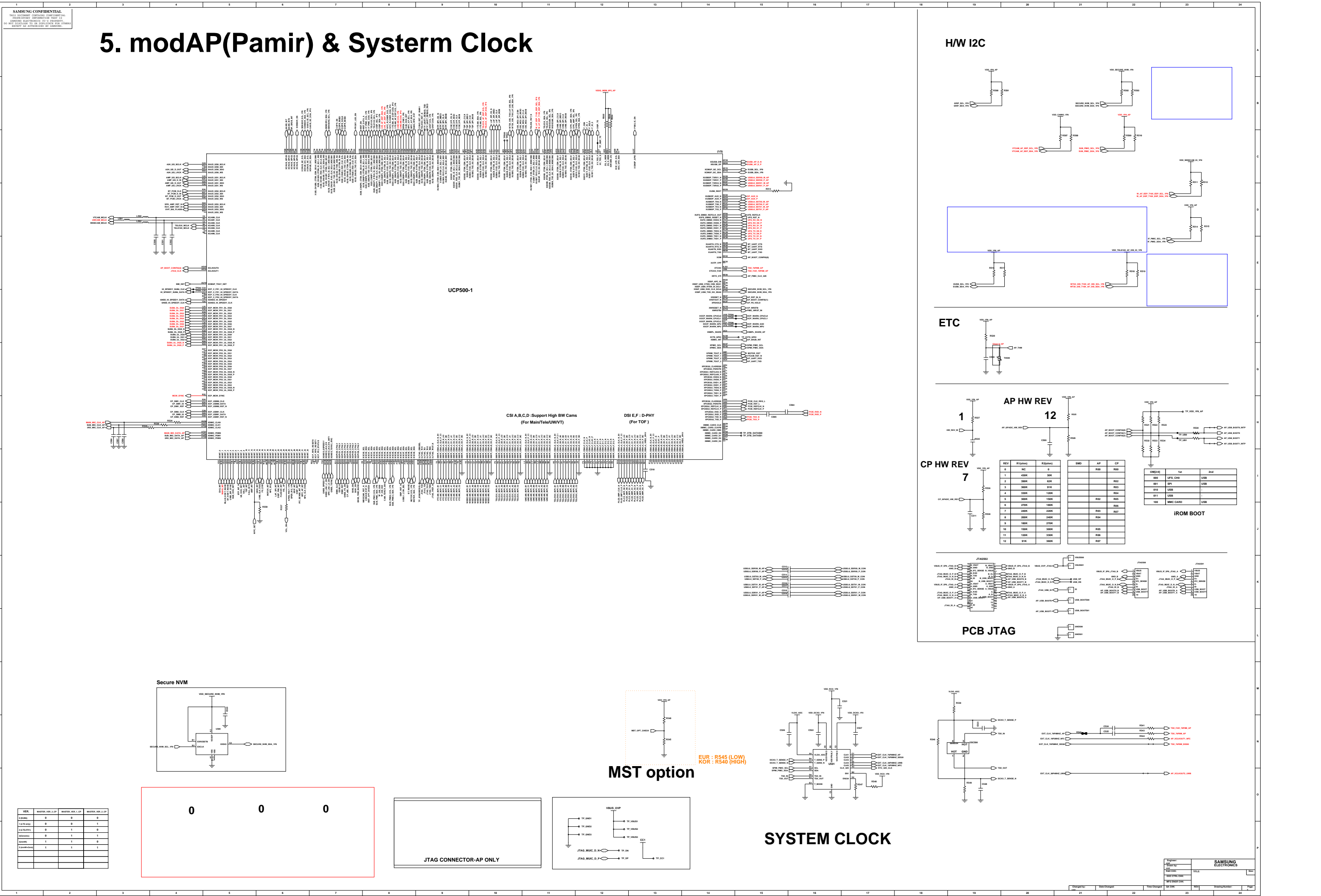


Proximeter/Ambient Light Sensor



Engineer:	<div style="text-align: center;">  </div>		
Drawn by:			
Check by:			
R&D CHK:	TITLE:	Size:	
DOC CTRL CHK:	U_LSI_Olympus_0206	16 I 12 A	
MFG ENGR CHK:			
QA CHK:	REV	Drawing Number:	Page:

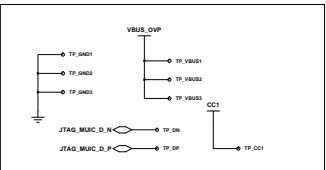
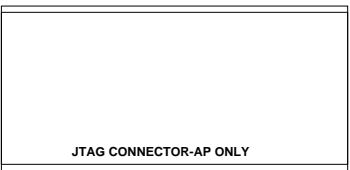




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VER.	MASTER VER. A, CP	MASTER VER. L, CP	MASTER VER. J, CP
1.0.0.0	0	0	0
1.0.1.0	0	0	1
1.0.2.0	0	1	0
1.0.3.0	0	1	1
1.0.4.0	1	1	0
1.0.5.0	1	1	1

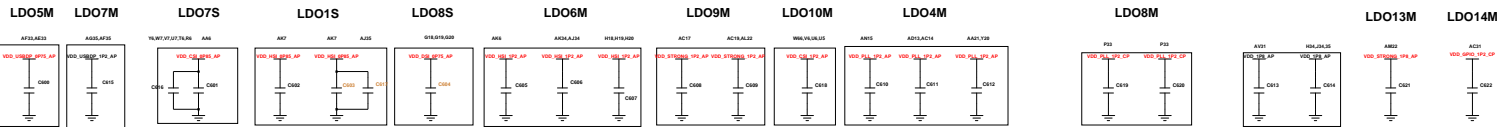
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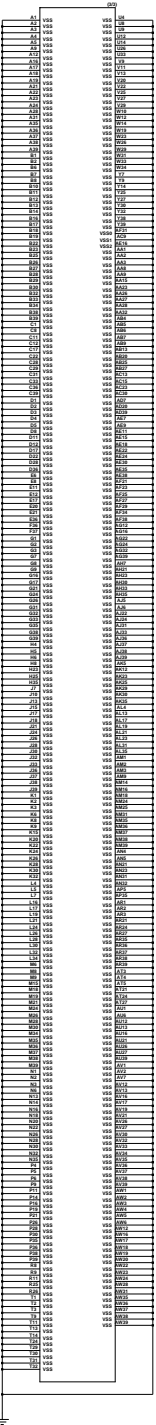
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6. modAP(OLYMPUS) Power_decoupling capacitor

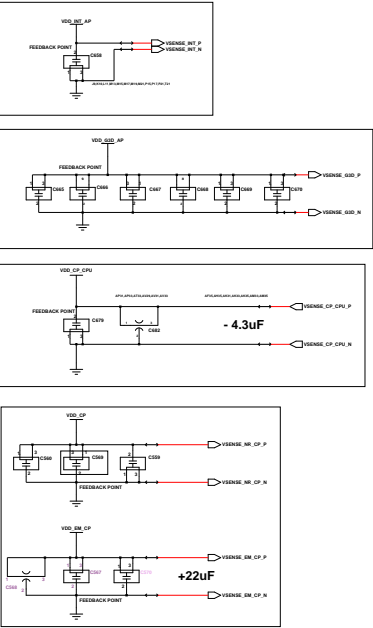
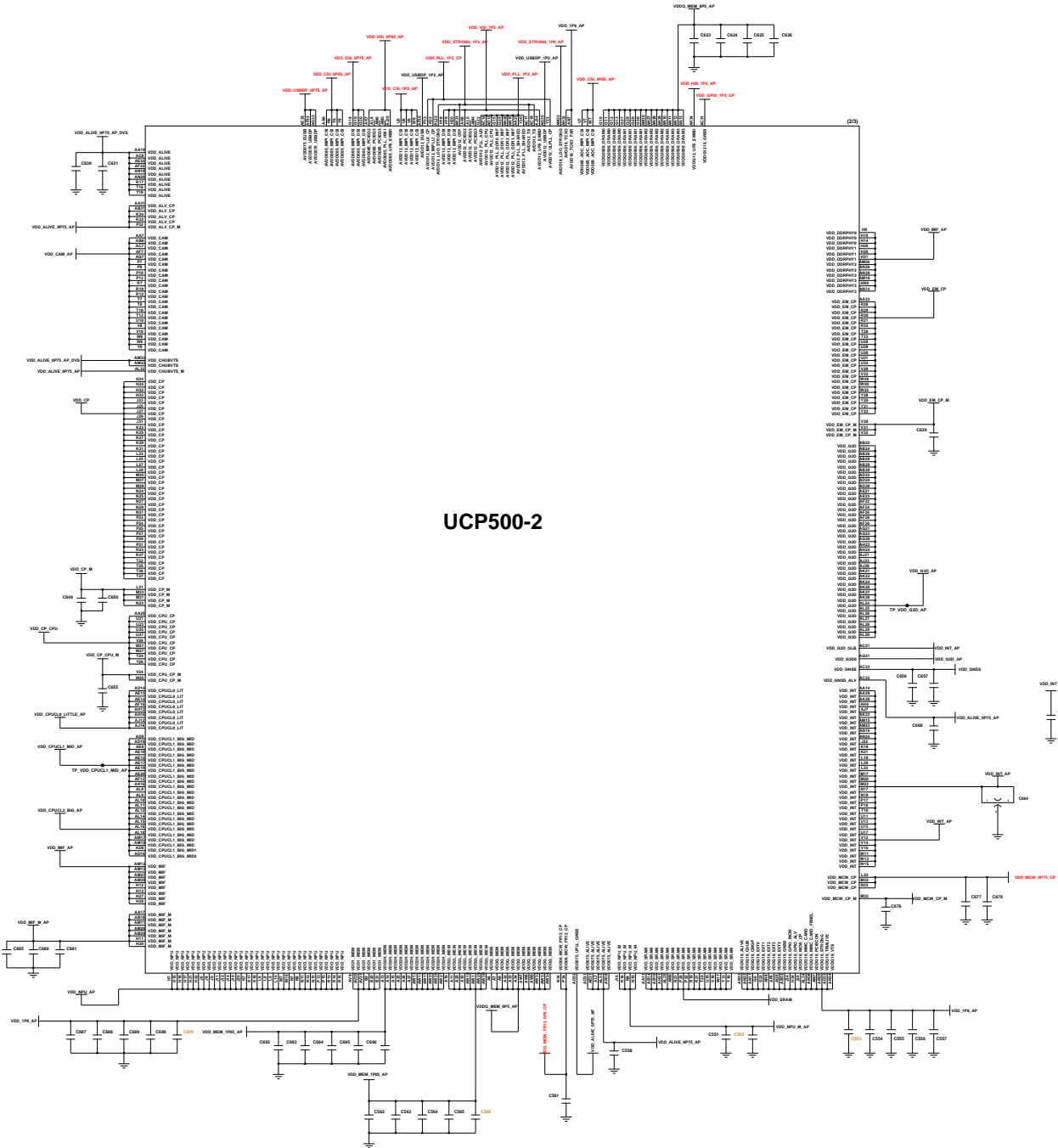
- 1. Place de-caps around assigned pins
- 2. De_caps could be removed(For powers that meet the Remote_cap specification)



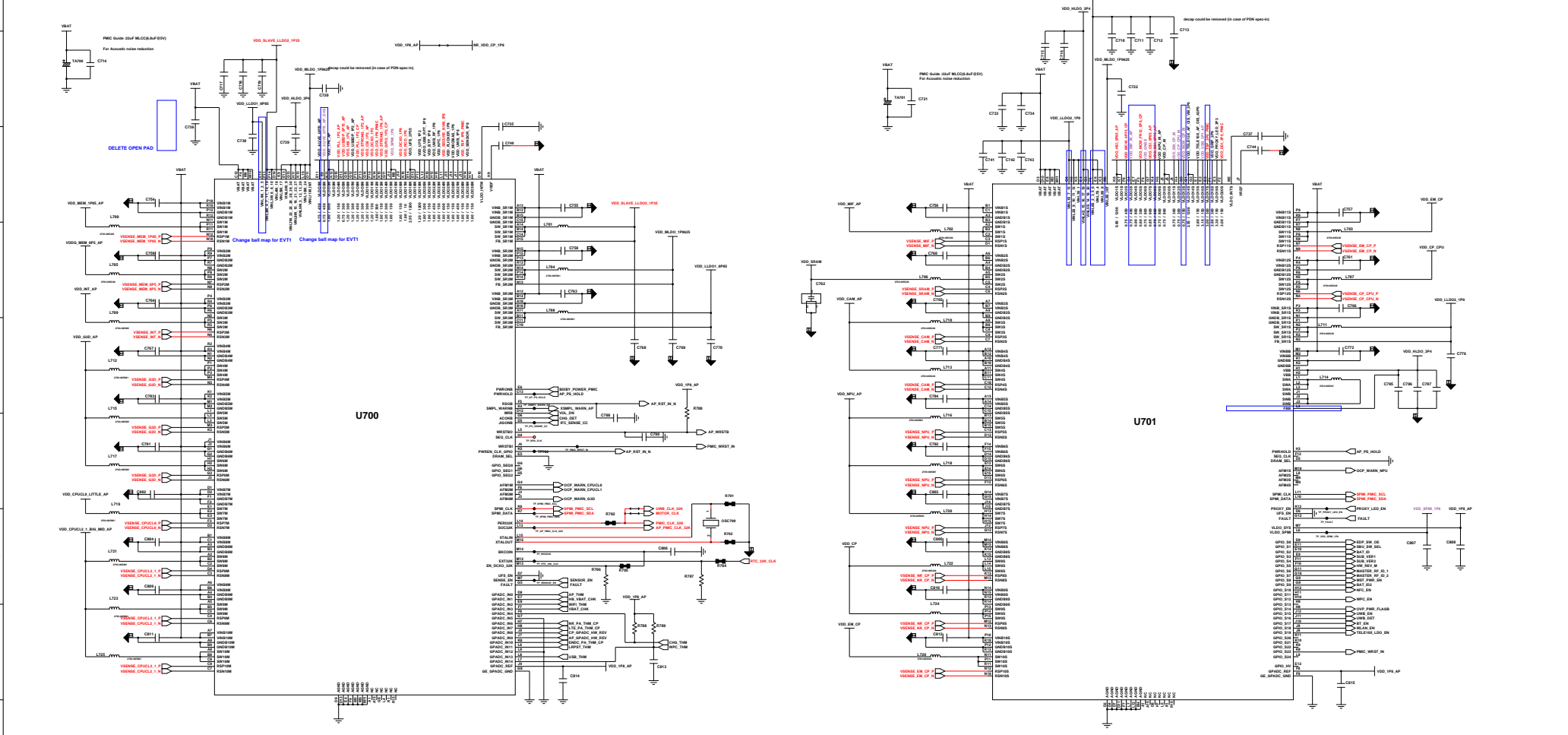
UCP500-3



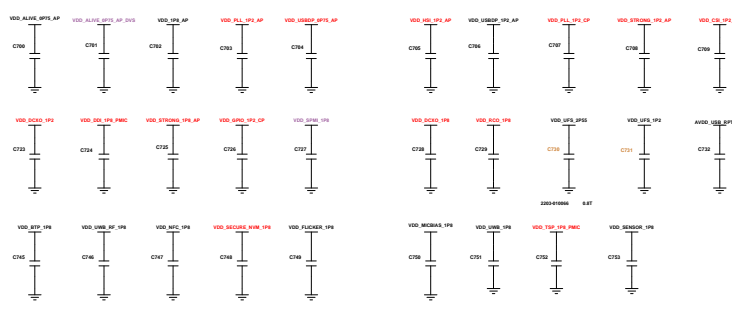
UCP500-2



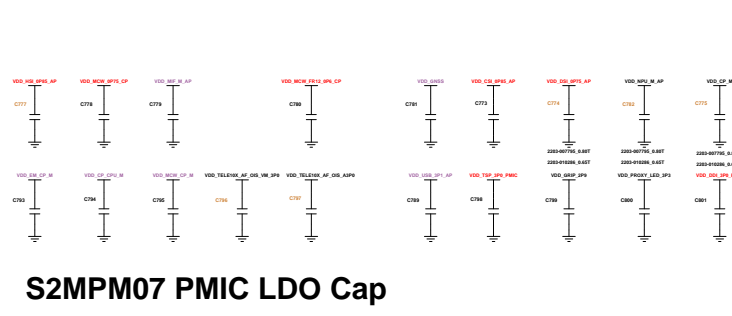
7. Power management IC (modAP/peri) & Charging



S2MPS25 PMIC LDO Cap



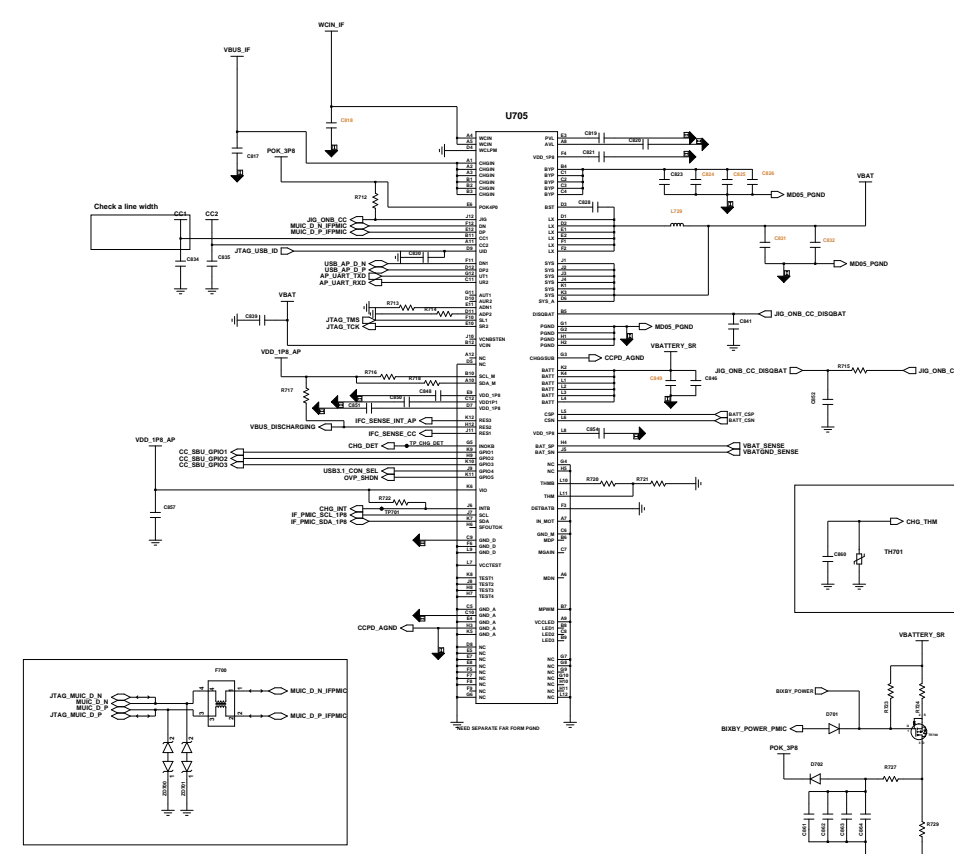
S2MPS26 PMIC LDO Cap



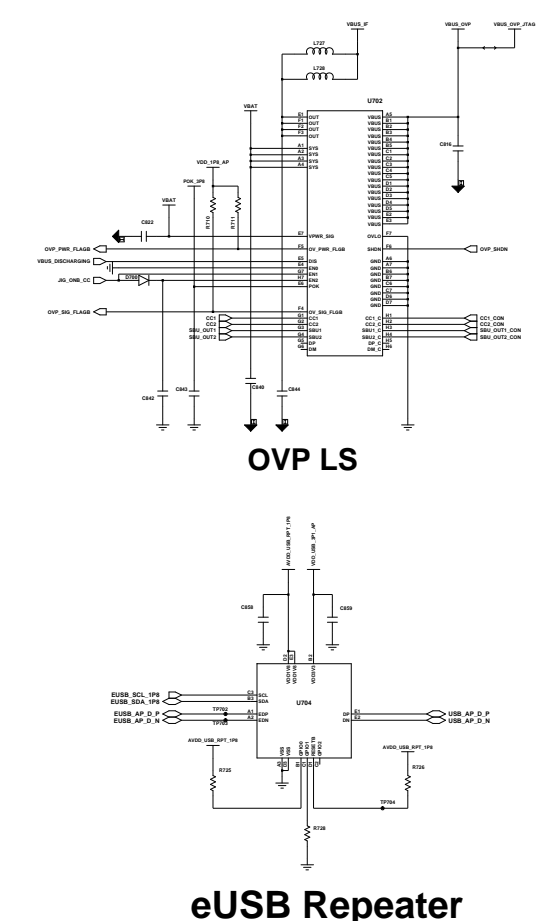
S2MPM07 PMIC LDO Cap



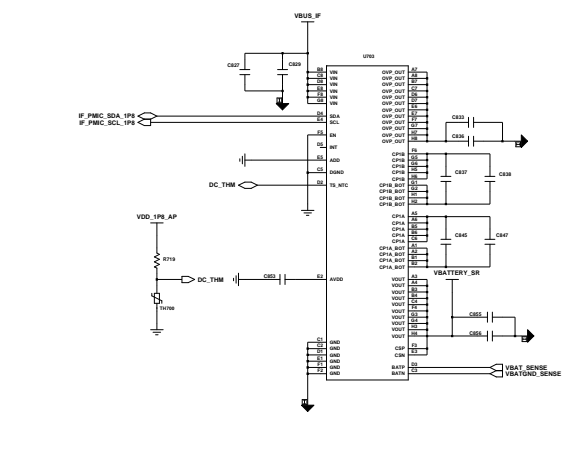
Interface PMIC



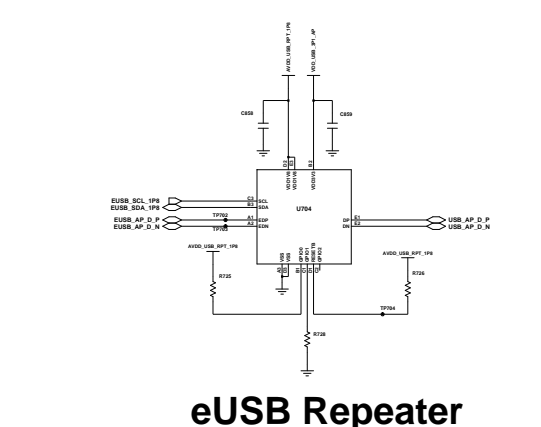
OVP LS



Direct charger



eUSB Repeater



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MASTER

AUDIO

SPK AMP

RCV AMP

Engineer: jmg@un.pac	SAMSUNG ELECTRONICS	
Drawn by: jmg@un.pac	TITLE:	Size:
R&D CHK:	U_LSI_Olympus_0206	12 x 8 A
DWG CTRL CHK:		
MFG ENGR CHK:		
Changed by: jmg@un.pac	Date Changed: Monday, February 10, 2020	Time Changed: 4:53:03 pm
QA CHK:	REV	Drawing Number:
		Page:

[illegible][illegible]

MASTER

AUDIO

SPK AMP

RCV AMP

Engineer: jmg@un.pac		SAMSUNG ELECTRONICS	
Drawn by: jmg@un.pac		Size:	
QA CHK:		TITLE: U_LSI_Olympus_0206	
DOC CTRL CHK:		12 x 8 A	
MFG ENGR CHK:			
Changed by: jmg@un.pac	Date Changed: Monday, February 10, 2020	Time Changed: 4:53:03 pm	QA CHK:
REV	Drawing Number:	Page: 8	

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jmg@un.pac

Drawn by:
jmg@un.pac

R&D CHK:

DWG CTRL CHK:

MFG ENGR CHK:

TITLE:
U_LSI_Olympus_0206

Size:
12 x 8 A

Changed by:
jmg@un.pac

Date Changed:
Monday, February 10, 2020

Time Changed:
4:53:03 pm

QA CHK:

REV

Drawing Number:

Page:
8

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AUDIO

SPK AMP

RCV AMP

SAMSUNG ELECTRONICS

Engineer:
jmg@un.pac
Drawn by:
jmg@un.pac
R&D CHK:
DOC CTRL CHK:
MFG ENGR CHK:

TITLE:
U_LSI_Olympus_0206

Size:
12 x 8 A

Changed by:
jmg@un.pac

Date Changed:
Monday, February 10, 2020

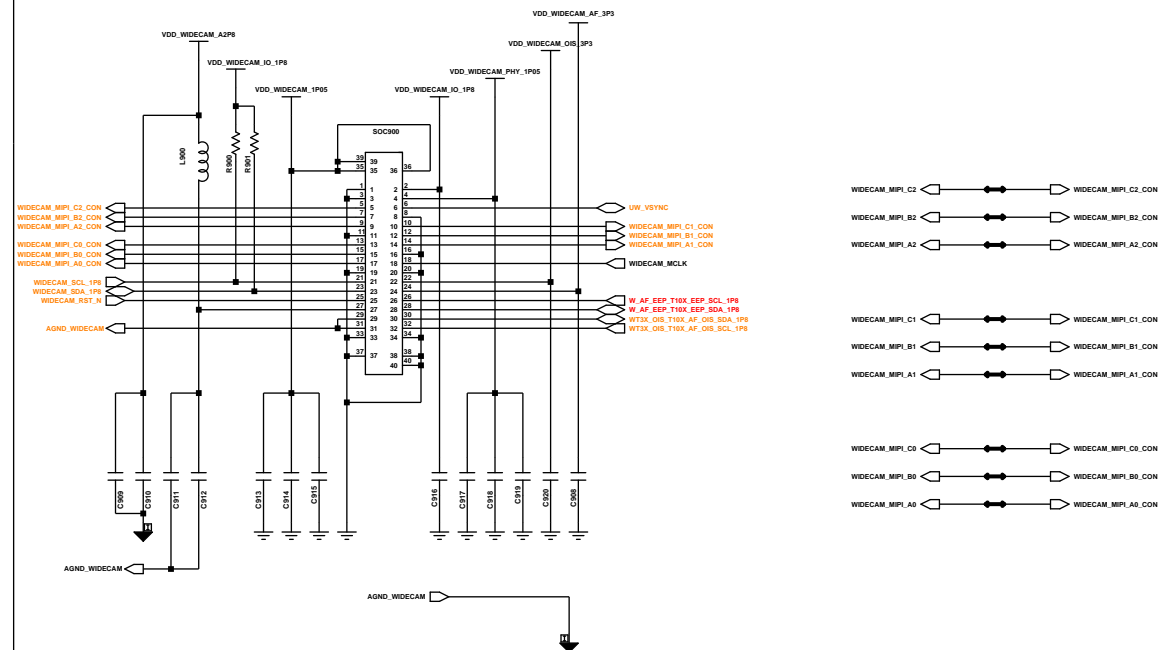
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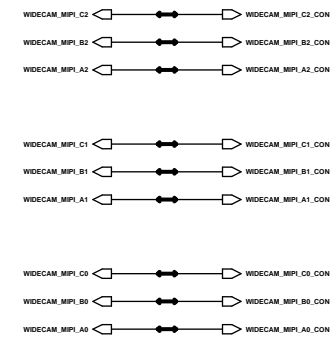
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Drawing Number:

Page:
8




Wide CAM



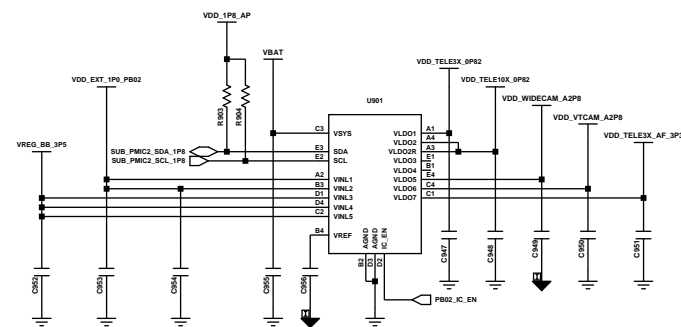
REV0.2

Dummy Ca

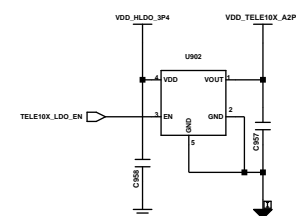
C946



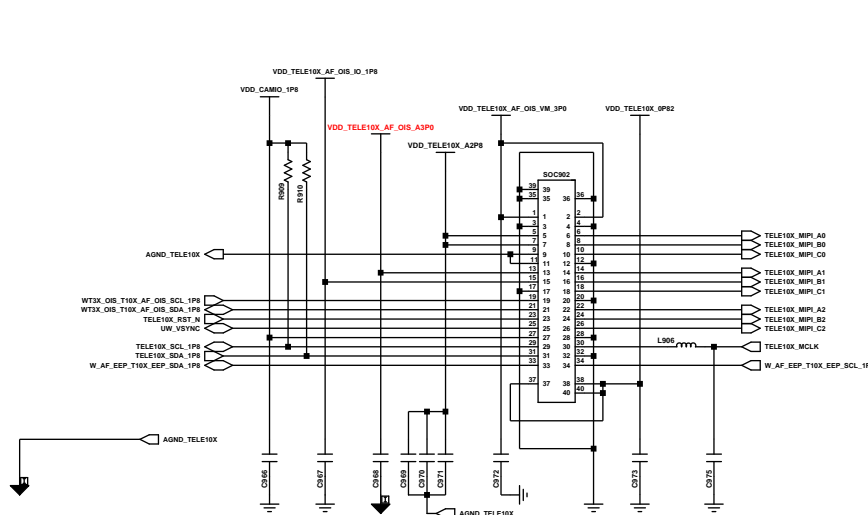
CAMERA POWER



PB03



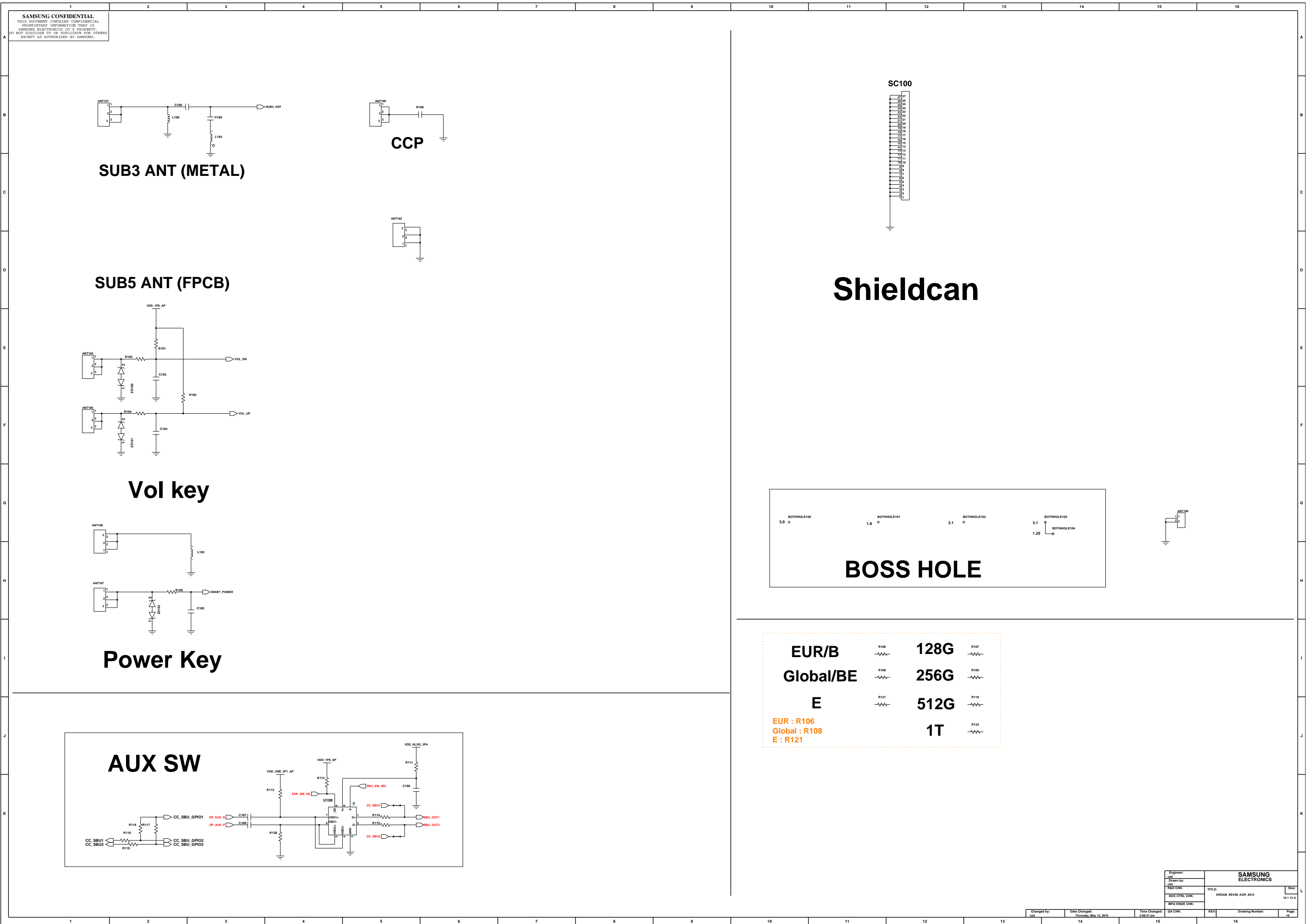
TELE x3 CAM

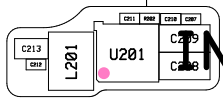


TELE x10 CAM

ENGINEER:	CAMERA, DISPLAY, MHL				
DRAWN BY:					
REV CHK:				TITLE:	SIZE:
DOC CTRL CHK:				DREAM2_KANGCHEN	
MFG ENGR CHK:					
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15		15	9		

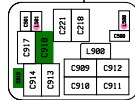
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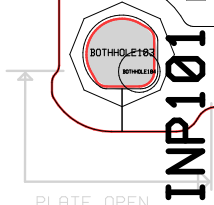


INP100

PLATE OPEN
GND OPEN

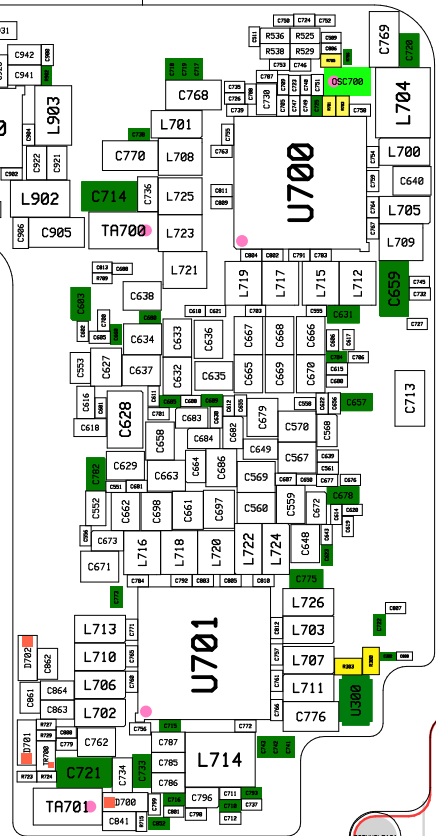


CON301



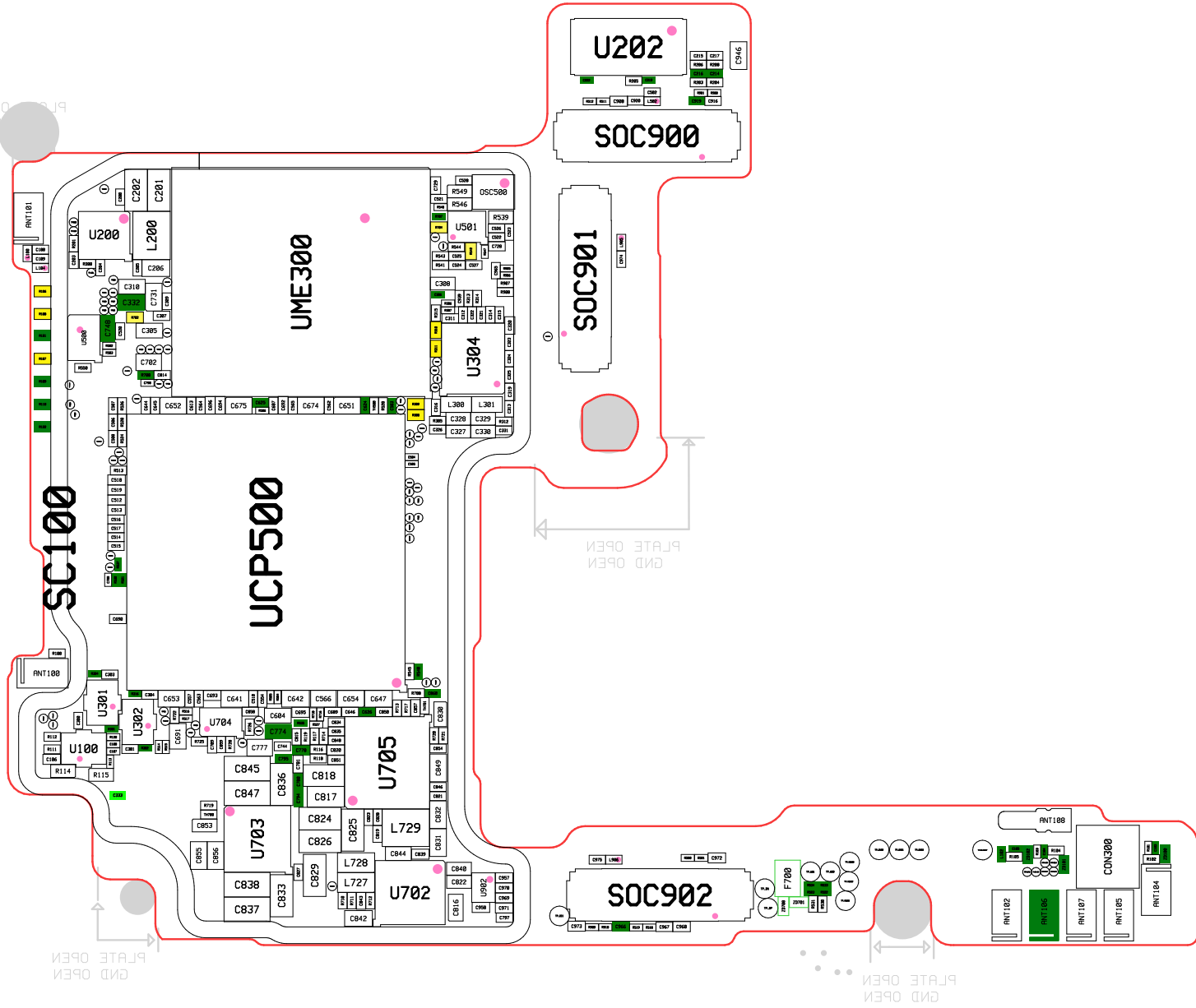
INP101

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GND OPEN

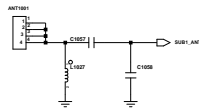


BOTH HOLE 102

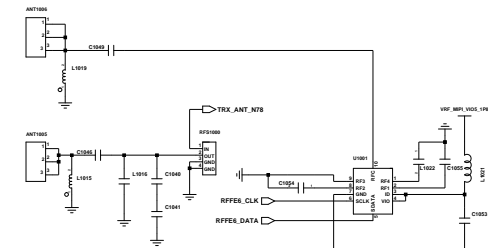
PLATE OPEN
GND OPEN



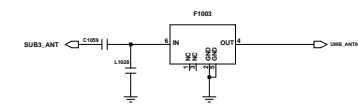
RF2 - SUB ANT



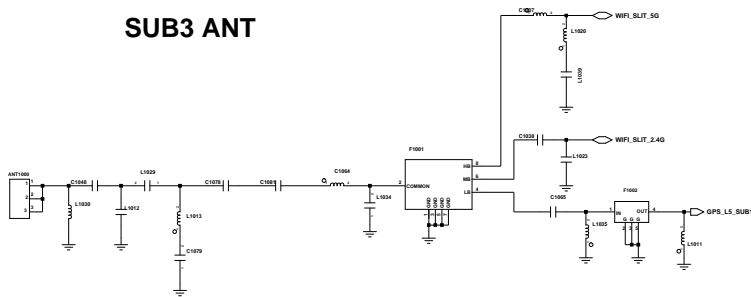
SUB1 ANT (Metal lb/mb/hb)



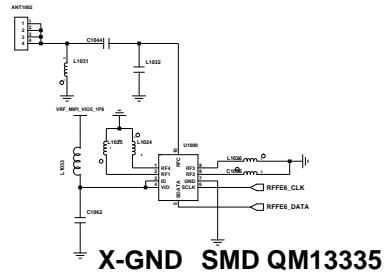
SUB2 ANT (Metal UHB) SDA Switch



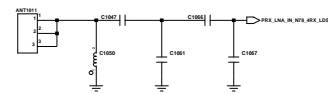
SUB3 ANT



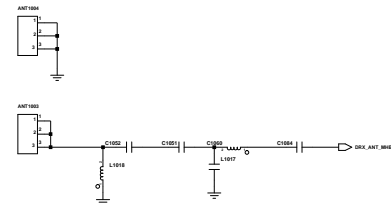
SUB4 ANT (Metal L5/Wifi)



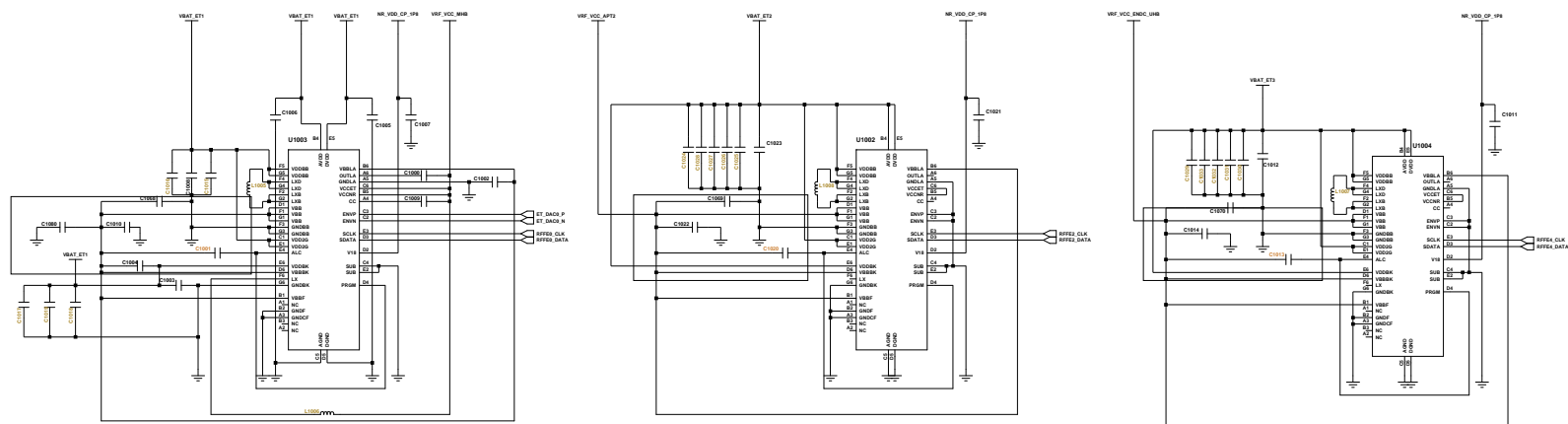
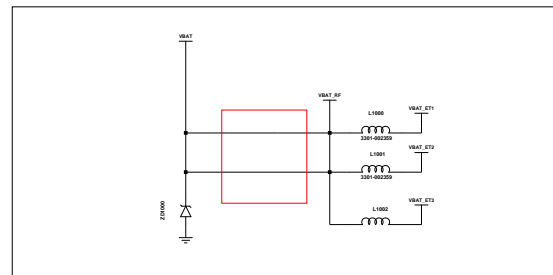
X-GND SMD QM13335



SUB5 ANT (LDS uhb)



SUB6 ANT (Metal MB/HB)

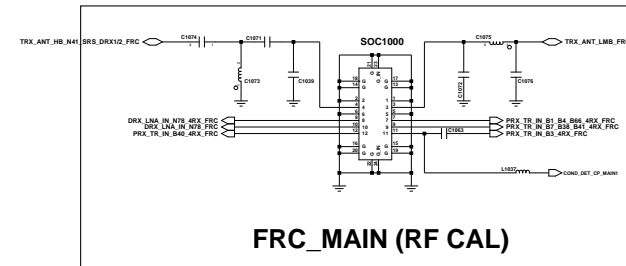


ET #1 2G/3G/LTE/NR MHB(OMH LPAMID)

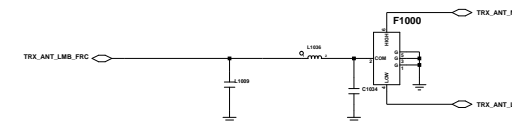
ET#2 3G/LTE/NR LB(LB LPAMID)

ET#3 NR MHB/UHB(ENDC MOD,UHB MOD)

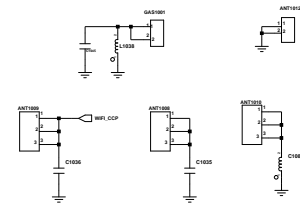
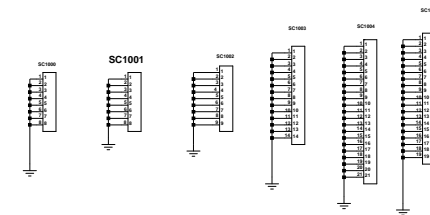
RF1 - MAIN_ANT



FRC_MAIN (RF CAL)



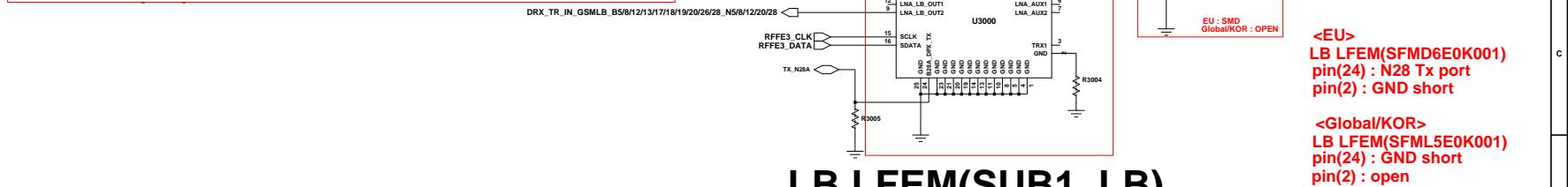
MAIN1-1 ANT(LB/MB)
(GSM LB, FDD B1/2/3/4/5/8/12/13/17/18/19/20/25/26/28/32/66,TDD B34/39)



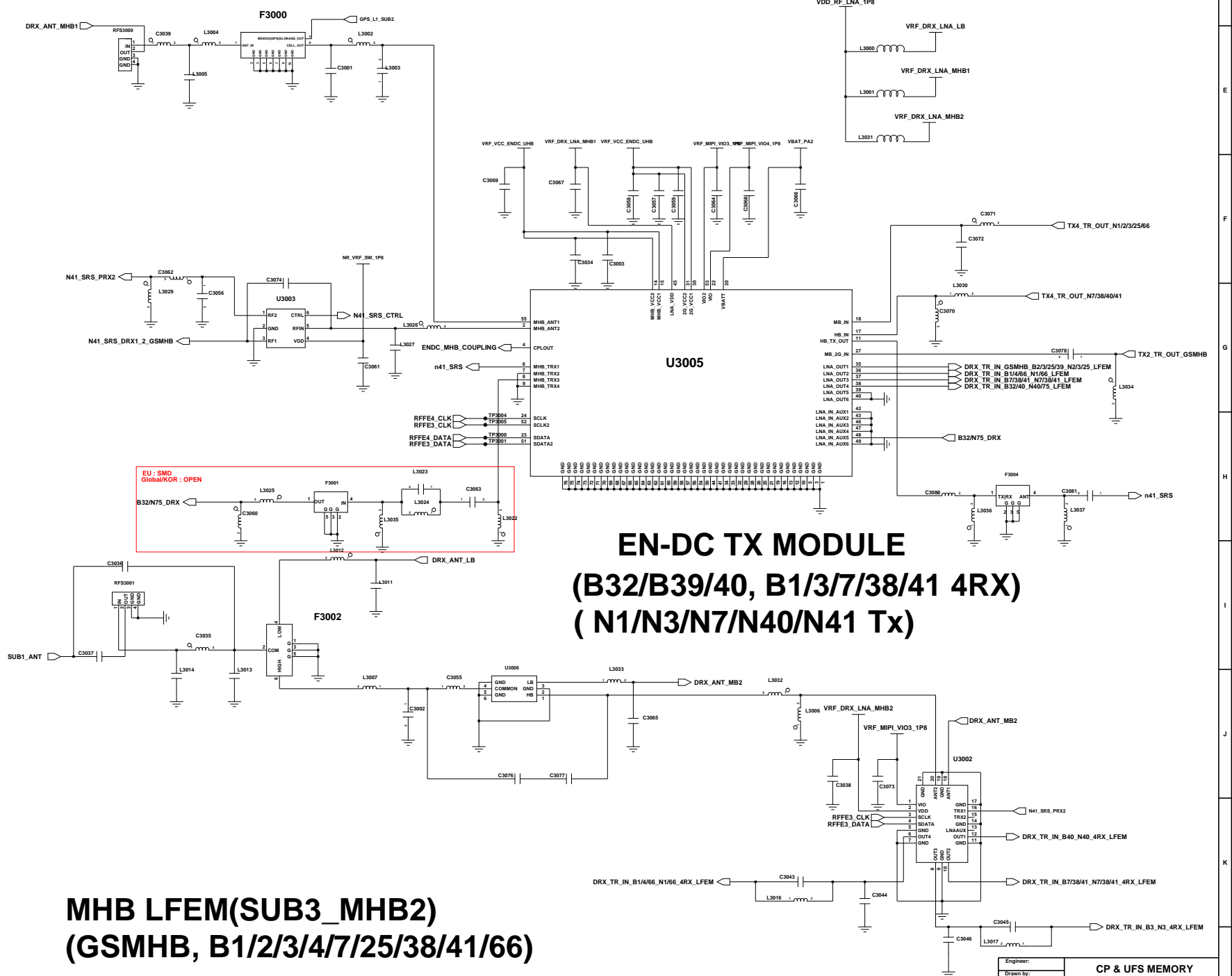
CCP



SUS



LB LFEM(SUB1_LB)
(GSMLB,B5/8/12/13/17/18/19/20/26/28,N8/28)

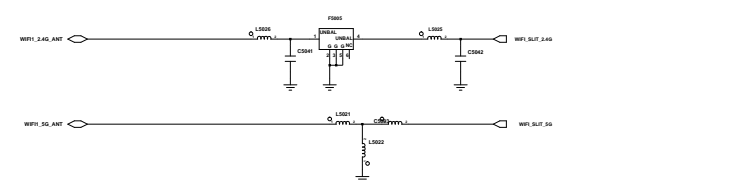
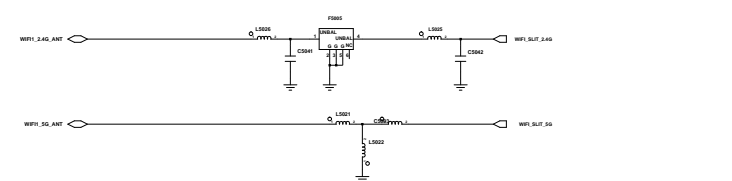
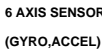


MHB LFEM(SUB3_MHB2)
(GSMHB, B1/2/3/4/7/25/38/41/66)

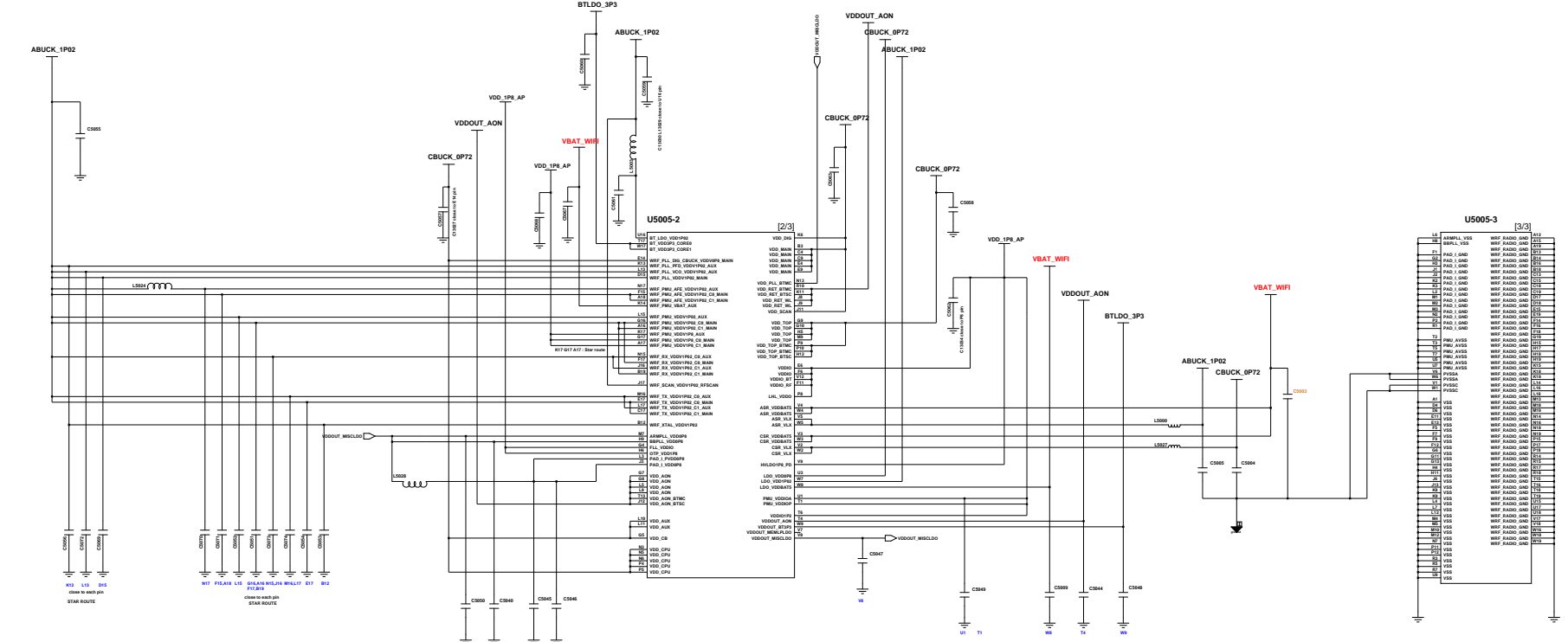
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Drawn by:					
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QA CHK:	REV:	Drawing Number:	Page:		



11

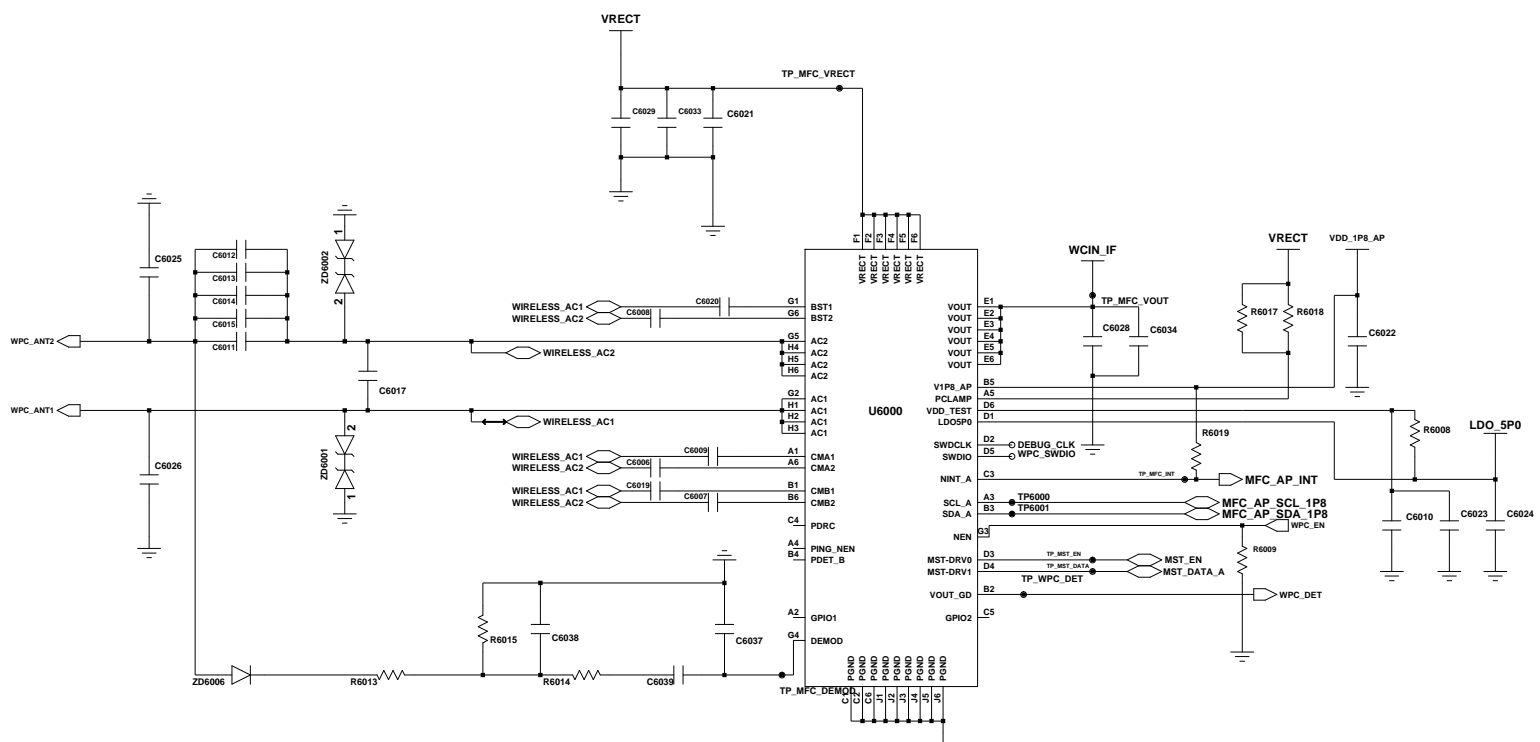


UWB(For U2)



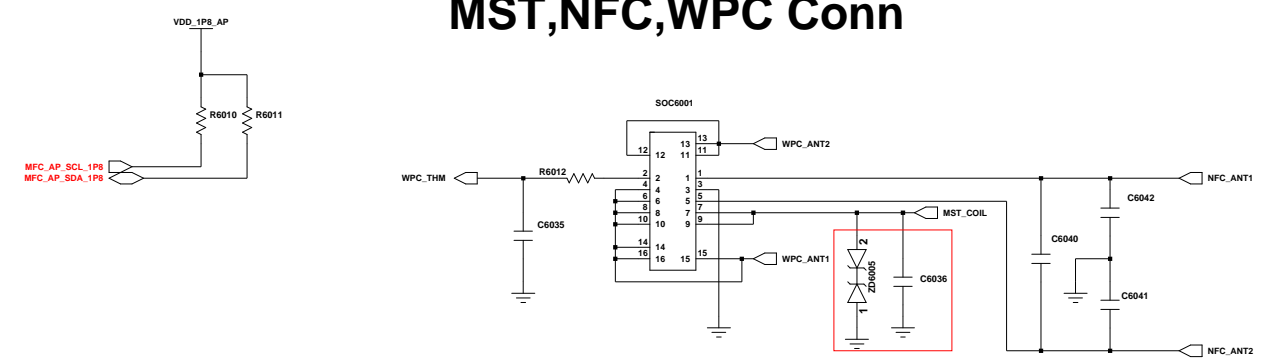
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CHARGER

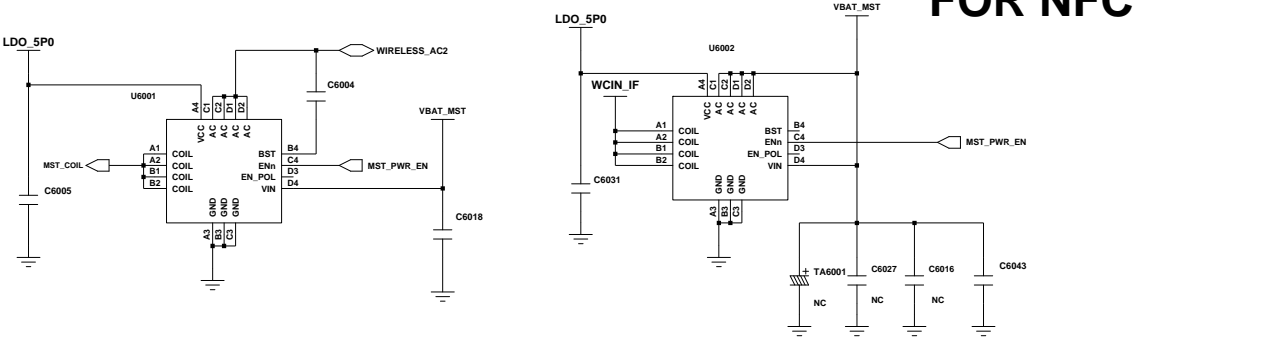


WIRELESS CHARGER IC

MST,NFC,WPC Conn



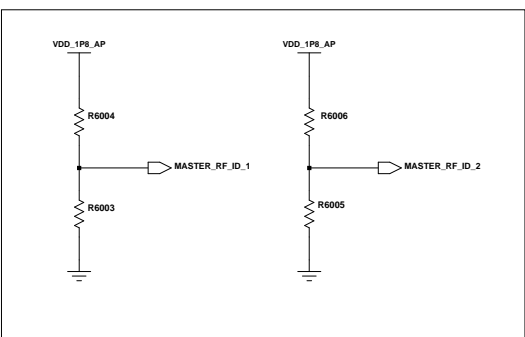
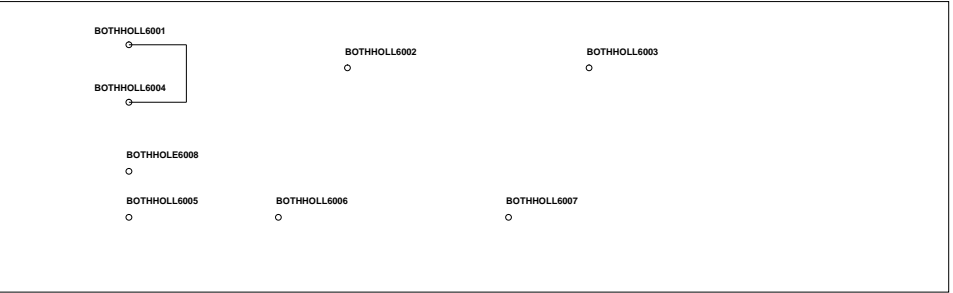
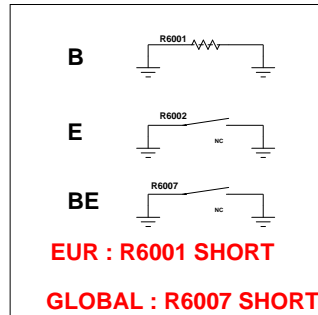
FOR NFC



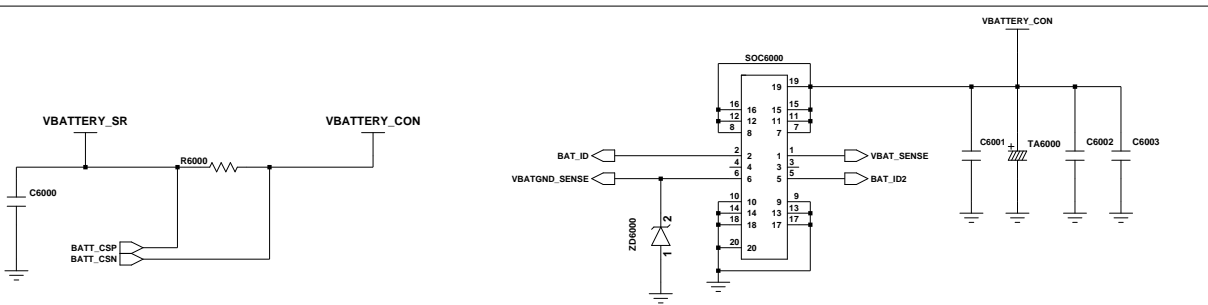
KOR : SMD
EUR : NC

MST SWITCH

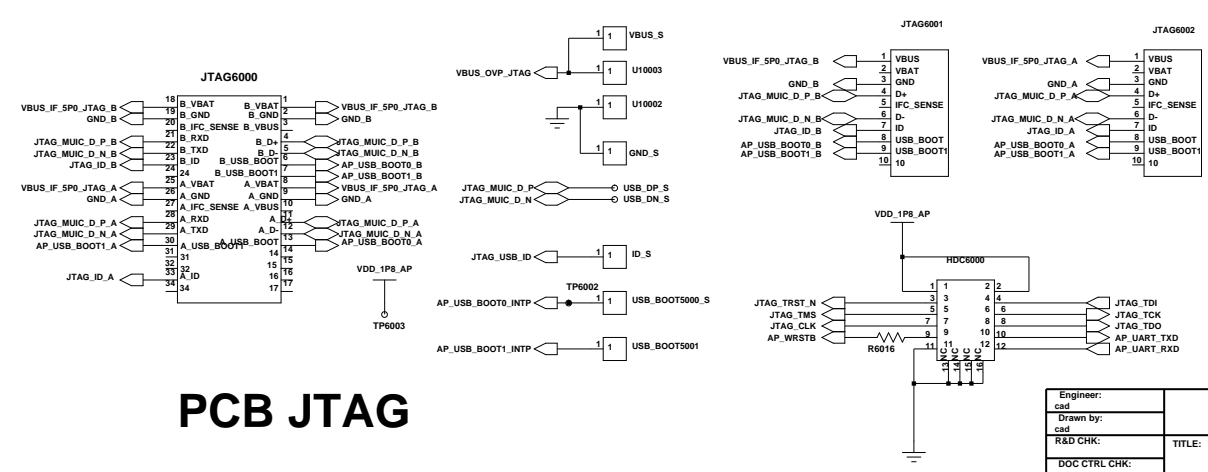
Boss Hole



		MASTER_RF_ID_1		MASTER_RF_ID_2	
		R6004	R6003	R6006	R6005
0	EUR	0	open 100K	0	open 100K
1	GLOBAL	0	open 100K	1	100K open

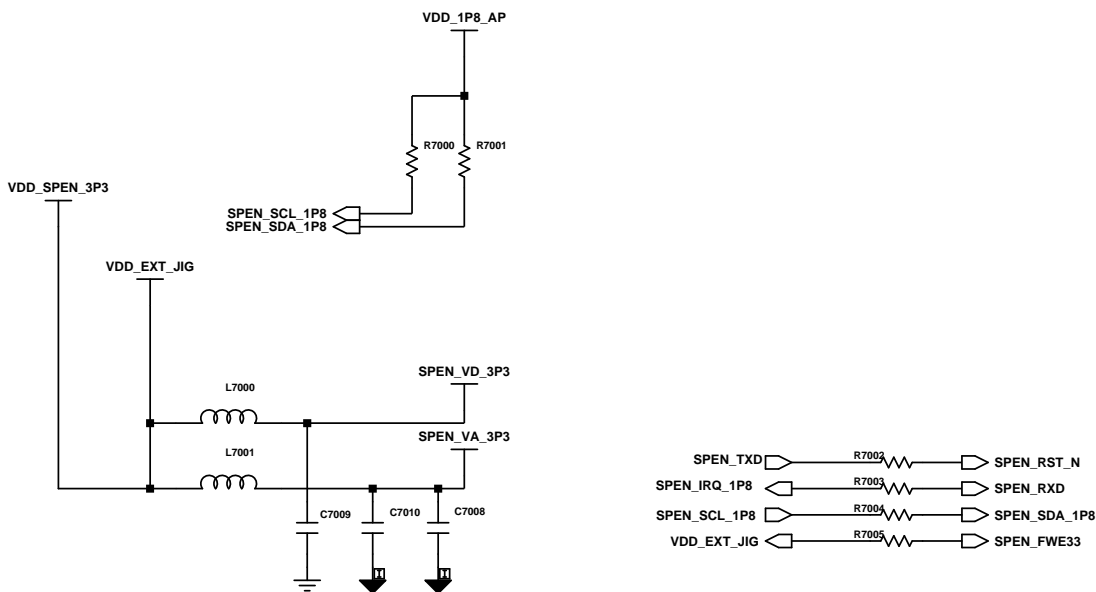
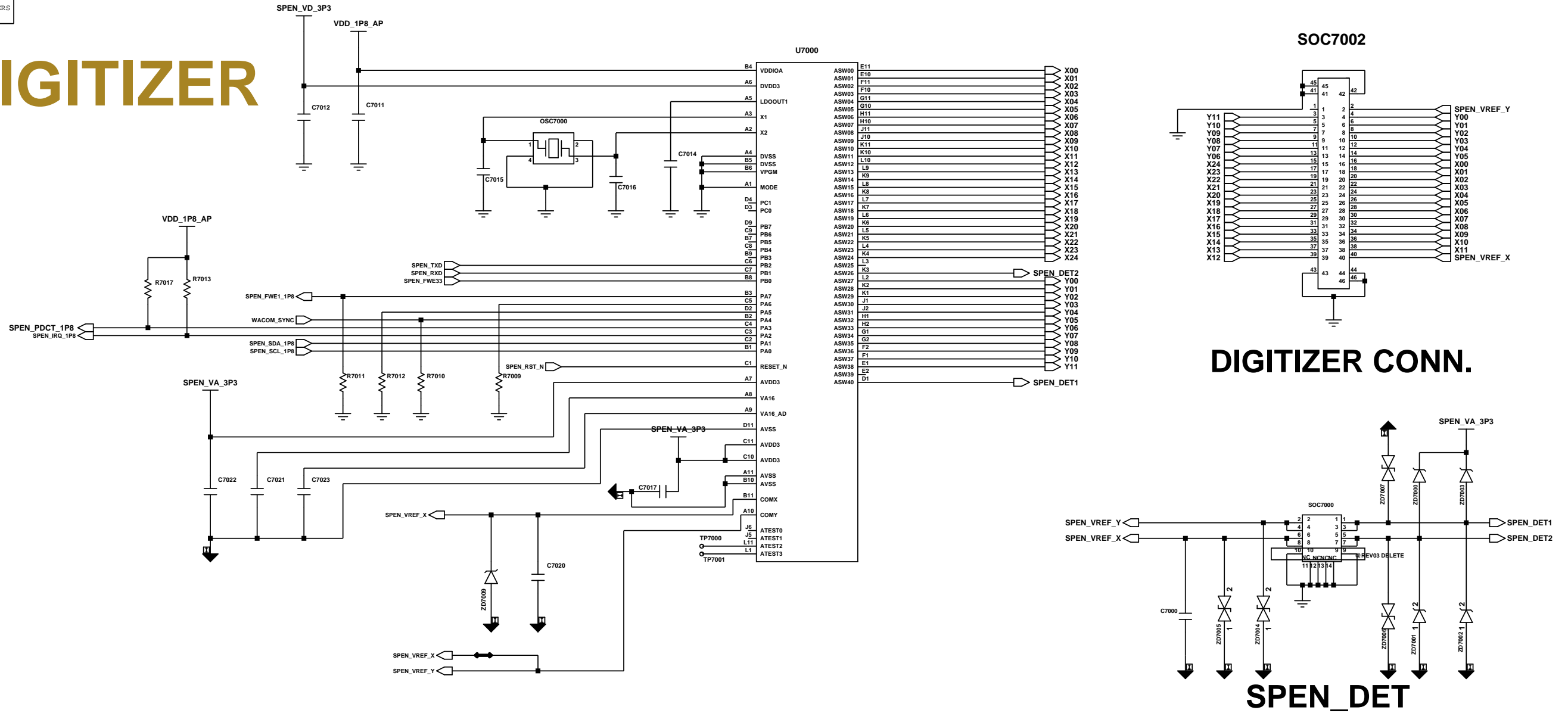


BATTERY CONNECTOR

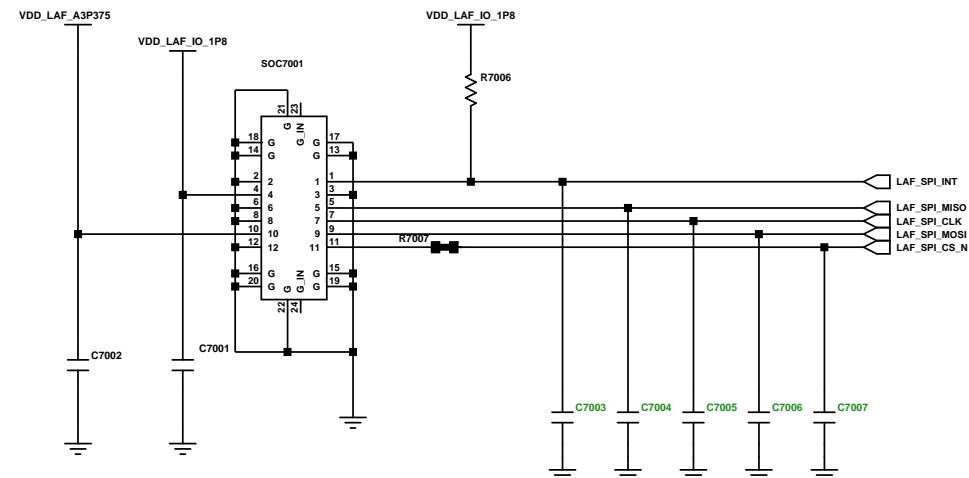


PCB JTAG

DIGITIZER



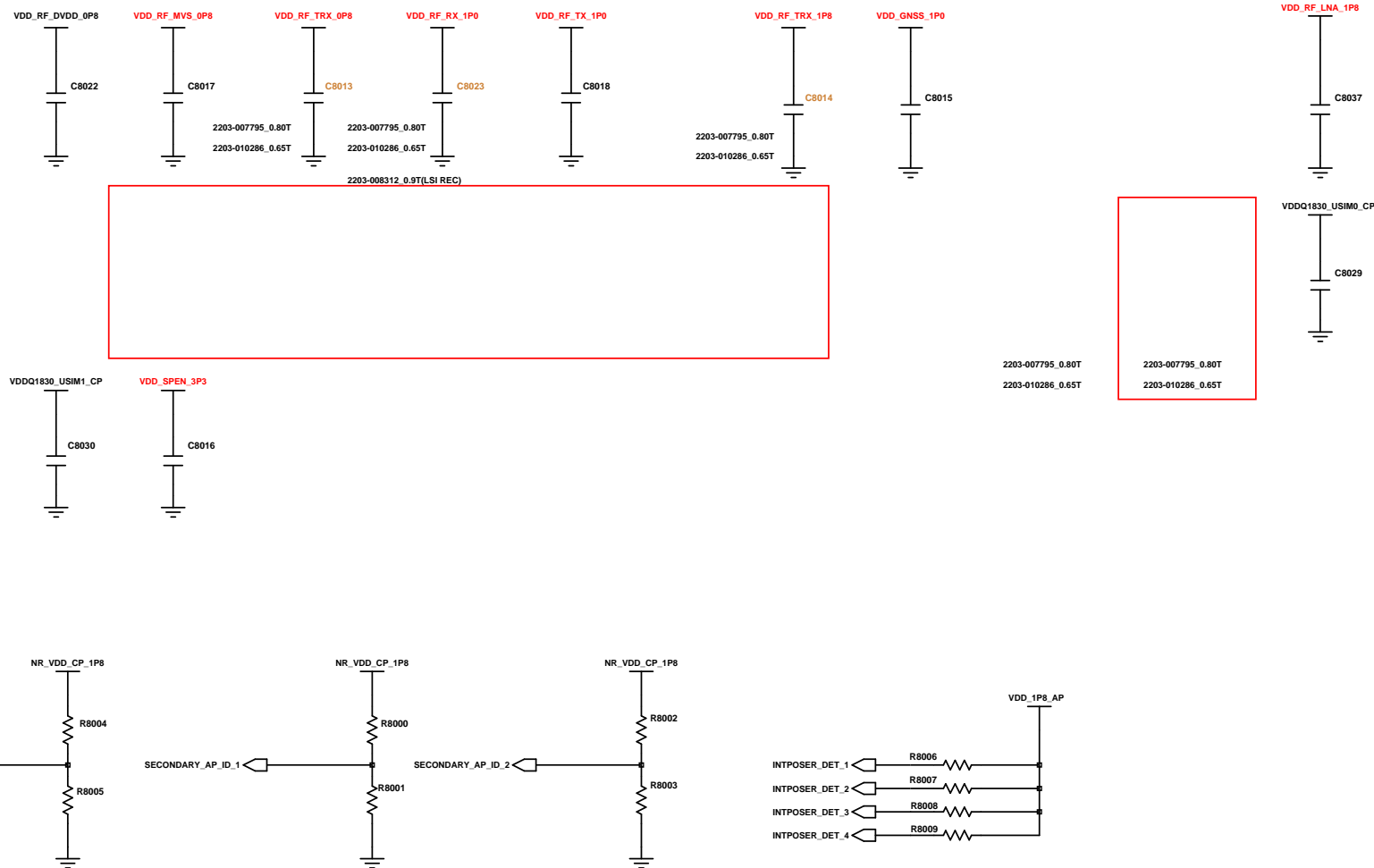
DIGITIZER TP



RANGING sensor

Engineer:		SAMSUNG ELECTRONICS	Size:
Drawn by:			
R&D CHK:			
DOC CTRL CHK:			
MFG ENGR CHK:			
TITLE:	Davinch_mmWave_0801		
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Changed by:	Date Changed:	Time Changed:	Page:
cad	Thursday, August 2, 2018	3:20:01 pm	14
Drawing Number:			

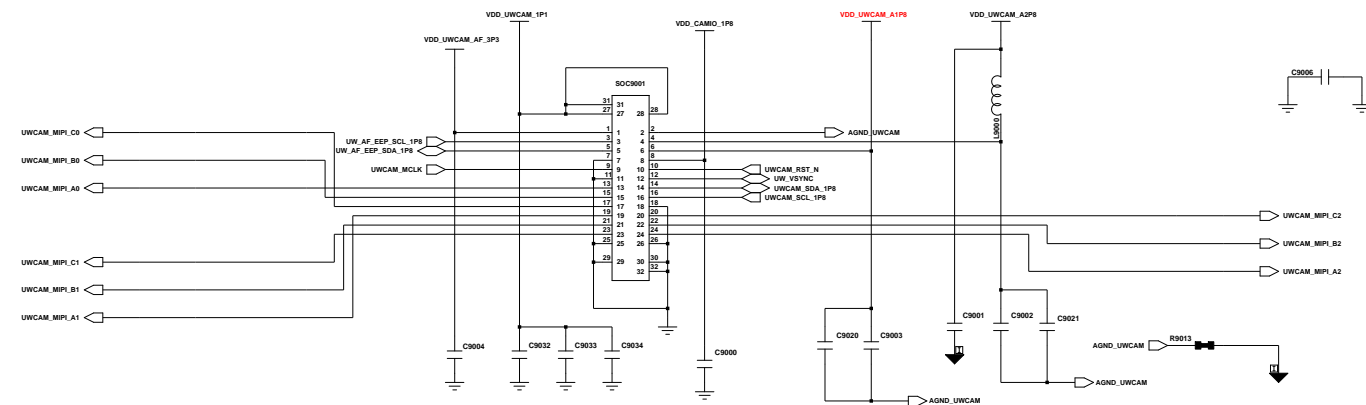
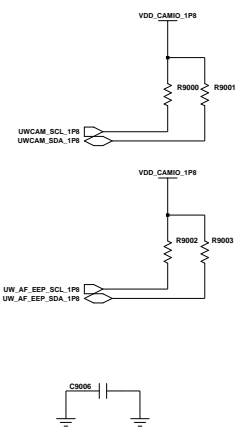
S2MPM07 PMIC LDO Cap



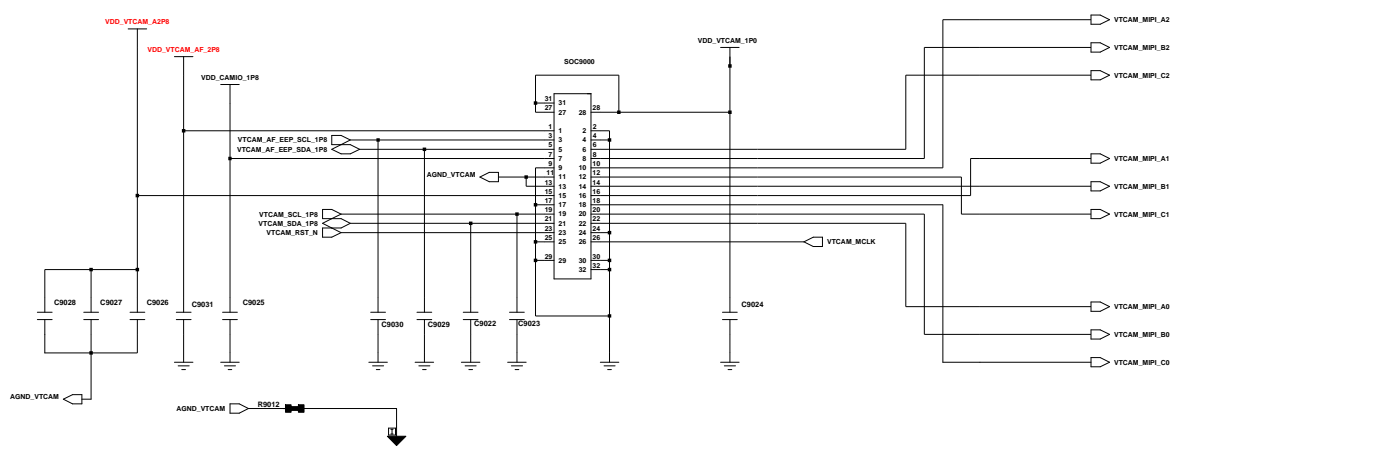
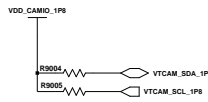
Changed by: junghun.pac	Date Changed: Monday, February 10, 2020	Time Changed: 4:53:03 pm	QA CHK:	REV	Drawing Number:	Page: 8
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9. CAMERA

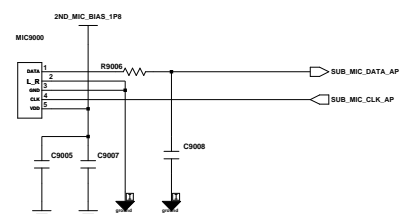
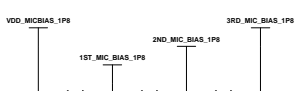
CAM PMIC(PB02)



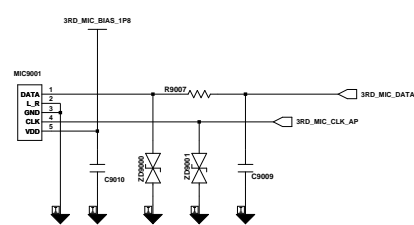
Ultra WIDE CAM CONN



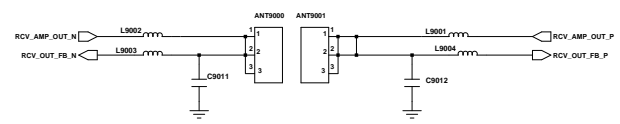
FRONT CAM



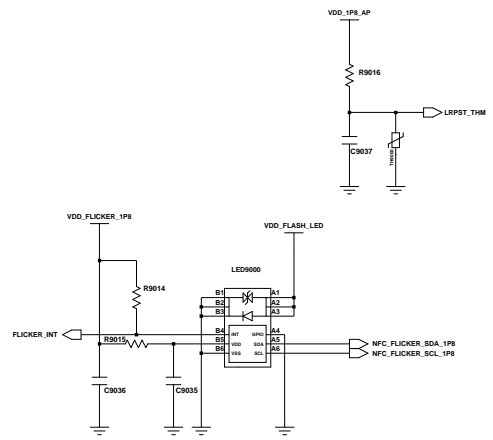
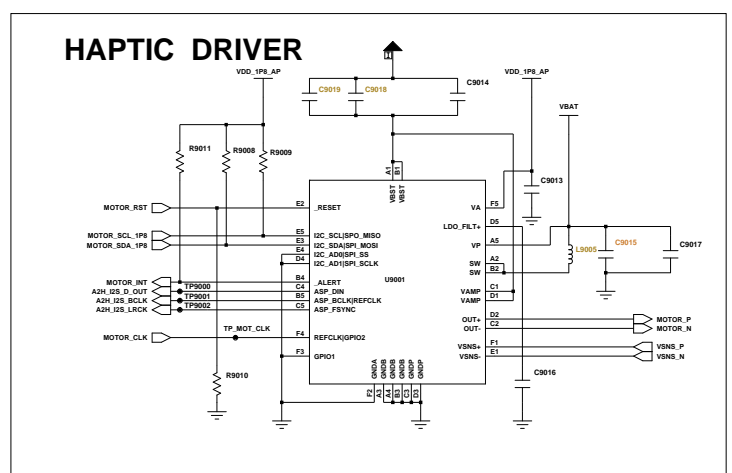
2nd MIC



3rd MIC



RCV Conn.



Flash LED

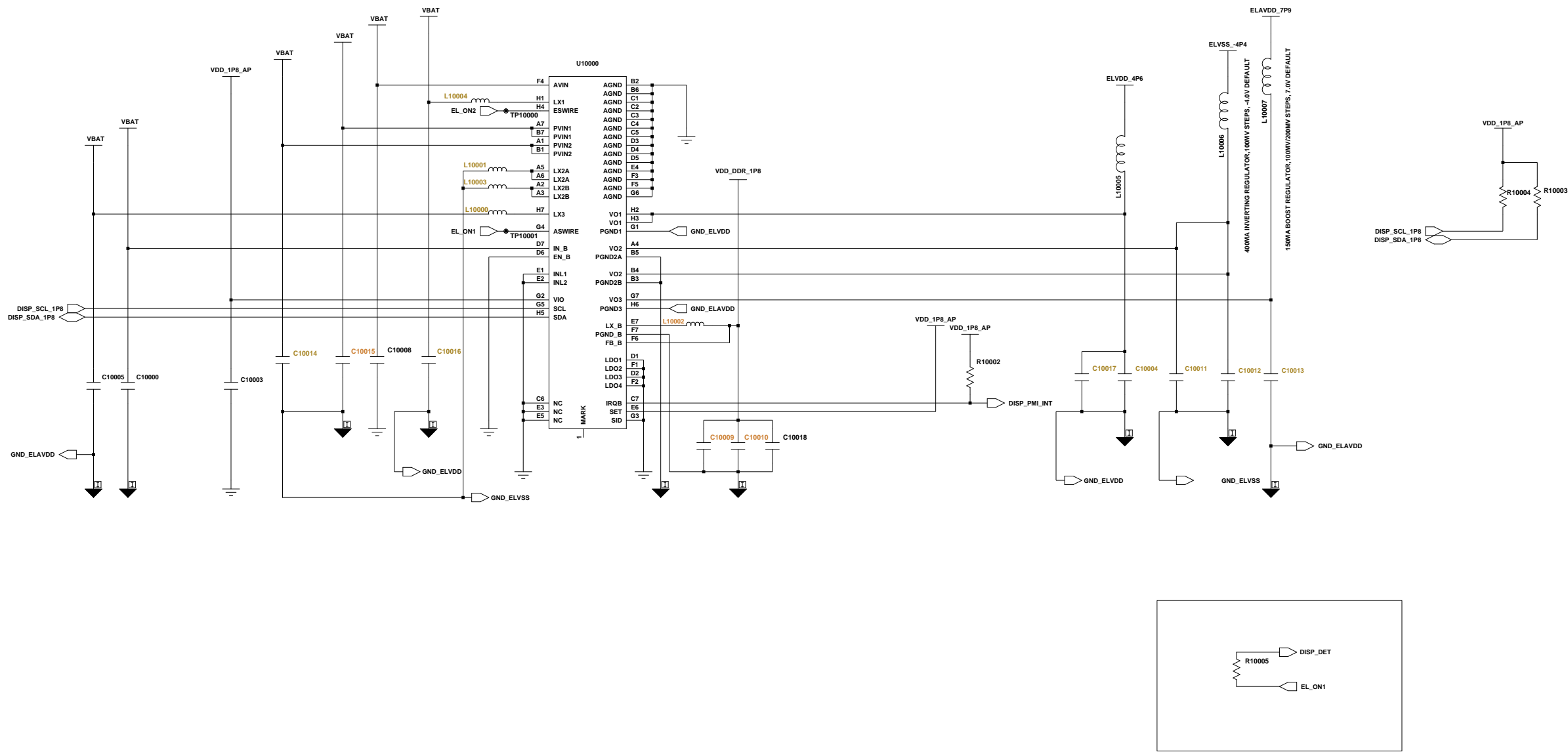
ENGINEER:	CAMERA, DISPLAY, MHL		
DRAWN BY:			
R&D CHK:			
DOC CTRL CHK:			
MFG ENGR CHK:	TITLE:	SIZE:	
	DREAM2_KANGCHEN		
QA CHK:	REV:	DRAWING NUMBER:	PAGE:
			9

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10.DISPLAY

DISPLAY PMIC

Power line is quite noisy, so lines must be shielded by GND plane
ELVDD,AVDD : Output > Input > Inductor
ELVSS : Output = Input > Inductor



MASTER INTERPOSER



