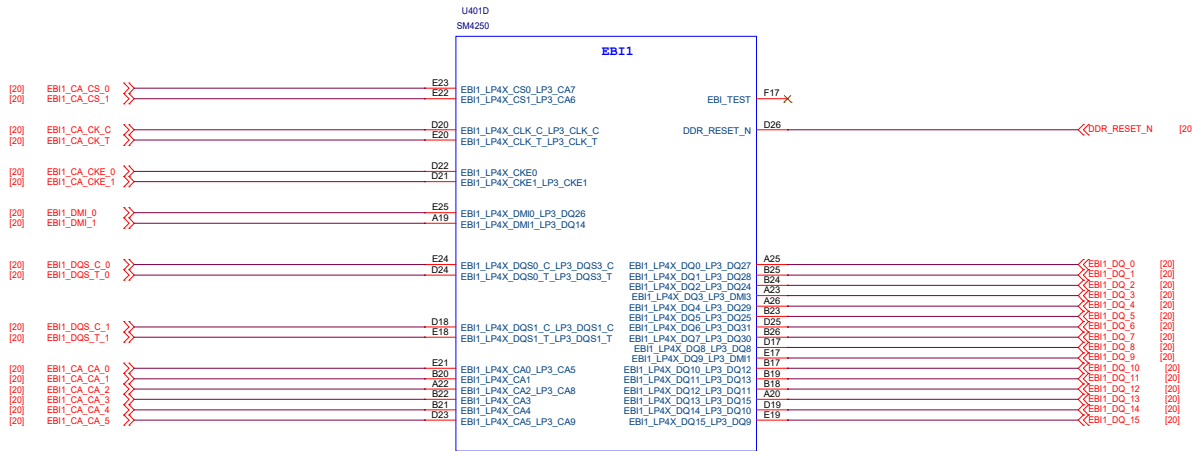
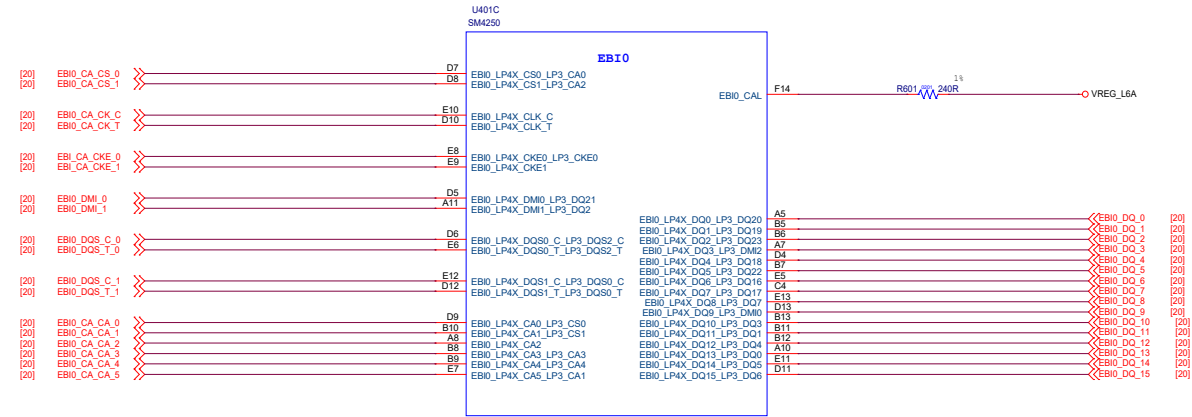
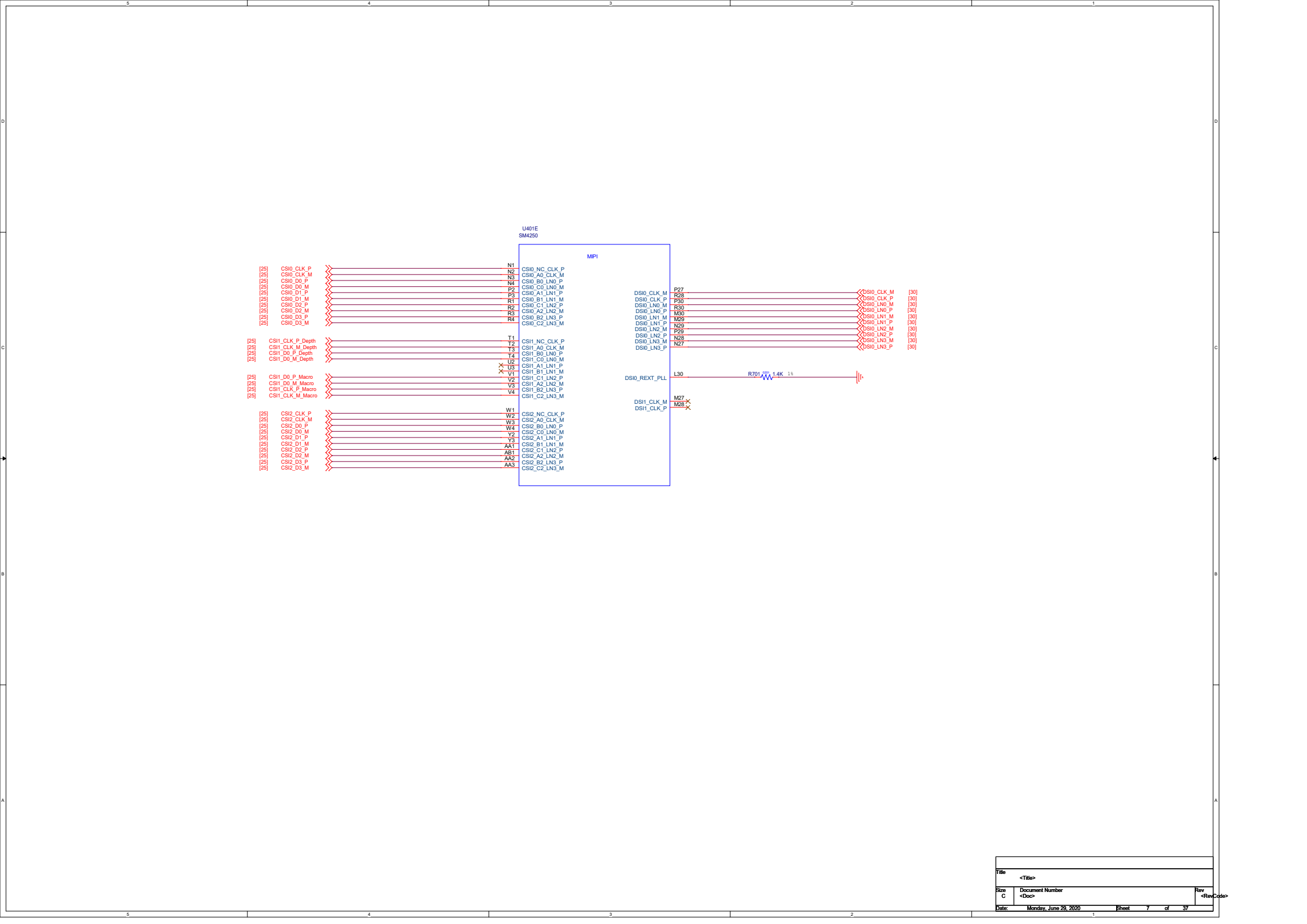


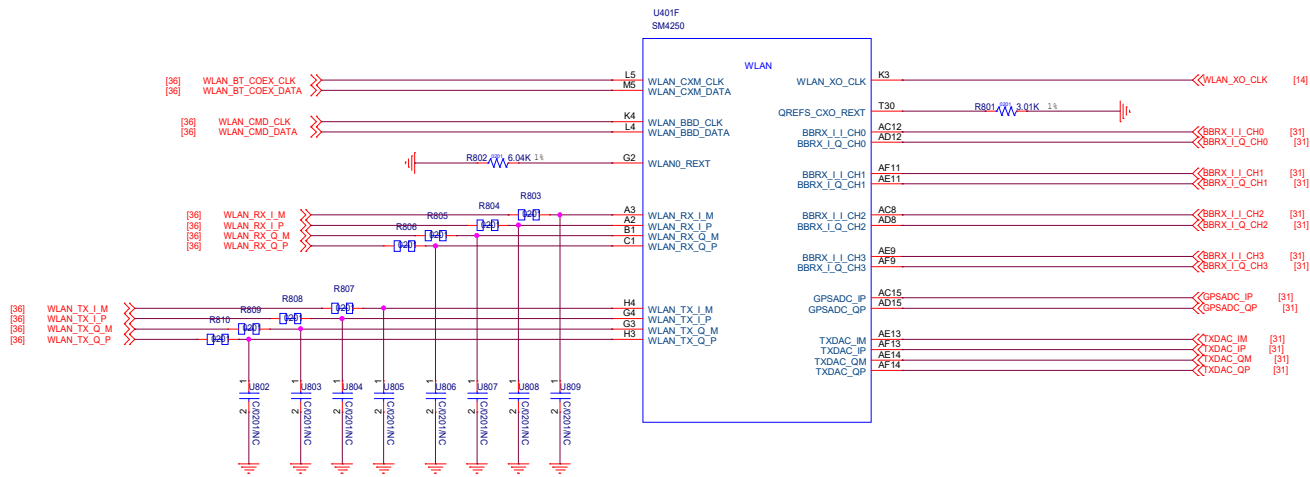
Sheet #	Content	Sheet #	Content	
01.	Table of Content	33.	WTR3925	
02.	Revision History	34.	TXM_MMMPA	
03.	Block Diagram	35.	PRX_DUPLEXER	
04.	SM4250_CONTROL	36.	DRX_SAW	
05.	SM4250_GPIO	37.	QET4101	
06.	SM4250_EBI01	38.	WCN3950	
07.	SM4250_MIPi-CSi&DSi	39.	WIFI_GPS	
08.	SM4250_WCSS&RF			
09.	SM4250_PWR_1			
10.	SM4250_PWR_2			
11.	SM4250_PWR_3			
12.	SM4250_DECAPs			
13.	SM4250_GND			
14.	PM4250_HK_IO			
15.	PM4250_BUCKs_S1-S8			
16.	PM4250_LDOS			
17.	PMI632_Control			
18.	PMI632_Charger			
19.	PMI632_LCD&ViB&LED&Flash			
20.	eMMC&LPDDR4			
21.	eMMC&LPDDR4_Power			
22.	Battery&USB_IF			
23.	Audio_codec			
24.	EAR&Smart_PA			
25.	CAMERA_IF			
26.	SENSOR			
27.	SIM_TF			
28.	KEY&TEST_POINT			
29.	SUB_CONN			
30.	Display_IF			

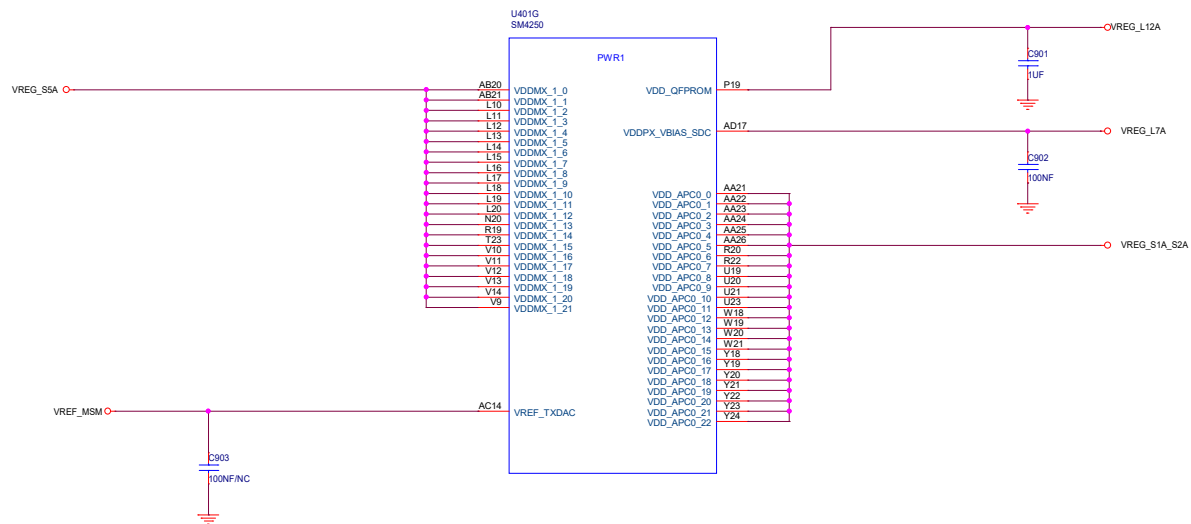
Revision History

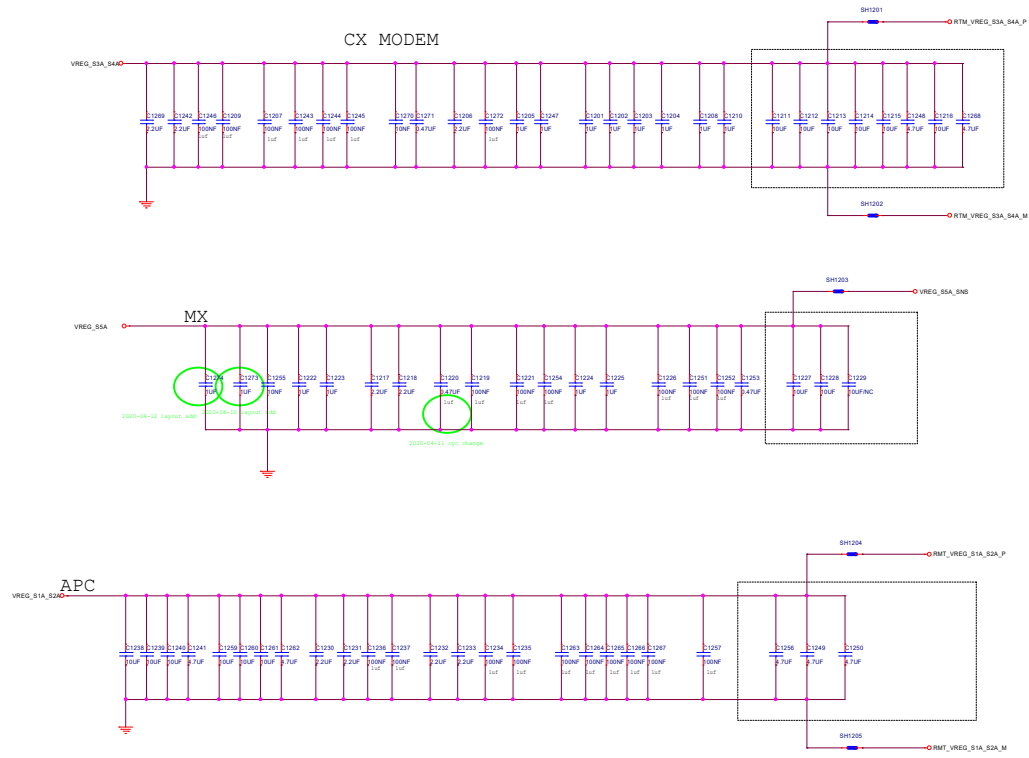
REV	DESCRIPTION
A	

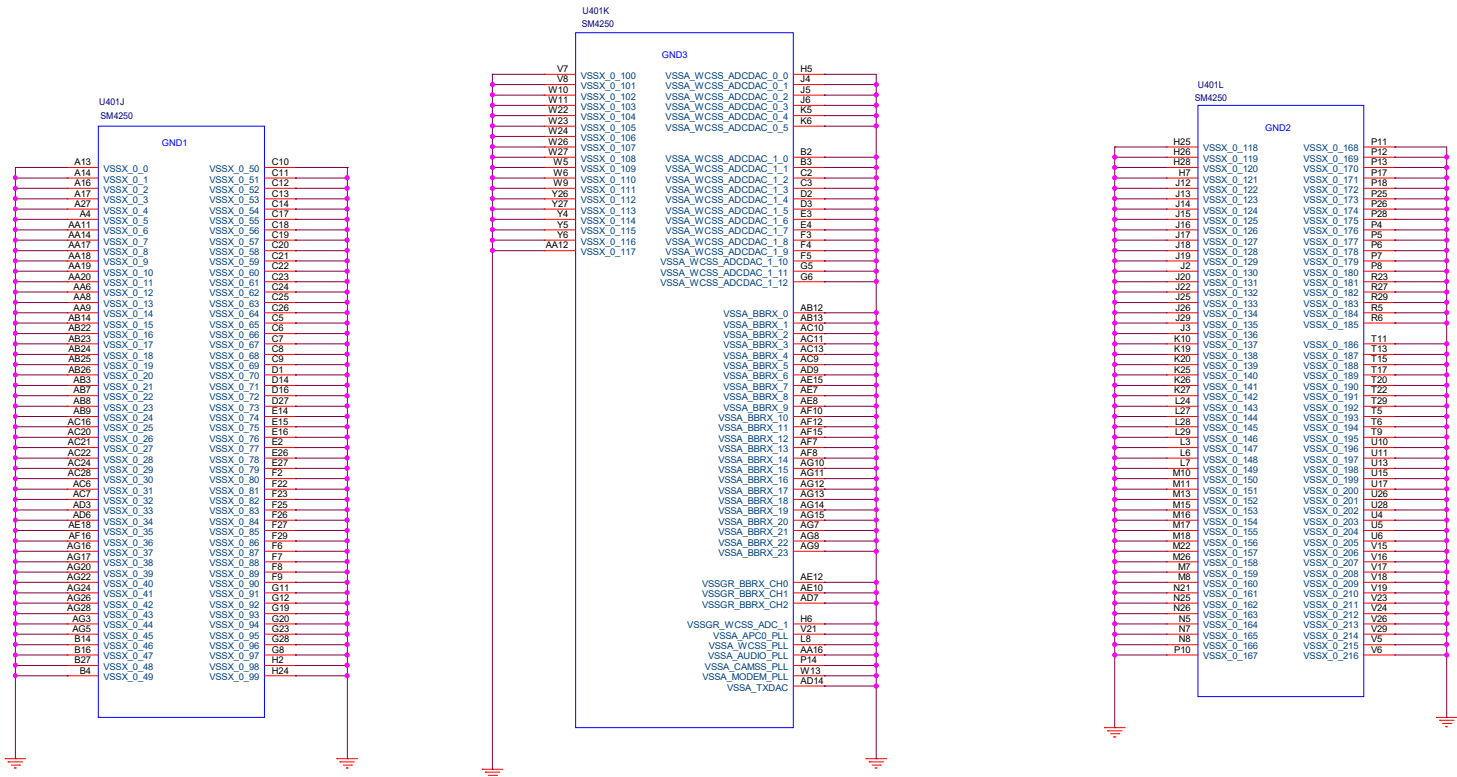


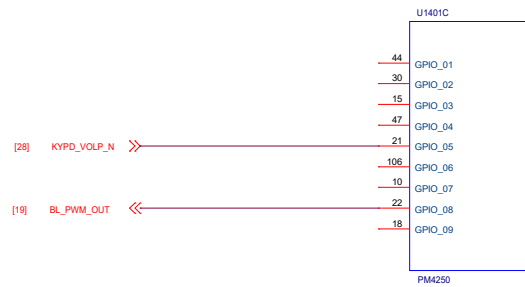
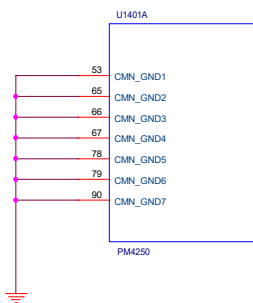
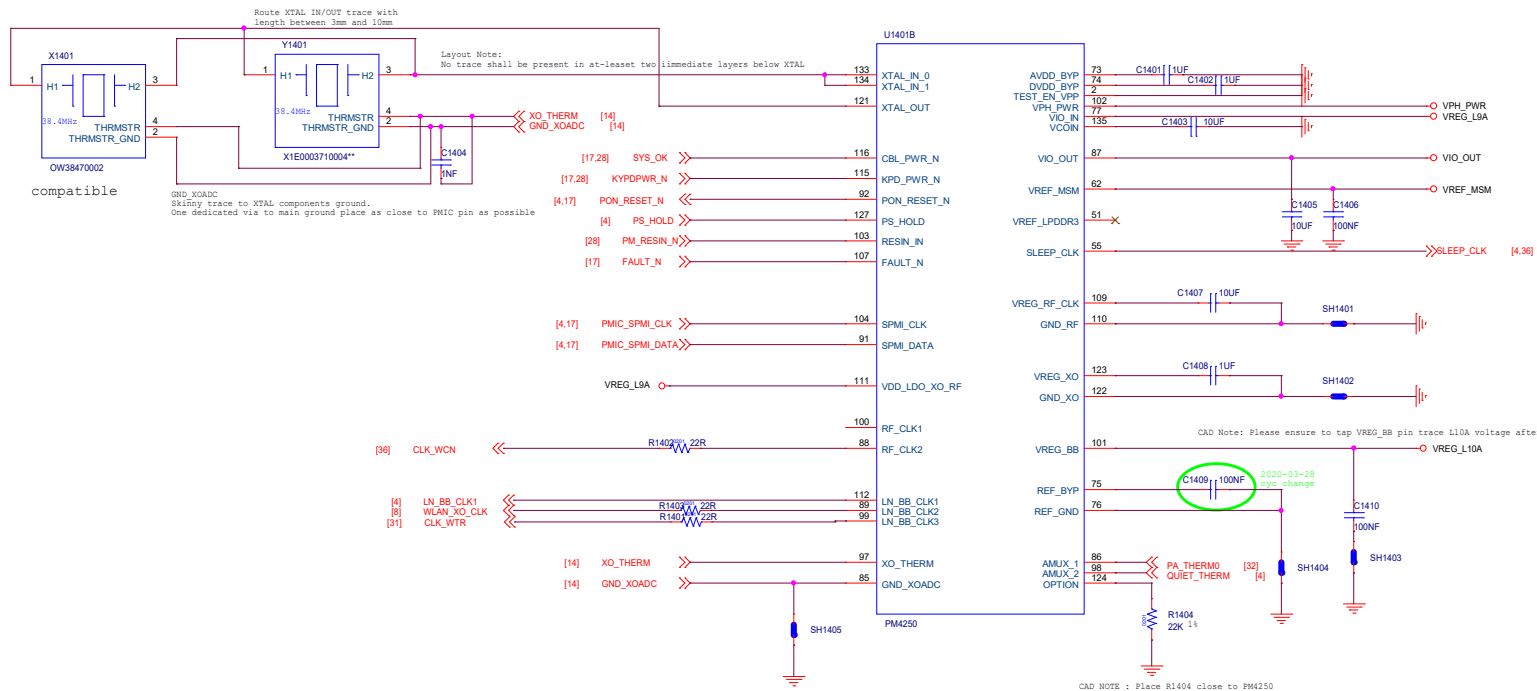


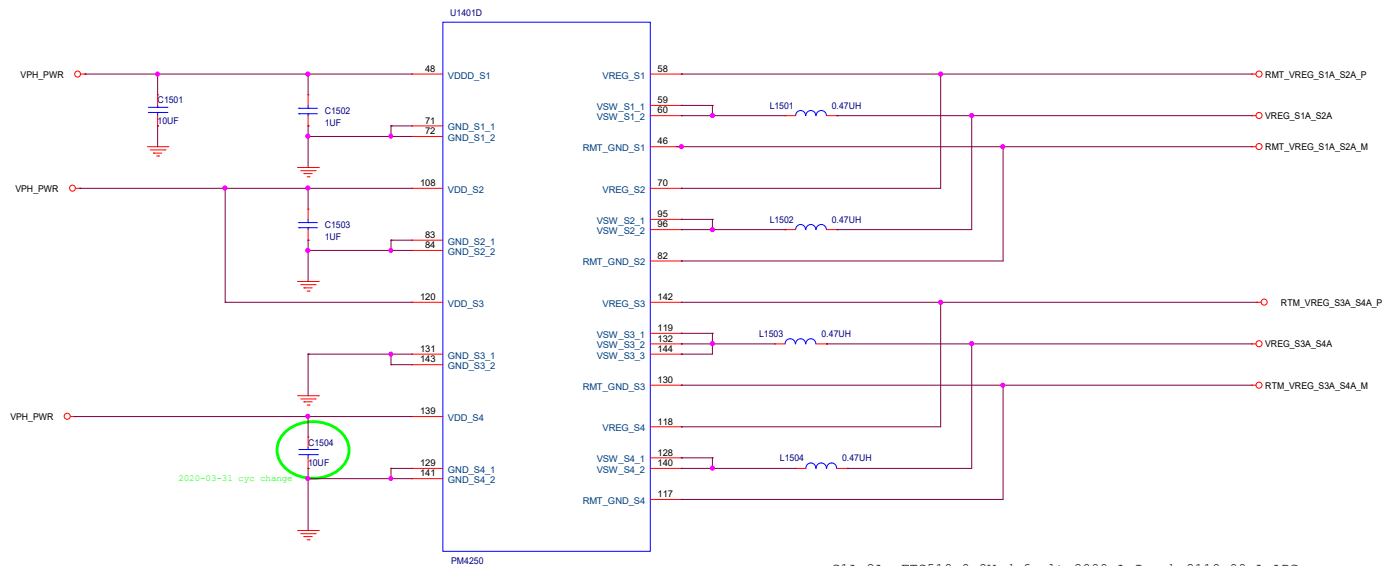




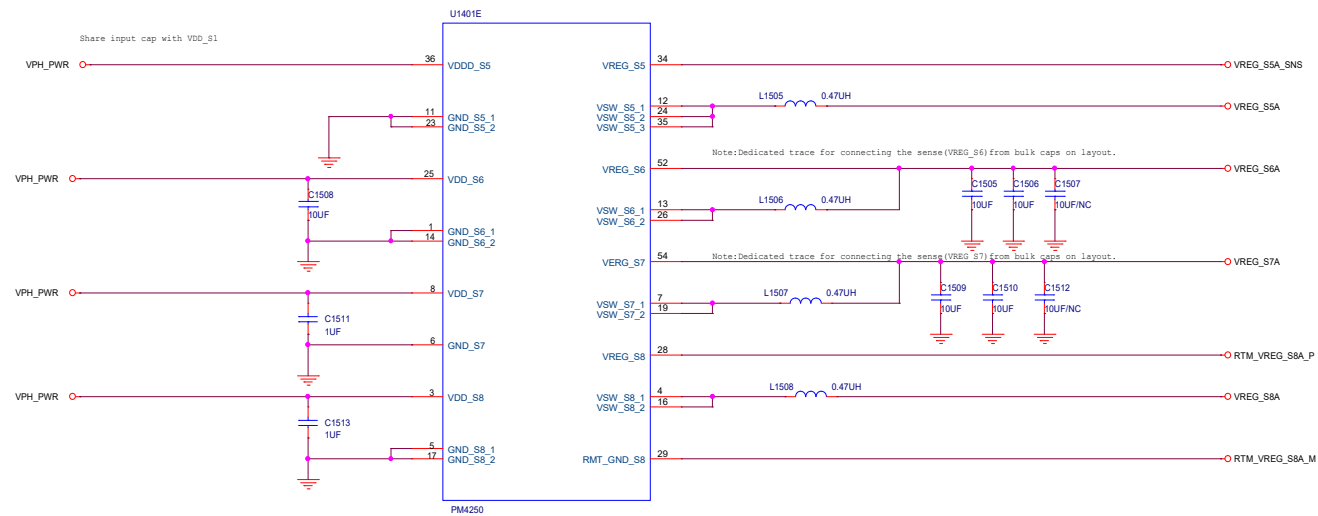


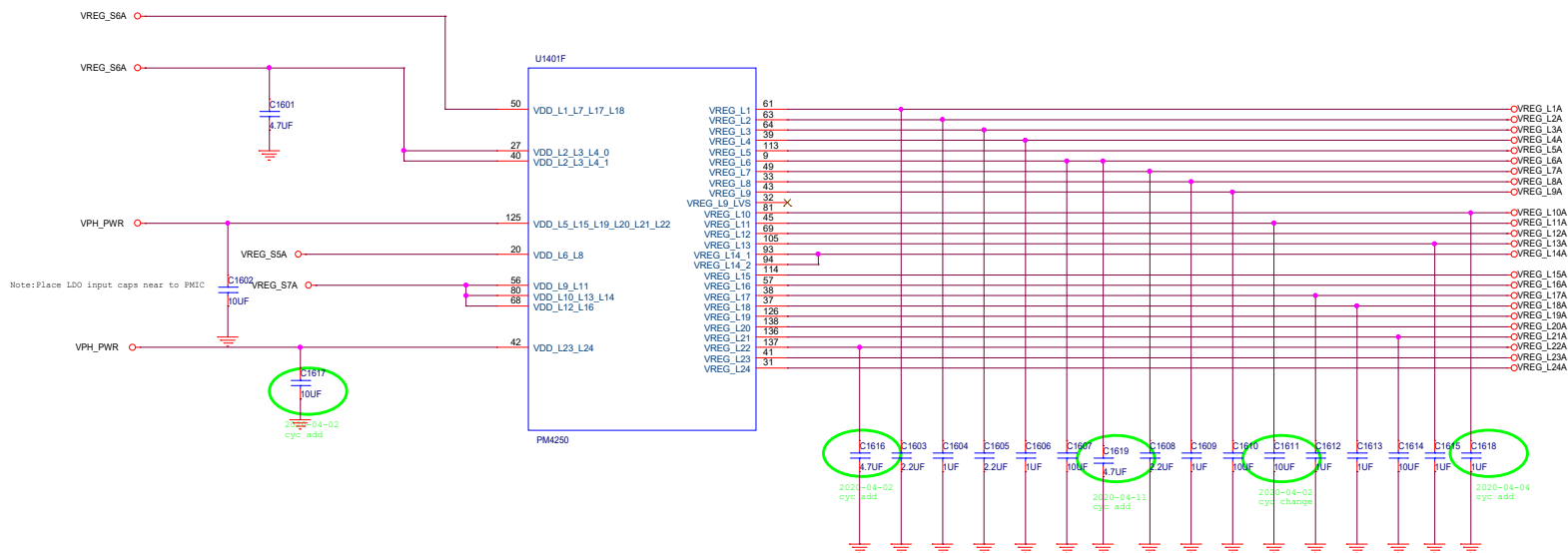






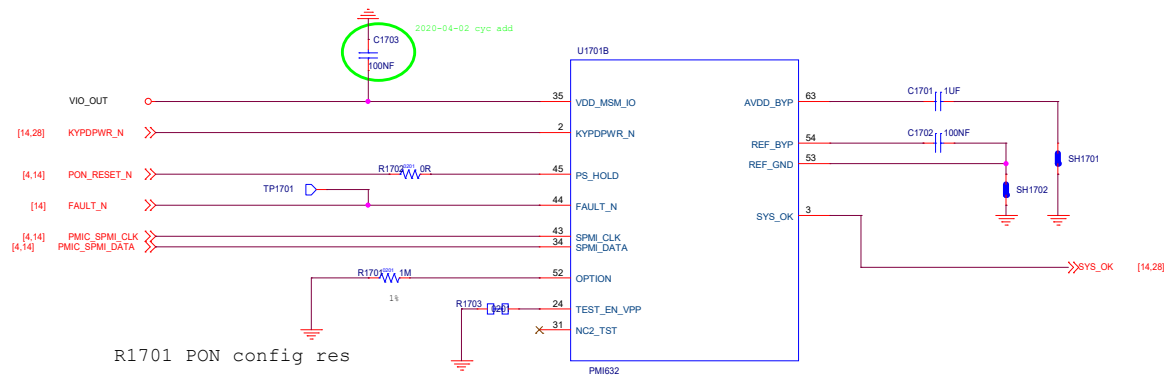
S1A-2A: FTS510 0.8V default 8000mA Ipeak 8110.00mA APC
 S3A-4A: FTS510 0.8V default 8000mA Ipeak 8480mA CX, MODEM
 S5A: HFS510 0.912V default 4000mA Ipeak 3620.00mA EBI, MX, nLDO
 S6A: HFS510 1.352V default 4000mA Ipeak 1560mA LDO
 S7A: HFS510 2.04V default 2500mA Ipeak 2350mA LDO
 S8A: FTS510 1.128V default 4000mA Ipeak 1790mA LDO, LPDDR4x



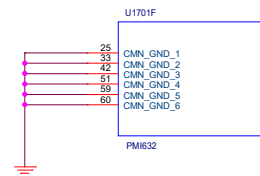
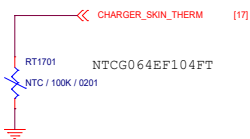
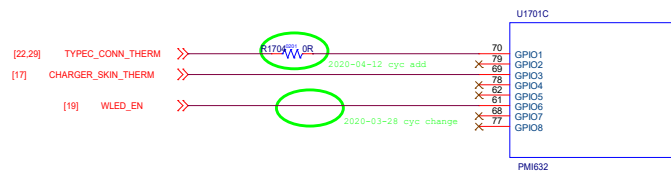


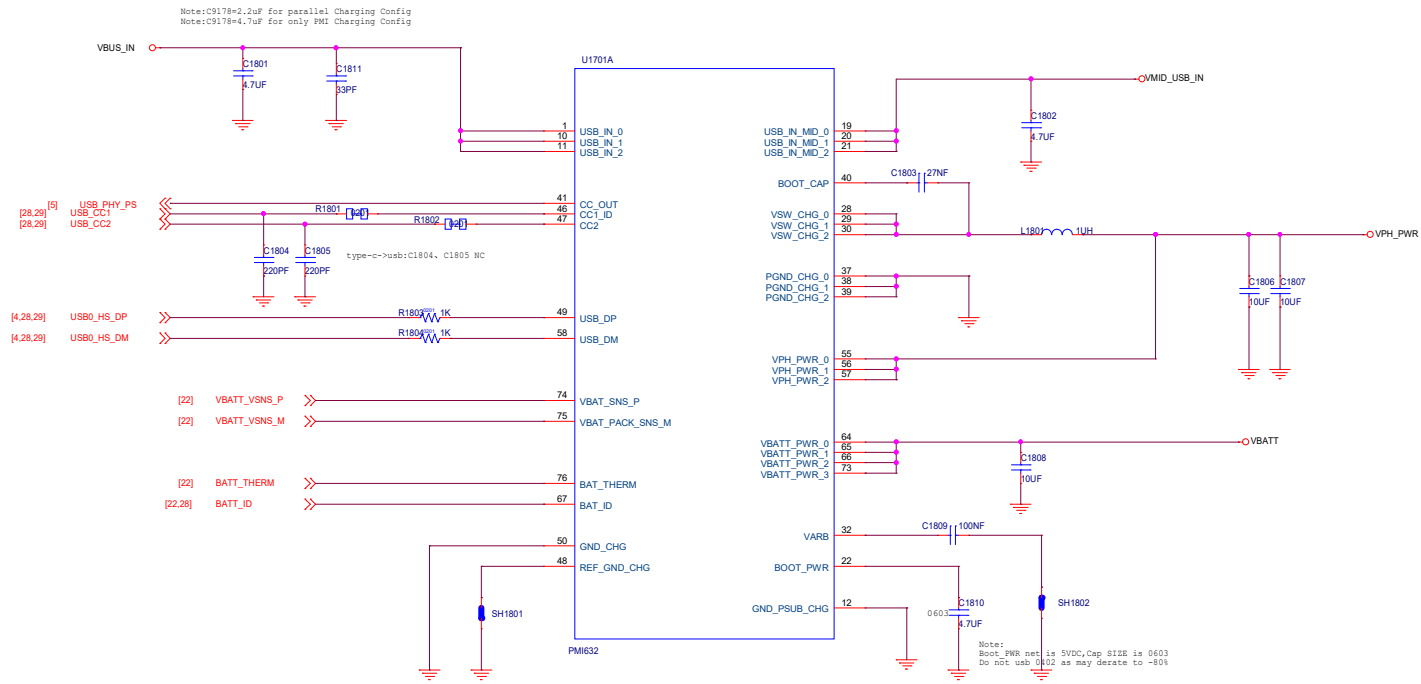
Regulator	Circuit type	I _{RATED} (mA)	Default on booting voltage (V) ¹	Specified range (V)	Programmable range (V)	Default on
L1	N600	600	1.0	0.95–1.15	0.312–1.304	Y
L2	N300	300	0.848	0.488–0.988	0.312–1.304	Y
L3	N600	600	0.8	0.4–1.08	0.312–1.304	Y
L4	N300	300	0.928	0.488–1.0	0.312–1.304	Y
L5	MV P50	50	2.98	1.85–3.05	1.504–3.544	Y
L6	N1200	1200	0.624	0.57–0.65	0.312–1.304	Y
L7	N600	600	1.256	1.2–1.3	0.312–1.304	Y
L8	N300	300	0.664	0.4–0.728	0.312–1.304	Y

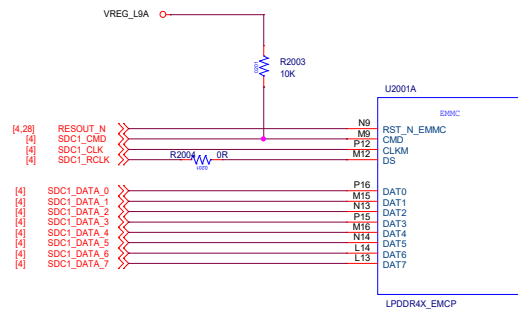
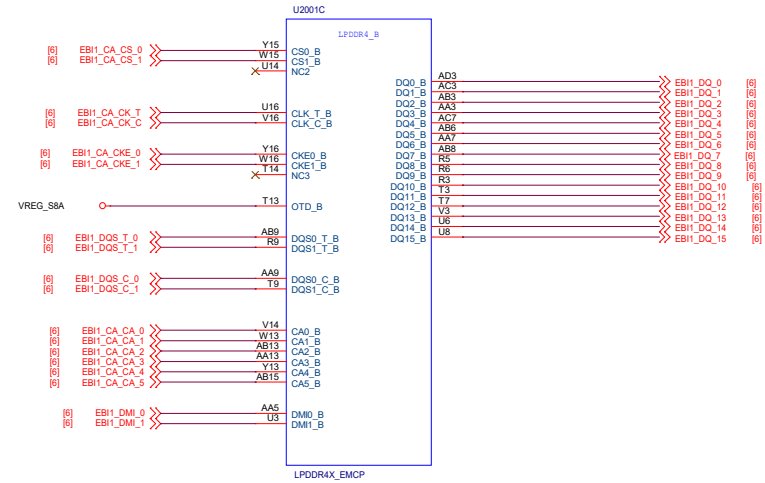
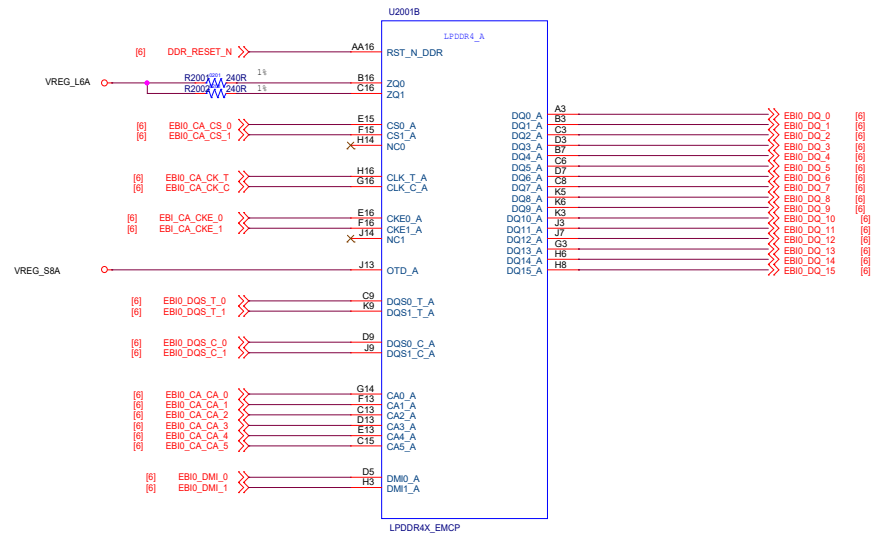
Regulator	Circuit type	I _{RATED} (mA)	Default on booting voltage (V) ¹	Specified range (V)	Programmable range (V)	Default on
L9 ²	LV P600	600	1.8	1.8–2.0	1.504–2.000	Y
L10	LV P150	150	1.8	1.7–1.9	1.504–2.000	Y
L11	LV P600	600	1.8	1.7–1.95	1.504–2.000	Y
L12	LV P300	300	1.8	1.62–1.98	1.504–2.000	Y
L13	LV P300	300	1.8	1.5–1.95	1.504–2.000	Y
L14	LV P600	600	1.8	1.7–1.9	1.504–2.000	N
L15	MV P150	150	3.128	2.92–3.23	1.504–3.544	Y
L16	LV P150	150	1.8	1.7–1.9	1.504–2.000	N
L17	N300	300	1.304	1.15–1.39	0.312–1.304	N
L18	N300	300	1.232	1.1–1.312	0.312–1.304	Y
L19	MV P150	150	1.8	1.62–3.3	1.504–3.544	N
L20	MV P150	150	1.8	1.62–3.3	1.504–3.544	N
L21	MV P600	600	2.704	2.4–3.6	1.504–3.544	N
L22 ³	MV P600	600	2.96	2.95–3.3	1.504–3.544	Y
L23	MV P600	600	3.304	3.2–3.4	1.504–3.544	N
L24	MV P600	600	2.96	2.7–3.6	1.504–3.544	Y

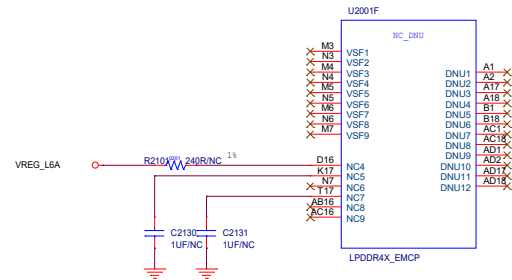
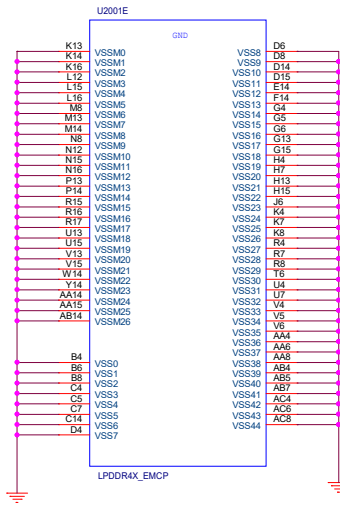
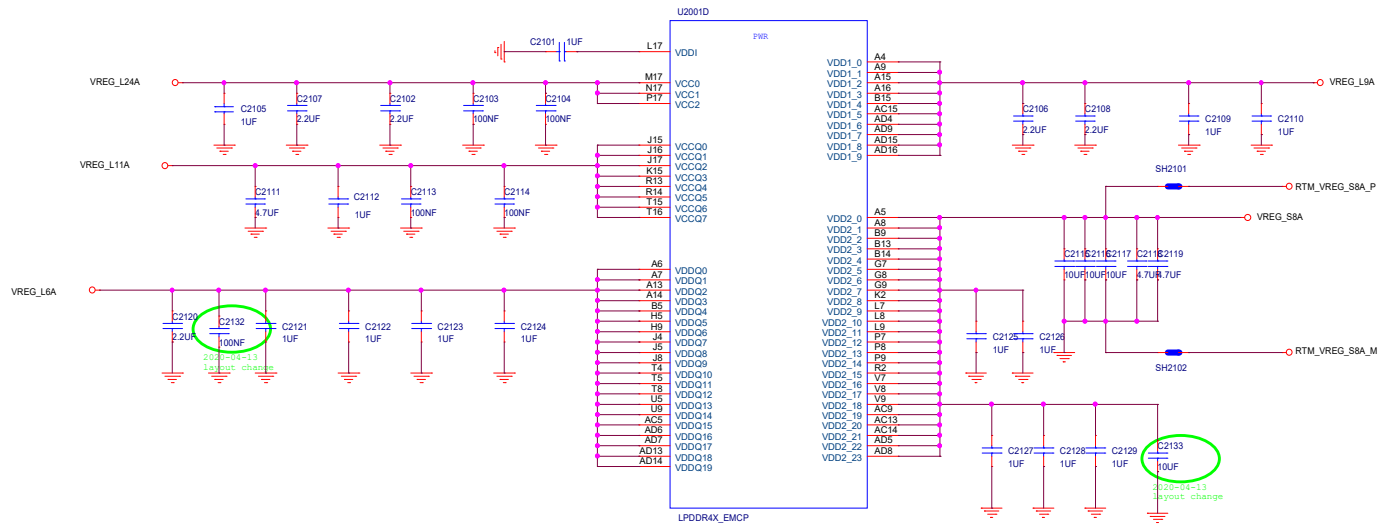


#	PON Configuration R1594		
1	SPMI, uUSB	1K	
4	I2C, uUSB	22K	
6	I2C, TypeC	150K	
8	SPMI, TypeC	1M	

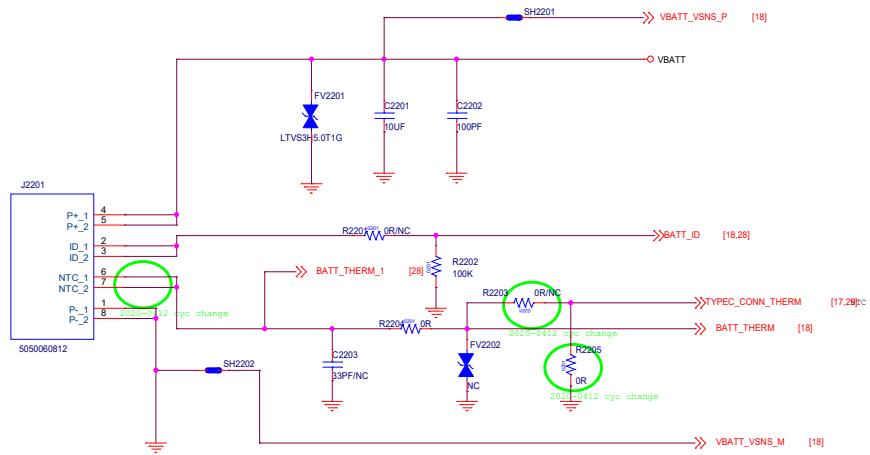




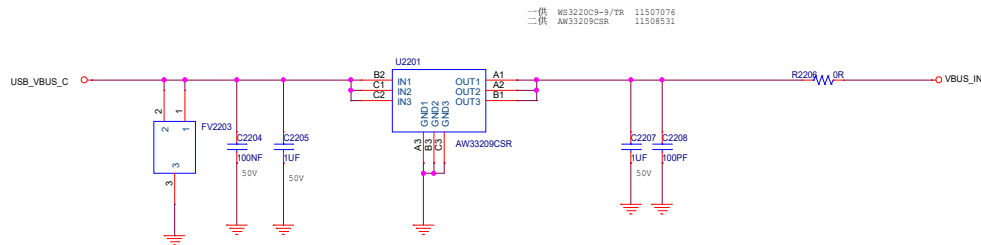




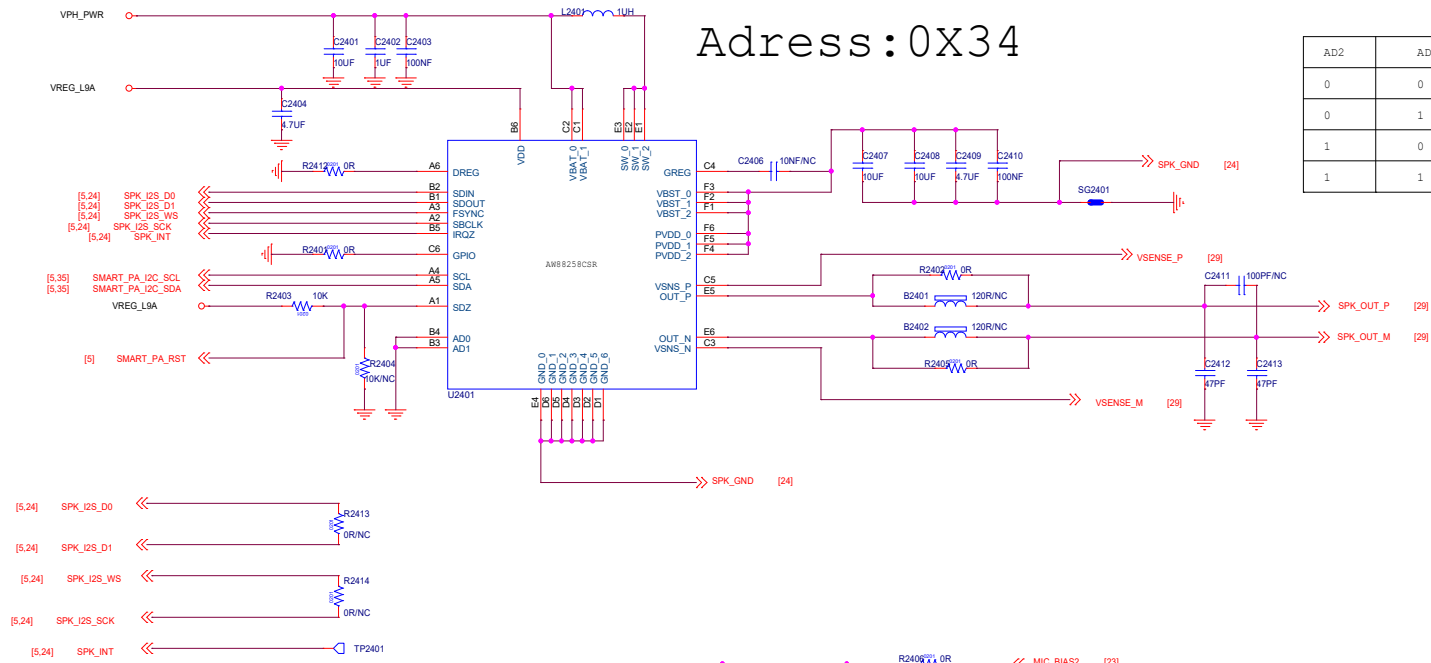
BATTERY



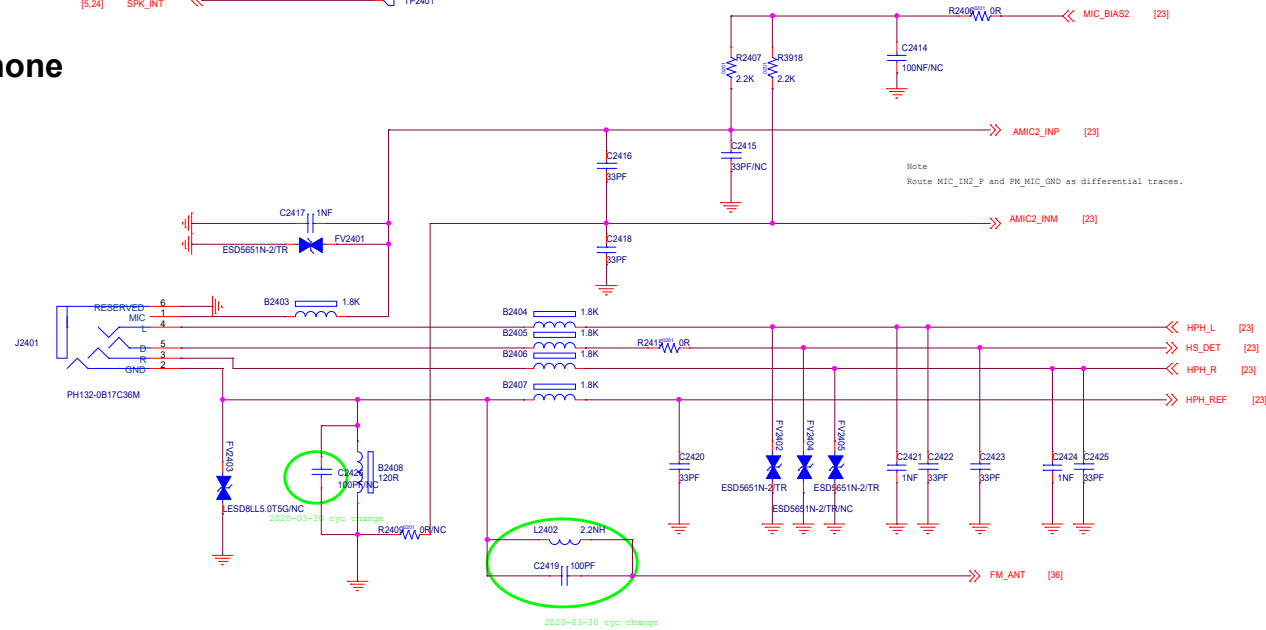
OVP



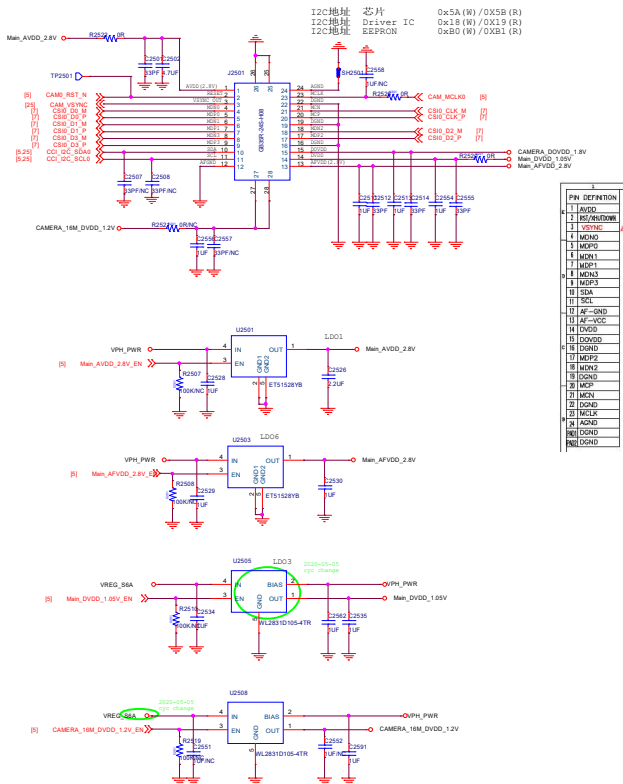
Smart PA



Earphone



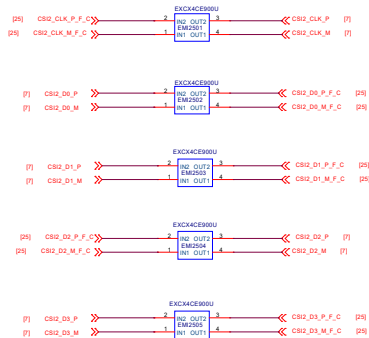
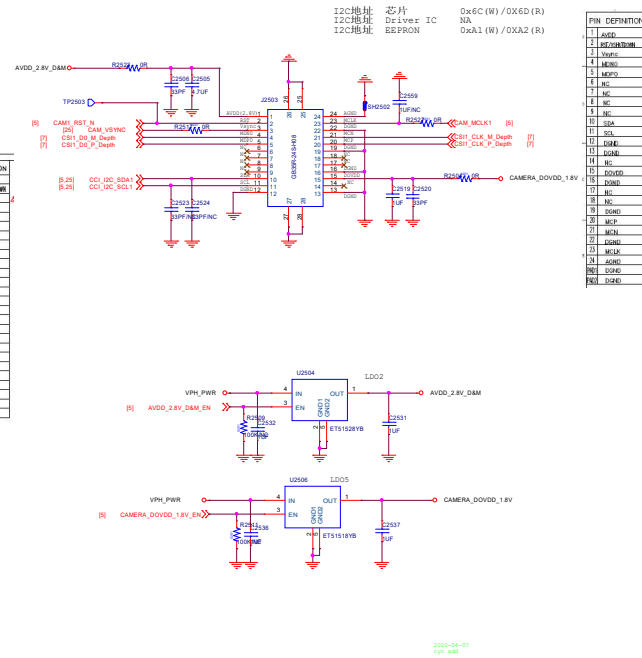
Main Camera A 13M/16M



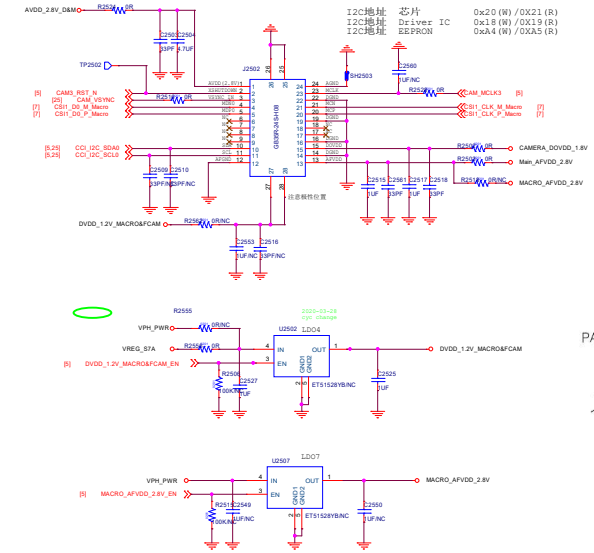
		13M	2M Macro	2M Depth	FRONT8M	Note
AVDD1	L001	1			1	
AVDD2	L002		1	1		
DVDD1 (1.05V)	L003	1				
DVDD2 (1.2V)	L004		1		1	
DVDD3	L005	1	1	1	1	
AFVDD1	L006	1	1			
AFVDD2	L007		0 (2)			兼容设计

Camera	I2C	MCLK	MIPI
Rear Main	I2C0	MCLK0	CSIPHY0
Depth	I2C1	MCLK1	CSIPHY1
Macro	I2C0	MCLK3	CSIPHY1
Front	I2C1	MCLK2	CSIPHY2

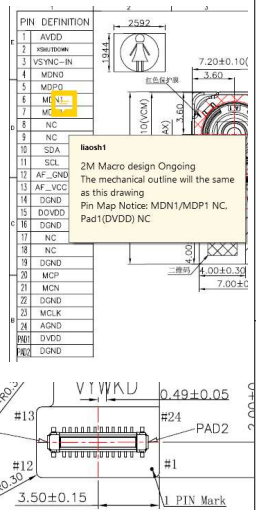
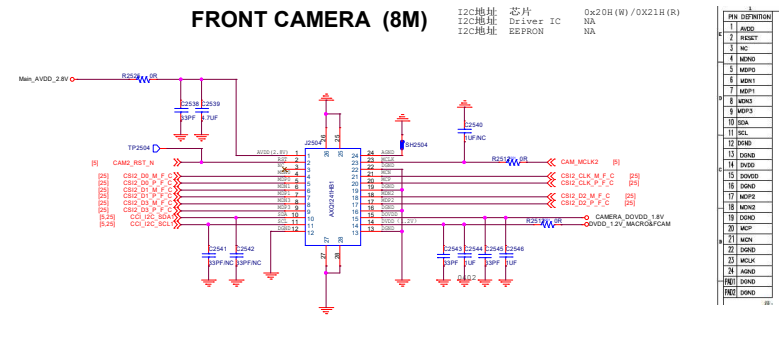
Main Camera B 2M depth (compatible)



Main Camera C 2M AF Macro

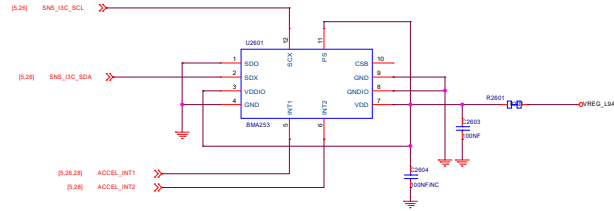


FRONT CAMERA (8M)

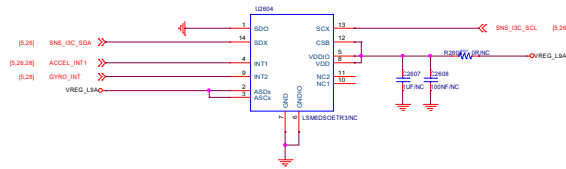


PN	DEFINITION
1	AVDD
2	RESET
3	NC
4	MEM0
5	MEM0
6	MEM1
7	MEM1
8	MEM3
9	MEM3
10	SDA
11	SCL
12	DGND
13	DGND
14	DVDD
15	DVDD
16	DGND
17	MDP2
18	MDM2
19	DGND
20	MCP
21	MCP
22	DGND
23	MCLK
24	AGND
25	DGND
26	DGND

G -sensor

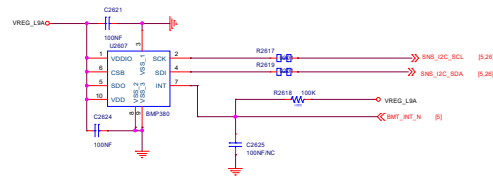


A+GYRO

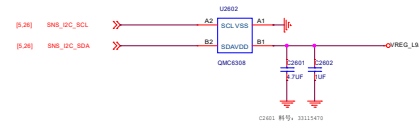


Barometer

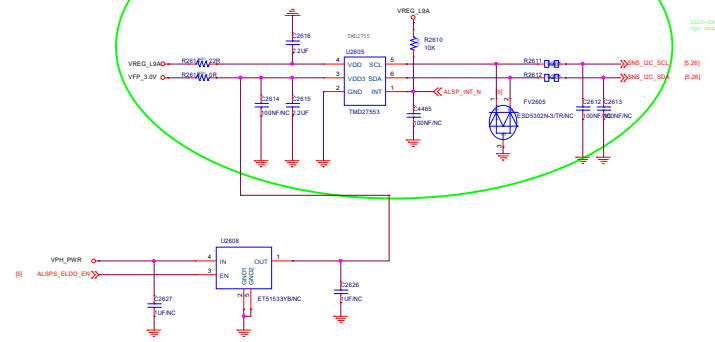
BMP380/ ICP10730/BMP390 pin-to-pin



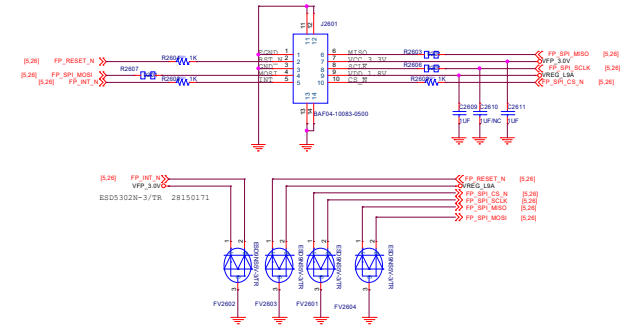
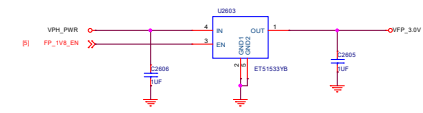
M SENSOR

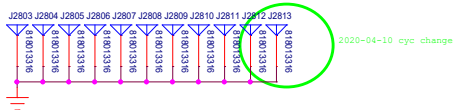
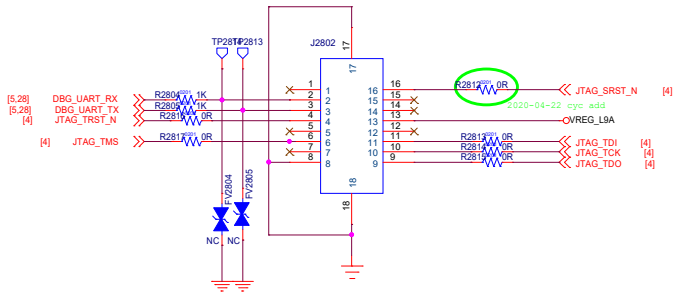
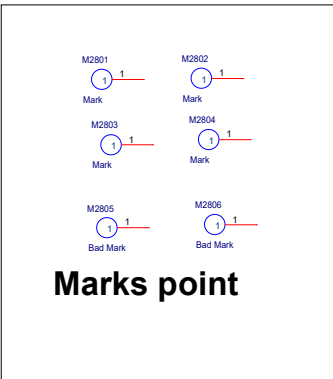
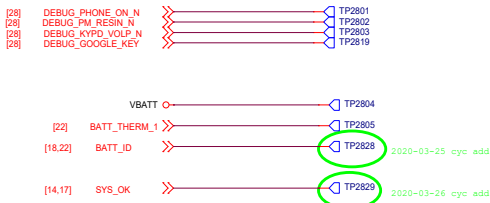
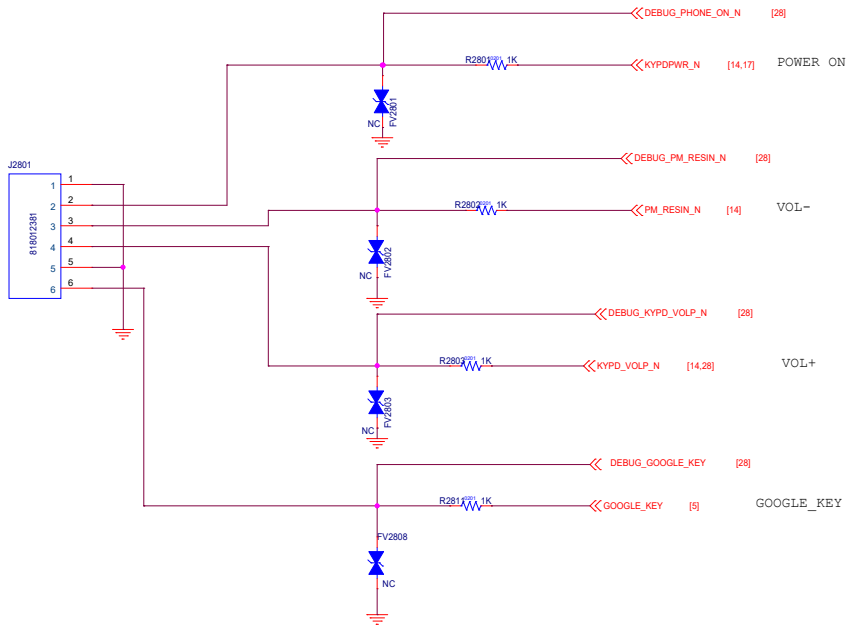


P/L SENSOR

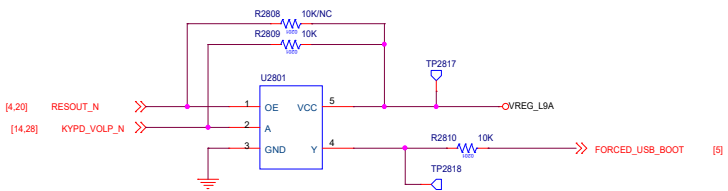


FP

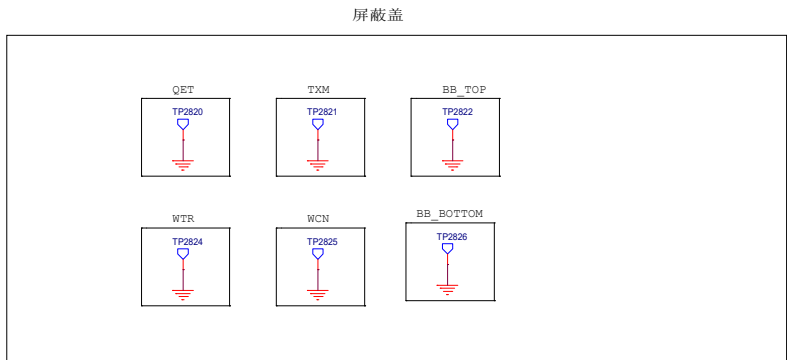




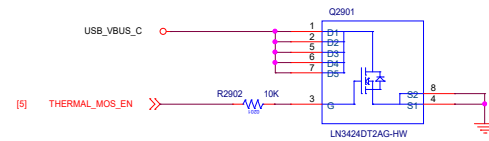
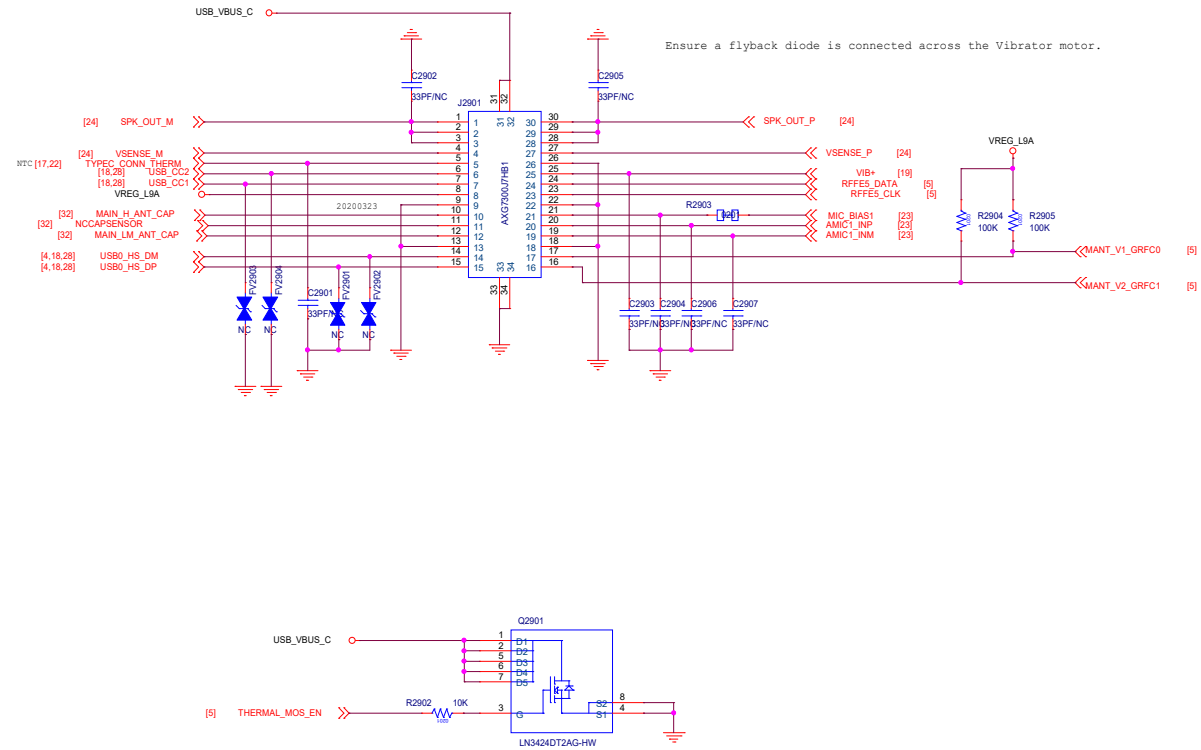
INPUT	OUTPUT
OE A	Y
L H	L
L L	H
H X	Z



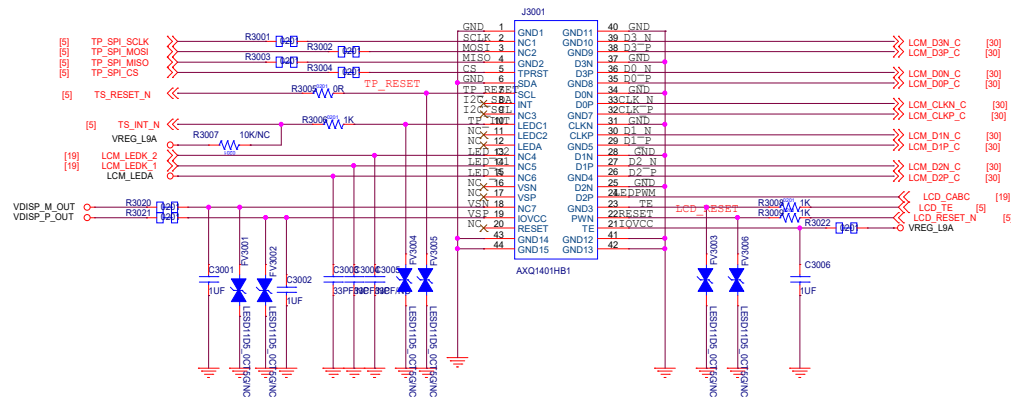
Power key+Vol up enter 9008 mode



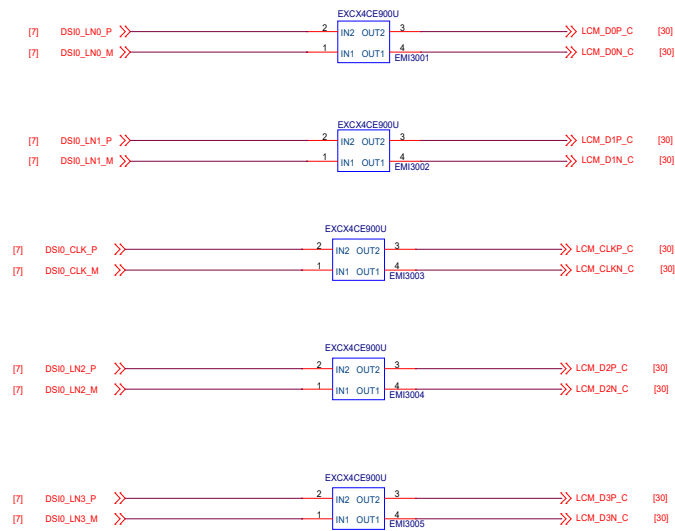
Sub Connector

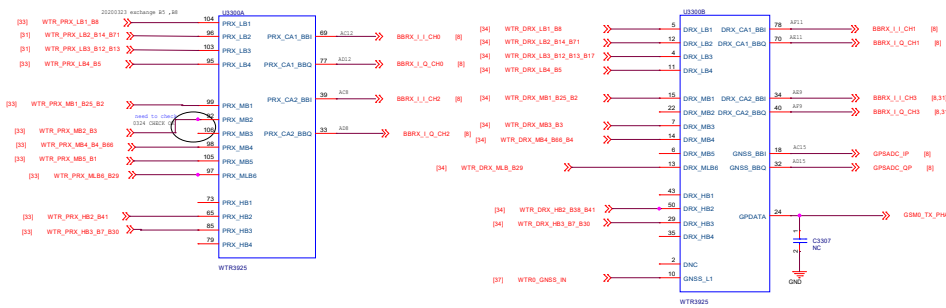


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Size	Document Number	Rev	<RevCode>
C	<Doc>		
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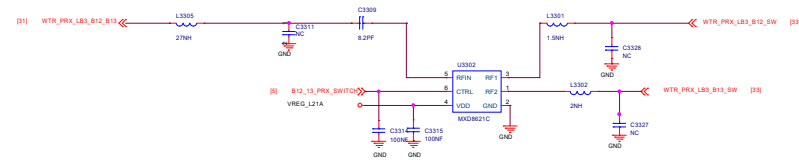


1	TP_RESET	1	TP_RESET
2	TP_INT_N	2	TP_INT_N
3	VREG_L9A	3	VREG_L9A
4	LCM_LEDK_2	4	LCM_LEDK_2
5	LCM_LEDK_3	5	LCM_LEDK_3
6	LCM_LED4	6	LCM_LED4
7	VDISP_M_OUT	7	VDISP_M_OUT
8	VDISP_P_OUT	8	VDISP_P_OUT
9	LCM_D3N_C	9	LCM_D3N_C
10	LCM_D3P_C	10	LCM_D3P_C
11	LCM_D0N_C	11	LCM_D0N_C
12	LCM_D0P_C	12	LCM_D0P_C
13	LCM_CLKN_C	13	LCM_CLKN_C
14	LCM_CLKP_C	14	LCM_CLKP_C
15	LCM_D1N_C	15	LCM_D1N_C
16	LCM_D1P_C	16	LCM_D1P_C
17	LCM_D2N_C	17	LCM_D2N_C
18	LCM_D2P_C	18	LCM_D2P_C
19	LCD_CABC	19	LCD_CABC
20	LCD_TE	20	LCD_TE
21	LCD_RESET_N	21	LCD_RESET_N
22	VREG_L9A	22	VREG_L9A

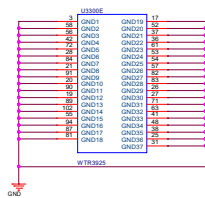
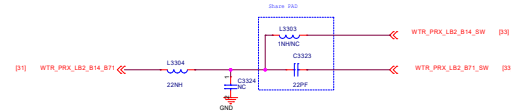




B12_B13 PRX SWITCH

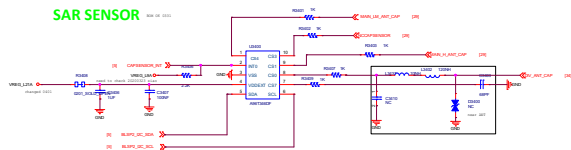


B14_B71 PRX

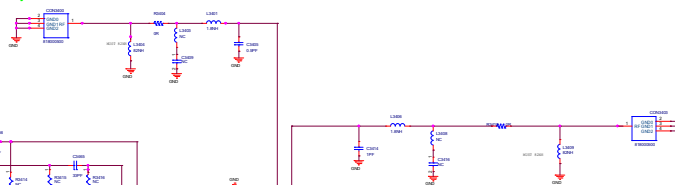


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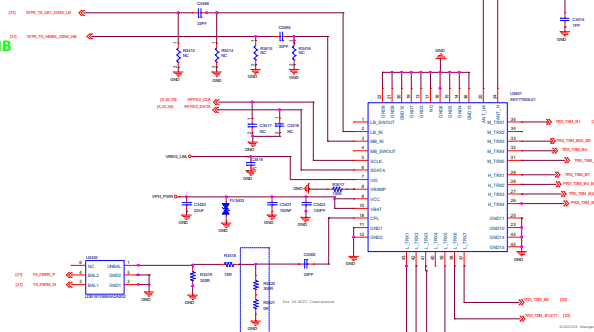
SAR SENSOR



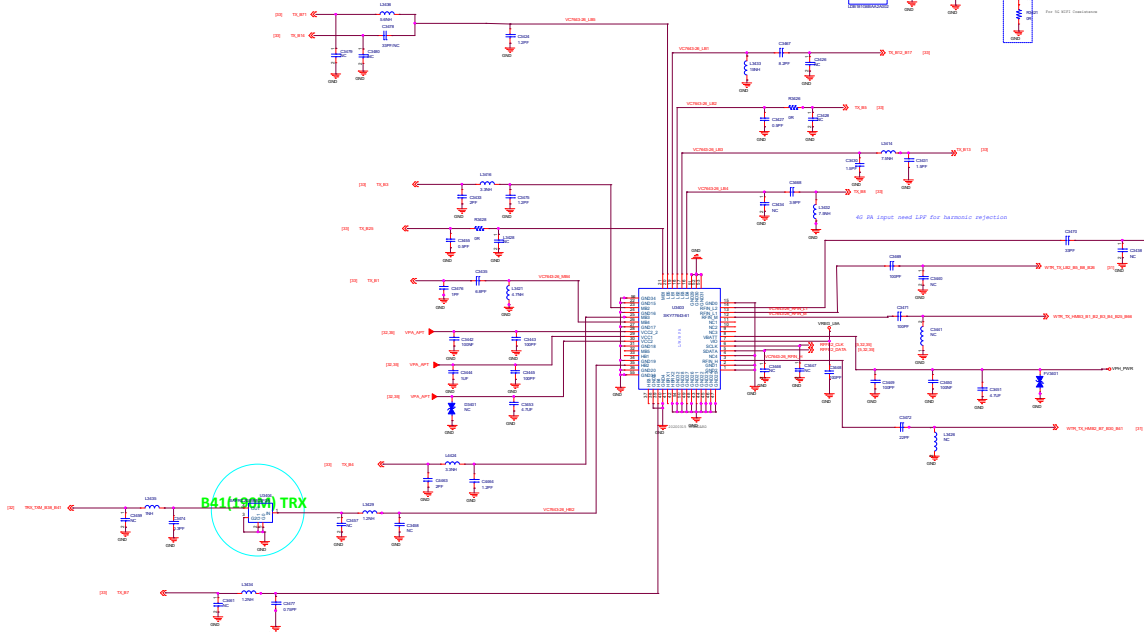
Primary-ANT



2G_PAIN_LB 2G_PAIN_HB



B41(190M) TRX



NTC3600 and C3669 close to PA, and located in the same layer

Rev	By	Appr	Date
1	1	1	1

