





AP (MT6739_11*11.8) Quad A53 1.5Ghz TX, 88P NO14 LTE, TX, 88, IPO TX, 88P NO14 LTE, TX, 88, INO TX, 88P NO14 LTE, TX, 88, OPO LTE, TX, 88, OPO LTE, TX, 88, ON0 TCP 122 MPI_DGB_CLK_P
TCN MPI_DGB_CLK_N
TDP0
125 MPI_DGB_CB_N RCAM1 MPL D1 P W3 80P1 RCAM1 MPL D1 P W3 80P2 RCAM1 MPL D1 P W3 80P3 RCAM1 MPL D1 P W3 80P1 RCAM1 TOP 100 ME ME TOP 100 PRX_BB_OP
PRX_BB_OP
PRX_BB_OP
PRX_BB_OP
PRX_BB_OP
RX_BB_OP
RX_BB_OP RCAM1 MIPI D3 P U2 RDP2 RCAM1 MIPI D3 N U2 RDN2 H:FM on L:FM off **I2C Pull-up Register** JTAG BOOT CONFIG MISDOR_CILK
MISDOR_CILK
MISDOR_CILK
MISDOR_DISL
MISDOR_CILK
MISDOR_DISL
MISDOR_CILK
MISDOR_DISL
MISDOR_CILK
MISDOR EMB_CK_T D7 DOR3_CLK_P DOR3_CLK_N **PCB JTAG** JTAG MUC D P B 22 22 23 JTAG MUC D N B 22 23 JTAG MUC D.P.B. AP_USB_BOOT1_B VBUS_IF_SP0_ITAG_A GND_A VBUS, F. SPO, JTAG, A **AP JTAG**





