

# 2018\_J4\_REV0.2

2018. 03. 07

**SHEET 1 : RF PART**

**SHEET 2 : TRCV,DIVERSITY,CONNECTYBITY**

**SHEET 3 : BASEBAND(JAVA)**

**SHEET 4 : PMIC(S2MPU06X), MUIC(SM5504),INTERFACE**

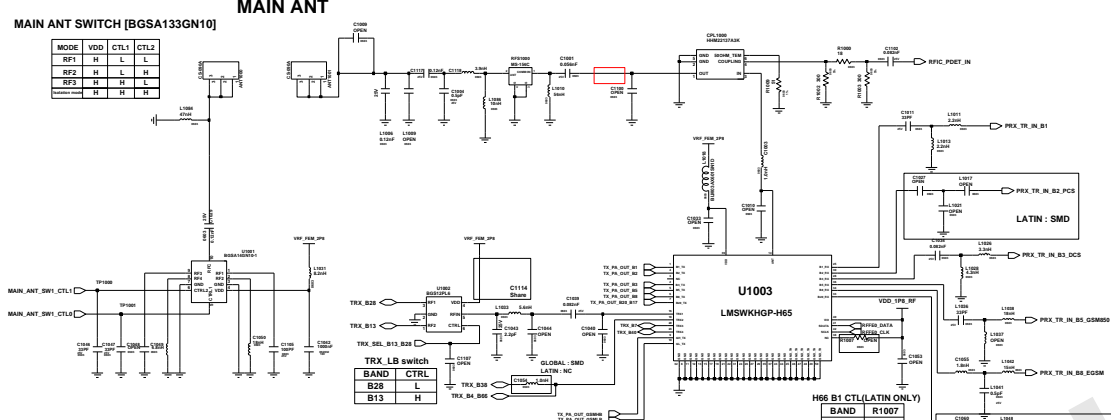
**SHEET 5 : MEMORY,AUDIO**

**SHEET 6 : CAMERA, DISPLAY, SENSOR,SOCKET,KEY**

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## MAIN ANT SWITCH [BGSA133GN10]

MODE	VDD	CTL1	CTL2
RF1	H	L	L
RF2	H	L	H
RF3	H	H	L
RF4	H	H	H



## FEMiD

### PA Thermister

## High band PA

FDD LTE : B7  
TDD LTE : B38 / B40

### CIS / MEA / SEA / SWA

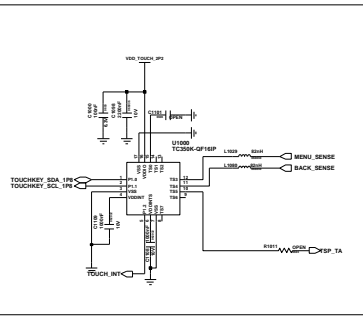
2G - EGSM / DCS / GSM850  
3G - W1 / W5 / W8  
LTE - B1 / B3 / B5 / B7 / B8 / B20 / B38 / B40

### TW / PHP

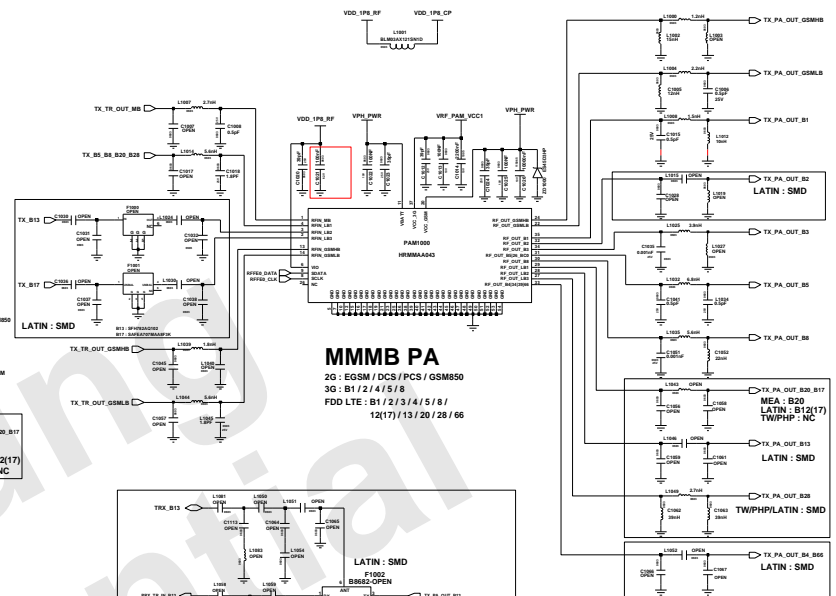
2G - EGSM / DCS / GSM850  
3G - W1 / W5 / W8  
LTE - B1 / B3 / B5 / B7 / B8 / B28 / B38 / B40

### LATIN

2G - EGSM / DCS / GSM850 / PCS  
3G - W1 / W2 / W4 / W5 / W8  
LTE - B1 / B3 / B5 / B7 / B8 / B12(17) / B13 / B28 / B66



### TOUCH\_KEY



### B13\_DUPLEX

### B66\_DUPLEX

### B28\_DUPLEX

### TX\_TR\_LB

GLOBAL : (1) NC (2) SMD  
LATIN : (1) SMD (2) NC

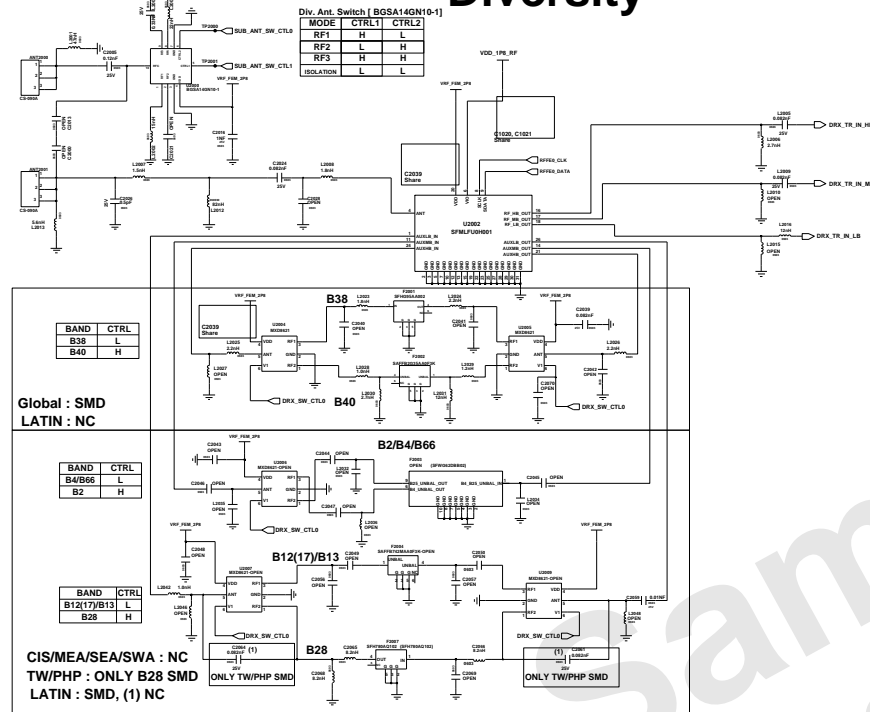
### LB TR TX switch

BAND	CTL0	CTL1
B5/B8/B20/B28	H	L
B13	H	H
B12(17)	L	H

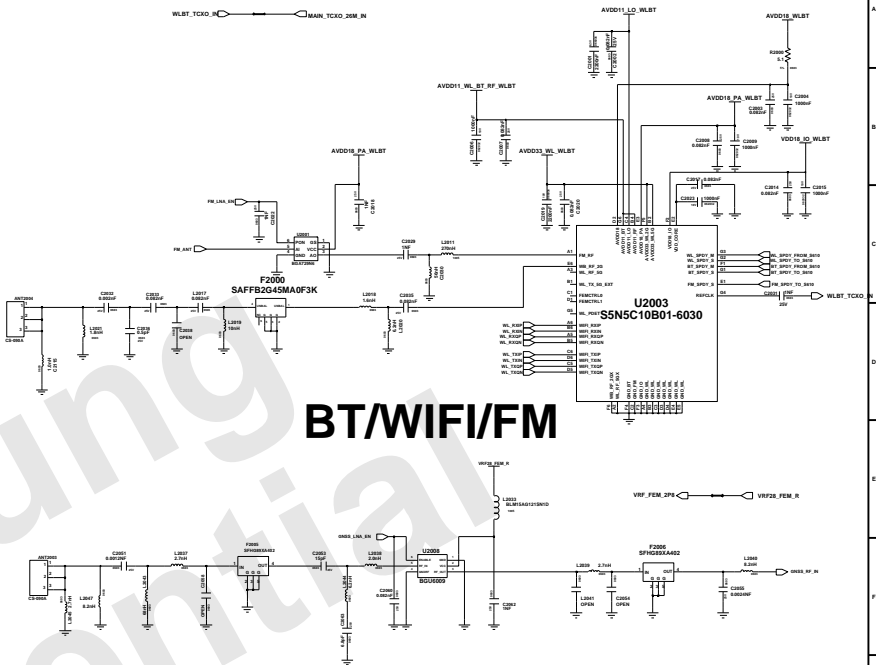
Project	Author	Reviewer	DATE
2018_J4_Main RF			18/10/18

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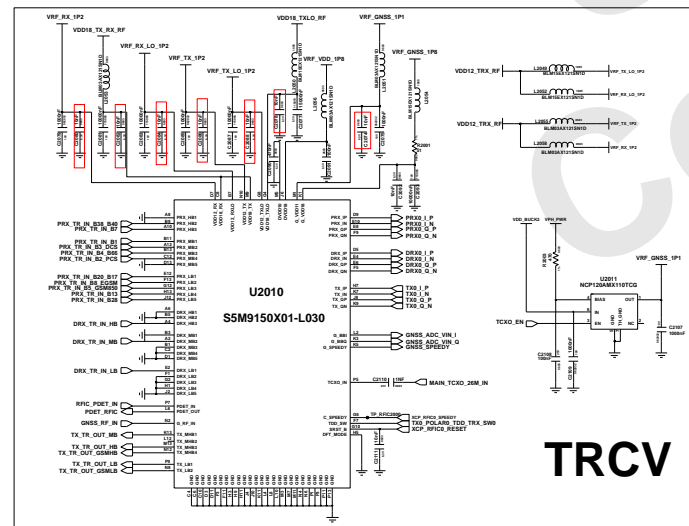
# Diversity



# BT/WIFI/FM

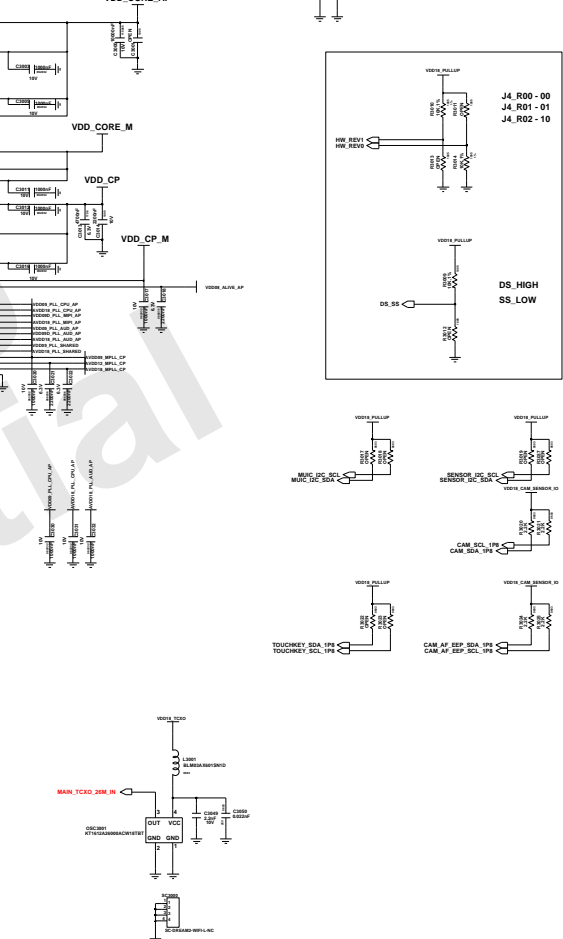
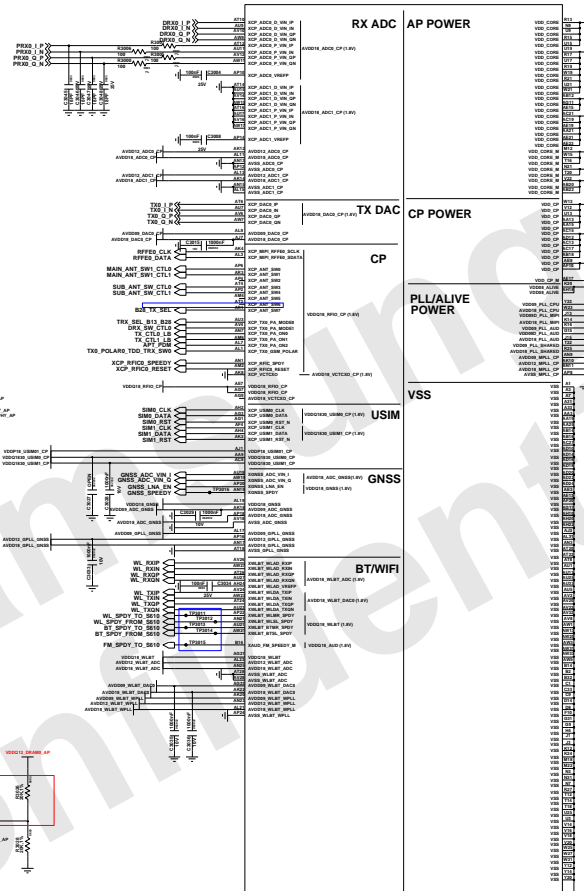
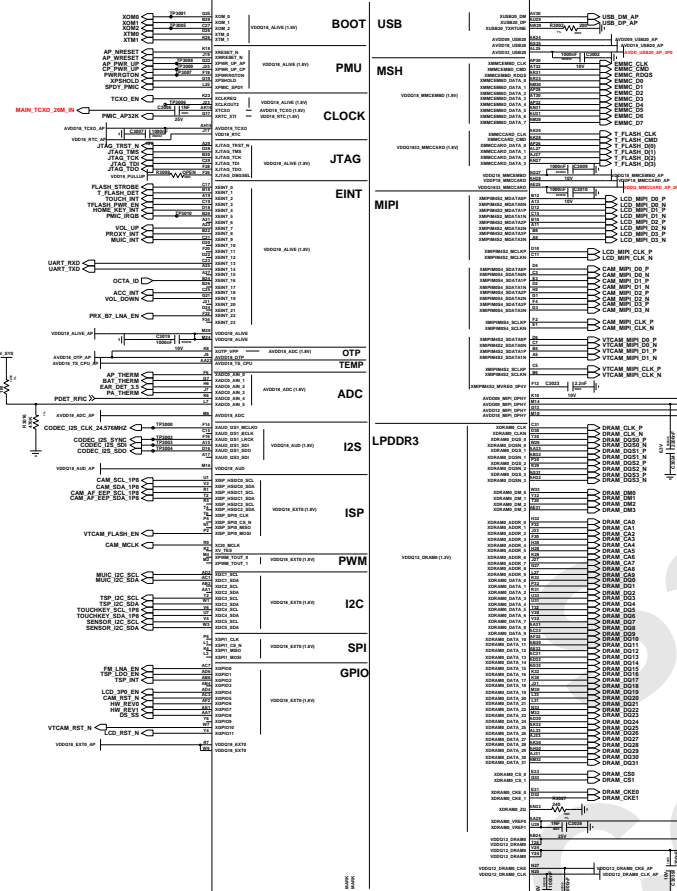


# GNSS



# TRCV

Project	SAMSUNG ELECTRONICS
File Name	2018_J4_RFIC_Diversity
File Path	2018_J4_RFIC_Diversity
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File Path	2018_J4_RFIC_Diversity
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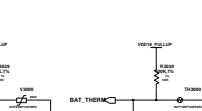
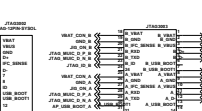
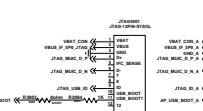
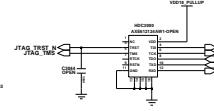


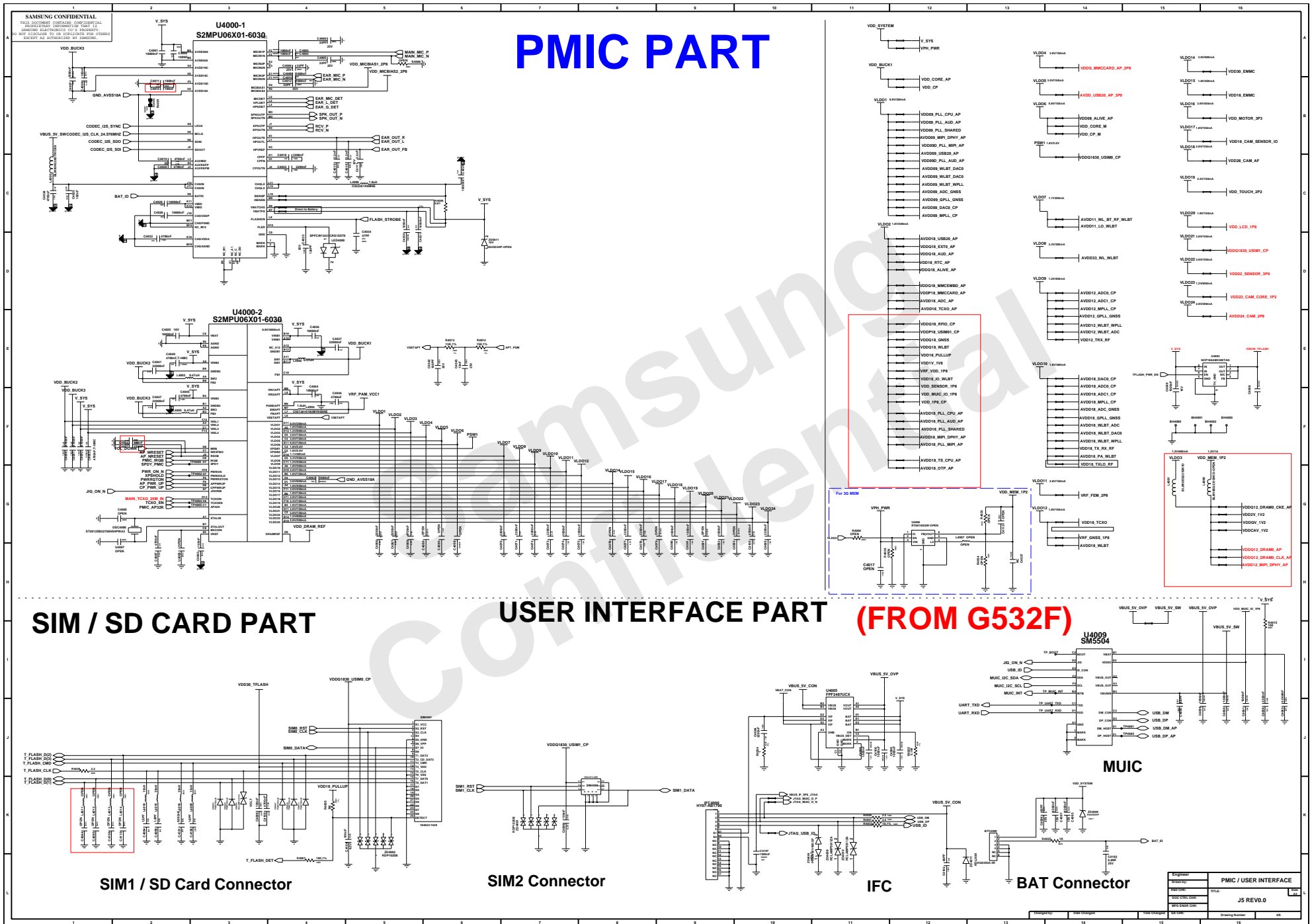
CONDENSED CONDENSED

2D-BARCODE 2D-BARCODE

**2D BARCODE**

SMD INC





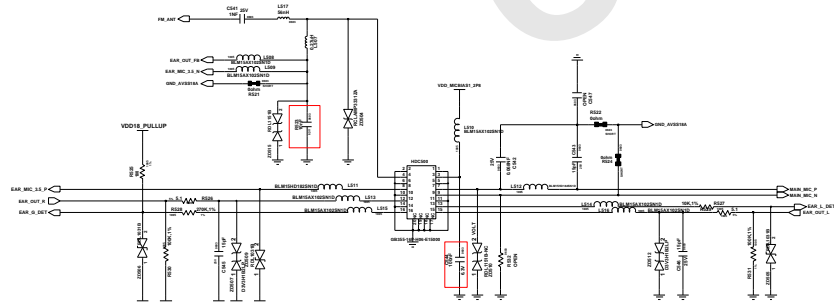


## MEMORY

**MEMORY(16GB eMMC + 2GB LPDDR3)**

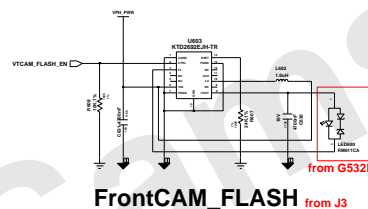


## EARJACK

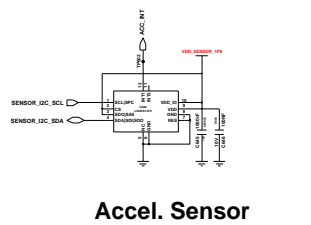


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user|swcad73|10.253.21.111|20180321-105344
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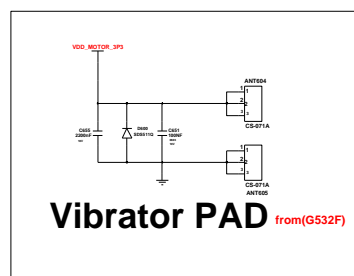
## CAMERA PART\_from (J330F)

[illegible]

## Proxi Sensor



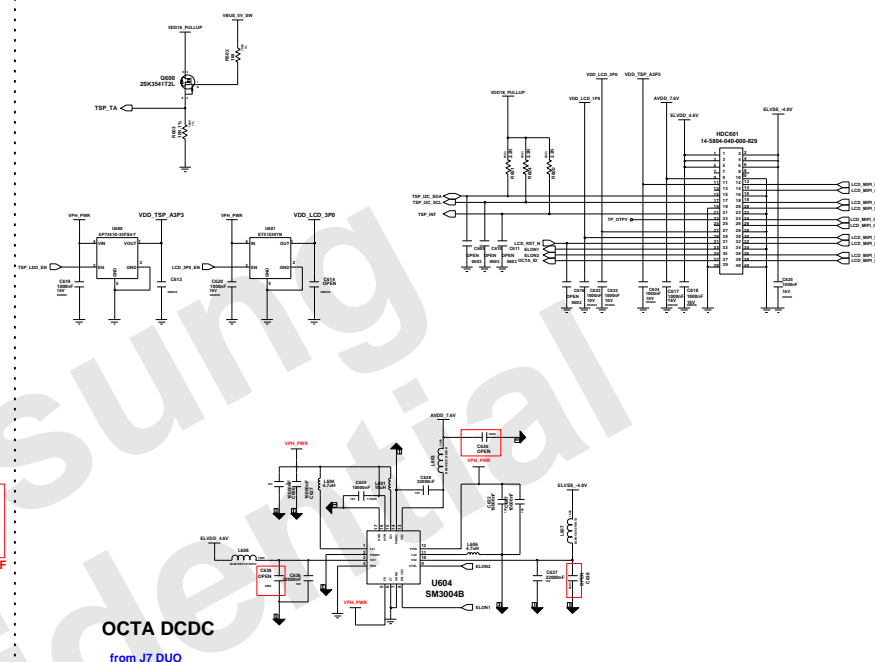
### Accel. Sensor



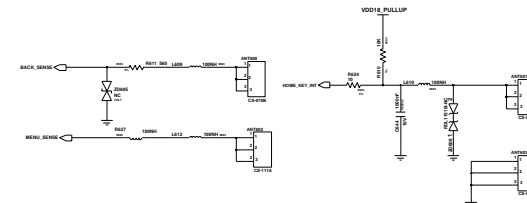
# Vibrator PAD from(G532F)

REGION		MEMORY	
RA14 14 bits 14 pins	F	RA04 4 bits 4 pins	2+16
	MEMOIR		
RA15 15 bits 15 pins	L	RA05 5 bits 5 pins	2+32
	RAM		
RA19 19 bits 19 pins	G	RA09 9 bits 9 pins	3+32
	TRIMON		
RA23 23 bits 23 pins	D		
	Dual Size		
RA27 27 bits 27 pins	S		

**DISPLAY PART\_From(J7 DUO)**  
**HD 5.5" OCTA**

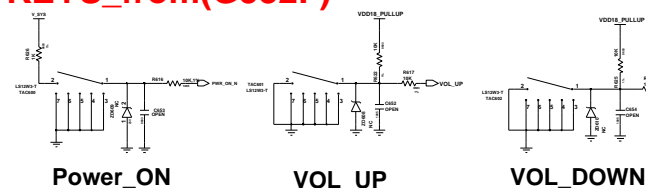


## Touch Key CON



## HOME KEY

## KEYS\_from(G532F)



Engineer:	CAMERA / LCD / SIM,SD CON
Drawn By:	
P&ID Code:	
DWG. CTR. CODE:	
WFO 47001 (C&I):	
DATE:	TITLE:
2017_J1_AUDIO,CAMERA	
Drawn By:	Rev:
Checked By:	Drawing Number: