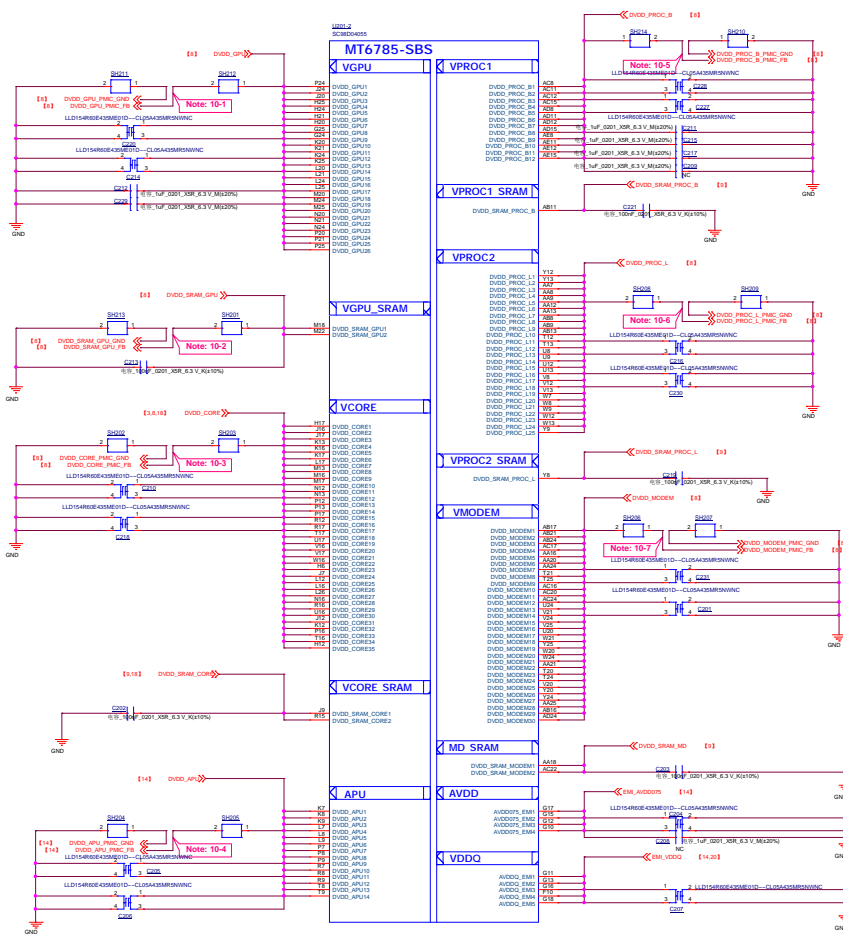


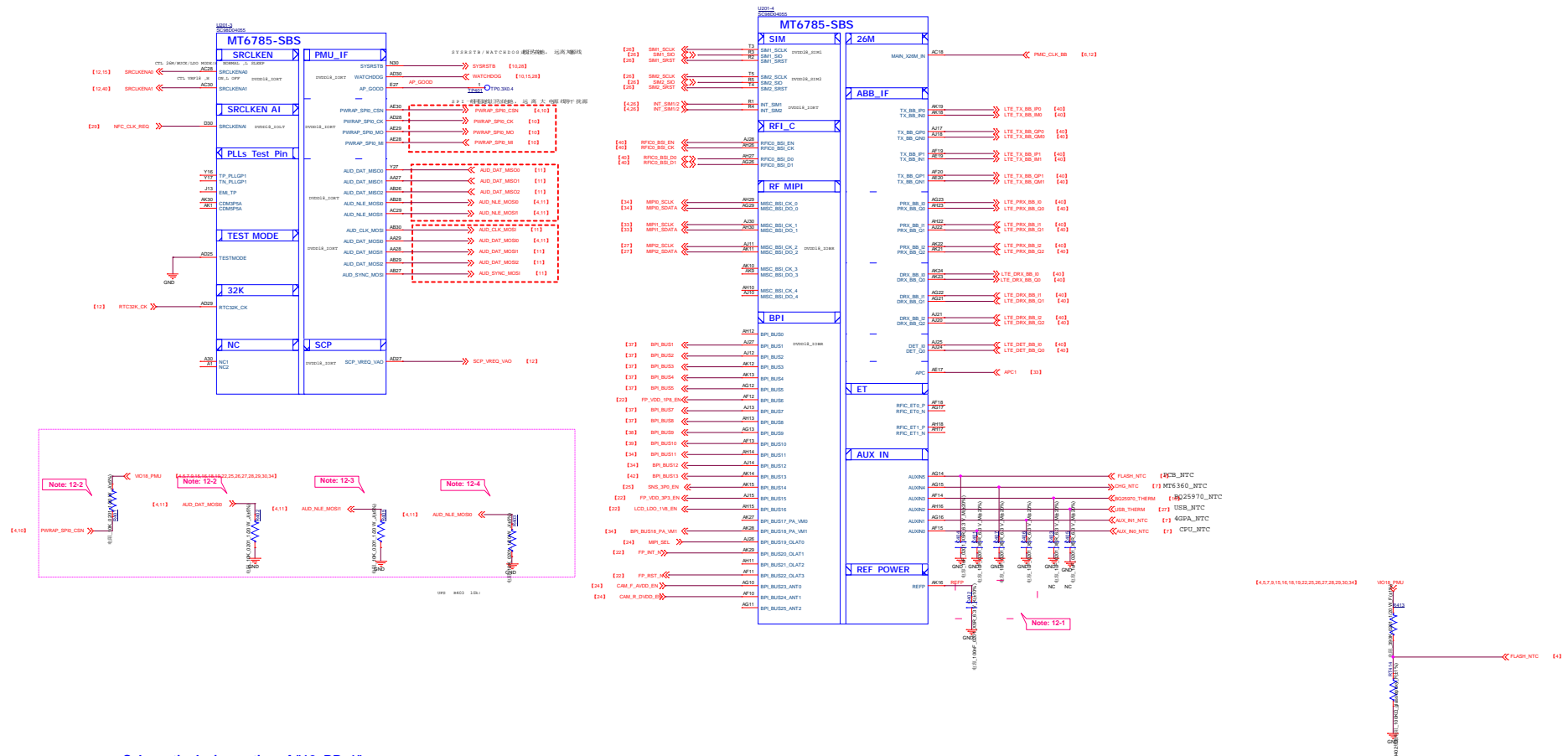
讯 Hua qin Tele com Technol ogy Com.Ltd			
Title Block Diagram			
Size D	Project Hanoi		Rev V1
Date: Wednesday, March 17, 2021		Sheet 1 of 99	



Schematic design notice of "10_BB_POWER_PDN" page.

- Note 10-1: Differential pair of DVDD_GPU remote sense must be close to BB's ball.
Remote sense trace with GND shielding to PMC (Differential)
- Note 10-2: Differential pair of DVDD_SRAM_GPU remote sense must be close to 22uF due to SRAM application.
Remote sense trace with GND shielding to PMC (Differential)
- Note 10-3: Differential pair of DVDD_CORE remote sense must be close to BB's ball.
Remote sense trace with GND shielding to PMC (Differential)
- Note 10-4: Differential pair of DVDD_APU remote sense must be close to BB's ball.
Remote sense trace with GND shielding to PMC (Differential)
- Note 10-5: Differential pair of DVDD_PROC_B remote sense must be close to BB's ball.
Remote sense trace with GND shielding to PMC (Differential)
- Note 10-6: Differential pair of DVDD_PROC_L remote sense must be close to BB's ball.
Remote sense trace with GND shielding to PMC (Differential)
- Note 10-7: Differential pair of DVDD_MODEM remote sense must be close to BB's ball.
Remote sense trace with GND shielding to PMC (Differential)

讯 Hua qin Tele com Technol ogy Com. Ltd		
Title BB_POWER_PDN		
Size D	Project Hanoi	Rev V1
Date: Wednesday, March 17, 2021 Sheet 2 of 99		



Schematic design notice of "12_BB_1" page:

Note 12-1: The de-coupling cap. for REFP (AK16 ball) have to be placed as close to BB as possible.

Note 12-2: PWRAP_SPI0_CSN and AUD_DAT_MOSI0 are JTAG feature in bootstrap.

PWRAP_SPI0_CSN	AUD_DAT_MOSI0	AP_JTAG	IO_JTAG
HI	LO	N/A	N/A
HI	HI (by external PU)	SPI_CS/SPI_CLK/ SPI_MO/SPI_MIEINT3	N/A
LO (by external PD)	LO	SPI_CS/SPI_CLK/ SPI_MO/SPI_MIEINT3	TDM_LRCK/TDM_BCK/TDM_MCLK/ TDM_DATA0/TDM_DATA1
LO (by external PD)	HI (by external PU)	MSDC1_CLK/CMD/ DAT0/DAT1/DAT2	N/A

Note 12-3: AUD_NLE_MOSI1 is USB JTAG feature in bootstrap.

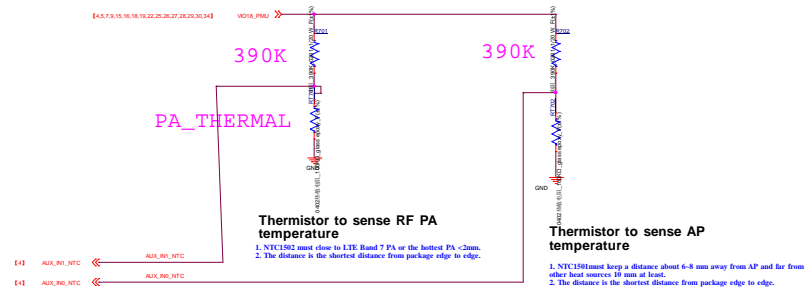
AUD_NLE_MOSI1	USB JTAG
LO	USB 2.0
HI (by external PU)	DP/DM output JTAG

Note 12-4: AUD_NLE_MOSIO is storage booting feature in bootstrap.

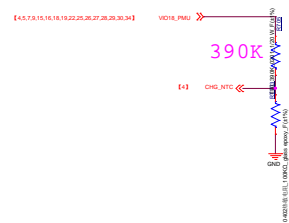
AUD_NLE_MOSIO	Storage Booting
LO	UFS only
HI (by external PU)	eMMC only

讯 Hua qin Tele com Technol ogy Com. Co., Ltd		
Title BB_1		
Size D	Project Hanoi	Rev V1
Date: Wednesday, March 17, 2021 Sheet 4 of 99		

4G PA NTC thermistor 3MM BB NTC thermistor 3~6MM

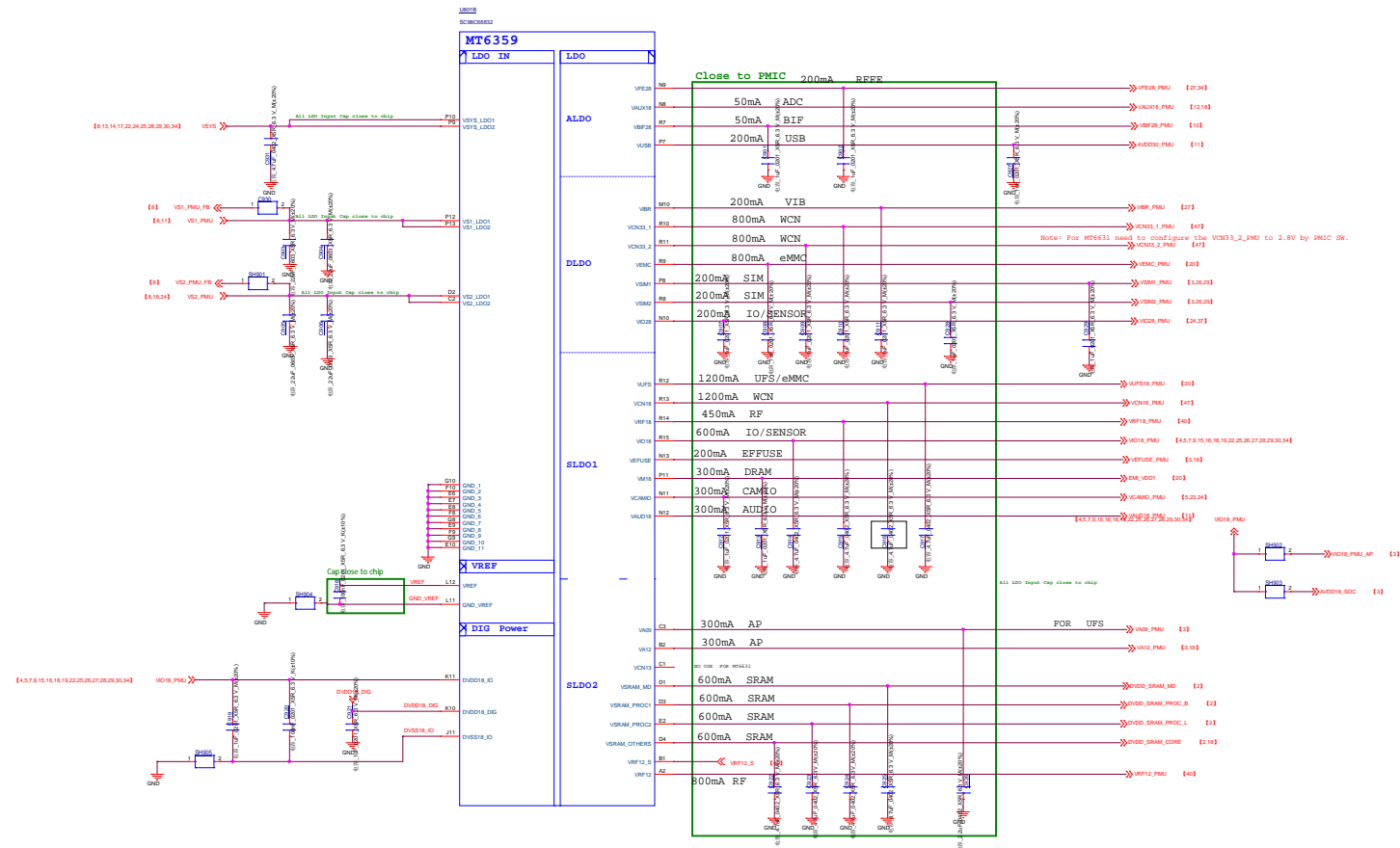


CHG NTC thermistor 3~5MM

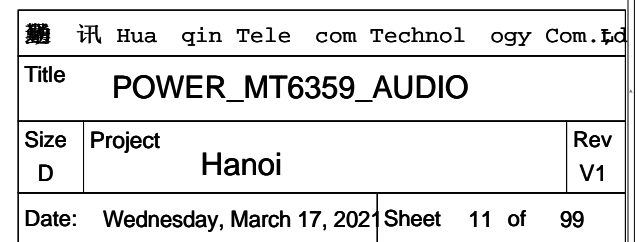


讯 Hua qin Tele com Technol ogy Com.Ltd		
Title BB_AUXADC_Thermal		
Size D	Project Hanoi	Rev V1
Date: Wednesday, March 17, 2021	Sheet 7 of 99	

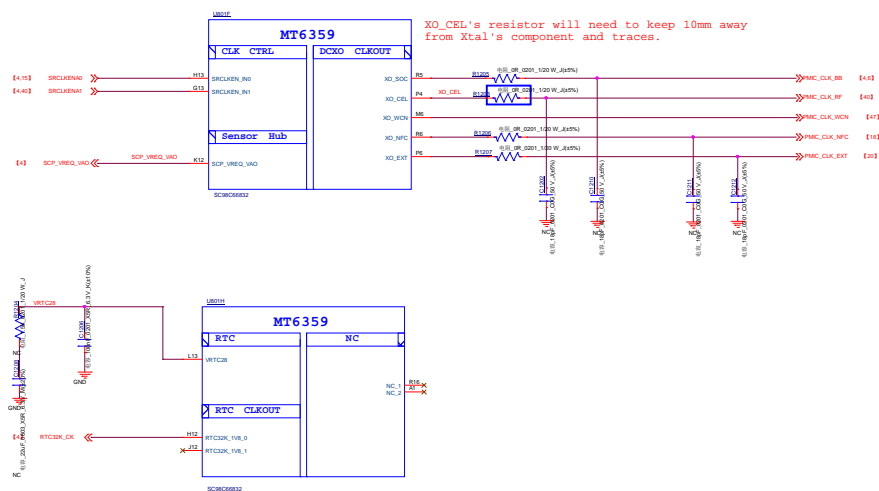
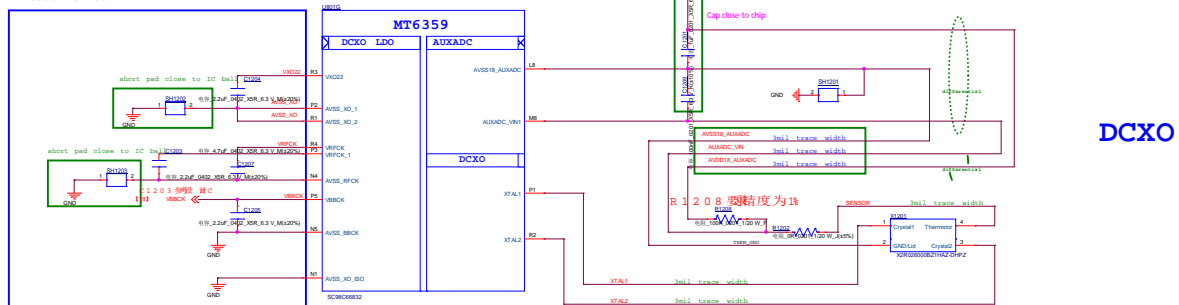
MT6359 LDO output



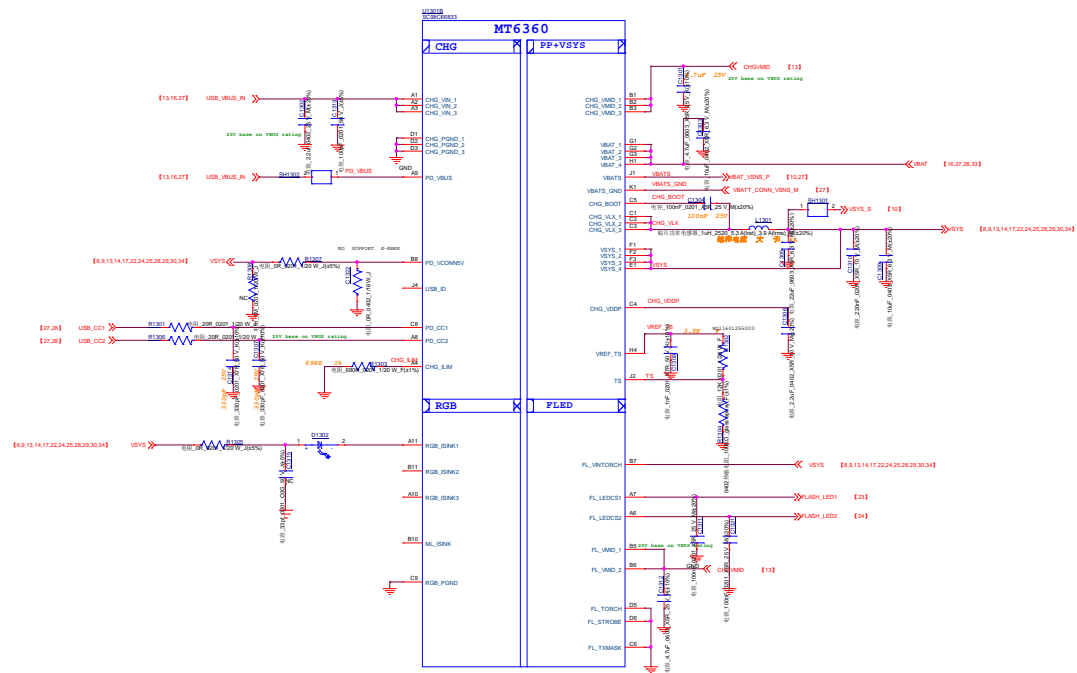
Input Power	Power Name	Output Voltage (V)	Output Current	Default Voltage
LDO from VSYS	VFE28	2.8	200mA	2.8V
	VXO22	2.24	50mA	2.24V
	VCN28	2.8	50mA	2.8V
	VCAMA	1.8/2.5/2.8	200mA	2.8V
	Vaux18	1.84	20mA	1.8V
	VUSB	3.07	200mA	3.07V
	VRIE28	2.8	50mA	2.8V
	VCN31_1	3.3/3.4/3.5/3.6	800mA	3.5V
	VCN33_2	3.3/3.4/3.5/3.6	800mA	3.5V
	VTR	3.3/3.1/3.5/1.8/2.0/2.8/3.0/3.3	200mA	2.8V
	VIO28	2.8/2.85/2.9/2.95/3.3/3.1/3.2/3.3	200mA	2.8V
	VMC	1.86/2.9/3.0/3.3	200mA	3.0V
	VMCH	2.9/3.0/3.3	800mA	3.0V
	VEMC	2.9/3.0/3.3	800mA	3V
	VSIM1	1.7/1.8/1.86/2.76/3.0/3.3	200mA	1.86V
	VSIM2	1.7/1.8/1.86/2.76/3.0/3.3	200mA	1.86V
LDO from VS1	VFUSE	1.7/1.8/1.84	300mA	1.8V
	VADD18	1.8	300mA	1.8V
	VRPCK_1	1.6	40mA	1.6V
	VM18	1.8	300mA	1.8V
	VRP18	1.8	450mA	1.8V
	WMPI	1.7/1.8/1.84	300mA	1.84V
	WC18	1.8	1200mA	1.8V
	VCAMIO	1.8	300mA	1.8V
	VIO18	1.8	600mA	1.8V
	VUPS	1.86	1200mA	1.86V
LDO from VS2	VEF12	1.2	800mA	1.2V
	VA09	0.9	300mA	0.9
	VA12	1.2	300mA	1.2V
	VSRAM_PROC1	0.6-1.2	600mA	0.9V
	VSRAM_OTHERS	0.6-1.2	600mA	0.9V
	VSRAM_MD	0.6-1.2	600mA	0.9V
	VSRAM_PROC2	0.6-1.2	600mA	0.9V



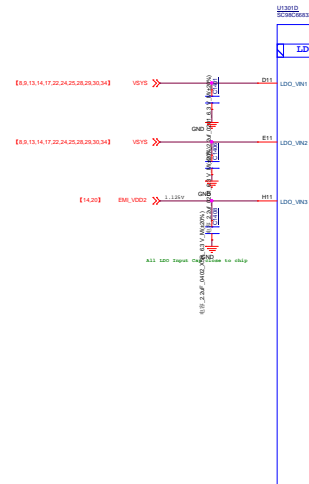
Please follow MMD.



讯 Hua qin Tele com Technol ogy Com. Co., Ltd		
Title POWER_MT6359_Clock		
Size D	Project Hanoi	Rev V1
Date: Wednesday, March 17, 2021	Sheet 12 of 99	



讯 Hua qin Tele com Technol ogy Com.Ltd		
Title POWER_MT6360_Charger_PP		
Size D	Project Hanoi	Rev V1
Date: Wednesday, March 17, 2021	Sheet 13 of 99	

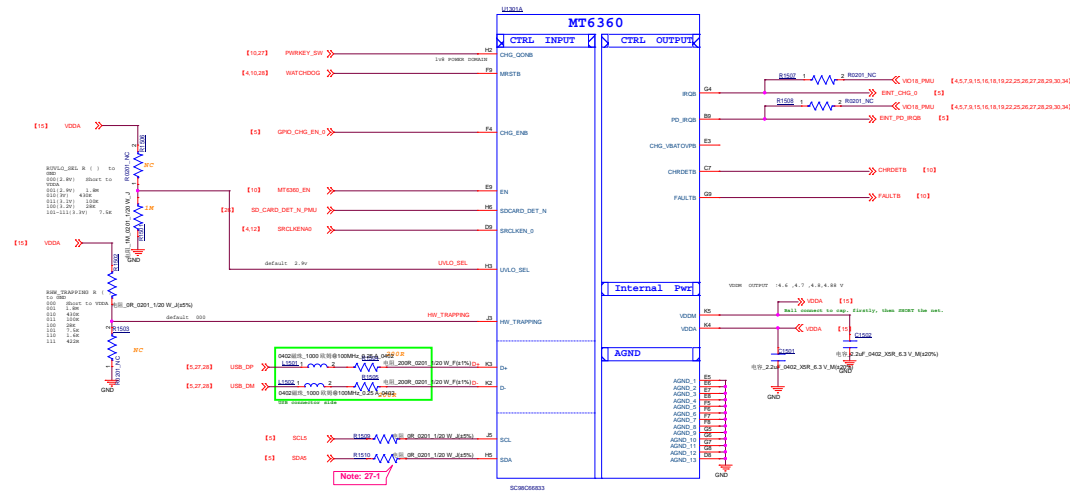


Input Power	Power Name	Output Voltage (V)	Output Current	Default Voltage
BUCK from VSYS	BUCK1 (APU)	0.3-1.3V	3A	0.825V on
	BUCK2 (VDRAM1)	0.3-1.3V	3A	1.125V on

IP	Inductance	Isat-max(A)	Iitem-max(A)
APU	0.33	3.1	2.1
VDRAM1	0.33	4	3

Input Power	Power Name	Output Voltage (V)	Output Current	Default Voltage
LDO from VIN1	LDO1	1.8~3.3	150mA	1.8V OFF
	LDO2	1.8~3.3	200mA	2.8V OFF
	LDO3	1.8~3.3	200mA	3V OFF
	LDO5	2.8~3.3	800mA	3V OFF
LDO from VIN2	LDO6	0.75	300mA	0.75 ON
LDO from VIN3	LDO7	0.6	600mA	0.6 ON

華訊 Hua qin Tele com Technolog y Com. Ltd			
Title POWER_M6360_BUCK_LDO			
Size D	Project Hanoi		Rev V1
Date: Wednesday, March 17, 2021		Sheet 14 of 99	



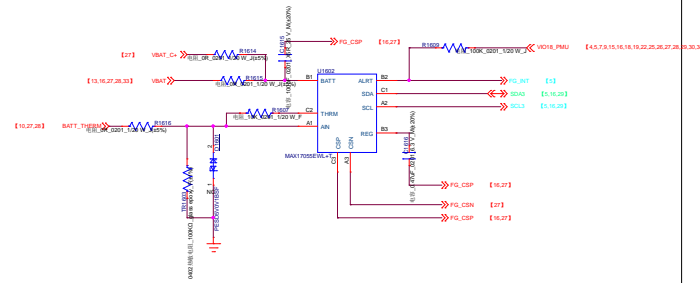
I2C ADDRESS	R/W
PMU	Write:0X68, Read:0X69
PMIC	Write:0X34, Read:0X35
LDO	Write:0XC8, Read:0XC9
USB PD	Write:0X9C, Read:0X9D

Schematic design notice of "27_POWER_MT6360_General" page:

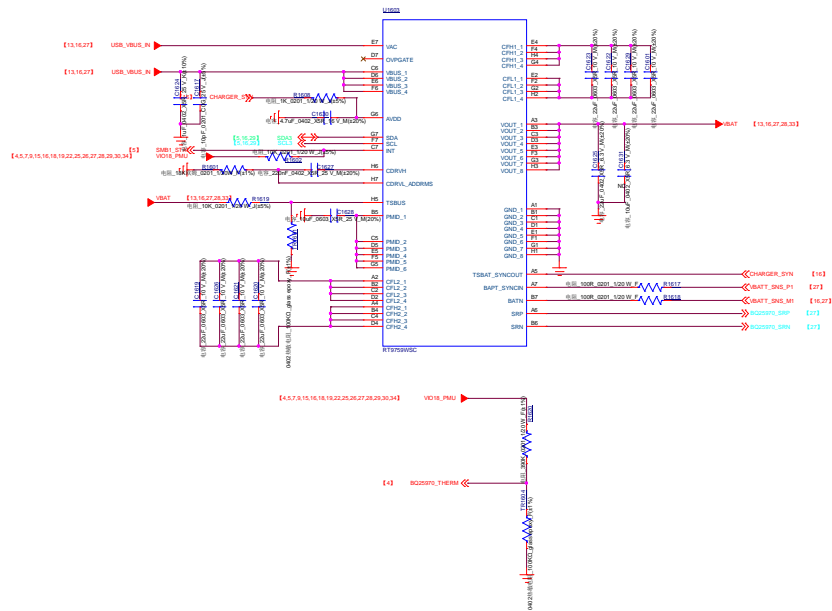
Note 27-1: To optimize driving and PCB routing , it is recommended to reserve R for BOM fine tuning.
The R vaule and rule refer to MT6360 desing notice.

讯 Hua qin Tele com Technol ogy Com. Co., Ltd		
Title POWER_M6360_General		
Size D	Project Hanoi	Rev V1
Date: Wednesday, March 17, 2021	Sheet 15 of 99	

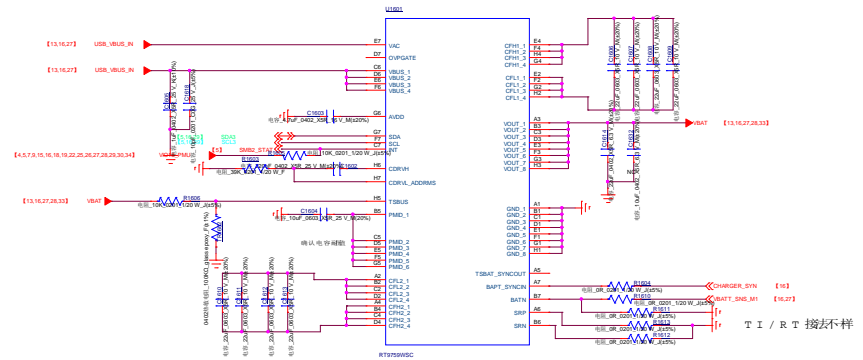
电量计



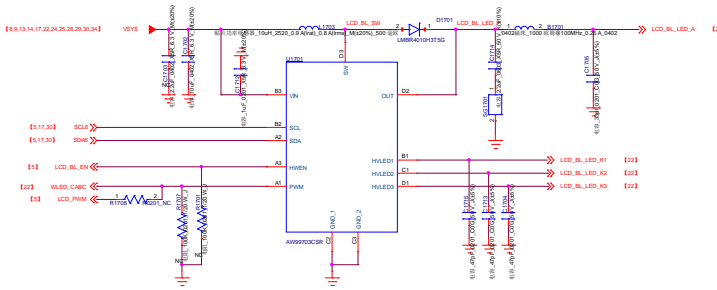
Master



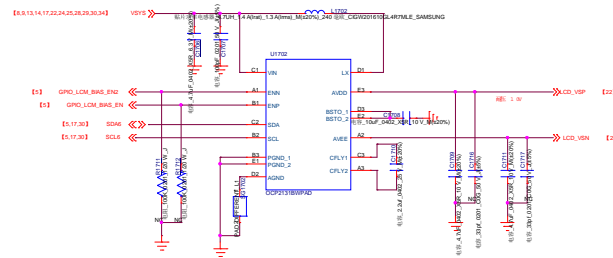
Slave



讯 Hua qin Tele com Technol ogy Com. Ltd		
Title PM7150L LDO		
Size D	Project Hanoi	Rev V1
Date: Wednesday, March 17, 2021 Sheet 16 of 99		

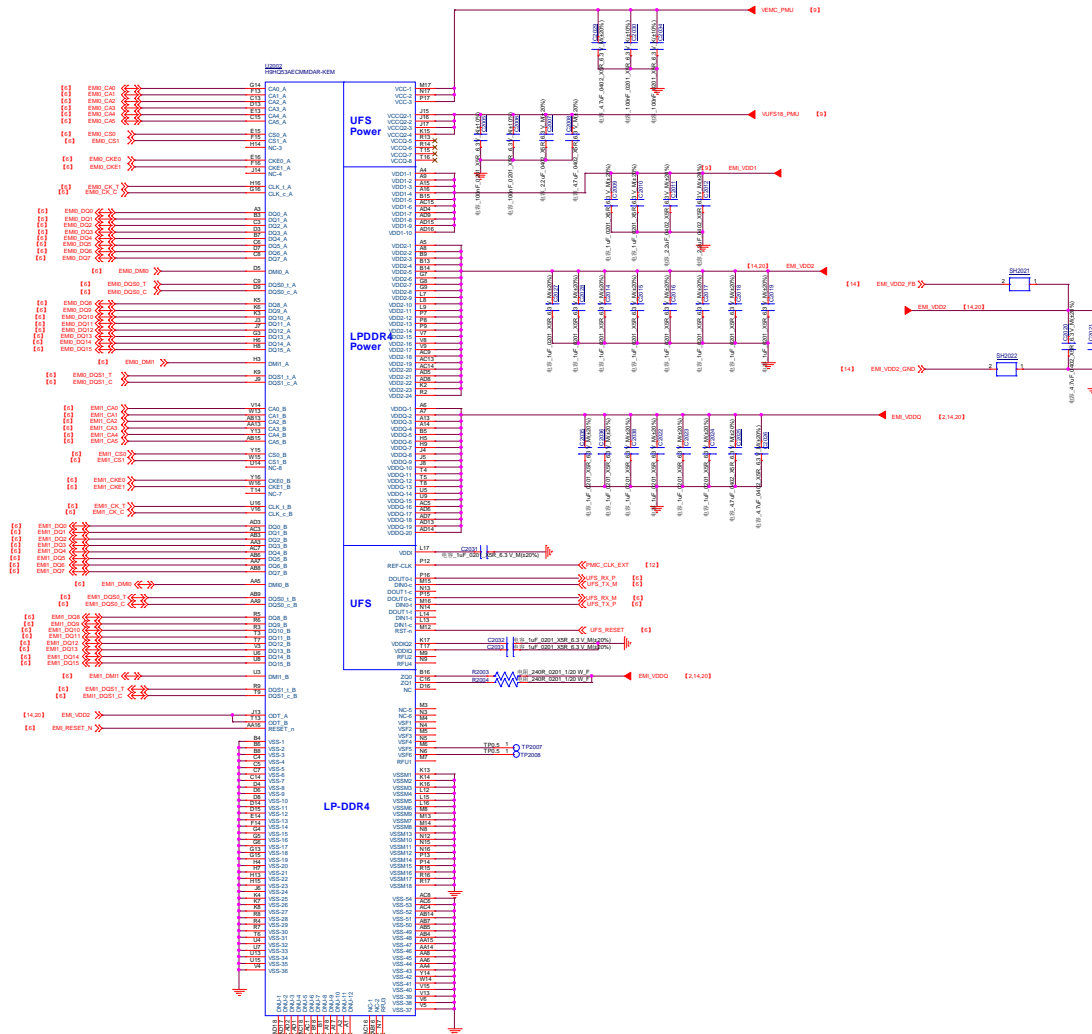


OCP2131 HQ11152662000 /TPS65132 HQ11140016000



I2C Address (R:0x7D;W:0x7C) SM5109

讯 Hua qin Tele com Technol ogy Com. Co., Ltd		
Title LCD Display		
Size D	Project Hanoi	Rev V1
Date: Wednesday, March 17, 2021 Sheet 17 of 99		

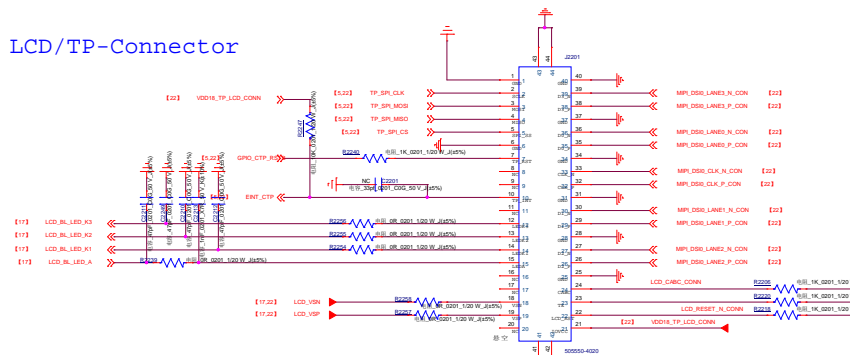


讯 Hua qin Tele com Technol ogy Com. Co., Ltd		
Title uMCP		
Size D	Project Hanoi	Rev V1
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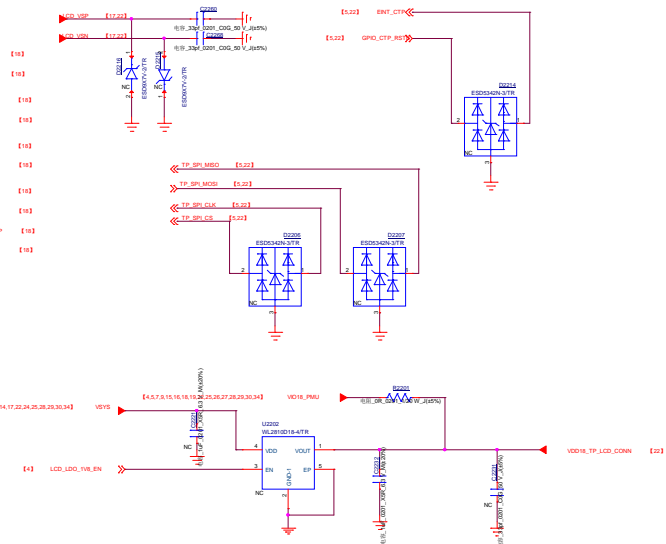
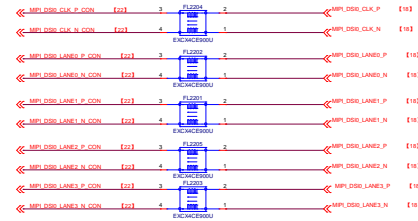
讯 Hua qin Tele com Technol ogy Com.Ltd			
Title BLANK			
Size D	Project Hanoi		Rev V1
Date: Wednesday, March 17, 2021		Sheet 21 of 99	

LCD/TP

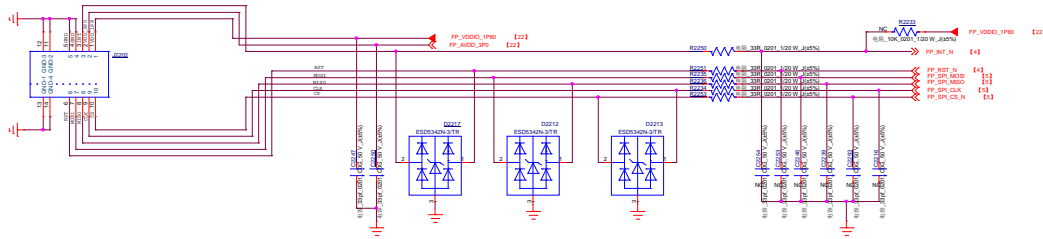
LCD/TP-Connector



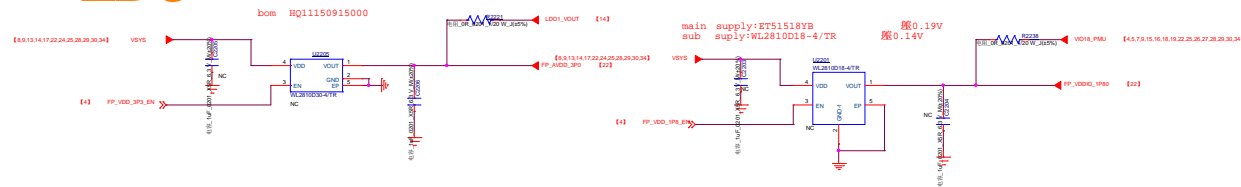
LCD MIPI Common Mode Filter



FPS

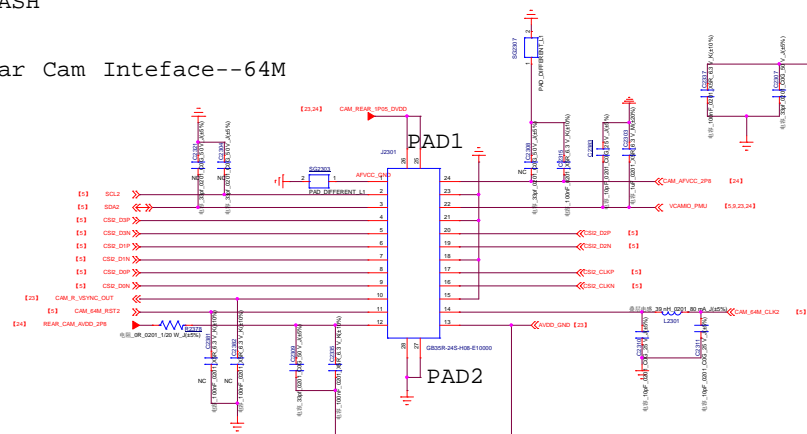


LDO

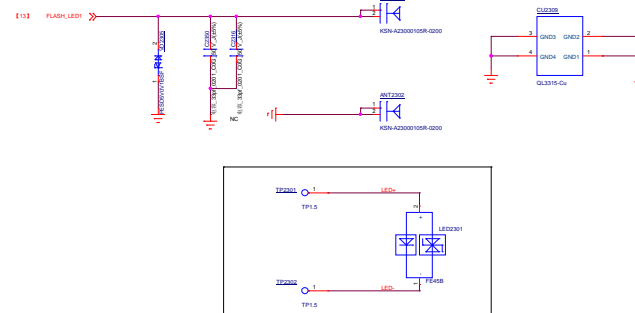


讯 Hua qin Tele com Technol ogy Com.Ltd		
Title LCD/TP/FPS		
Size D	Project Hanoi	Rev V1
Date: Wednesday, March 17, 2021	Sheet 22 of 99	

Rear Cam Interface--64M

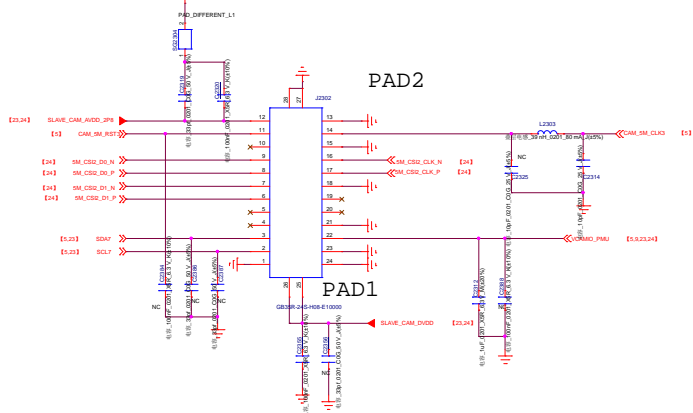


Rear Cam FLASH LED

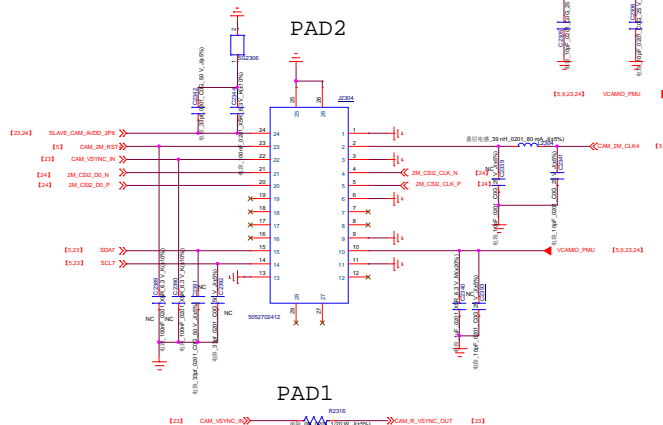


后闪需要放在闪光灯小板上

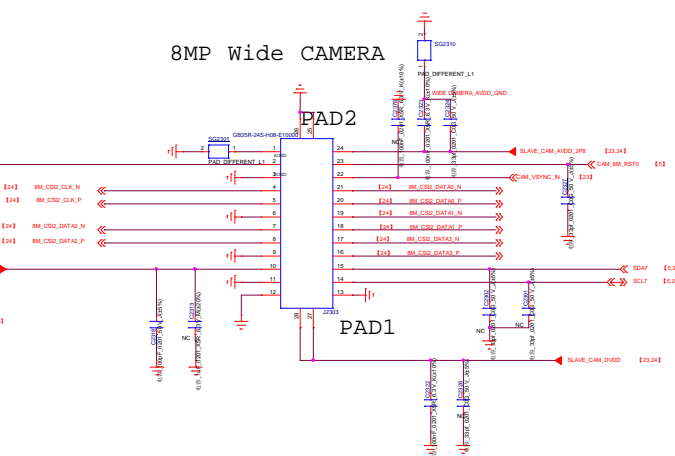
5M Marco Camera



2M Depth Camera



8MP Wide CAMERA



讯 Hua qin Tele com Technol ogy Com. Co., Ltd.

Title REAR CAMERA		
Size D	Project Hanoi	Rev V1
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OV12A10-GASA-Front Cam I2C Adress:0x10 (Write:0x20 Read:0x21)

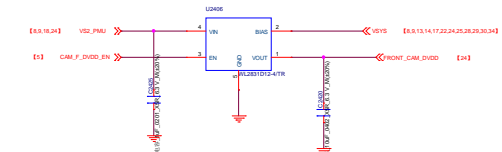
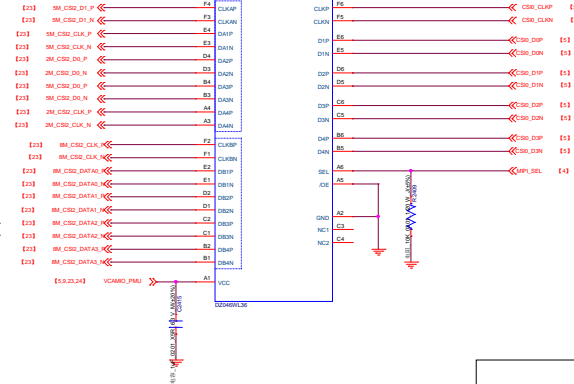
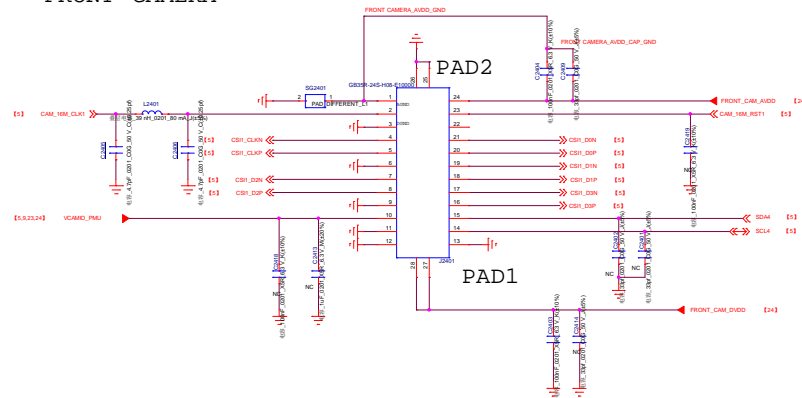
5M CAM

2M CAM

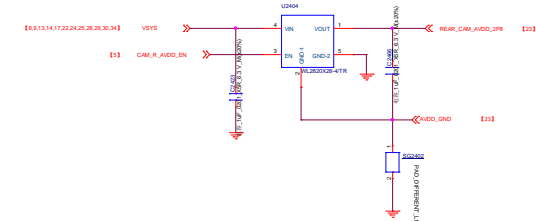
8M CAM

SEL	Control Pin	SEL=0	CLKPIN=CLKAPIN, DnPIN=DnAPIN
		SEL=1	CLKPIN=CLKBPIN, DnPIN=DnBPIN

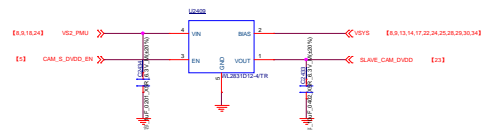
Front Cam FLASH LED



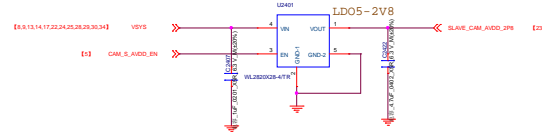
Rear-Main AVDD 2.8V



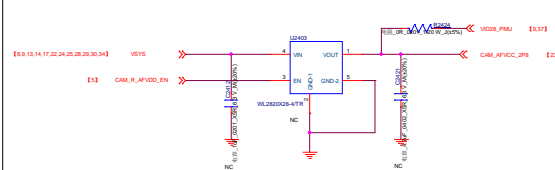
Rear-Slave DVDD 1.2V



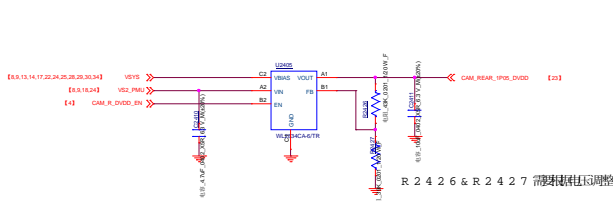
Rear-2M/5M/8M	AVDD	2.8V
---------------	------	------



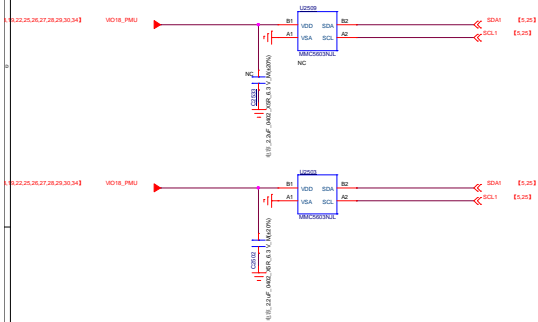
Rear-Main/2M Marco_AFVCC 2.8V



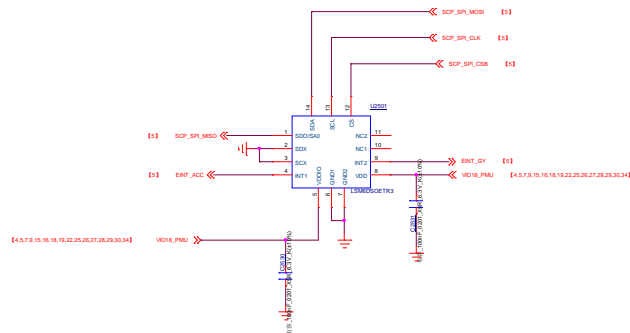
Rear-Main DVDD 1.05V



eCOMPASS

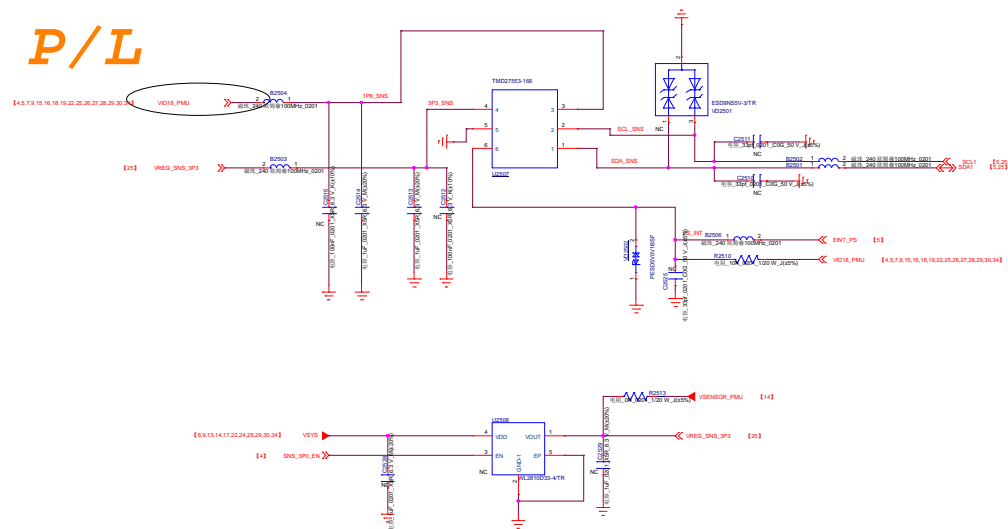


A+G-sensor



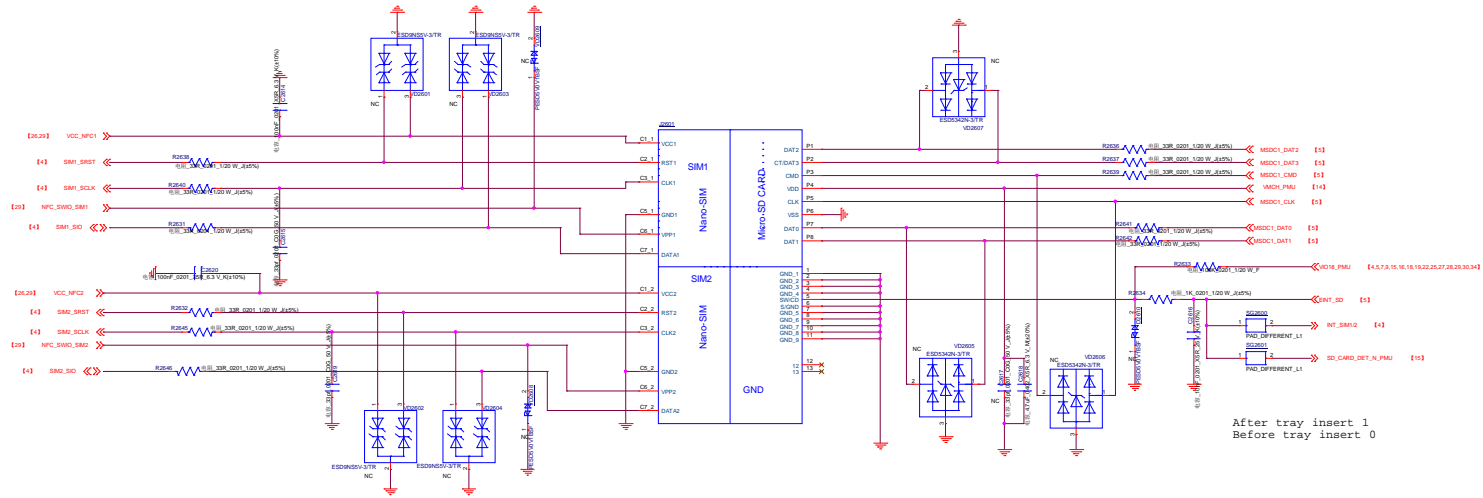
G-sensor

P/L

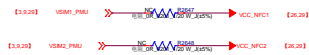


讯 Hua qin Tele com Technol ogy Com. Co., Ltd		
Title Sensor		
Size D	Project Hanoi	Rev V1
Date: Wednesday, March 17, 2021	Sheet 25 of 99	

卡座 T Flash card



无NFG需贴 R2647/R2648



```
After tray insert 1
Before tray insert 0
```

讯 Hua qin Tele com Technol ogy Com.Ltd

Title	SIM/SD
-------	--------

Size	Project	Rev
D	Hanoi	V1

Rev
V1

Date: Wednesday, March 17, 2021 Sheet 26 of 99

$$\text{OVP set } 13.2\text{V} = 1.2 * (1 + R2707/R2708)$$

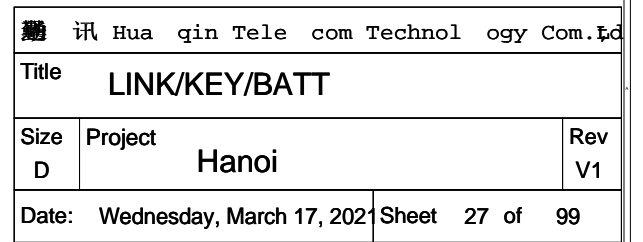
OVP 电压 13.2V



串位电阻 靠近 I C 摆放 TV 靠近弹片摆放



Connector HQ11152462000



接地弹片



QL3315_buqiang_BOT

1 [0242811](#)

1

わ た

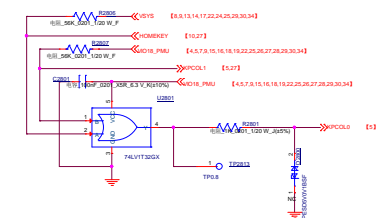
PCBA AUTOMATIC TEST point




The schematic diagram illustrates the proposed system. It features a power supply (Vcc) connected to a microcontroller (MCU) and a relay. The MCU is connected to the relay and a sensor (S1). The sensor is connected to the relay and a load (L1). The relay is connected to the load and a ground (GND). The load is connected to the ground and a power supply (Vcc). The ground is connected to the power supply (Vcc).

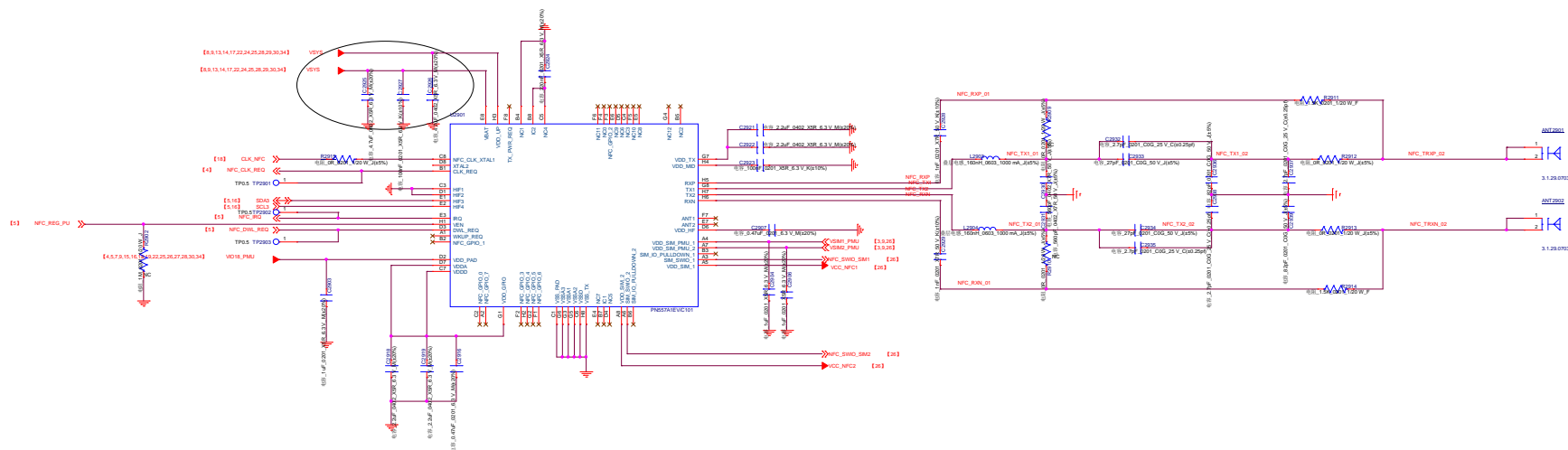
enter 9008 mode

Force USB boot



 Hanoi University of Science and Technology		
Title Test Points/Shield		
Size D	Project Hanoi	Rev V1
Date: Wednesday, March 17, 2021		Sheet 28 of 99

PN557 I2C Address:0x28 (Write:0x50 Read:0x51)



讯 Hua qin Tele com Technol ogy Com.Ltd		
Title NFC		
Size D	Project Hanoi	Rev V1
Date: Wednesday, March 17, 2021 Sheet 29 of 99		

5

4

3

2

1

D

D

C

C

B

B

A

A

5

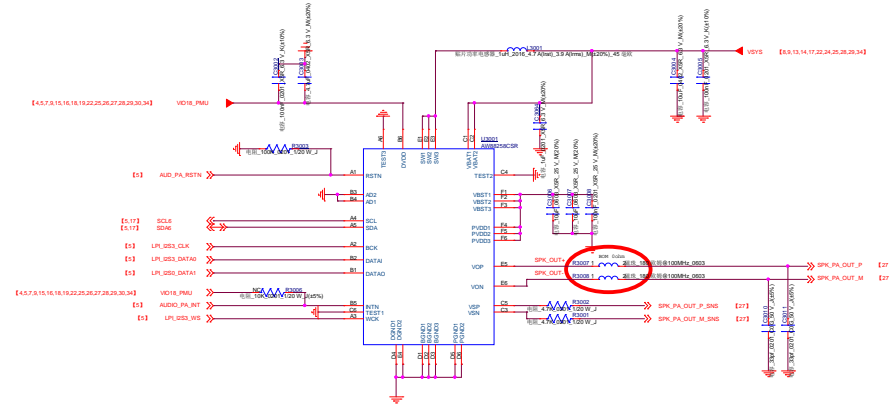
4

3

2

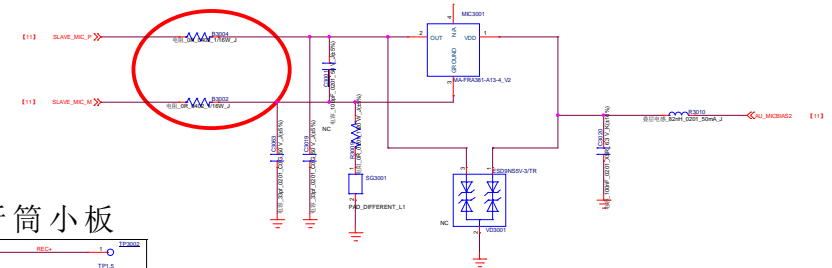
1

SmartPA

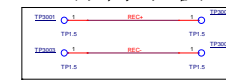


AW88258 的 IIC 地址图上将 A 和 0 都接地地址为 0x4

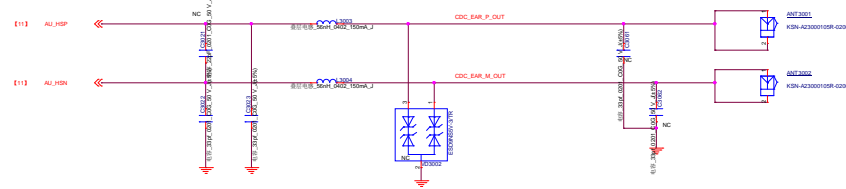
Slave MIC



听筒小板

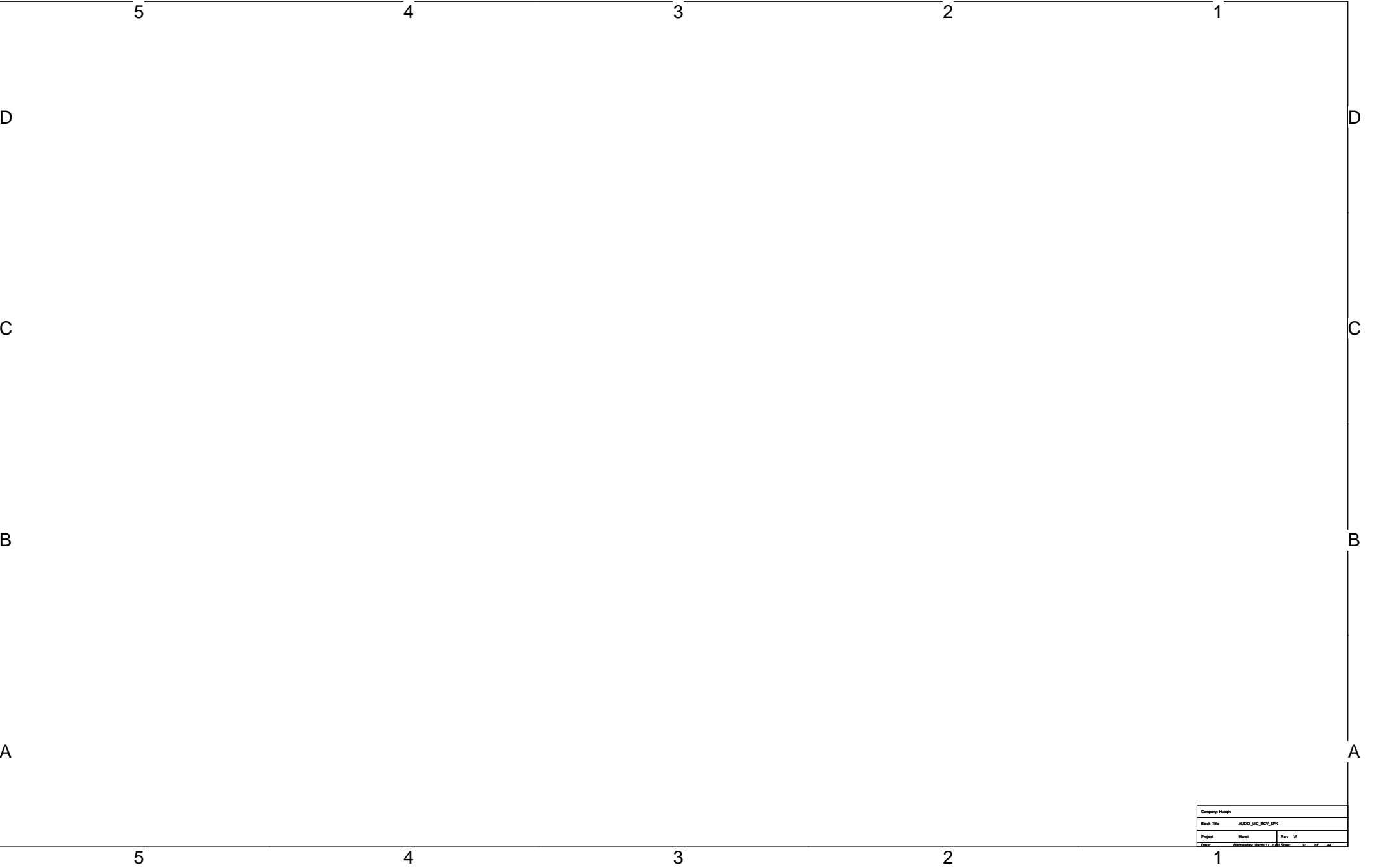


RECIVER



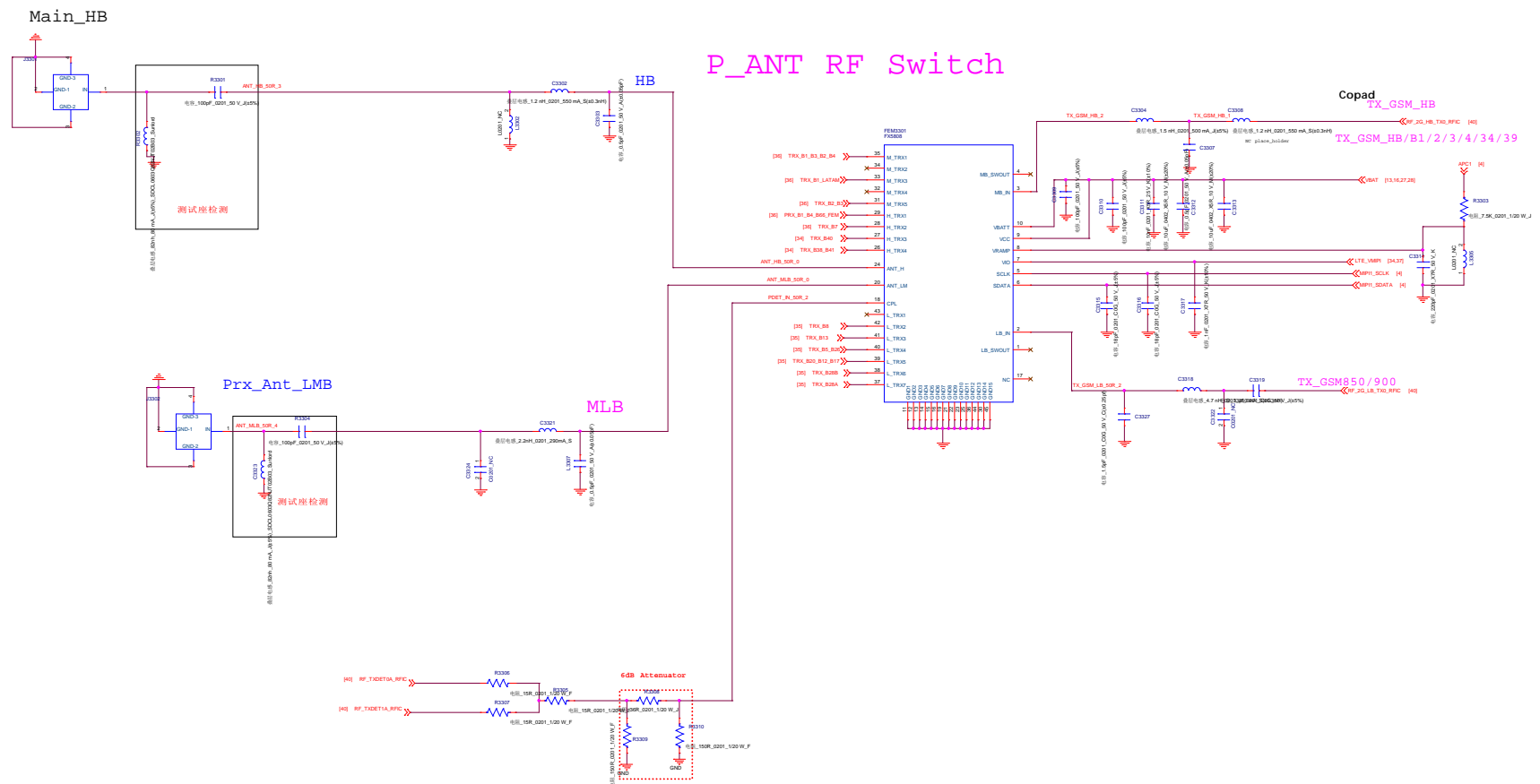
Company: Hasep		
Blank Title		
Project	Rev	Ver
Date	Version	Rev

讯 Hua qin Tele com Technol ogy Com.Ld		
Title Hanoi		
Size D	Project	Rev v1
Date: Wednesday, March 17, 2021	Sheet 31 of 44	



Company: Harsco		
Blank Title		
Project: Harsco		
Date: Wednesday, March 17, 2010 10:30 AM		

PRIMARY_ANTENNA



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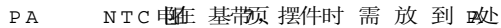


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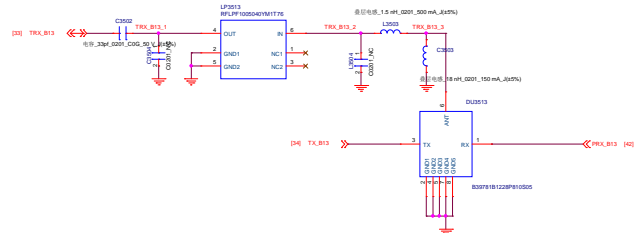
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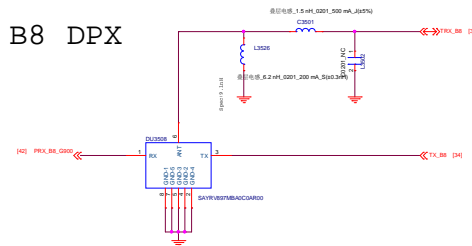
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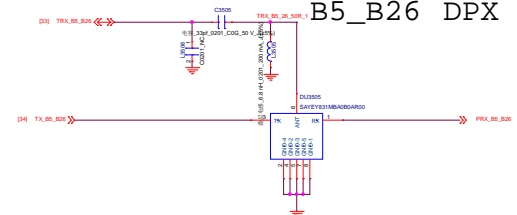
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B8 DPX

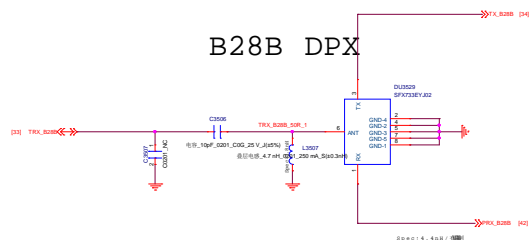


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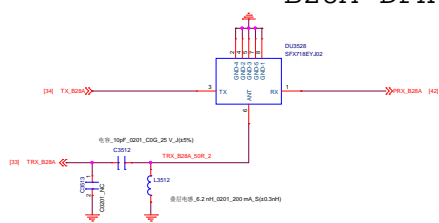


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B5 DPX	SFX836EYJ02	wisol	LATAM&Brazil

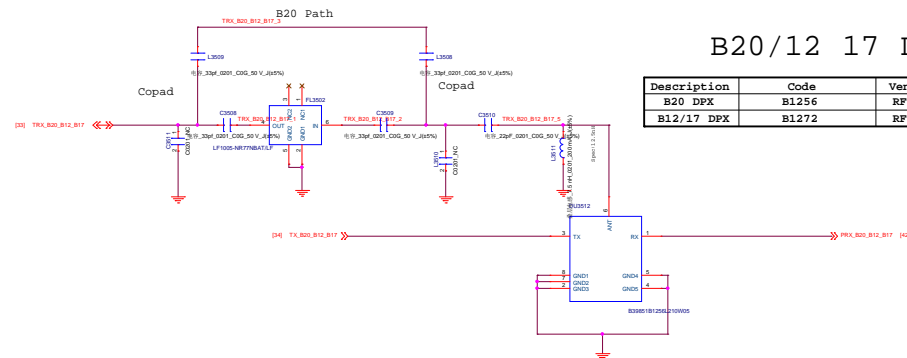
B28B DPX



B28A DPX



B20/12 17 DPX



Description	Code	Vendor	SKU
B20 DPX	B1256	RF360	Europe+APEM
B12/17 DPX	B1272	RF360	LATAM&Brazil

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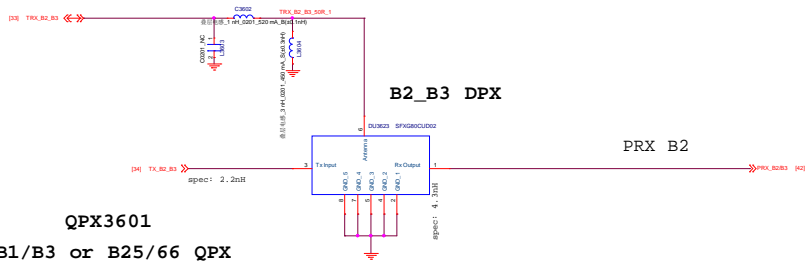
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Rev: 1.1

Date: Wednesday, March 17, 2016

DU3602

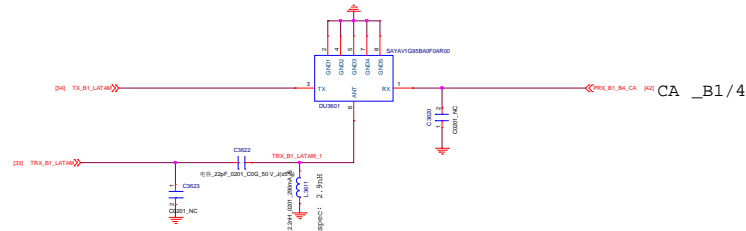
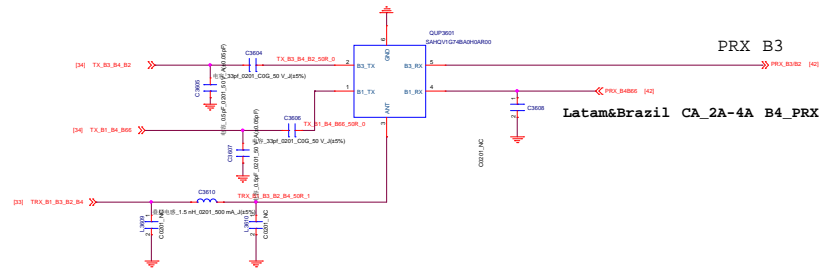
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B2 Duplexer	SFXG80CUD02	wiso1	Europe+JANZ+CA
B3 Duplexer(1814)	SAYEY1G74BC0B0A	MURATA	Latam+CA



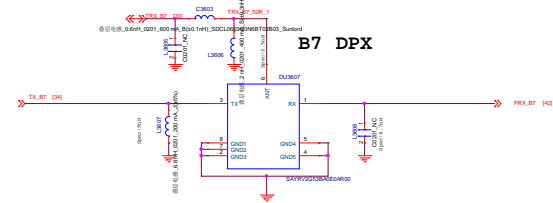
QPX3601

B1/B3 or B25/66 QPX

Description	Code	Vendor	SKU
B1/3 Quplexer	SAHQV1G74BA0H0AR00	MURATA	Europe+JANZ+CA
B25+66 Quplexer	B8932	RF360	Latam+CA

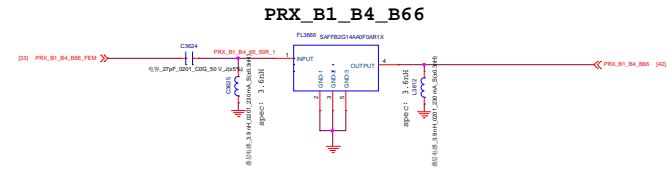


B1 DPX_LATAM_Only_(1612)



FL3604

Description	Code	Vendor	SKU
B1 PRX SAW	SAFFB2G14AA0F0A	MURATA	Europe+JANZ+CA
B66 PRX SAW	SAFFB2G15AA0F0A	MURATA	Latam+CA



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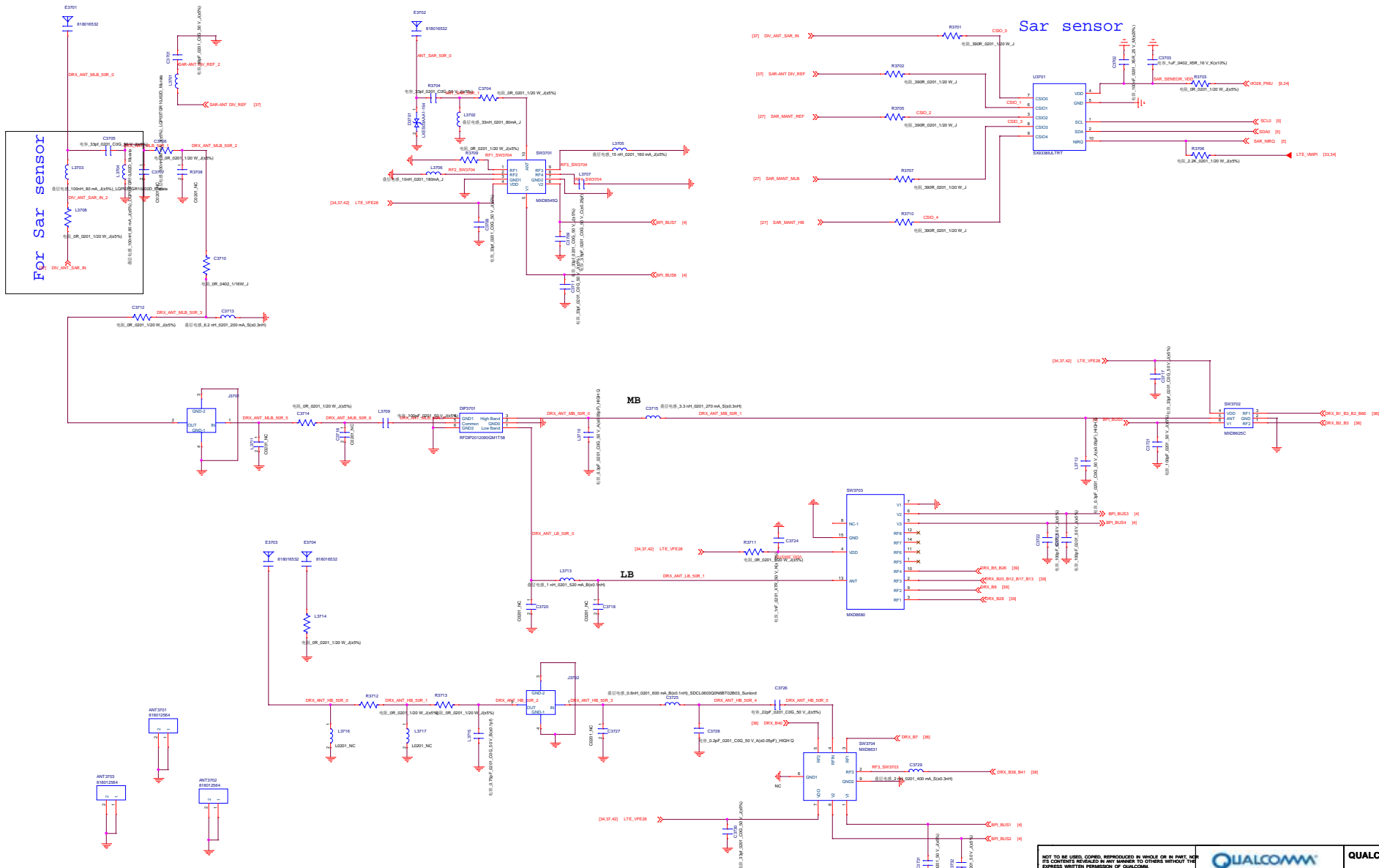
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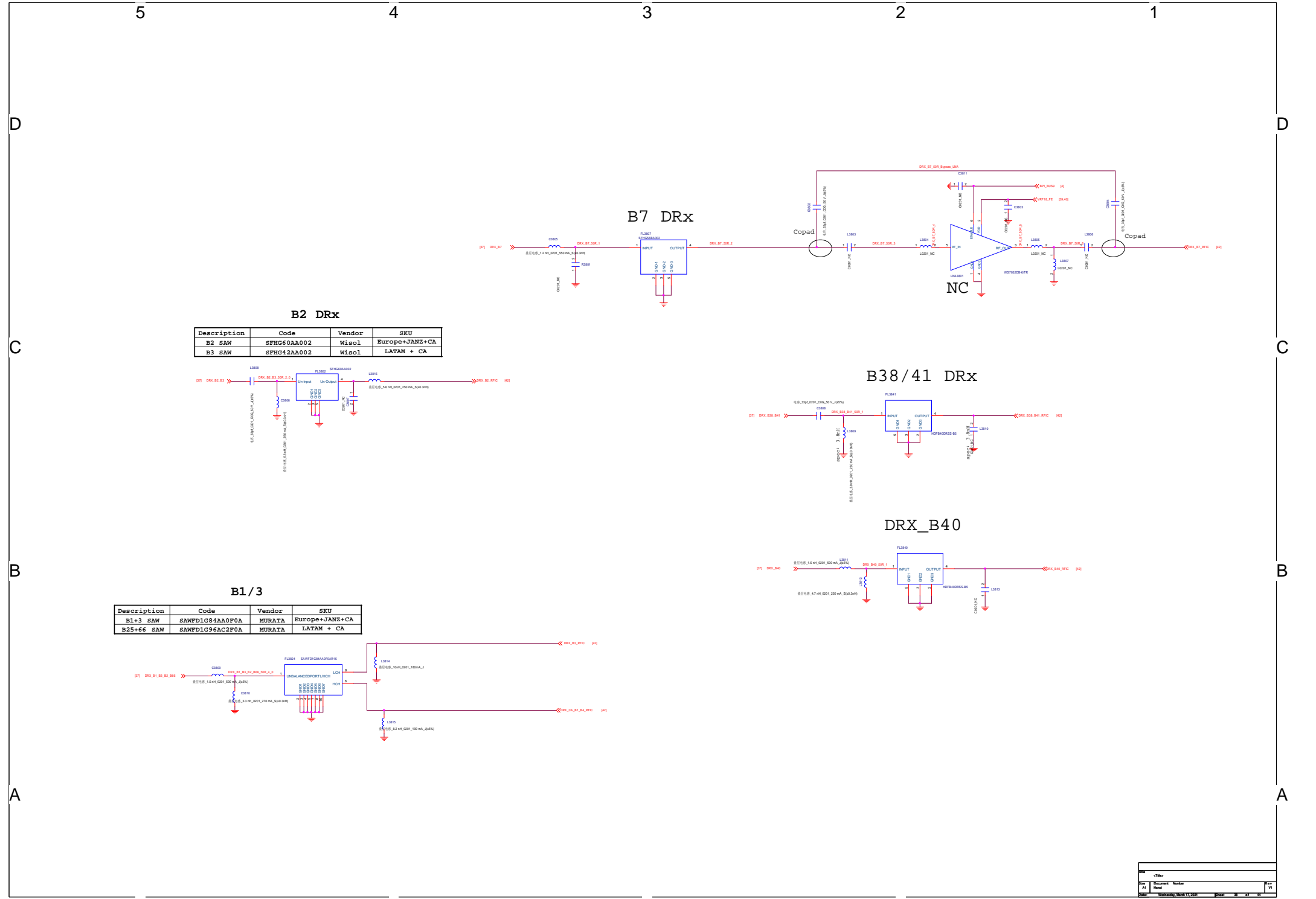
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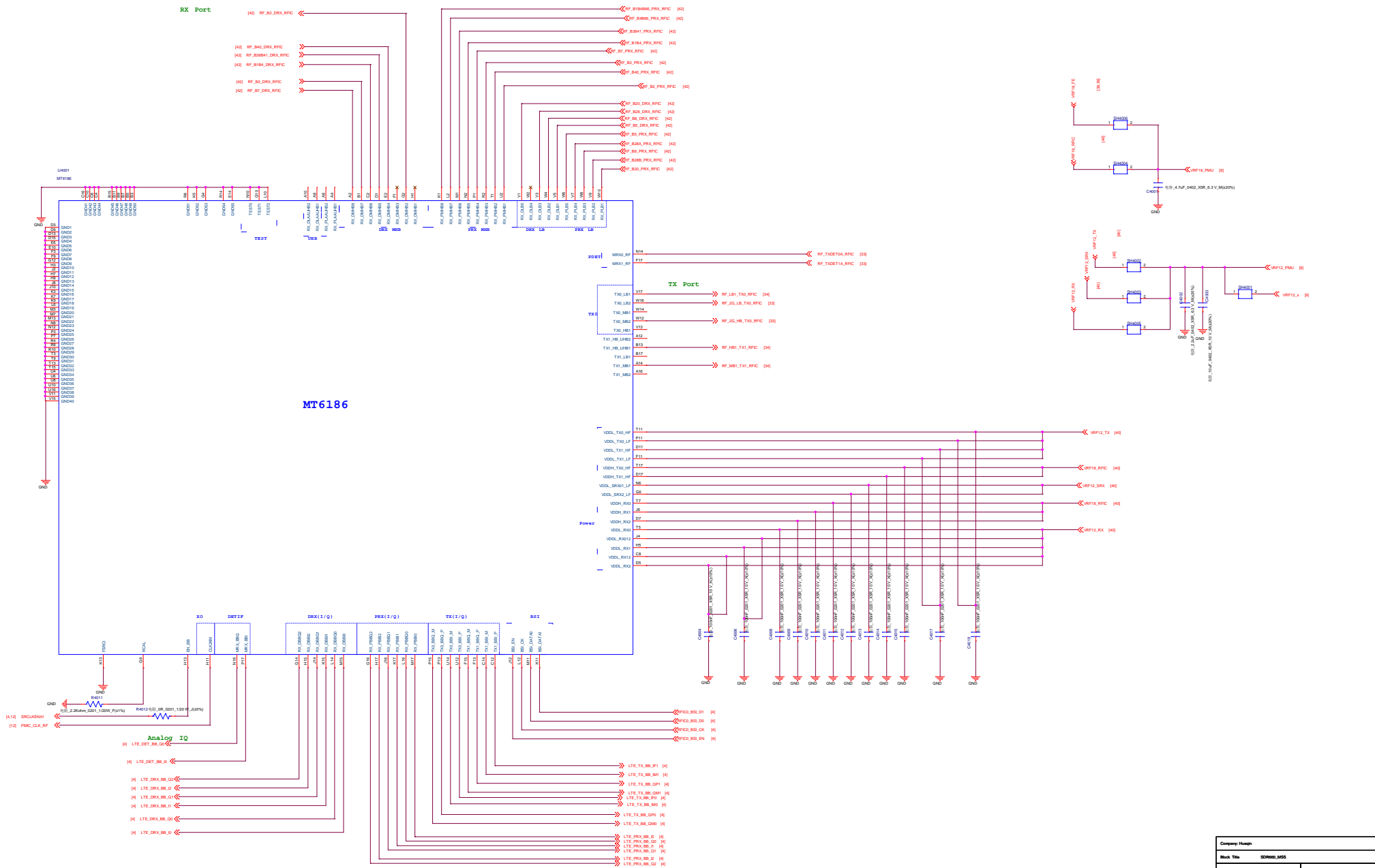
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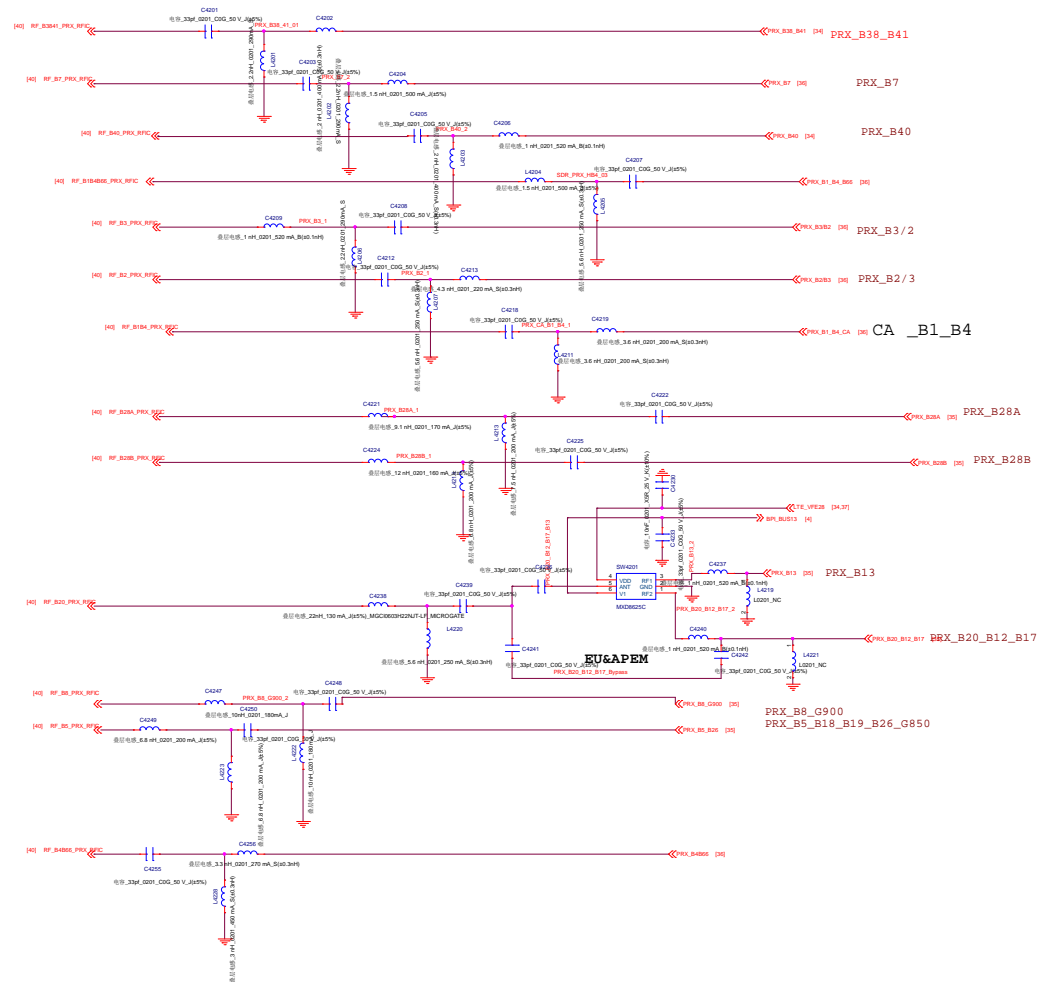
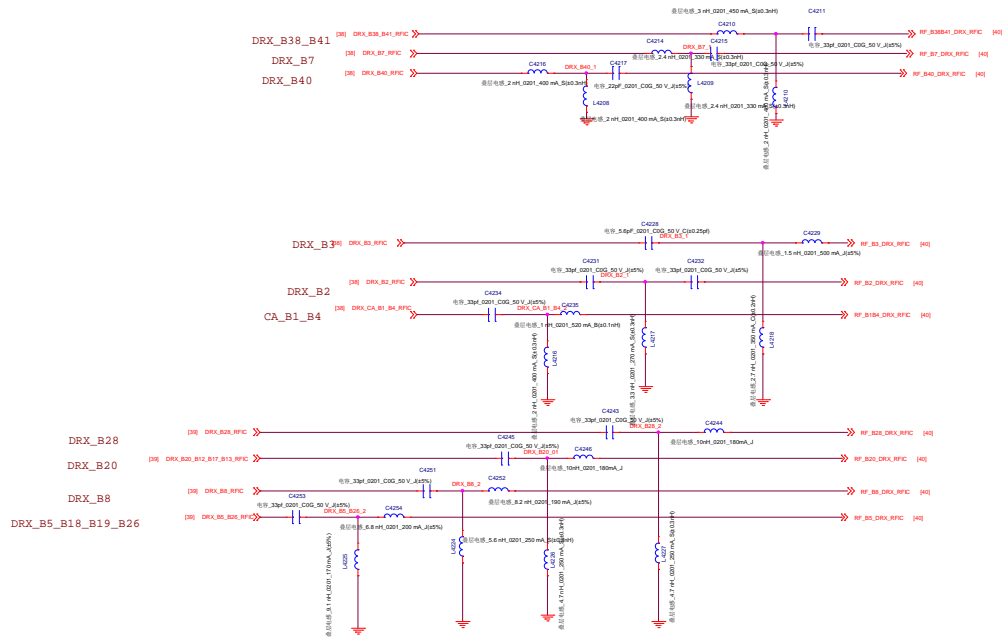




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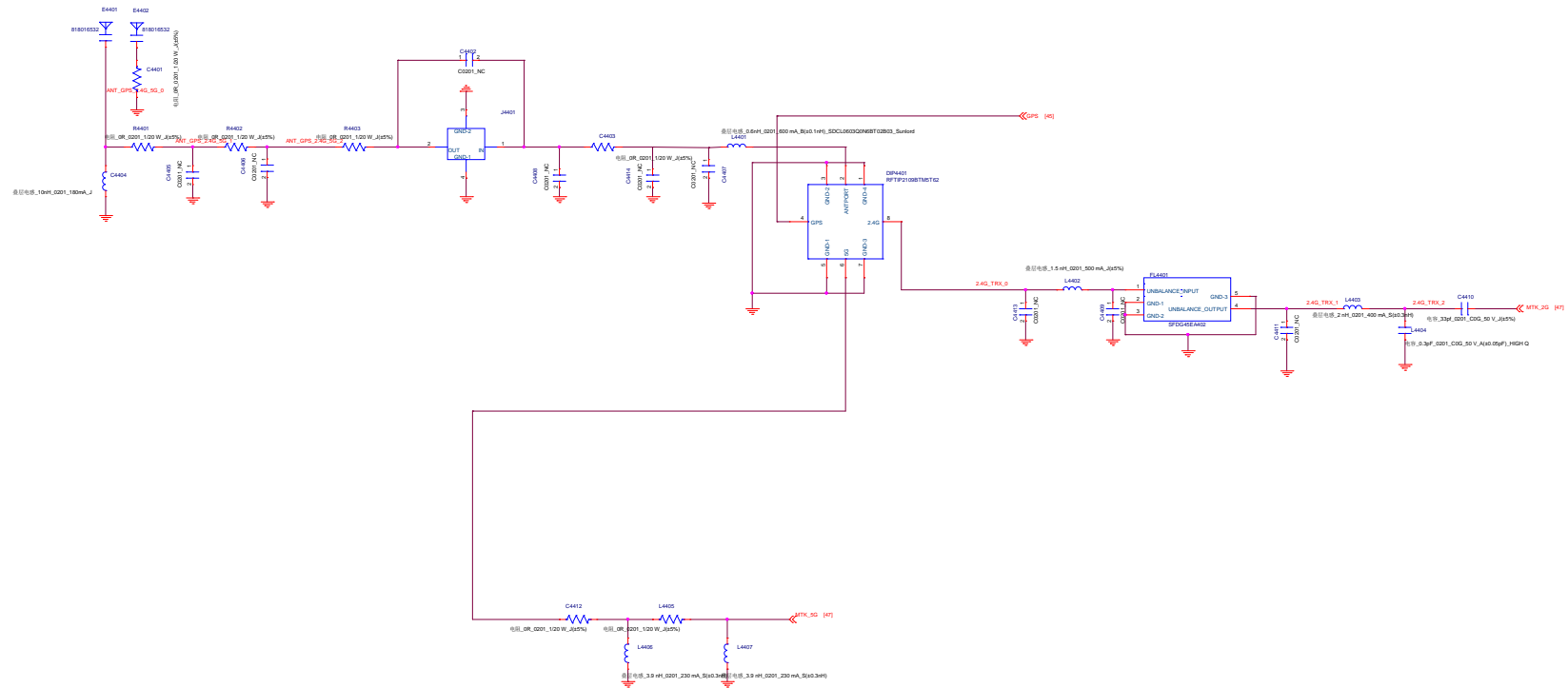
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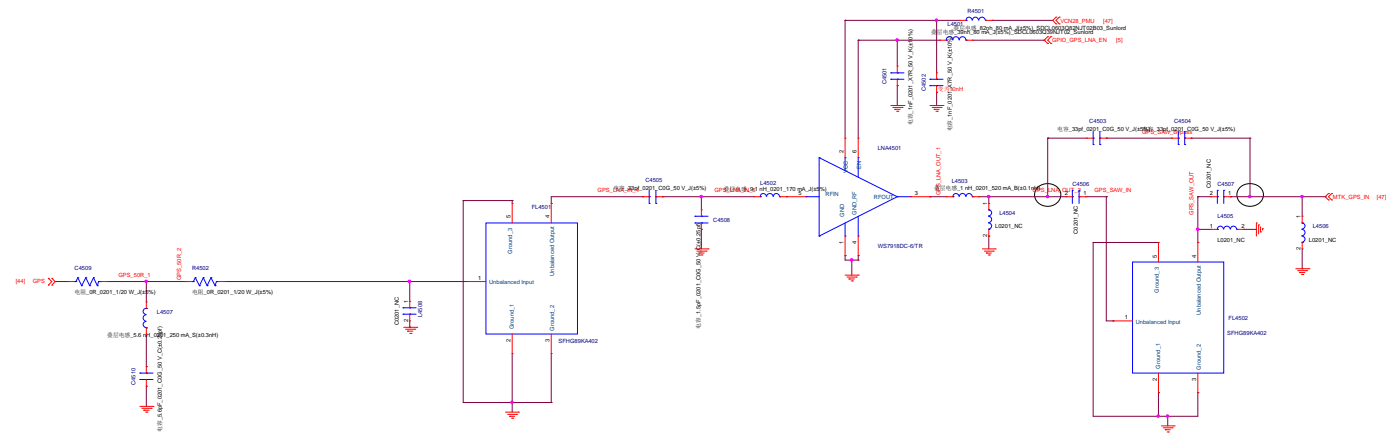
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Project			
Rev			

WIFI 2.4G_BT_5G_GPS



Company Name			
Block Title			
Project Name			
Rev			
Date			

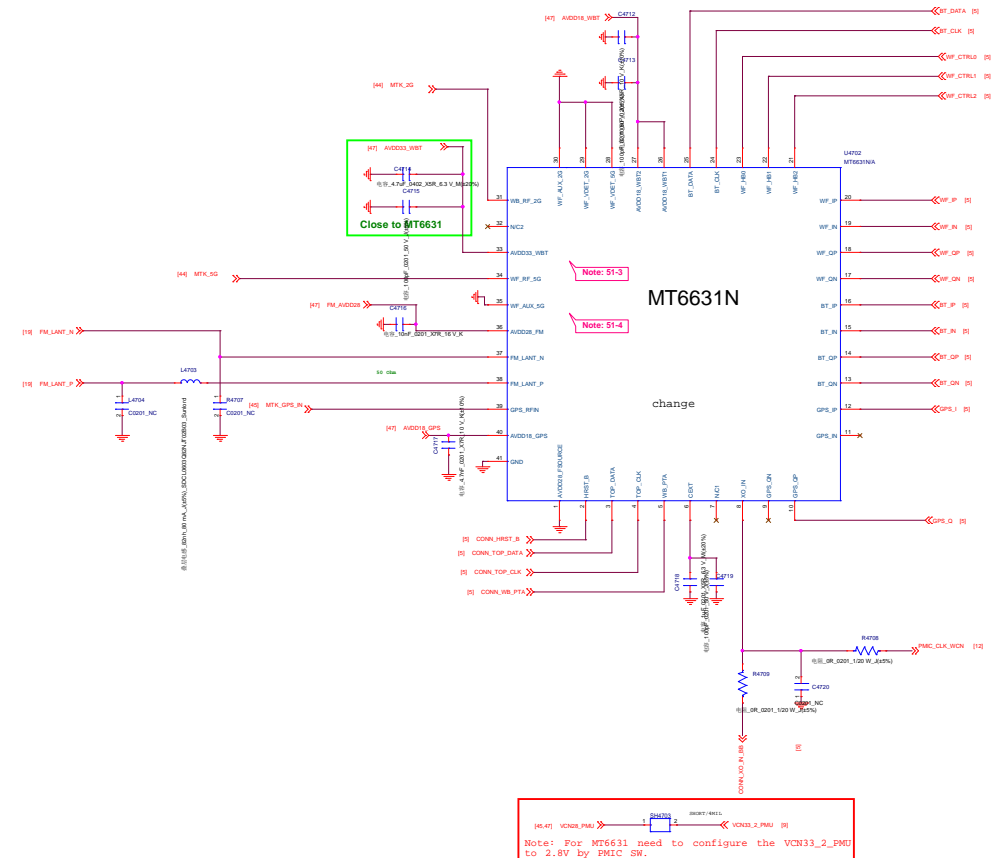
GPS



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Block Title: WCNC880	
Project: Hanzl	Rev: V1
Date: Wednesday, March 17, 2010	Sheet: 45 of 44

Company: Huang		
Block Title: WCH3080		
Project: Hanoi	Rev: V1	
Date: Wednesday, March 17, 2010	Sheet: 46	of: 44

WIFI2.4G/5G_ANT



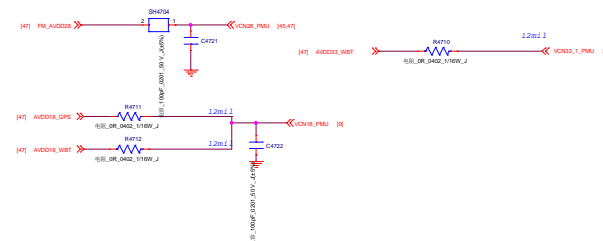
Schematic design notice of "51_CONNECTIVITY_CONSYS_MT6631"

Note 51-1: For R5015 size, please select 0402 size or larger one

Note 51-2: Please refer to MT6765 Baseband design notice for VCN33 LDO selection guide

Note 51-3: If WiFi 5G not support, connect pin 34(WF_RF_5G) to GND

Note 51-4: Pin 36 (AVDD28_FM) must be connected to VCN28 even if FM not support



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Title Hanoi		
Size D	Project	Rev v1
Date: Wednesday, March 17, 2021	Sheet 47 of 44	