

Company Name			
Block Title	Block Diagram		
Project	Baron M1	Rev	1.0
Date	2016-05-26-2016	Sheet	1 of 16

I2C Configuration Table

Company Name		
Block Title I2C Configuration Table		
Project	Bonus NA	Rev 1.0
Date	Friday, May 26, 2006	Sheet 3 of 46

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Company Name		
Block Title	Table of Content	
Project	Business NA	Rev: 1.0
Date	Friday, May 26, 2023	Sheet 9 of 46

SM6115 GPIO Configuration

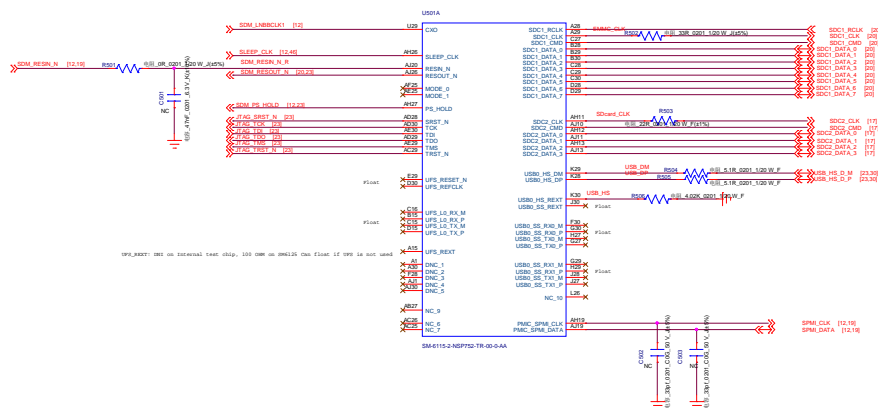
GPIO_0	LCD_SPI_MISO	GPIO_42	Sar_LDO_EN	GPIO_84	HAC_PA_EN
GPIO_1	LCD_SPI_MOSI	GPIO_43	GRFC1_ANT_SEL1	GPIO_85	HW_ID_2
GPIO_2	LCD_SPI_CLK	GPIO_44	GRFC1_ANT_SEL2	GPIO_86	NFC_CLK_REQ
GPIO_3	LCD_SPI_CS	GPIO_45	GRFC_ANT_SEL_SDR_2	GPIO_87	WCN_SW_CTRL
GPIO_4	NFC_I2C_SDA	GPIO_46	GRFC3_ANT_SEL3	GPIO_88	SD_SIM_DET
GPIO_5	NFC_I2C_SCL	GPIO_47	GRFC_ANT_SEL_SDR_1	GPIO_89	USB_SS_SEL
GPIO_6	TP_I2C_SDA	GPIO_48	WDOG_DISABLE	GPIO_90	MSS_LTE_COXM_TXD
GPIO_7	TP_I2C_SCL	GPIO_49	GRFC9_ANT_SEL9	GPIO_91	MSS_LTE_COXM_RXD
GPIO_8	WCN_UART_CTS_N	GPIO_50	LNA_EN_B2/25_DRX	GPIO_92	WCD9370_RST_N
GPIO_9	WCN_UART_RFR_N	GPIO_51	LNA_EN_B12	GPIO_93	CAM_R_DVDD_EN
GPIO_10	WCN_UART_TX_N	GPIO_52	LNA_EN_B5	GPIO_94	FORCED_USB_BOOT_POL_SEL
GPIO_11	WCN_UART_RX_N	GPIO_53	NC	GPIO_95	FORCED_USB_BOOT
GPIO_12	UART_TX_N	GPIO_54	NC	GPIO_96	CAM_LDOEN_V18
GPIO_13	UART_RX_N	GPIO_55	RFFE2_DATA	GPIO_97	FP_INT_N
GPIO_14	FP_SPI_MISO	GPIO_56	RFFE2_CLK	GPIO_98	LPI_I282_CLK
GPIO_15	FP_SPI_MOSI	GPIO_57	RFFE3_DATA	GPIO_99	LPI_I282_WS
GPIO_16	FP_SPI_CLK	GPIO_58	RFFE3_CLK	GPIO_100	LPI_I282_DATA0
GPIO_17	FP_SPI_CS_N	GPIO_59	NC	GPIO_101	LPI_I282_DATA1
GPIO_18	CAM_R_RST	GPIO_60	NC	GPIO_102	FP_LDOEN_3V0
GPIO_19	CAM_F_RST	GPIO_61	RFFE5_DATA	GPIO_103	ANT_MATCH_LM
GPIO_20	CAM_R_MCLK	GPIO_62	RFFE5_CLK	GPIO_104	FP_RST_N
GPIO_21	CAM_F_MCLK	GPIO_63	EXT_GPS_LNA_EN0	GPIO_105	FP_LDOEN_V18
GPIO_22	CAM_I2C_SDA0	GPIO_64	GSM0_TX_PHASE_D	GPIO_106	AUDIO_PA_INT
GPIO_23	CAM_I2C_SCL0	GPIO_65	CAM_2M_RST	GPIO_107	ANT_MATCH_H
GPIO_24	CAM_5M_RST	GPIO_66	LCD_LDO_V18_EN	GPIO_108	AUDIO_sm_PA_EN
GPIO_25	CAM_R_AVDD_EN	GPIO_67	PS_BMP380_INT	GPIO_109	SENSOR_I2C_SDA
GPIO_26	CAM_R_AVDD_EN	GPIO_68	SNS_3P0_EN	GPIO_110	SENSOR_I2C_SCL
GPIO_27	CAM_5M_MCLK	GPIO_69	NFC_REG_PU	GPIO_111	CAM_S_AVDD_EN
GPIO_28	CAM_2M_MCLK	GPIO_70	NFC_IRQ	GPIO_112	Google key
GPIO_29	CAM_I2C_SDA1	GPIO_71	TP_RST_N	LPI_GPIO_0	SWR_TX_CLK
GPIO_30	CAM_I2C_SCL1	GPIO_72	UIM2_DATA	LPI_GPIO_1	SWR_TX_DATA0
GPIO_31	NFC_DWL_REQ	GPIO_73	UIM2_CLK	LPI_GPIO_2	SWR_TX_DATA1
GPIO_32	A_GYRO_INT2	GPIO_74	UIM2_RESET	LPI_GPIO_3	SWR_RX_CLK
GPIO_33	A_GYRO_INT1	GPIO_75	SD_SIM_DET	LPI_GPIO_4	SWR_RX_DATA0
GPIO_34	SAR_INT	GPIO_76	UIM1_DATA	LPI_GPIO_5	SWR_RX_DATA1
GPIO_35	ALSP_INT_N	GPIO_77	UIM1_CLK	LPI_GPIO_14	QCA_SB_CLK
GPIO_36	CAM_S_DVDD_EN	GPIO_78	UIM1_RESET	LPI_GPIO_15	QCA_SB_DATA
GPIO_37	GRFC11_ANT_SEL13	GPIO_79	SD_SIM_DET	LPI_GPIO_21	GSSENSOR_I3C_SDA
GPIO_38	GRFC8_ANT_SEL8	GPIO_80	TP_INT_N	LPI_GPIO_22	GSSENSOR_I3C_SCL
GPIO_39	GRFC_B12_B13_1	GPIO_81	LCD_TE	LPI_GPIO_25	LPI_UART_1_TX
GPIO_40	W_GRFC_2	GPIO_82	LCD_RST_N	LPI_GPIO_26	LPI_UART_1_RX
GPIO_41	LNA_EN_B4/66	GPIO_83	HW_ID_1		

[illegible]

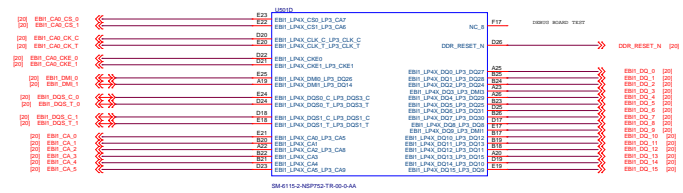
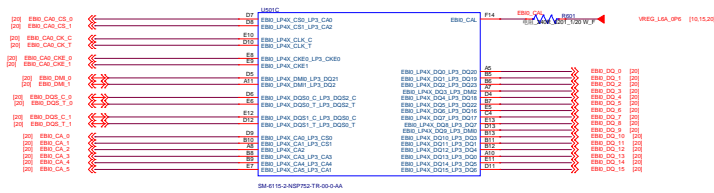
GPIO_1	NC	GPIO_8	WLED_CABC_PM
GPIO_2	NC	GPIO_9	
GPIO_3	CAM_THERM		
GPIO_4	CPU_THERM		
GPIO_5	KEY_VOL_UP		
GPIO_6	LCD_LDO_1V8_EN		
GPIO_7	NC		

GPIO_1	NC	GPIO_8	NC
GPIO_2	BL_ISET_CONTR		
GPIO_3	PMI632_THERM		
GPIO_4	NC		
GPIO_5	NC		
GPIO_6	LCD_BL_EN		
GPIO_7	NC		

Company: Huzhin		
Block Title	GPIO MAP	
Project	Borneo NA	Rev 1.0

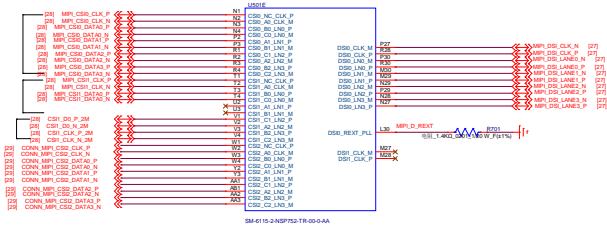


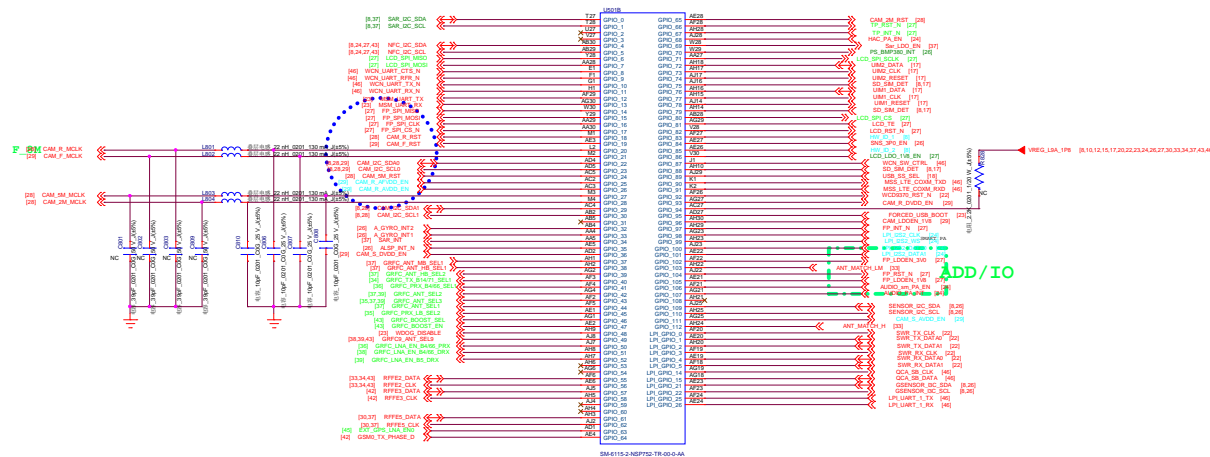
Company: Huzhin		
Block Title	SME115 ControlUPS/SDC1/SDC2	
Project	Borneo NA	Rev 1.0



Company Name		
Black Title		
Project		
Date		
Rev		

REAR_CAM
2M_CAM_MACRO
2M_CAM_DEP
FRONT CAMERA



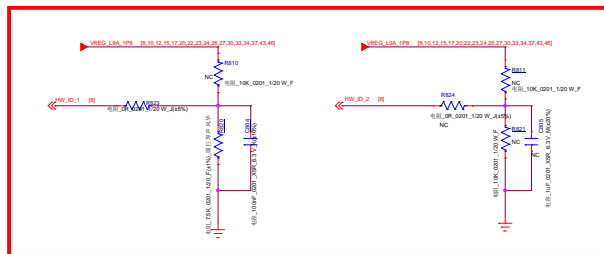


BOOT Config GPIOs	Functionality
GPIO_95	Forced_USB_BOOT
BOOT Config[0]:GPIO_48	WDOG_DISABLE
BOOT Config[1]:GPIO_50	FASTBOOT_SEL[0]
BOOT Config[2]:GPIO_51	FASTBOOT_SEL[1]
BOOT Config[3]:GPIO_53	FASTBOOT_SEL[2]
BOOT Config[4]:GPIO_55	FASTBOOT_SEL[3]
GPIO_94	Forced_USB_BOOT_POL_SEL

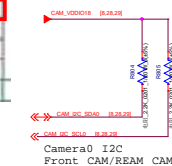
FASTBOOT_SEL[3:0]	BOOT OPTIONS
0b0000	0:Default(eMMC>SD>eDL(eDL path-USB only))
0b0001	1:SD>eMMC>eDL(eDL path-USB only))
0b0010	2:SD>eDL(eDL path-USB only)
0b0011	3:USB>eDL(eDL path-SD then USB)
0b0100	5:UFS HS G1>SD>eDL(eDL path-USB only))
0b0101	4:SD>UFS HS G1>eDL(eDL path-USB only))
0b0110	6:UFS HS G1>eDL(eDL path-USB only))
0b0111	7:eMMC>SD>eDL(eDL path-USB only))
0b1000	8:eMMC>eDL(eDL path-USB only))

	HW ID 1 (GPIO 83)				HW ID 2 (GPIO 85)			
Notes	R1131 (R1) R823	R1127 (R2) R810	R1129 (R3) R820	G1130 (C1) C804	R1132 (R1) R824	R1128 (R2) R811	R1130 (R3) R821	G1131 (C1) C805
EVB NA	Debm - 0613652Y86	DNP	7.5k - 0613652X04	DNP	DNP	DNP	DNP	DNP
EVT NA	Debm - 0613652Y86	DNP	7.5k - 0613652X04	DNP	DNP	DNP	DNP	DNP
OVT1 NA	Debm - 0613652Y86	DNP	7.5k - 0613652X04	DNP	DNP	DNP	DNP	DNP
OVT2 NA	Debm - 0613652Y86	DNP	7.5k - 0613652X04	DNP	DNP	DNP	DNP	DNP
PVT NA	Debm - 0613652Y86	DNP	7.5k - 0613652X04	DNP	DNP	DNP	DNP	DNP

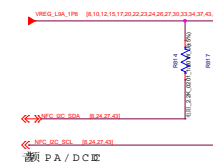
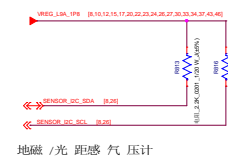
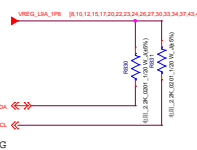
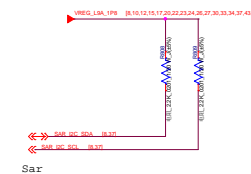
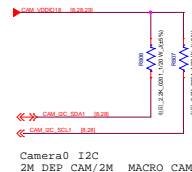
Hardware ID EVB



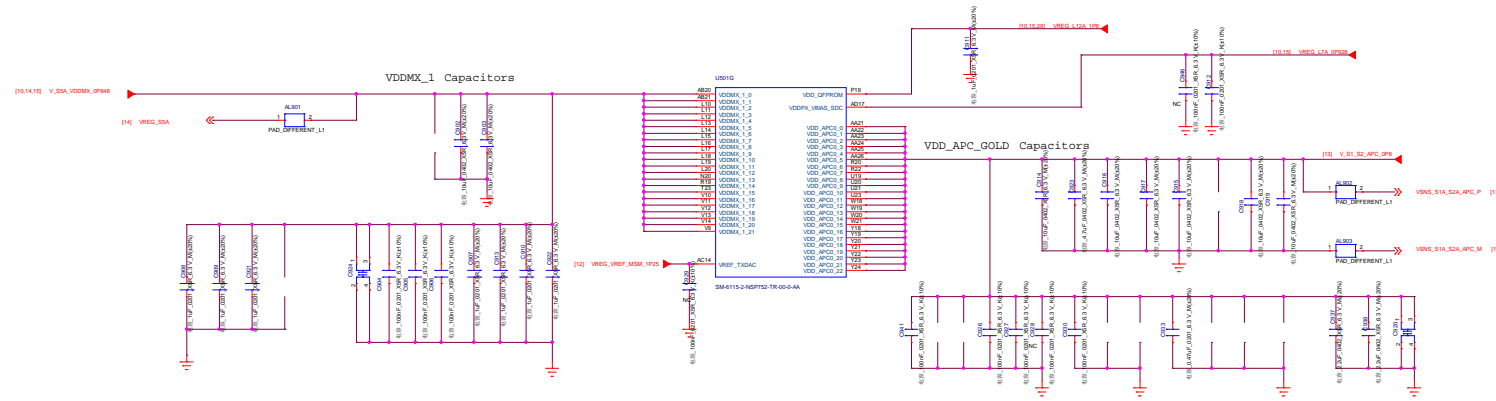
I2C pull up



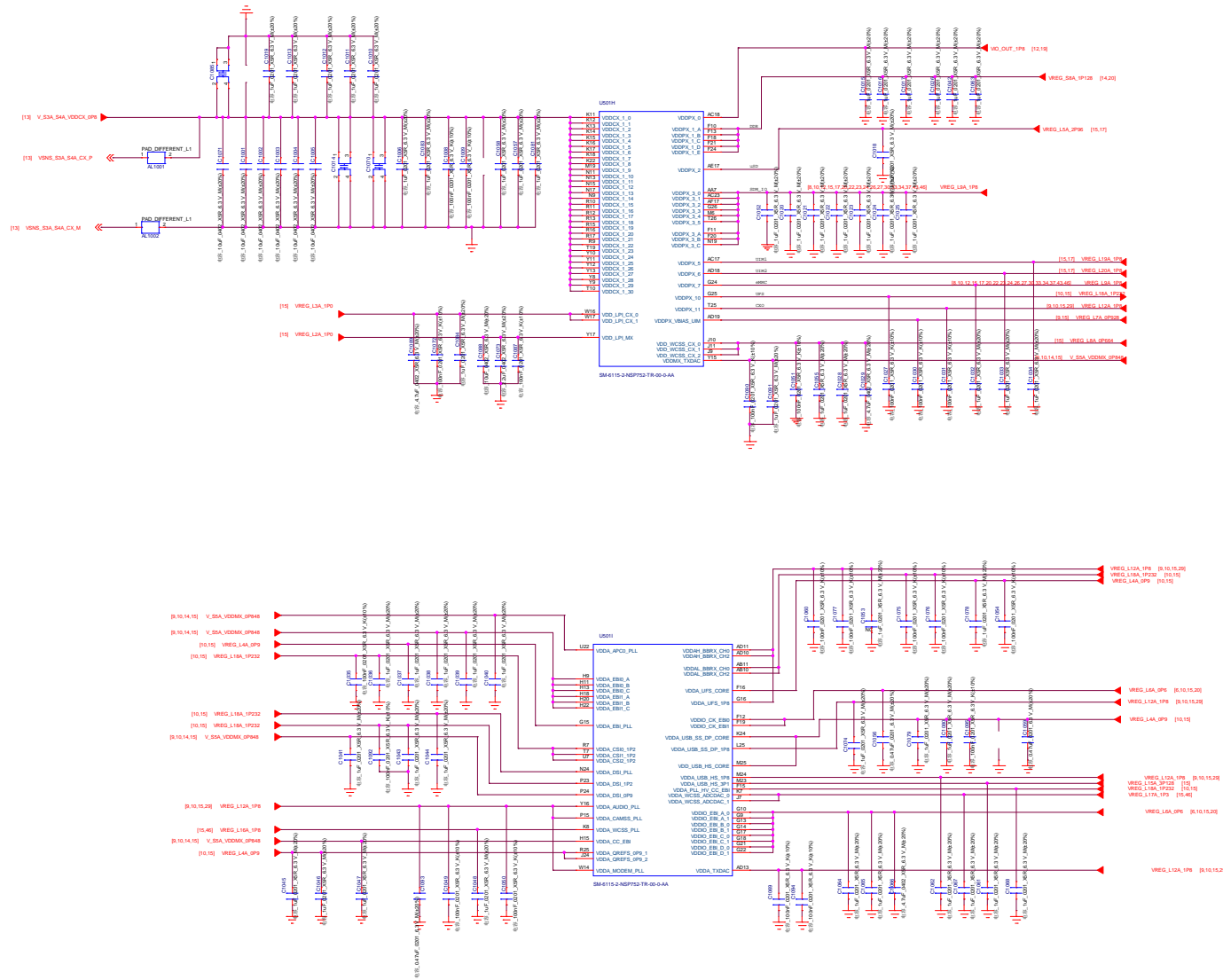
Camera I2C



Company Header		
Block Title	SM6115 GPIO Map	
Project	hormo NA	Rev 1.0
Date	2022/04/18/2022	Drawn by 07



Company Name		
Block Title SM6125 Power (Core)		
Project	Revision	Rev 1.0
Date	Project Manager	Sheet 9 of 9



Company Header		
Block Title SM6115 Power (Other, L-Pad)		
Project	Bonus NA	Rev 1.0
Author	Power, May 26, 2009	Sheet 10 of 10

U001J		
411	VSSA_S_0	VSSA_S_0
412	VSSA_S_1	VSSA_S_1
413	VSSA_S_2	VSSA_S_2
414	VSSA_S_3	VSSA_S_3
415	VSSA_S_4	VSSA_S_4
416	VSSA_S_5	VSSA_S_5
417	VSSA_S_6	VSSA_S_6
418	VSSA_S_7	VSSA_S_7
419	VSSA_S_8	VSSA_S_8
420	VSSA_S_9	VSSA_S_9
421	VSSA_S_10	VSSA_S_10
422	VSSA_S_11	VSSA_S_11
423	VSSA_S_12	VSSA_S_12
424	VSSA_S_13	VSSA_S_13
425	VSSA_S_14	VSSA_S_14
426	VSSA_S_15	VSSA_S_15
427	VSSA_S_16	VSSA_S_16
428	VSSA_S_17	VSSA_S_17
429	VSSA_S_18	VSSA_S_18
430	VSSA_S_19	VSSA_S_19
431	VSSA_S_20	VSSA_S_20
432	VSSA_S_21	VSSA_S_21
433	VSSA_S_22	VSSA_S_22
434	VSSA_S_23	VSSA_S_23
435	VSSA_S_24	VSSA_S_24
436	VSSA_S_25	VSSA_S_25
437	VSSA_S_26	VSSA_S_26
438	VSSA_S_27	VSSA_S_27
439	VSSA_S_28	VSSA_S_28
440	VSSA_S_29	VSSA_S_29
441	VSSA_S_30	VSSA_S_30
442	VSSA_S_31	VSSA_S_31
443	VSSA_S_32	VSSA_S_32
444	VSSA_S_33	VSSA_S_33
445	VSSA_S_34	VSSA_S_34
446	VSSA_S_35	VSSA_S_35
447	VSSA_S_36	VSSA_S_36
448	VSSA_S_37	VSSA_S_37
449	VSSA_S_38	VSSA_S_38
450	VSSA_S_39	VSSA_S_39
451	VSSA_S_40	VSSA_S_40
452	VSSA_S_41	VSSA_S_41
453	VSSA_S_42	VSSA_S_42
454	VSSA_S_43	VSSA_S_43
455	VSSA_S_44	VSSA_S_44
456	VSSA_S_45	VSSA_S_45
457	VSSA_S_46	VSSA_S_46
458	VSSA_S_47	VSSA_S_47
459	VSSA_S_48	VSSA_S_48
460	VSSA_S_49	VSSA_S_49

SM 6115-2-ASPT52-TR-00-0-AA

U001K			
461	VSSA_S_50	VSSA_WCSS_A0C2AC_0, 1	46
462	VSSA_S_51	VSSA_WCSS_A0C2AC_0, 1	47
463	VSSA_S_52	VSSA_WCSS_A0C2AC_0, 1	48
464	VSSA_S_53	VSSA_WCSS_A0C2AC_0, 1	49
465	VSSA_S_54	VSSA_WCSS_A0C2AC_0, 1	50
466	VSSA_S_55	VSSA_WCSS_A0C2AC_0, 1	51
467	VSSA_S_56	VSSA_WCSS_A0C2AC_0, 1	52
468	VSSA_S_57	VSSA_WCSS_A0C2AC_0, 1	53
469	VSSA_S_58	VSSA_WCSS_A0C2AC_0, 1	54
470	VSSA_S_59	VSSA_WCSS_A0C2AC_0, 1	55
471	VSSA_S_60	VSSA_WCSS_A0C2AC_0, 1	56
472	VSSA_S_61	VSSA_WCSS_A0C2AC_0, 1	57
473	VSSA_S_62	VSSA_WCSS_A0C2AC_0, 1	58
474	VSSA_S_63	VSSA_WCSS_A0C2AC_0, 1	59
475	VSSA_S_64	VSSA_WCSS_A0C2AC_0, 1	60
476	VSSA_S_65	VSSA_WCSS_A0C2AC_0, 1	61
477	VSSA_S_66	VSSA_WCSS_A0C2AC_0, 1	62
478	VSSA_S_67	VSSA_WCSS_A0C2AC_0, 1	63
479	VSSA_S_68	VSSA_WCSS_A0C2AC_0, 1	64
480	VSSA_S_69	VSSA_WCSS_A0C2AC_0, 1	65
481	VSSA_S_70	VSSA_WCSS_A0C2AC_0, 1	66
482	VSSA_S_71	VSSA_WCSS_A0C2AC_0, 1	67
483	VSSA_S_72	VSSA_WCSS_A0C2AC_0, 1	68
484	VSSA_S_73	VSSA_WCSS_A0C2AC_0, 1	69
485	VSSA_S_74	VSSA_WCSS_A0C2AC_0, 1	70
486	VSSA_S_75	VSSA_WCSS_A0C2AC_0, 1	71
487	VSSA_S_76	VSSA_WCSS_A0C2AC_0, 1	72
488	VSSA_S_77	VSSA_WCSS_A0C2AC_0, 1	73
489	VSSA_S_78	VSSA_WCSS_A0C2AC_0, 1	74
490	VSSA_S_79	VSSA_WCSS_A0C2AC_0, 1	75
491	VSSA_S_80	VSSA_WCSS_A0C2AC_0, 1	76
492	VSSA_S_81	VSSA_WCSS_A0C2AC_0, 1	77
493	VSSA_S_82	VSSA_WCSS_A0C2AC_0, 1	78
494	VSSA_S_83	VSSA_WCSS_A0C2AC_0, 1	79
495	VSSA_S_84	VSSA_WCSS_A0C2AC_0, 1	80
496	VSSA_S_85	VSSA_WCSS_A0C2AC_0, 1	81
497	VSSA_S_86	VSSA_WCSS_A0C2AC_0, 1	82
498	VSSA_S_87	VSSA_WCSS_A0C2AC_0, 1	83
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501	VSSA_S_90	VSSA_WCSS_A0C2AC_0, 1	86
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512	VSSA_S_101	VSSA_WCSS_A0C2AC_0, 1	97
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538	VSSA_S_127	VSSA_WCSS_A0C2AC_0, 1	123
539	VSSA_S_128	VSSA_WCSS_A0C2AC_0, 1	124
540	VSSA_S_129	VSSA_WCSS_A0C2AC_0, 1	125
541	VSSA_S_130	VSSA_WCSS_A0C2AC_0, 1	126
542	VSSA_S_131	VSSA_WCSS_A0C2AC_0, 1	127
543	VSSA_S_132	VSSA_WCSS_A0C2AC_0, 1	128
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558	VSSA_S_147	VSSA_WCSS_A0C2AC_0, 1	143
559	VSSA_S_148	VSSA_WCSS_A0C2AC_0, 1	144
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563	VSSA_S_152	VSSA_WCSS_A0C2AC_0, 1	148
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565	VSSA_S_154	VSSA_WCSS_A0C2AC_0, 1	150
566	VSSA_S_155	VSSA_WCSS_A0C2AC_0, 1	151
567	VSSA_S_156	VSSA_WCSS_A0C2AC_0, 1	152
568	VSSA_S_157	VSSA_WCSS_A0C2AC_0, 1	153
569	VSSA_S_158	VSSA_WCSS_A0C2AC_0, 1	154
570	VSSA_S_159	VSSA_WCSS_A0C2AC_0, 1	155
571	VSSA_S_160	VSSA_WCSS_A0C2AC_0, 1	156
572	VSSA_S_161	VSSA_WCSS_A0C2AC_0, 1	157
573	VSSA_S_162	VSSA_WCSS_A0C2AC_0, 1	158
574	VSSA_S_163	VSSA_WCSS_A0C2AC_0, 1	159
575	VSSA_S_164	VSSA_WCSS_A0C2AC_0, 1	160
576	VSSA_S_165	VSSA_WCSS_A0C2AC_0, 1	161
577	VSSA_S_166	VSSA_WCSS_A0C2AC_0, 1	162
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581	VSSA_S_170	VSSA_WCSS_A0C2AC_0, 1	166
582	VSSA_S_171	VSSA_WCSS_A0C2AC_0, 1	167
583	VSSA_S_172	VSSA_WCSS_A0C2AC_0, 1	168
584	VSSA_S_173	VSSA_WCSS_A0C2AC_0, 1	169
585	VSSA_S_174	VSSA_WCSS_A0C2AC_0, 1	170
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589	VSSA_S_178	VSSA_WCSS_A0C2AC_0, 1	174
590	VSSA_S_179	VSSA_WCSS_A0C2AC_0, 1	175
591	VSSA_S_180	VSSA_WCSS_A0C2AC_0, 1	176
592	VSSA_S_181	VSSA_WCSS_A0C2AC_0, 1	177
593	VSSA_S_182	VSSA_WCSS_A0C2AC_0, 1	178
594	VSSA_S_183	VSSA_WCSS_A0C2AC_0, 1	179
595	VSSA_S_184	VSSA_WCSS_A0C2AC_0, 1	180
596	VSSA_S_185	VSSA_WCSS_A0C2AC_0, 1	181
597	VSSA_S_186	VSSA_WCSS_A0C2AC_0, 1	182
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599	VSSA_S_188	VSSA_WCSS_A0C2AC_0, 1	184
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613	VSSA_S_202	VSSA_WCSS_A0C2AC_0, 1	198
614	VSSA_S_203	VSSA_WCSS_A0C2AC_0, 1	199
615	VSSA_S_204	VSSA_WCSS_A0C2AC_0, 1	200
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617	VSSA_S_206	VSSA_WCSS_A0C2AC_0, 1	202
618	VSSA_S_207	VSSA_WCSS_A0C2AC_0, 1	203
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701	VSSA_S_290	VSSA_WCSS_A0C2AC_0, 1	286
702	VSSA_S_291	VSSA_WCSS_A0C2AC_0, 1	287
703	VSSA_S_292	VSSA_WCSS_A0C2AC_0, 1	288
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705	VSSA_S_294	VSS	

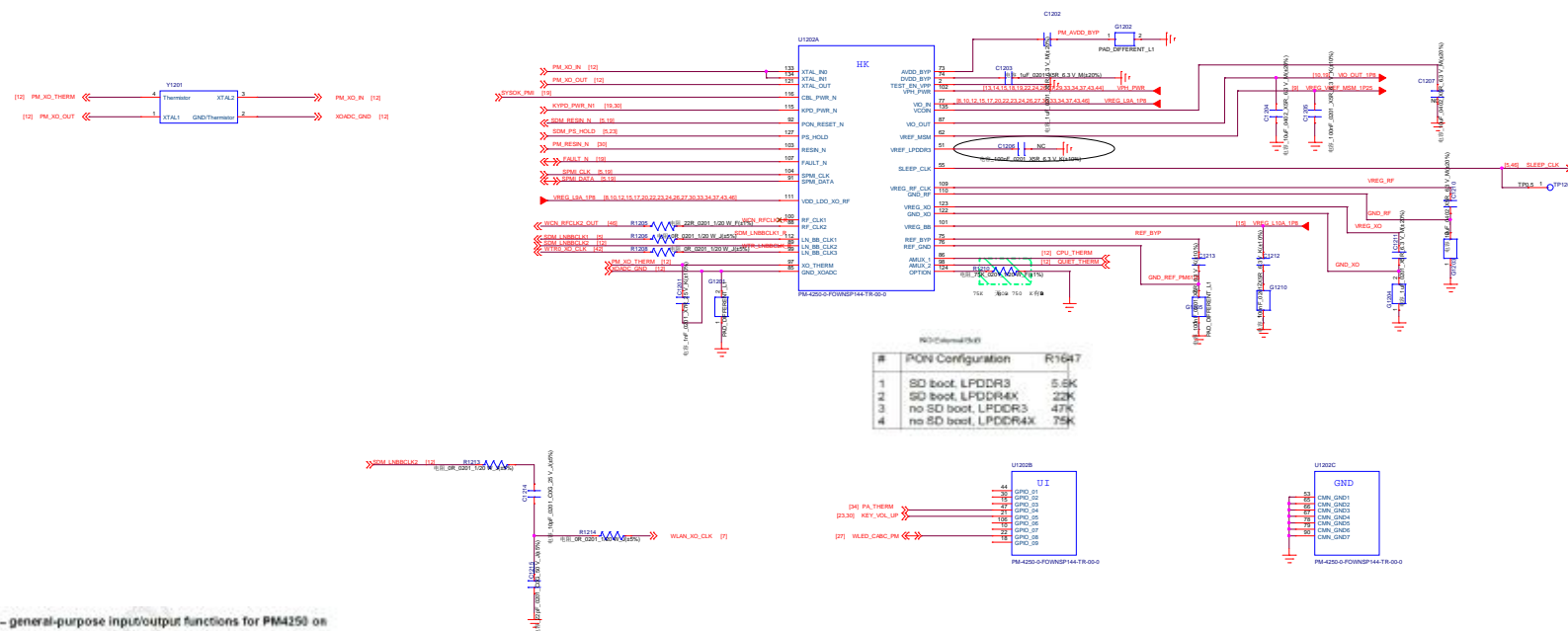
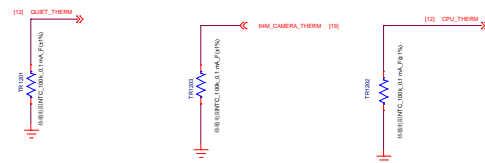


Table 2-7 Pin descriptions – general-purpose input/output functions for PM4250 on 8M4250/8M6115 platform

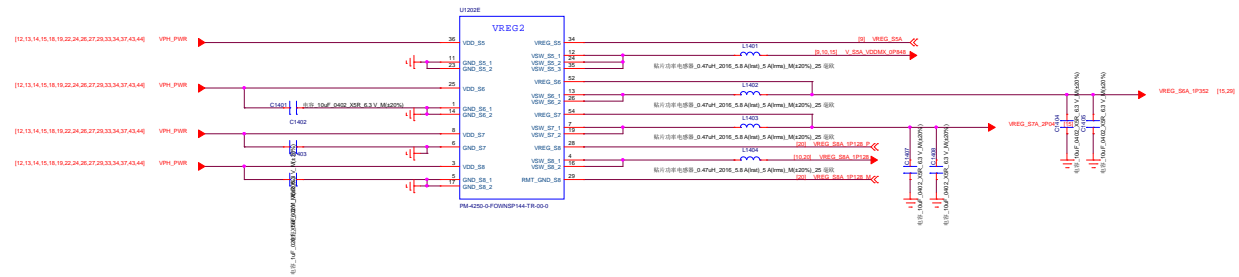
Pad #	Pad name	Configurable function	Pad type	Functional description
Predefined GPIO functions – available only at the assigned GPIOs				
44	GPIO_01	USB_SS_DP_Core, BOP_enable	LV	Configurable; default digital input with 10 μ A pull-down USB_SS_DP_Core_BOP_enable
30	GPIO_02	SLEEP_CLK_IN	LV	Configurable; default digital input with 10 μ A pull-down SLEEP_CLK_IN (optional)
15	GPIO_03	CAMFLASH_THERM	Configurable	default digital input with 10 μ A pull-down CAMFLASH_THERM
47	GPIO_04	EMMC_LFS_THERM	LV	Configurable; default digital input with 10 μ A pull-down EMMC_LFS_THERM
21	GPIO_05	Volume + Key/ SLEEP_CLK2	LV	Configurable; default digital input with 10 μ A pull-down Volume plus key is additional SLEEP_CLK output
106	GPIO_06		MV	Configurable; default digital input with 10 μ A pull-down Unused
18	GPIO_07	PA_THERM1	MV	Configurable; default digital input with 10 μ A pull-down RF PA_THERM1
22	GPIO_08	PWM_OUT	MV	Configurable; default digital input with 10 μ A pull-down PWM_OUT for backlight
16	GPIO_09		MV	Configurable; default digital input with 10 μ A pull-down Unused



REF 1301-1399

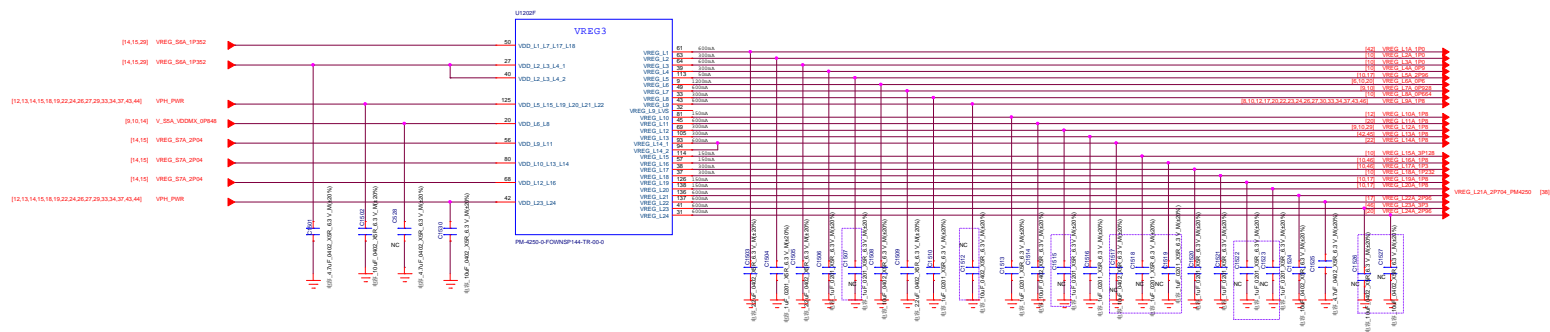


Company: Huang	
Block Title PMA250 Buck Converter	
Project Borneo NA	Rev 1.0
Author: [illegible]	Checked: [illegible]



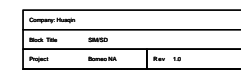
Company: Huajin	
Block Title PFM239 LDO Circuits	
Project Borneo NA	Rev 1.0
Date: Friday, May 26, 2006	Sheet 14 of 46

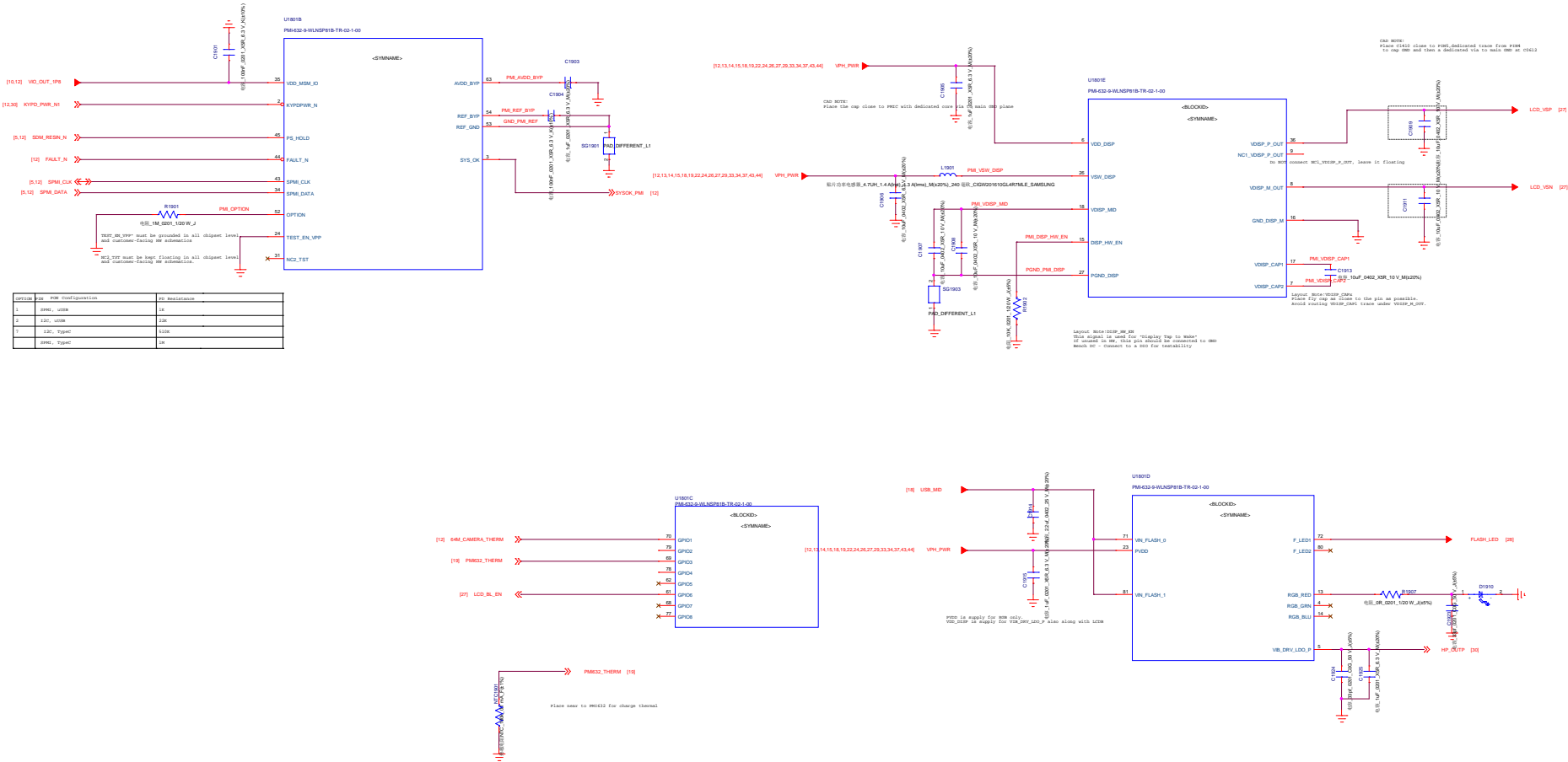
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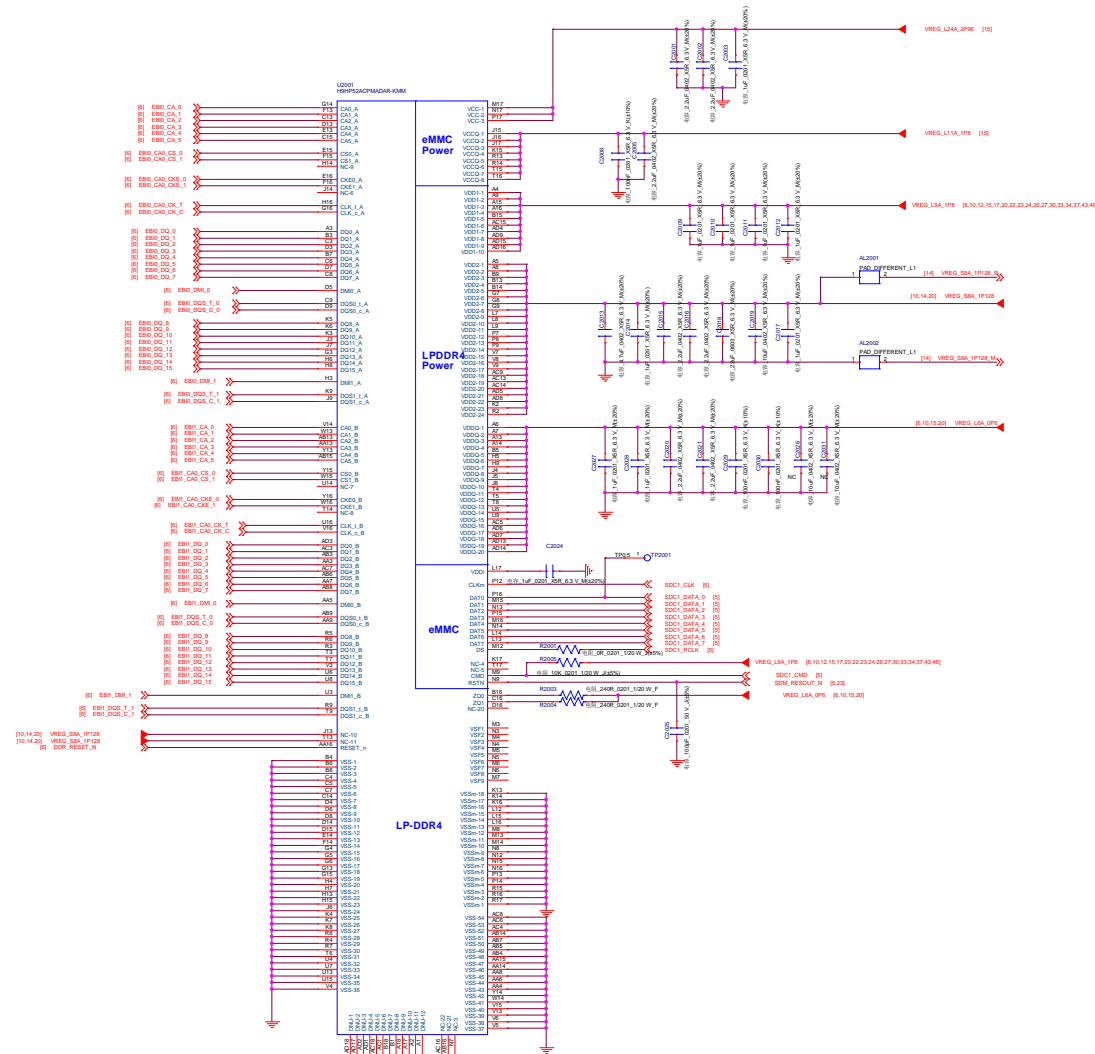
Company: Huzqin	
Block Title PSM250 Charger_FG	
Project Borneo NA	Rev 1.0

This figure is a large, empty rectangular area, likely a placeholder for a diagram or drawing. It is divided into two main sections by a vertical line. The left section is a large rectangle, and the right section is a slightly smaller rectangle. The entire area is enclosed in a thin black border.





Company Name		
PMI632 controlDisplay		
Project	Rev. 1.0	Rev. 1.0
Author	Wang, Jia	Rev. 1.0

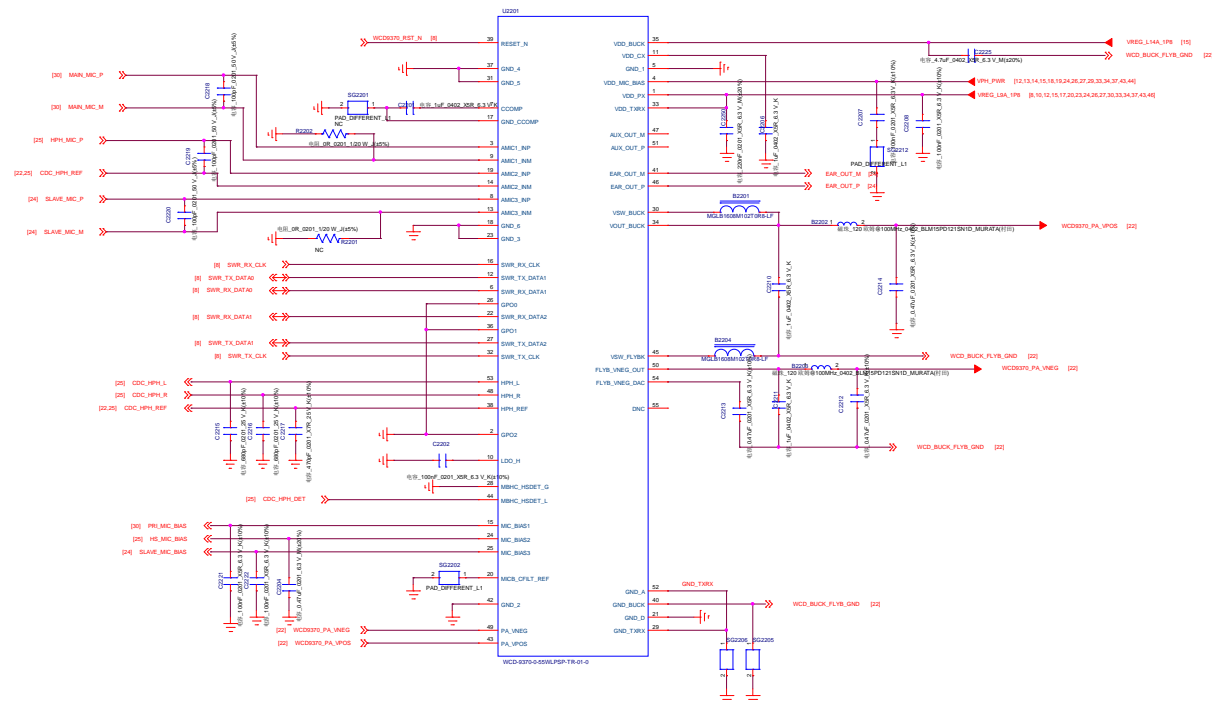


Company Header		
Block Title	eMCP	
Project	Bonus NA	Rev 1.0
Author	Project Manager	2009

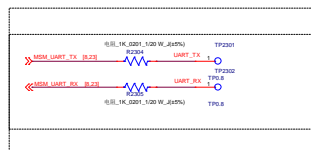
24W CHARGE
REF 2101-2199

Company Name		
Blank Title		
Project		
Date		Rev
2024, May 26, 2024		1.0
Sheet		1 of 1

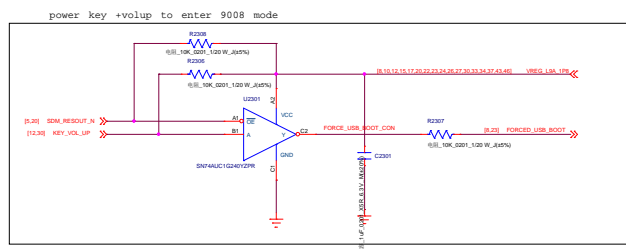
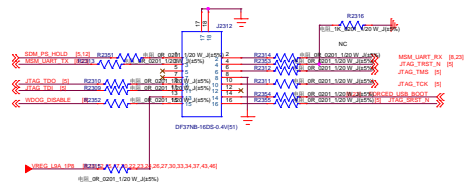
WCD9370



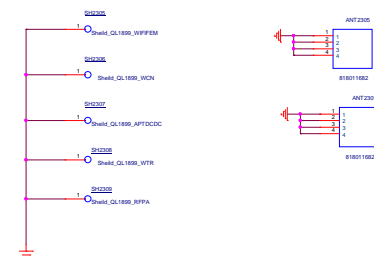
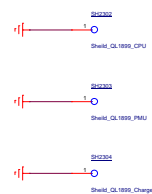
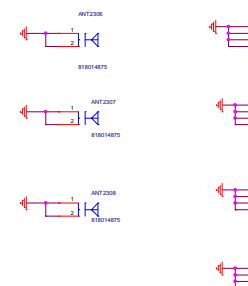
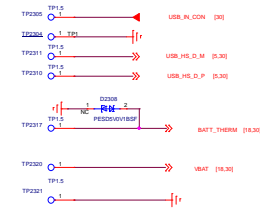
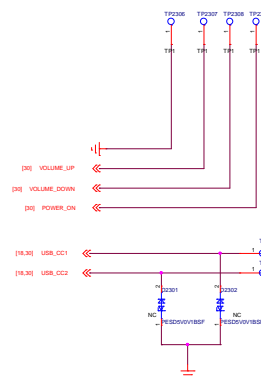
Company: Huzain			
Block Title CODEC			
Project Borneo NA		Rev 1.0	
Date:	Tuesday, June 02, 2020	Sheet	22 of 48



JTAG CONNECTOR



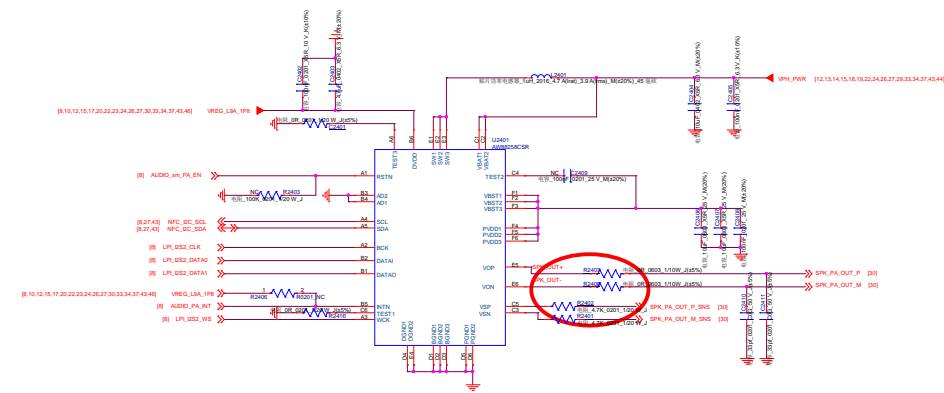
PCBA AUTOMATIC TEST point



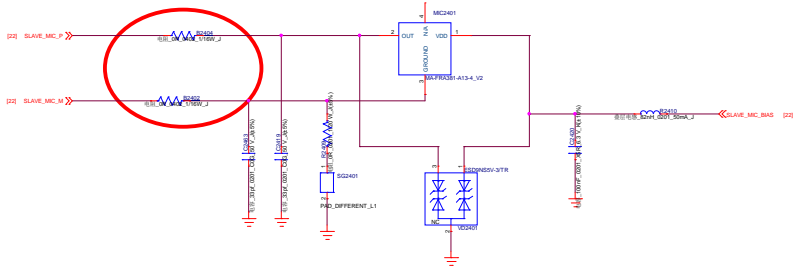
TP/GND/Shielding

Company Name		
Block Title	Test Points	
Project	Ref: 2301-2399	Rev: 1.0
Author	Project Manager	2023-01-01

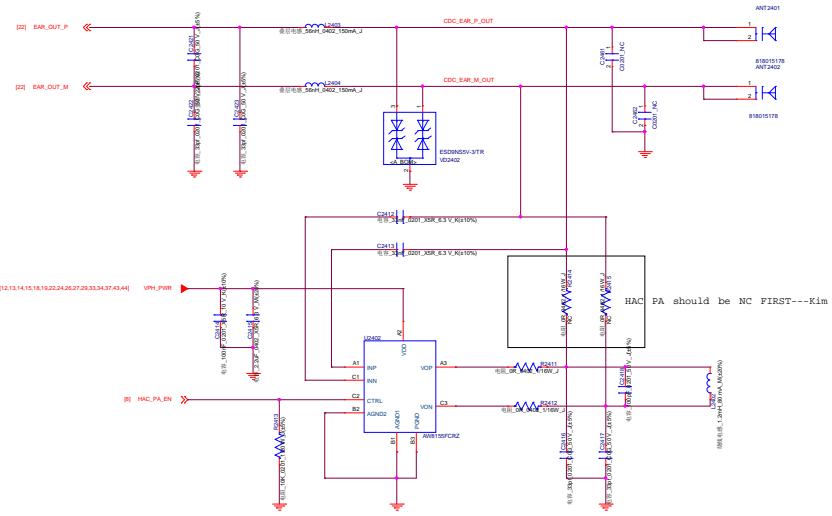
SmartPA



副 M C

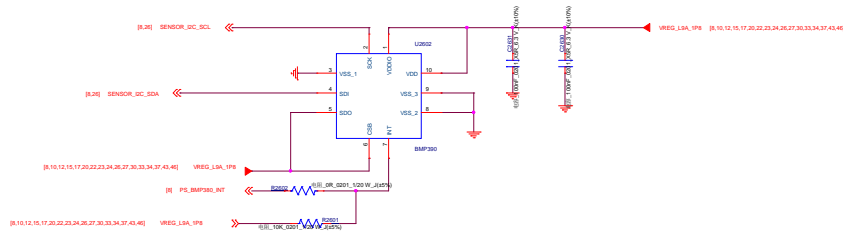


REC&HAC



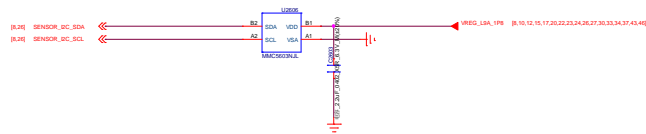
Company Name		
Block Title	AUDIO_MC_RECV_SPM	
Project	Bonus NA	Rev 1.0
Author	Project Manager	2020-04-01

應用BM390, HQ111805000

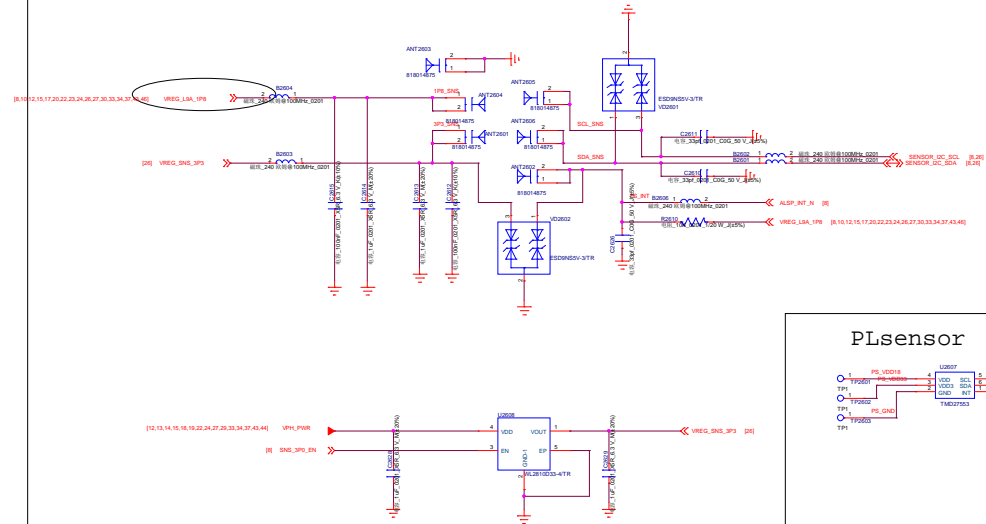
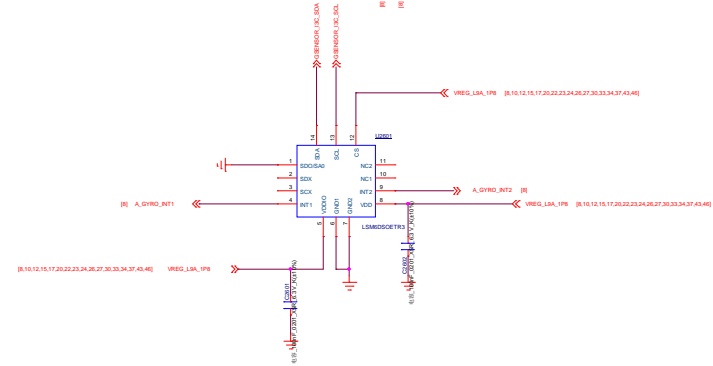
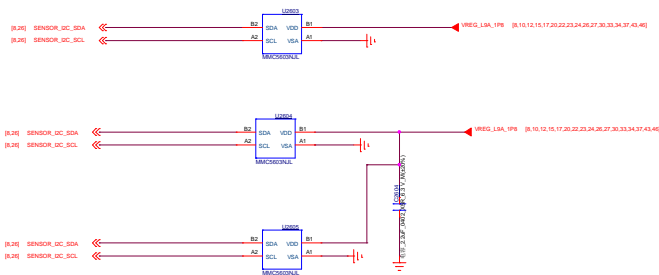


供 BMP39 0 II C 0X7
ICP1010 1 II C 0X8

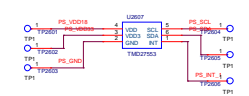
Magnetic Sensor



address of AK09918 is 0X0C
MMC5603NJL is 0X30

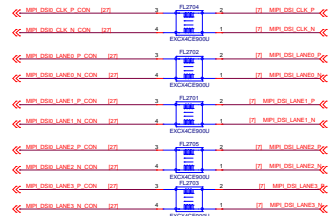


PLSensor 小板

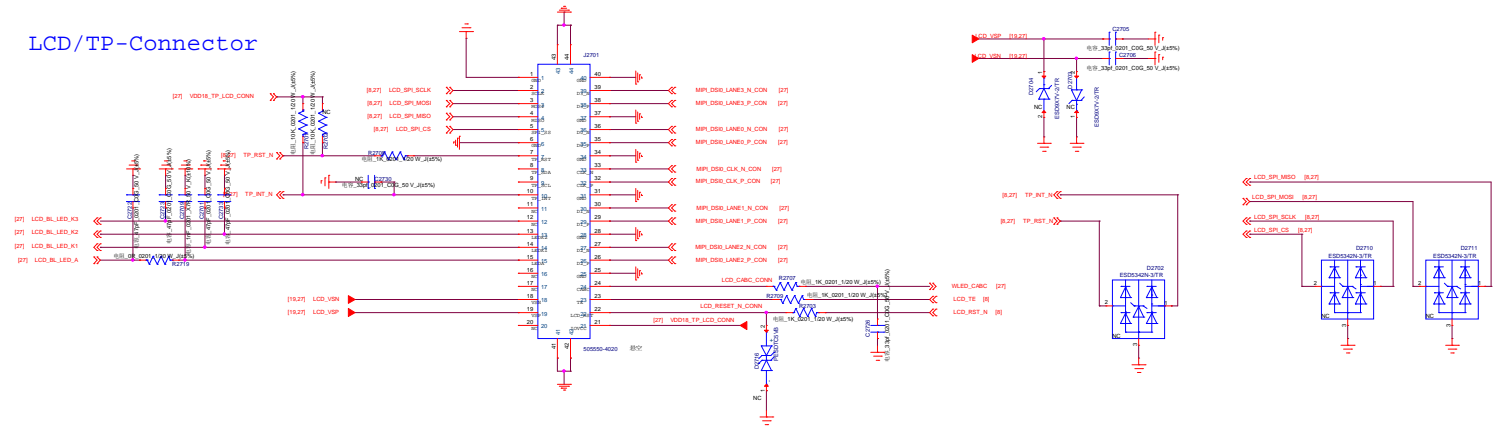


Company Name		
Block Title		
Project		
Rev		

LCD MIPI Common Mode Filter

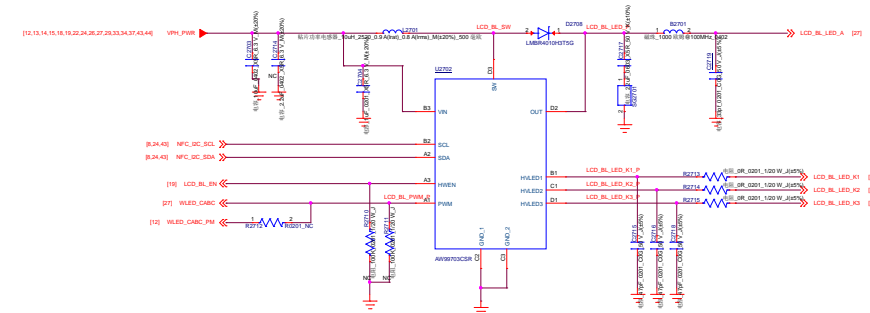
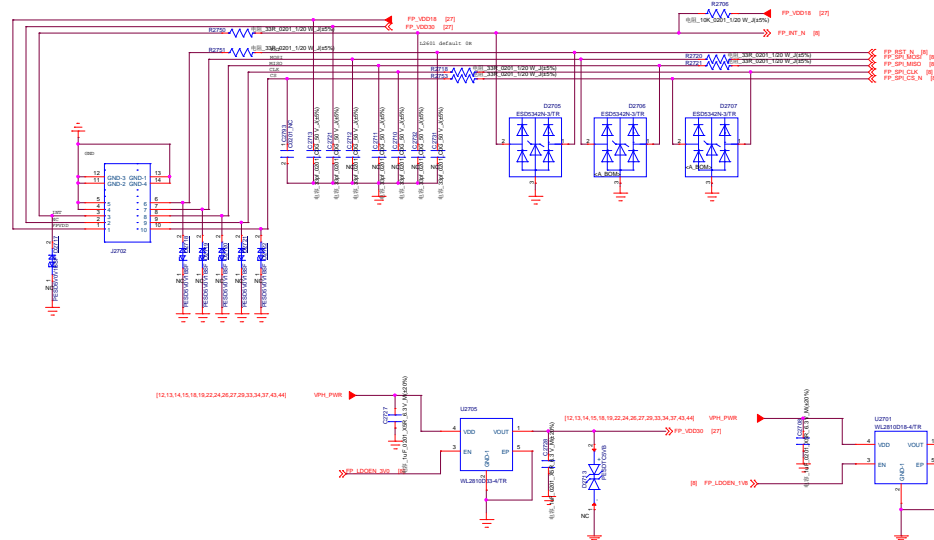


LCD/TP-Connector



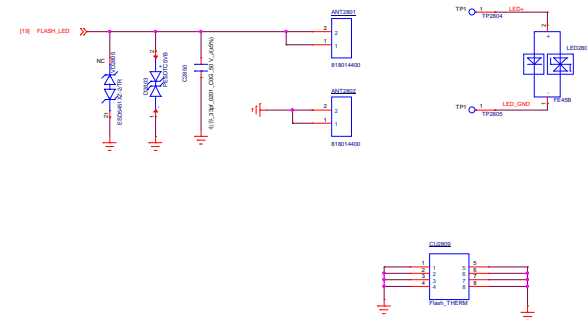
FPS-Connector

J2603 HQ11910378000

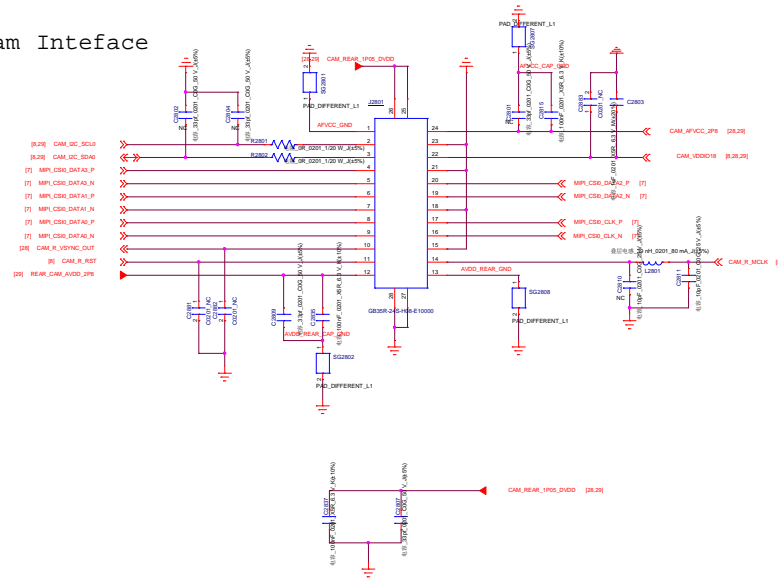


Company Name		
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Project	Bonus NA	Rev 1.0
Author	Wolfgang, Anna, Peter, Rainer	27 07 08

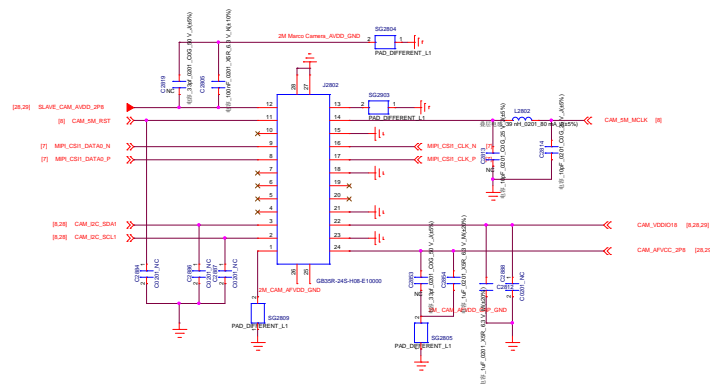
Rear Cam FLASH LED



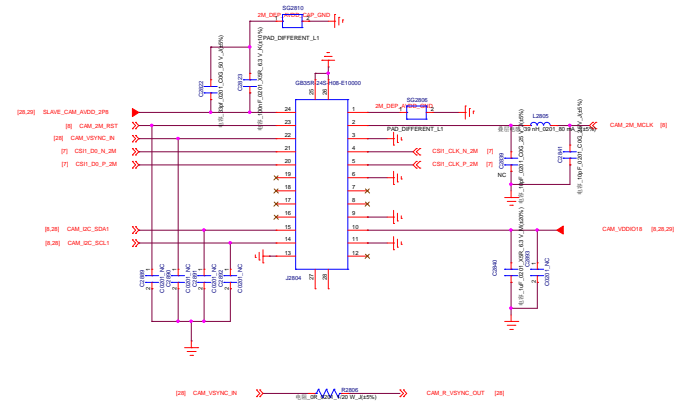
Rear Cam Interface



2M Marco Camera



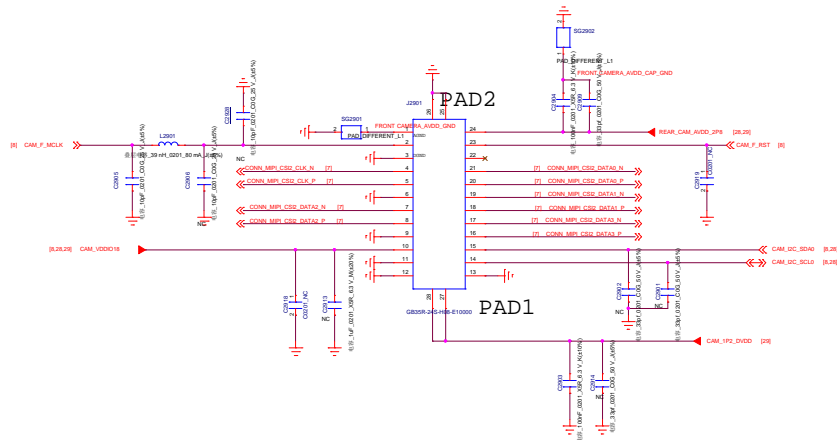
2M Depth Camera



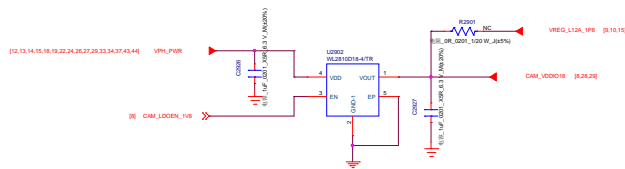
Company Name		
Block Title		
Project		
Rev		
Date		

OV12A10-GASA-Front Cam I2C Adress:0x10 (Write:0x20 Read:0x21)

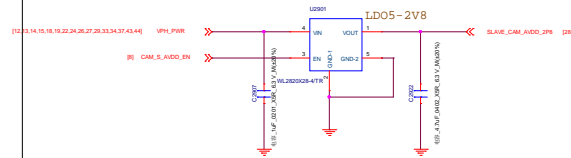
FRONT CAMERA



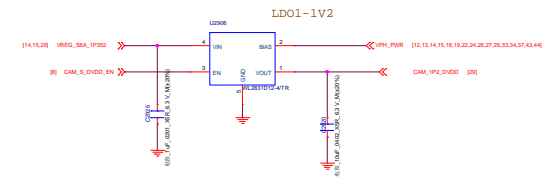
CAMERA LDO



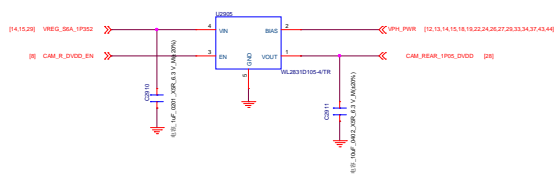
Rear-Slave AVDD 2.8V



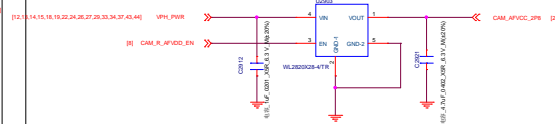
Rear-Slave DVDD 1.2V Also 8M Front DVDD



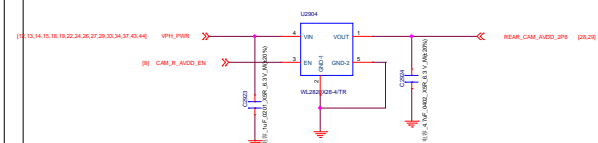
Rear-Main DVDD 1.05V



Rear-Main/2M Marco_AFVCC 2.8V



Rear-Main	AVDD	2.8V	also	Front	AVDD
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Company: Hualin	
Block Title FRONT_CAM/CAM_POWER	
Project Borneo NA	Rev 1.0
Author: [illegible]	Check: [illegible]

KEY_LINK_BATT

Company: Huang	
Block Title KEY_LINK_BATT	
Project Borneo NA	Rev 1.0

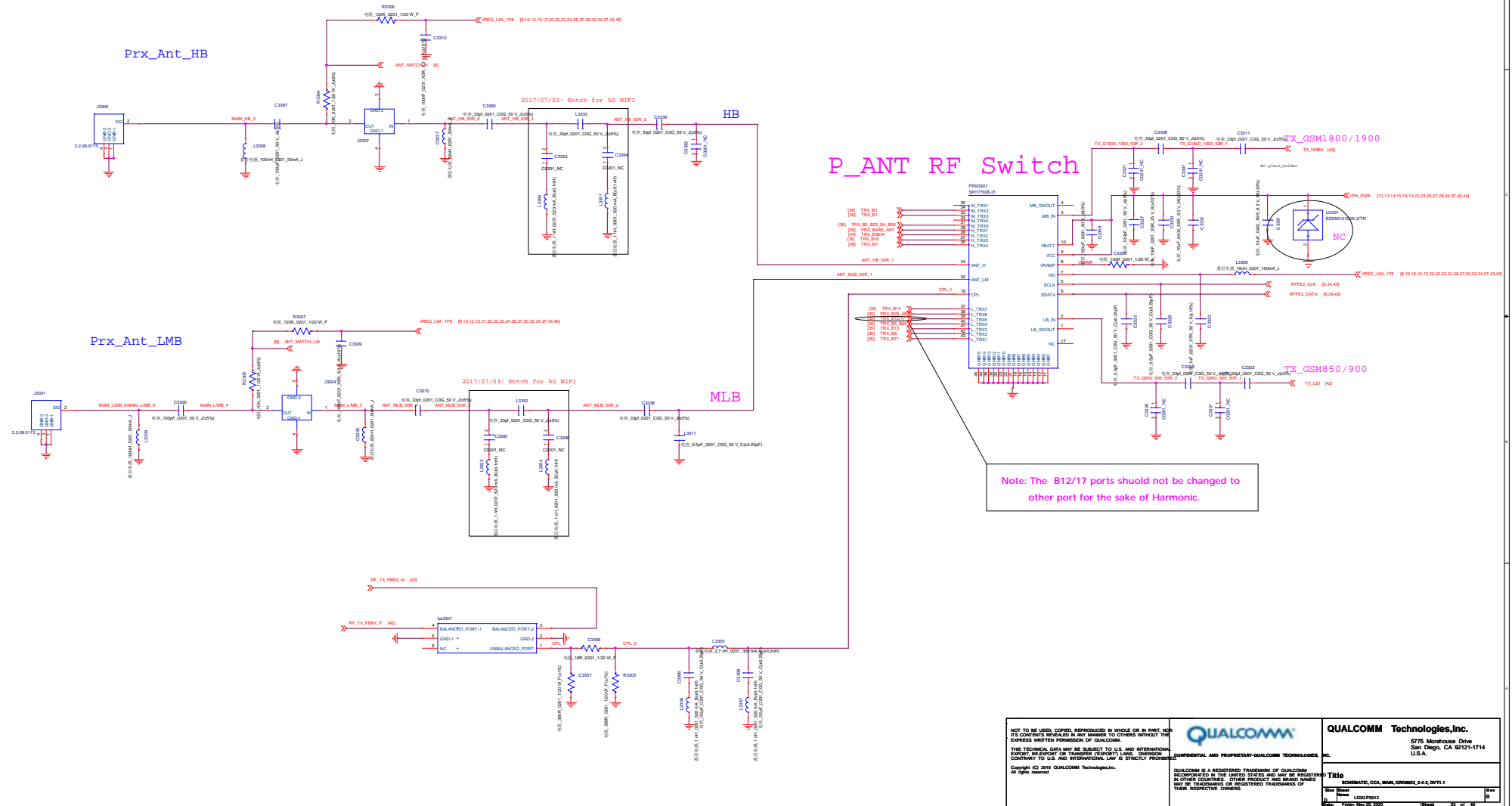
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Block Title	NC	
Project	Bureau NA	Rev: 1.0
Date	Friday, May 26, 2006	Sheet 31 of 46

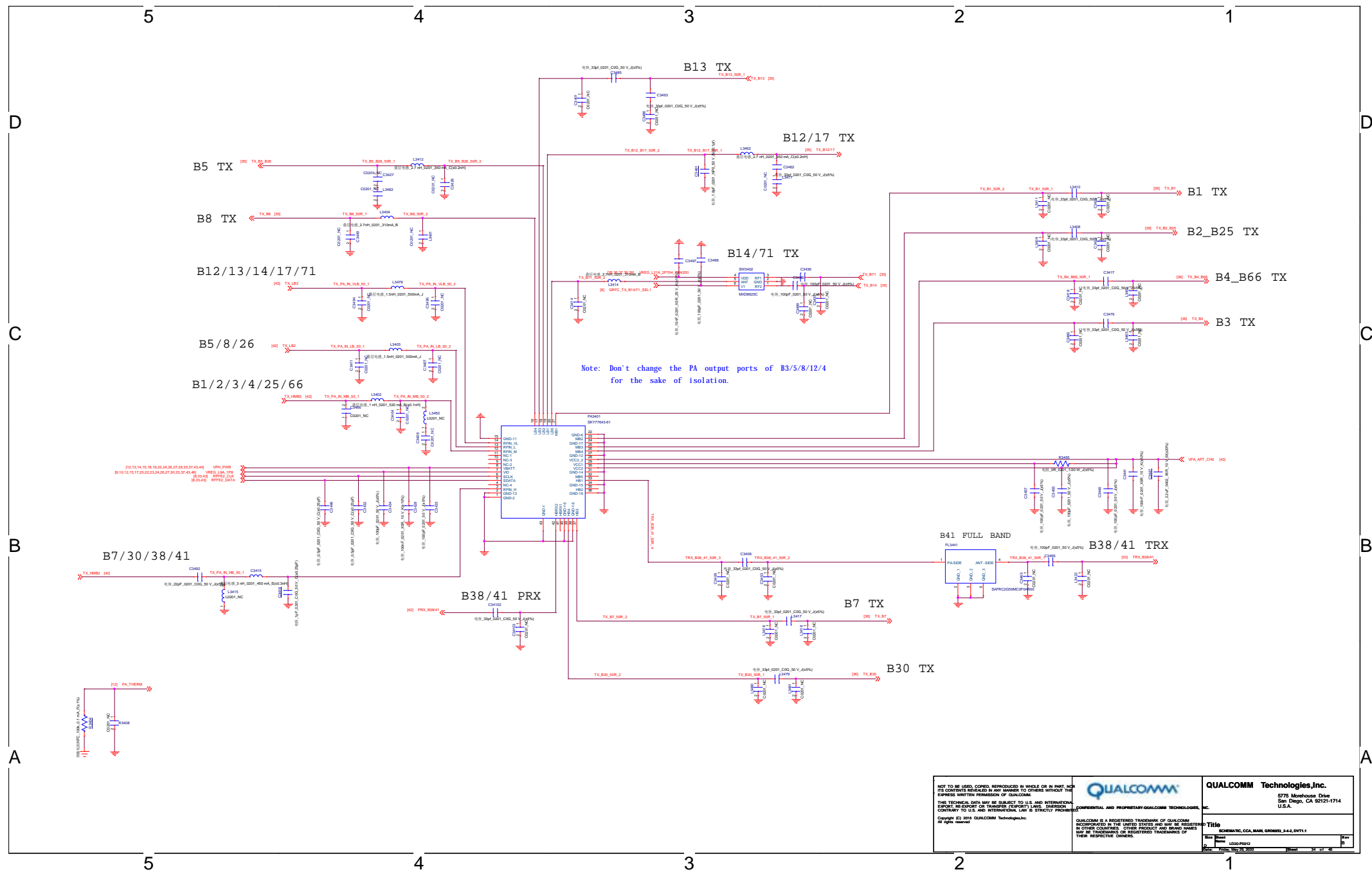


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Date	Friday, May 26, 2023	Sheet	30 of 40

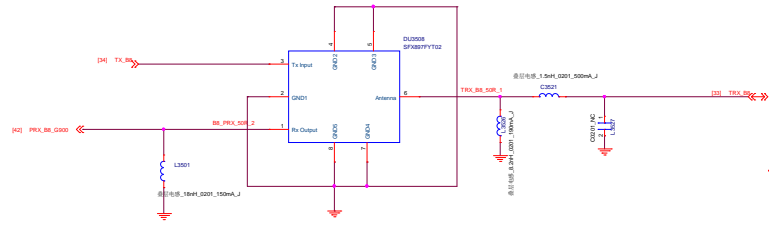


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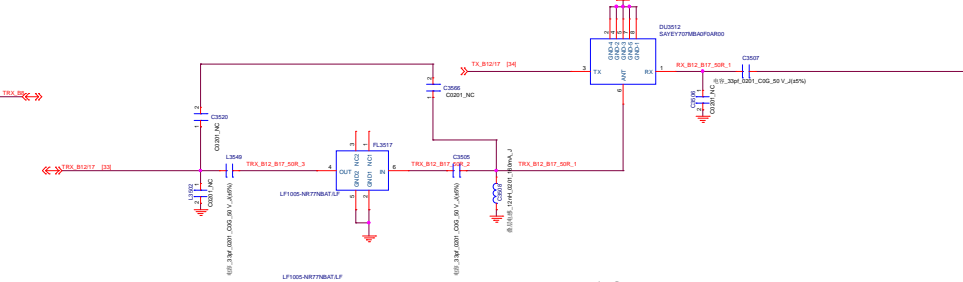




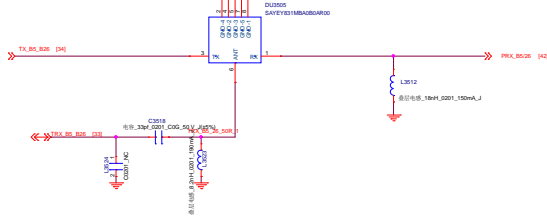
B8 DPX



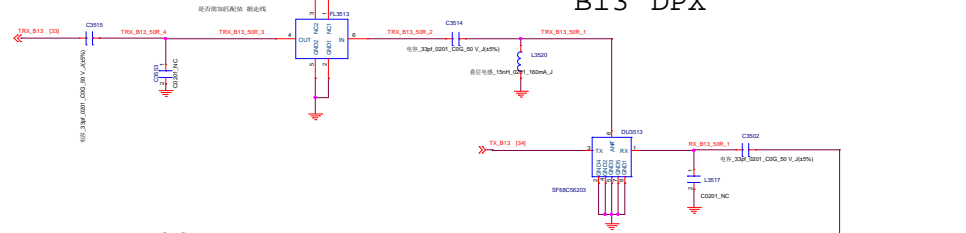
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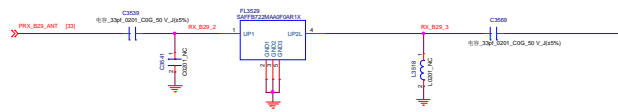
B5/26 DPX



B13 DPX



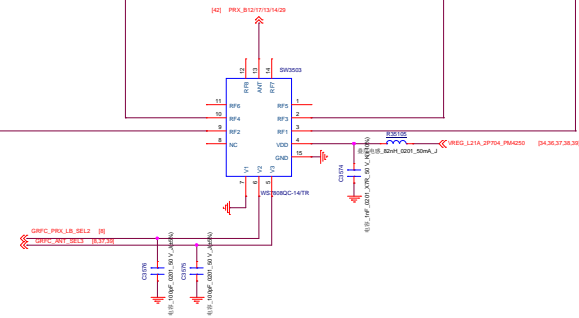
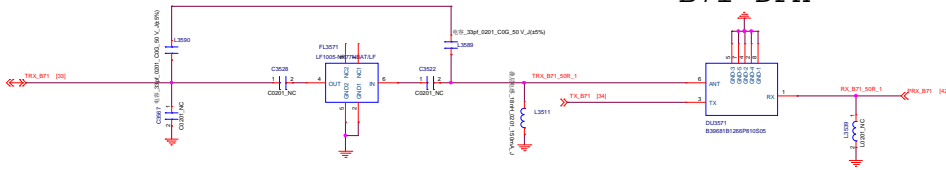
B29



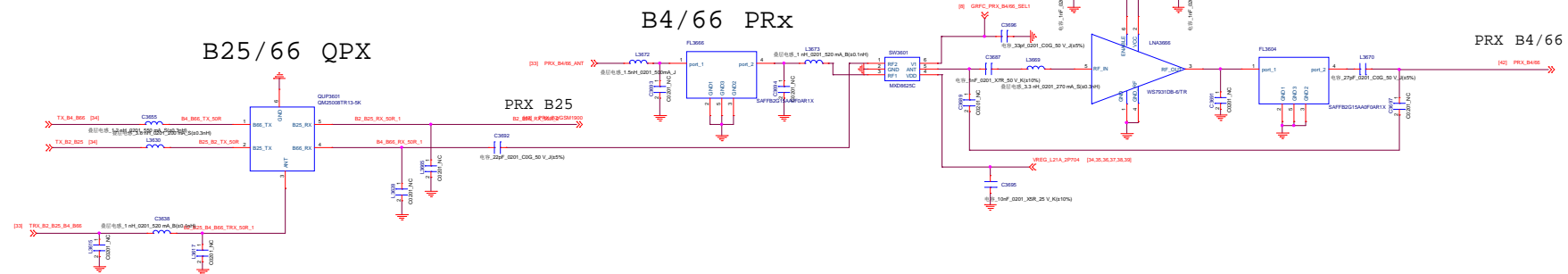
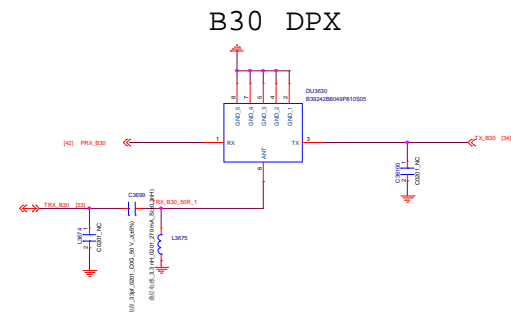
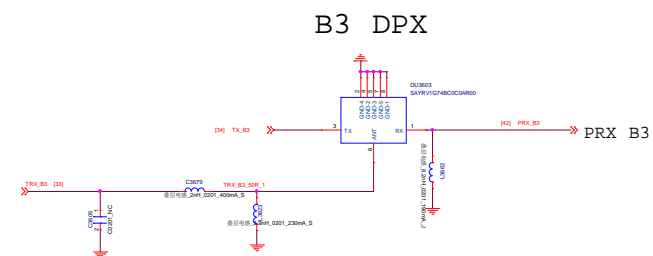
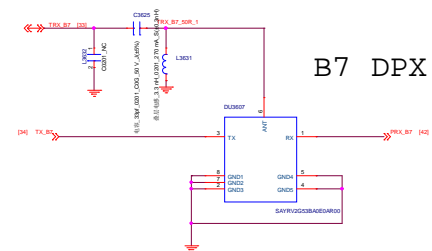
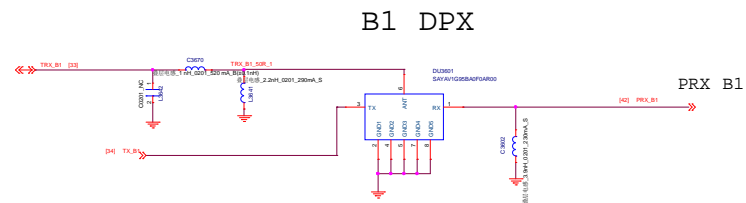
B14 DPX



B71 DPX



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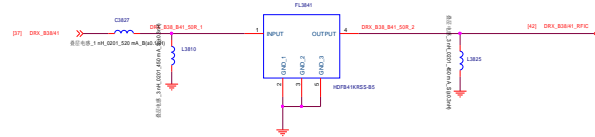
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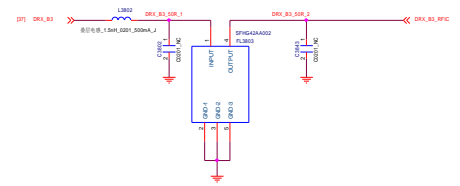
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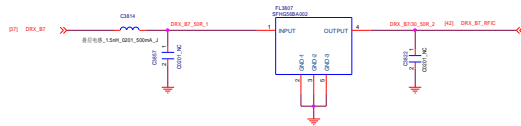
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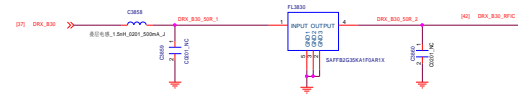
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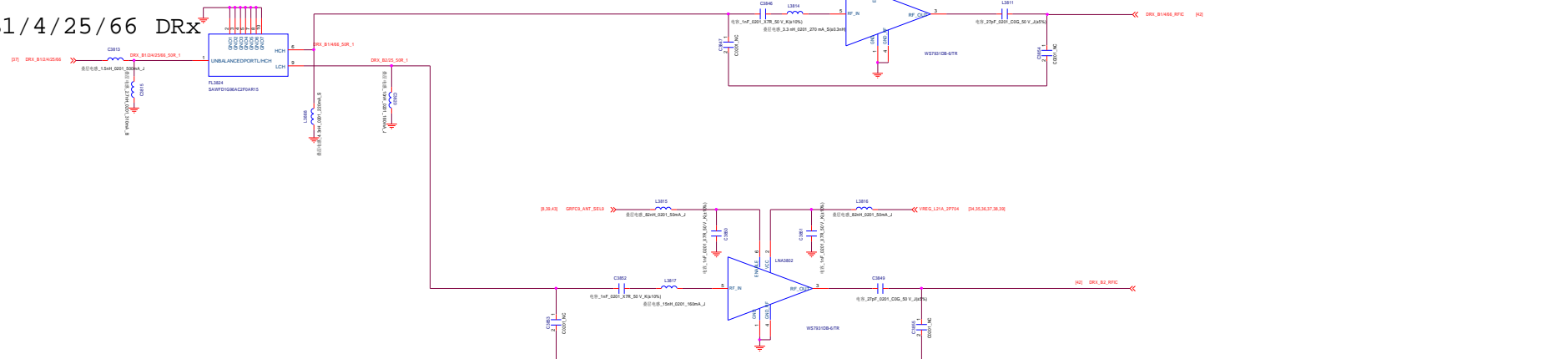
B7 DRx



B30 DRx



B1/4/25/66 DRx



Rev	01	Docu	Rev	01
Rev	01	Docu	Rev	01
Rev	01	Docu	Rev	01

[illegible]

The schematic diagram illustrates a power system with two buses, B1 and B2, connected by a transmission line. Bus B1 is connected to a generator (G1) and a load (L1). Bus B2 is connected to a load (L2). The transmission line is represented by a series of inductors and capacitors. The diagram is labeled with various components and their values.

[illegible][illegible]

DRX LB

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<p>Rev 1</p>	<p>Rev 1</p>	<p>Date</p> <p>10/10/2016</p>
<p>Rev 2</p>	<p>Rev 2</p>	<p>Date</p> <p>10/10/2016</p>
<p>Rev 3</p>	<p>Rev 3</p>	<p>Date</p> <p>10/10/2016</p>
<p>Rev 4</p>	<p>Rev 4</p>	<p>Date</p> <p>10/10/2016</p>
<p>Rev 5</p>	<p>Rev 5</p>	<p>Date</p> <p>10/10/2016</p>
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<p>Rev 7</p>	<p>Rev 7</p>	<p>Date</p> <p>10/10/2016</p>
<p>Rev 8</p>	<p>Rev 8</p>	<p>Date</p> <p>10/10/2016</p>
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<p>Rev 12</p>	<p>Rev 12</p>	<p>Date</p> <p>10/10/2016</p>
<p>Rev 13</p>	<p>Rev 13</p>	<p>Date</p> <p>10/10/2016</p>
<p>Rev 14</p>	<p>Rev 14</p>	<p>Date</p> <p>10/10/2016</p>
<p>Rev 15</p>	<p>Rev 15</p>	<p>Date</p> <p>10/10/2016</p>

WTR_P001	0
WTR_P002	1

WTR 2965_0

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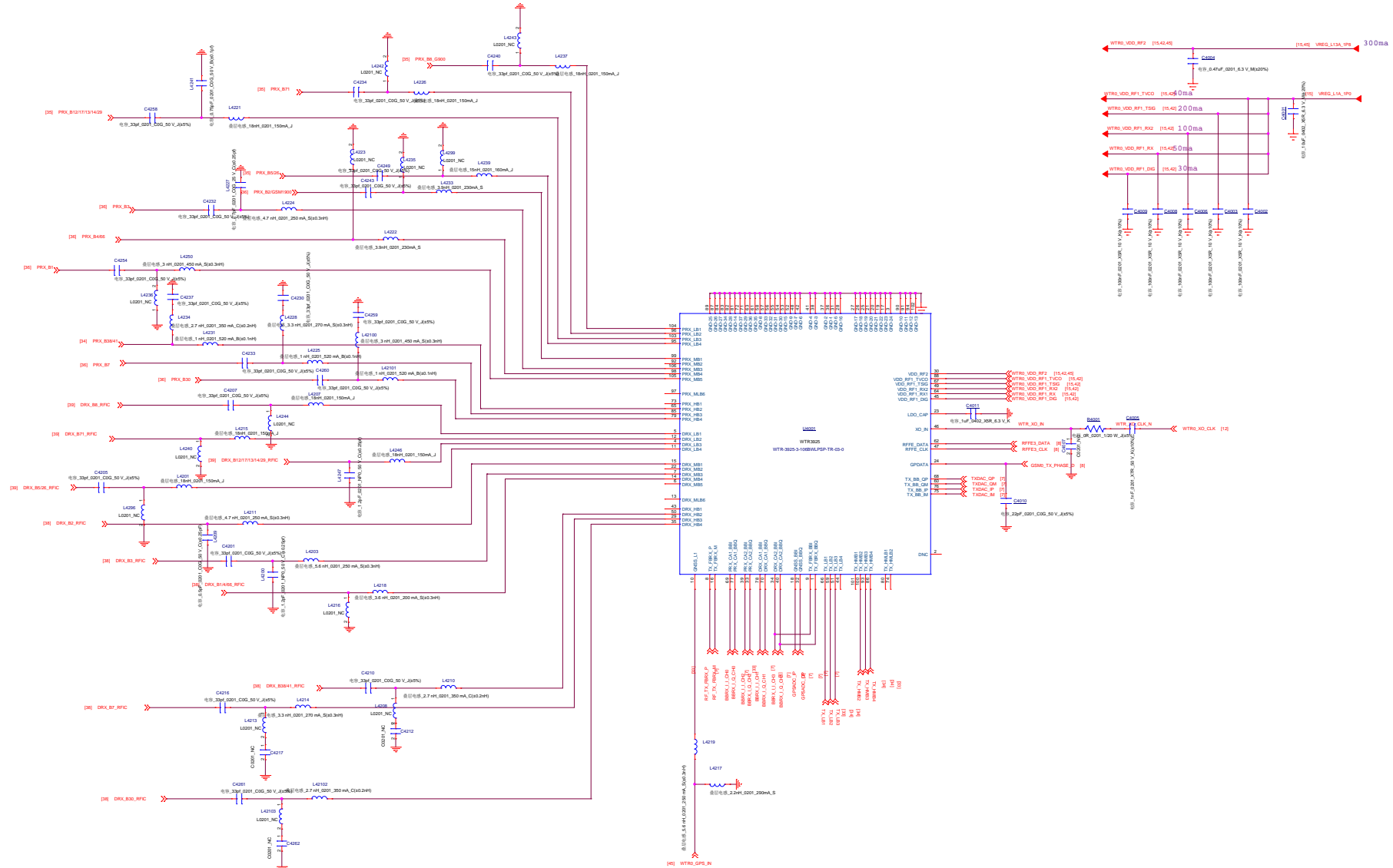
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Rev	Rev
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1/20/2016, May 26, 2016	1/20/2016, May 26, 2016

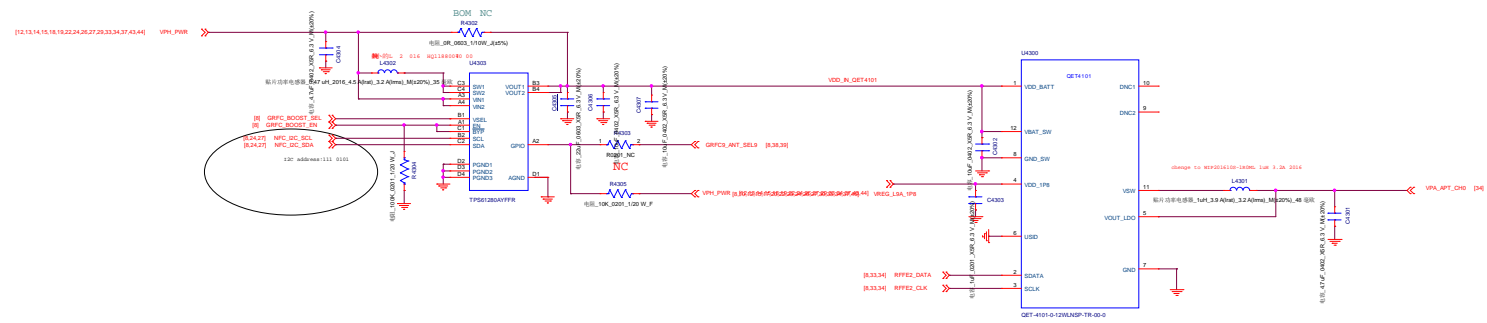
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Project Title		Sheet 1 of 10

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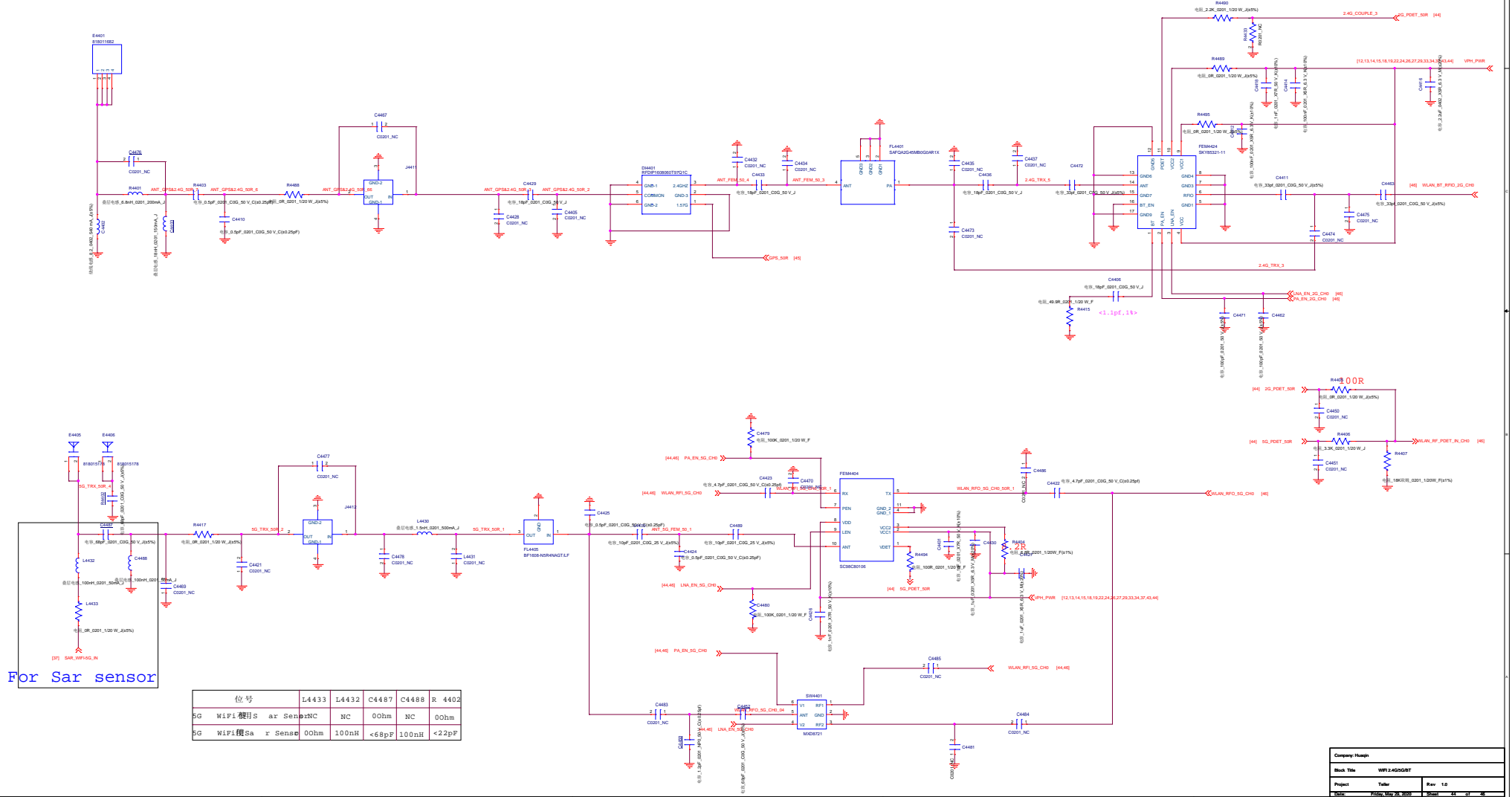
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Author	WTR3925	2023-01-01

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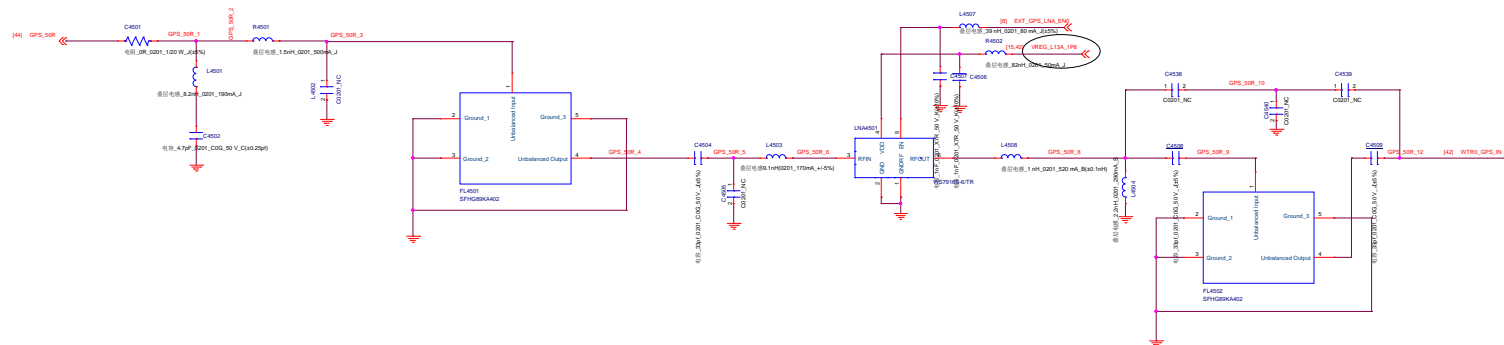


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Date		Printed	2010-01-01

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GPS



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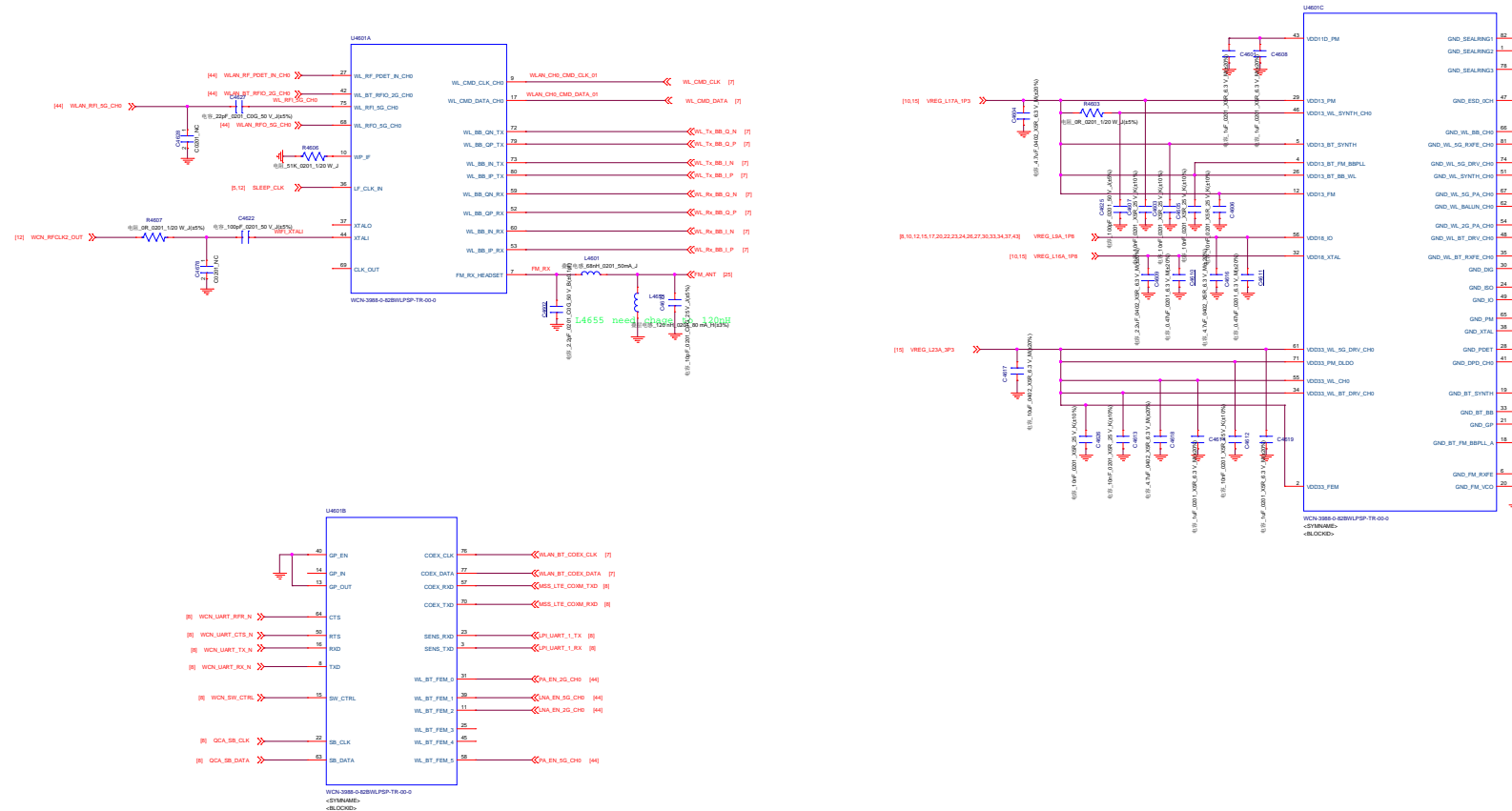


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Company: Huang	
Block Title: WCH3080	
Project: Teller	Rev: 1.0
Date: Friday, May 20, 2020	Sheet: 46 of 46