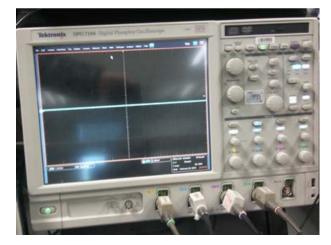
SM - A750FN/F/GN/G A7 2018 Block Diagram SUB ANT LB/MB/HB **MEMORY** KLMBG2JETD-B041: 64GB GNSS SPEEDY EMMC DI0:71 KLMDG4UCTA-B041: 128GB DRX0_I_N/P, DRX0_Q_N/P EMMC CMD/CLK/RST N DRY ANT HR/MR/IR DRX1_I_N/P, DRX1_Q_N/P DRX_TR_IN_B1_4 DRX_TR_IN_B2_3_34_39_66 EMMC RCIK ■ B40 **LFEM** PRX1_I_N/P, PRX1_Q_N/P RCAM3 SFML7E0H001 GNSS_ADC_VIN_I/Q RCAM3 MIPI 3LANE I R/MR ANT SUPER WIDE 8M RCAM3 EEP 12C RCAM2_RCAM3_SENSOR_I2C TX PA OUT GSM LB TX_TR_OUT_B1_2_3_4_66 RCAM2 TX_TR_OUT_B5_8_18_19_20_26_28 TY PA OUT GSM HR RCAM2 MIPI 2LANE TY PA OUT R1 5M TX TR OUT B13 TX_PA_OUT_B2 RCAM2_RST_N TX_TR_OUT_GSM_HB TX_PA_OUT_B3 TX TR OUT GSM LB RCAM1 TX PA OUT B8 MAIN TX_PA_OUT_B12_17 RCAM1 MIPL 41 ANE 24M **FEMID** TY PA OUT ROD RCAM1 FCAM I2C. RCAM1 RST N **TRANCEIVER** PRX_ANT_MB TX_PA_OUT_B5_18_19_26 PRX_ANT_LB TX PA OUT B4 66 RCAM1_FCAM_SENSOR_I2C LMSWKQGS-J79 TX_PA_OUT_B13 FRONT CAM VTCAMMIPI 4LANE 24M S5M925DA02-L630 VTCAM AF EEP 12C **SENSOR** SENSOR I2C PRX_TR_IN_B1, PRX_TR_IN_B2_G1900 6 Axis Sensor (ACC+GRYO) GYRO_INT, 6AXIS_INT PRX_TR_IN_G1800_B3, PRX_TR_IN_EGSM_B8 **HBANT** PRX TR IN B12 17, PRX TR IN B20 PRX_TR_IN_B5_18_19_26_G850, PRX_TR_IN_B4_66 SENSOR SENSOR_I2C Magnetic Sensor PRX_TR_IN_B7 VBAT_RF, VRF_PAM_VCC TRX_B7 SAW COUPLER HIGH-BAND PRX TR IN B40 **SENSOR** AP PAM PRX_TR_IN_B38_41 TRX B40 PRO XY 12C Proxy Sensor TRX B38 41 TY TR OUT B7 38 40 4 PRO XY_INT GPS ANT GPS LNA VDD TSP A3P3, VDD LCD 1P8, VDD LCD 3P0, VDD 1P8 A **OCTA DISPLAY** OCTA DC-DC G TCXO IN VCTCXO IN (26MHz) M IPI_DS IO 4 LAN E SM3004B LASSEN WLAN/BT/FM_SPDY WLAN_TXIP, WLAN_TXIN DIPLEXER WB RF 2G WIFI WLAN_TXQP, WLAN_TXQN VBUS_5V_OVP, V_BATTERY, VBAT, VDD_1P8 WL_TX_5G **BATTERY CON** SWITCH S5N5C12X01-6630 WLAN_RXIP, WLAN_RXIN FG_SENSE_P/N WL_RX_5G WLAN_RXQP, WLAN_RXQN **IF PMIC** IISB AP D P/D N IF CON AP_UART_TXD/RXD, CP_UART_TXD/RXD USB_D_P/N S2MU005X03 IF PMIC I2C. FG I2C/ALERT N MAIN FLASH CAM_TORCH_EN, CAM_FLASH_EN WLBT_TCXO_IN_26M NFC_CLK_26M NFC NFC_IRQ IF_PMIC_ACOR PN553A1EV/C102Y VRAT, VDD MI DO 2P0 VDD II DO 1P3 NFC_SWP OSC PMIC_AP32K 32.768kHz G3D/MIF/PWR EN SIM1_DATA SPEEDY PMC SIM1 CLK/RST POWER KEY BOOT LDO SIM & SD TRAY DET POWER_KEY SOC_PSHOLD (3 in 3) T_FL AS H_D[0:3]/CLK SOC_RESET_N, SOC_WRESET_N MOTOR SIM2_CLK/RST/DATA OCP CPUCLO CP PWR UP MAIN PMIC osc PMIC_ONOB, PMIC_IRQB_N S2MPU08X01-6030 26MHz PMIC_TCXO_IN_26M AP_TCXO_IN_26M HALL IC HALL_IN1 AH1897S RECEIVER SPK_AMP_I2C RCV_P/N **GRIP IC** GRIP_I2C AMP SPK_I2S3 TC305K MAIN MIC GRIP INT **AUDIO CODEC** MAIN MIC P/N FB_I2S1, PM_I2S0 SUB MIC **BTP CON** SAMSUNG EAR CON EAR_OUT_L/R SUB MIC P/N BTP IRQ

- 8-3. Flow chart of Troubleshooting.
- 8-3. Fluxograma de solução de

problemas.





osciloscopio

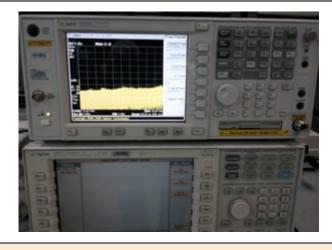
multimetro digital





fonte de bancada

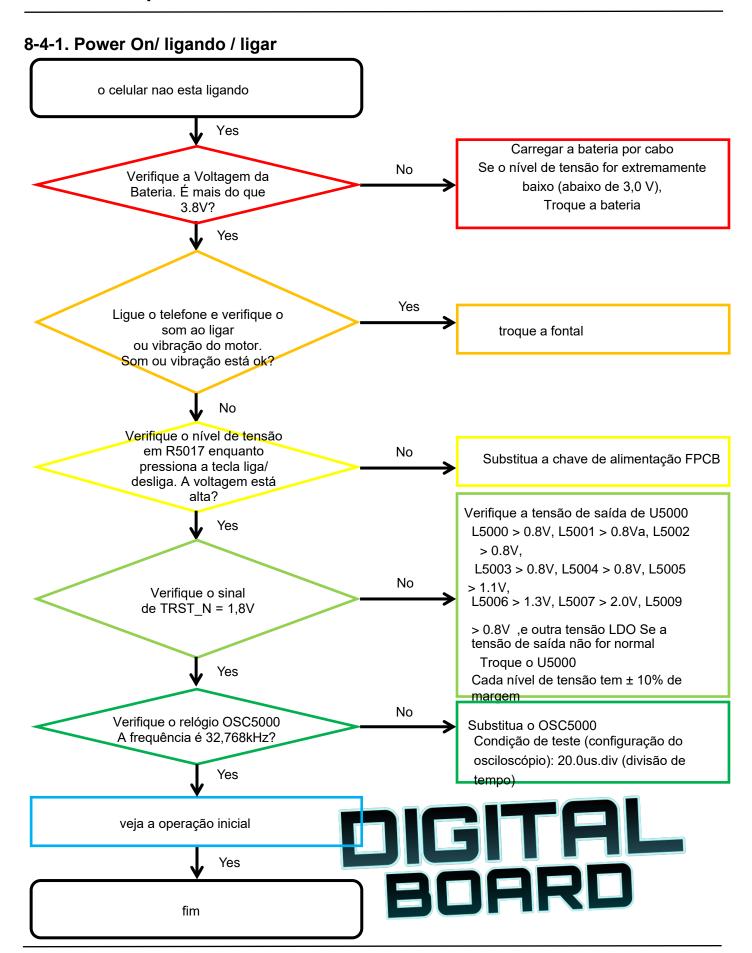
chaves e ferramentas



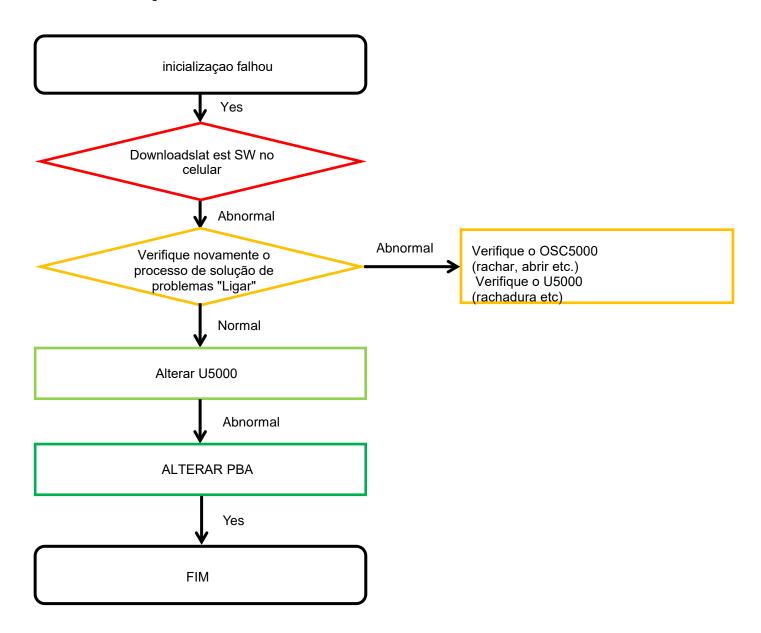


8960 & Spectrum Analyzer

ferro de solda

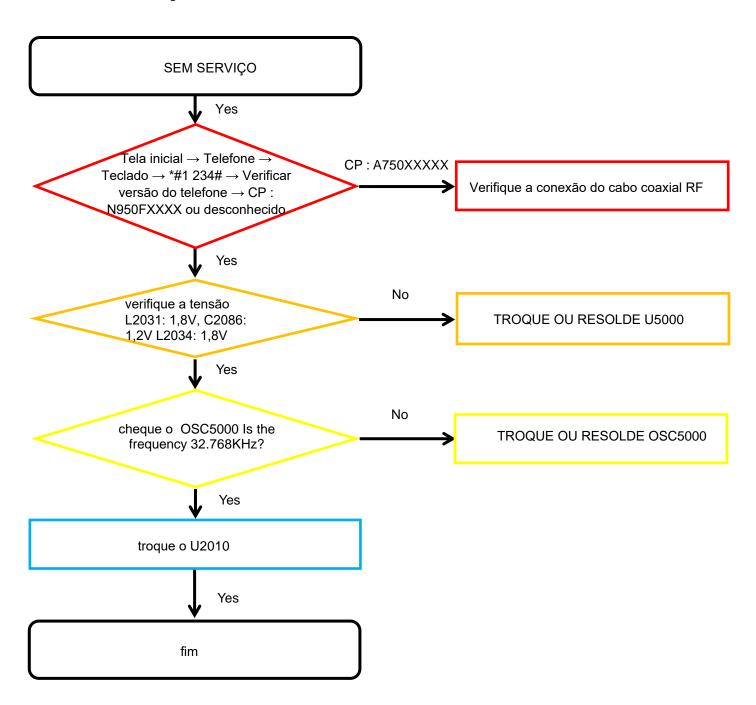


8-4-2. inicialização



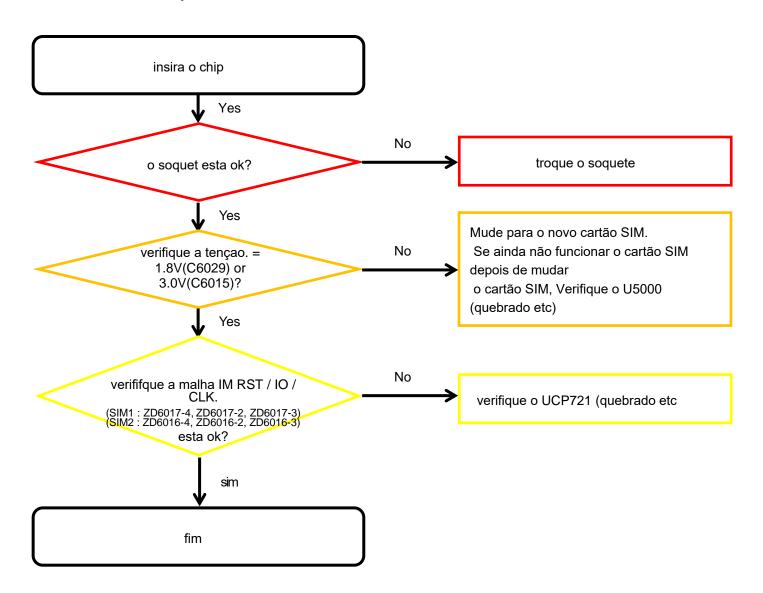


8-4-3. SEM SERVIÇO



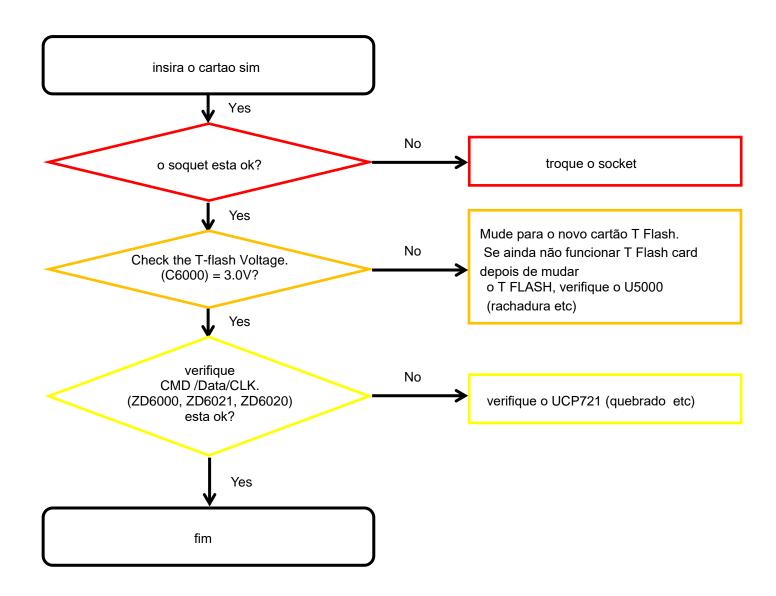


8-4-4. SIM Part/ chip



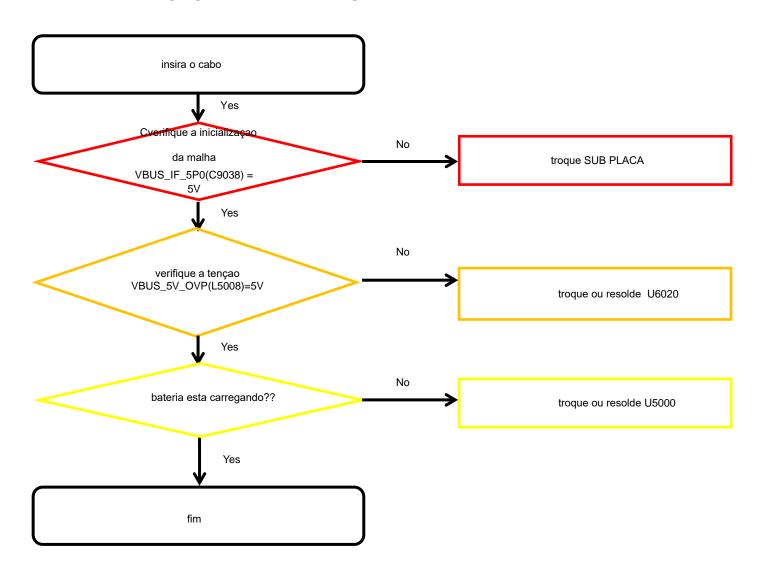


8-4-5. T-Flash Part/ cartao de memoria



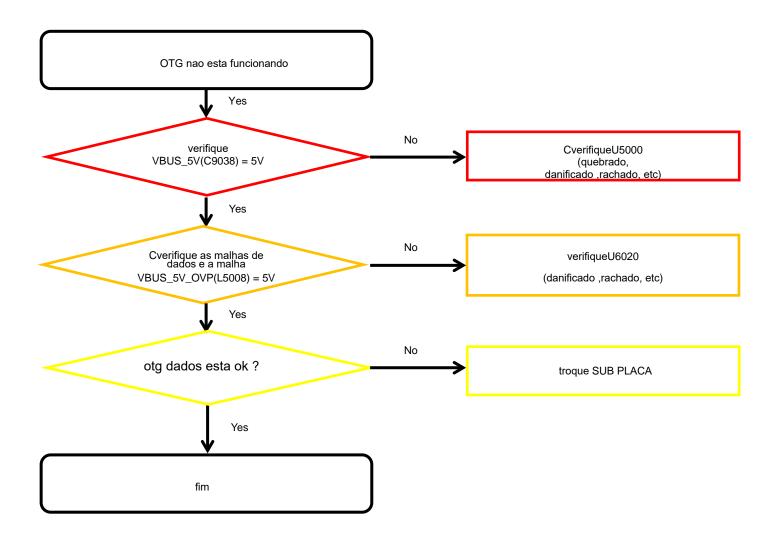


8-4-6. Cable Charging Part/ setor de carga /vbus



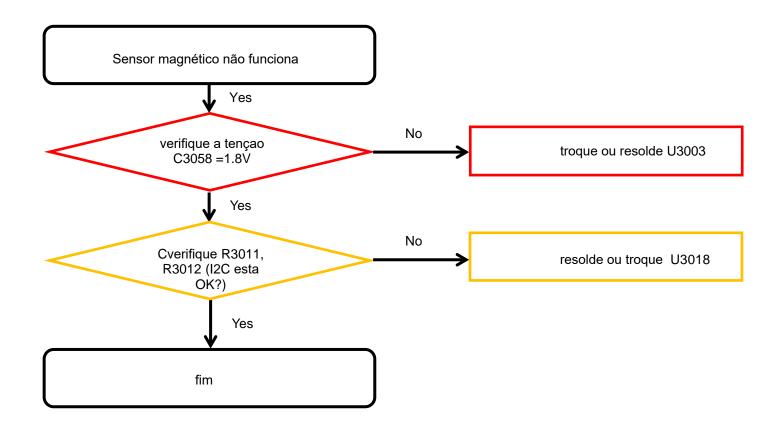


8-4-7. OTG/ dados otg



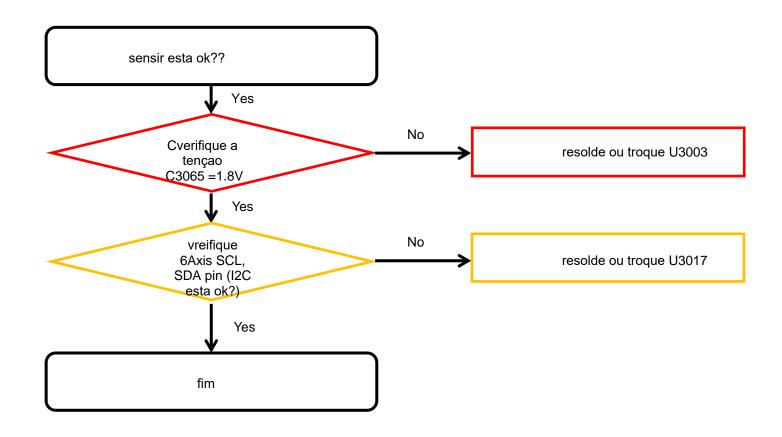


8-4-8-1. Magnetic Sensor/ sensor magnetico



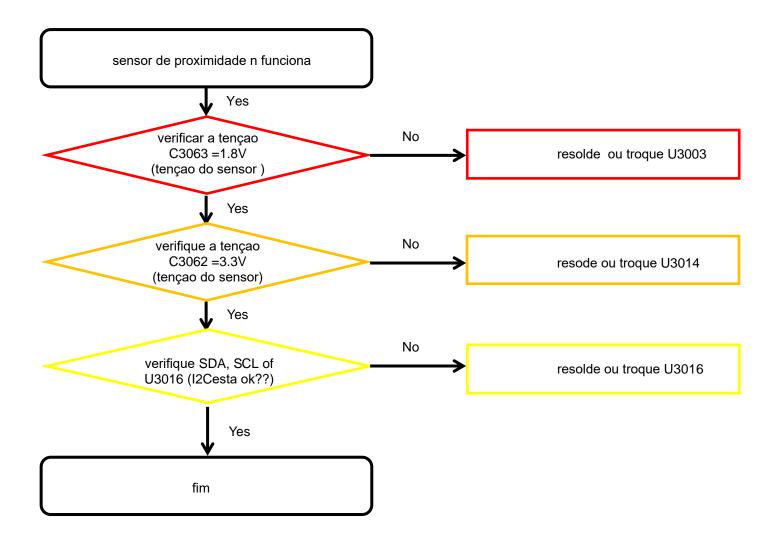


8-4-8-2. 6Axis Sensor /8-4-8-2. Sensor de 6 Eixos



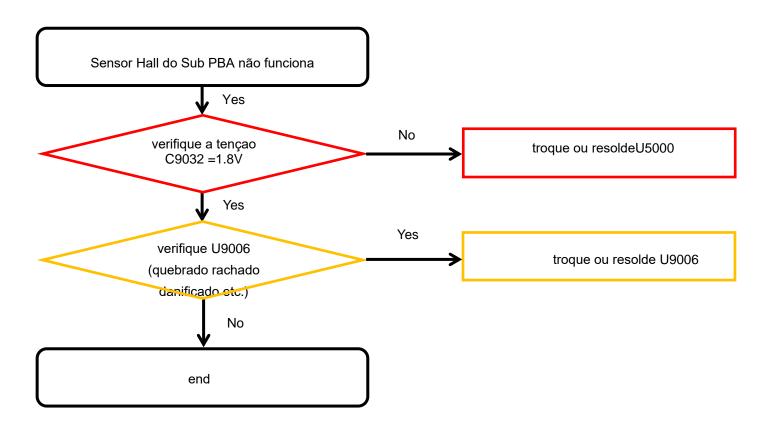


8-4-8-3. Proximity/8 sensor de proximidade



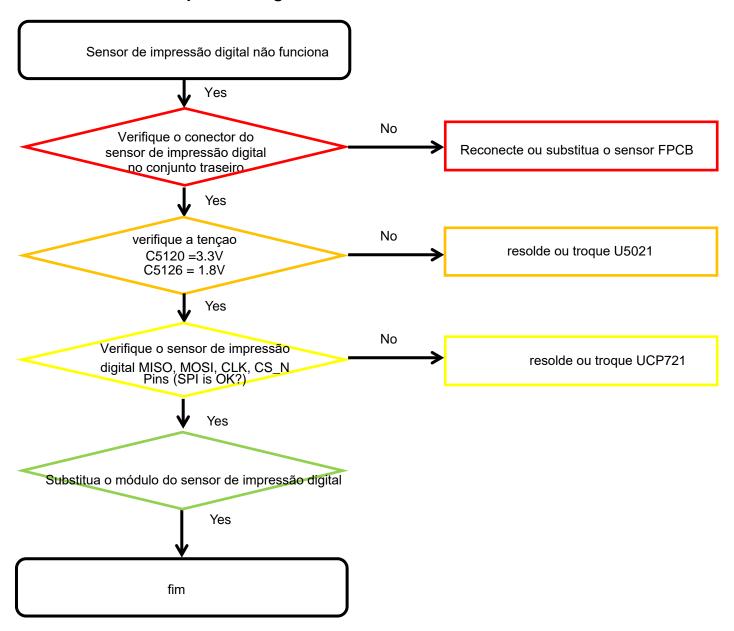


8-4-8-4. Hall Sensor/sensor hall



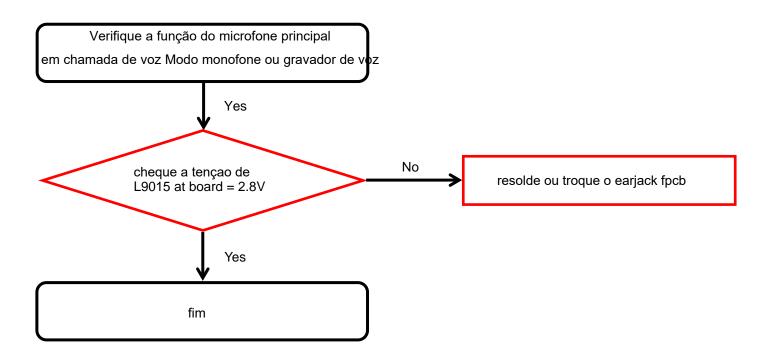


8-4-8-5. Sensor de impressão digital



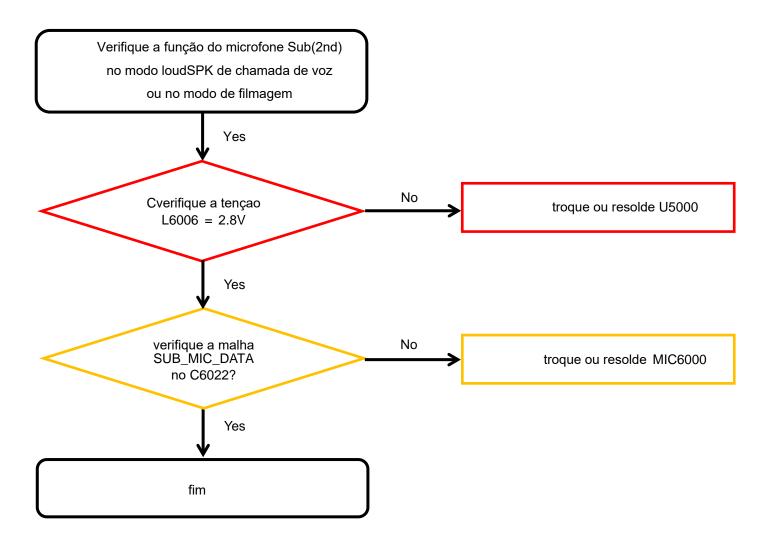


8-4-9-1. Microphone Part - Main MIC/ microfone



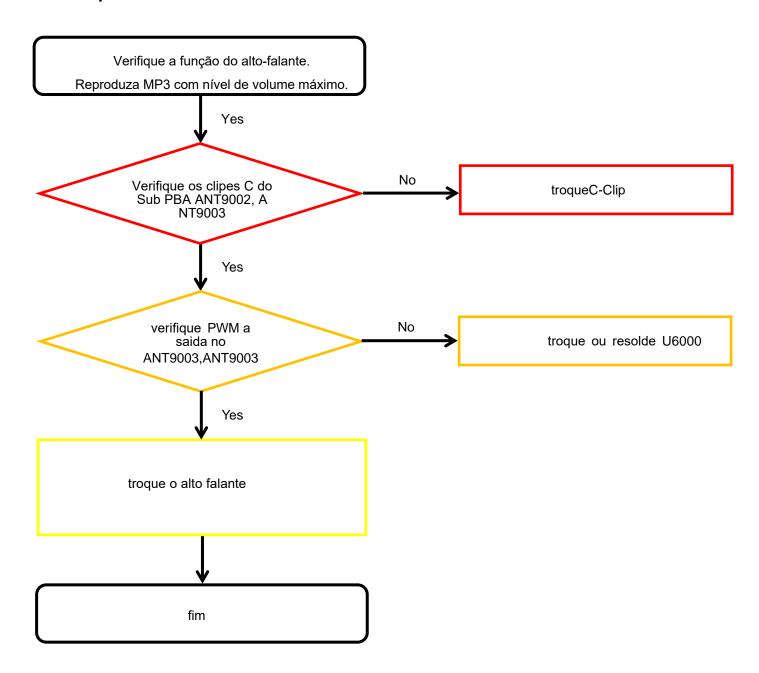


8-4-9-2. Microphone Part - Sub(2nd) MIC/microfone



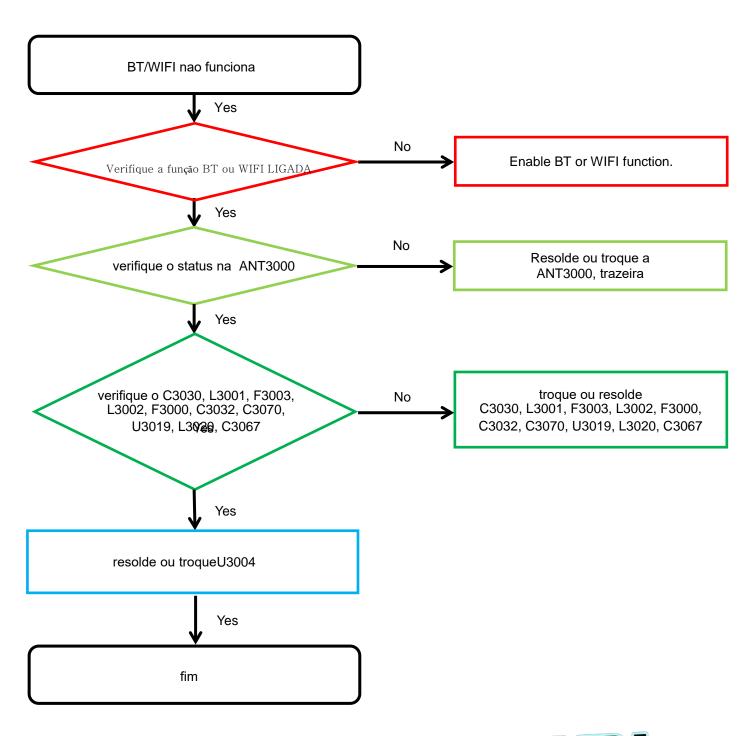


8-4-10. Speaker Part/ alto falante



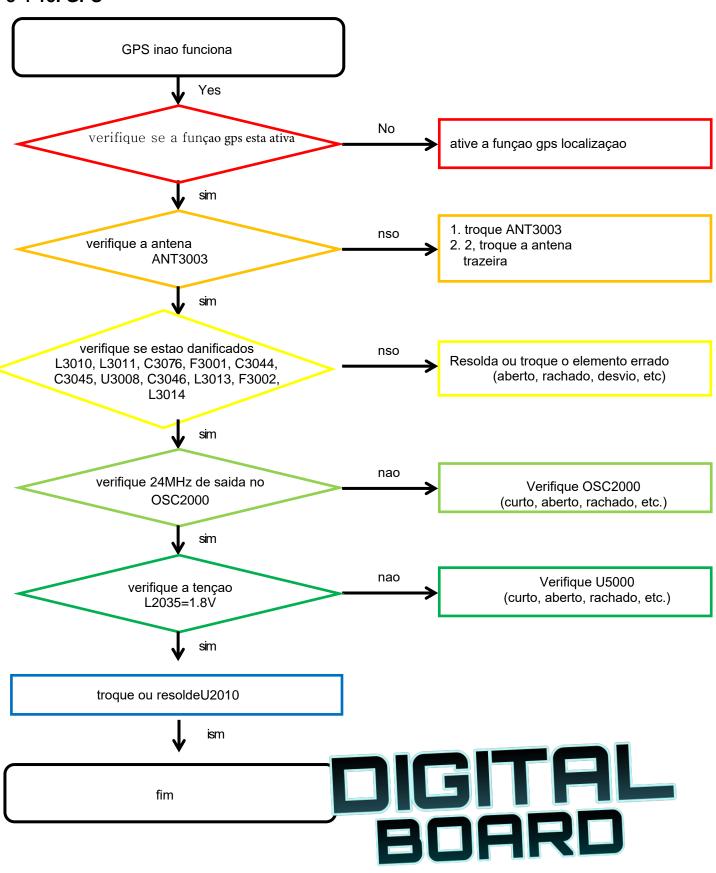


8-4-12. BT/WIFI

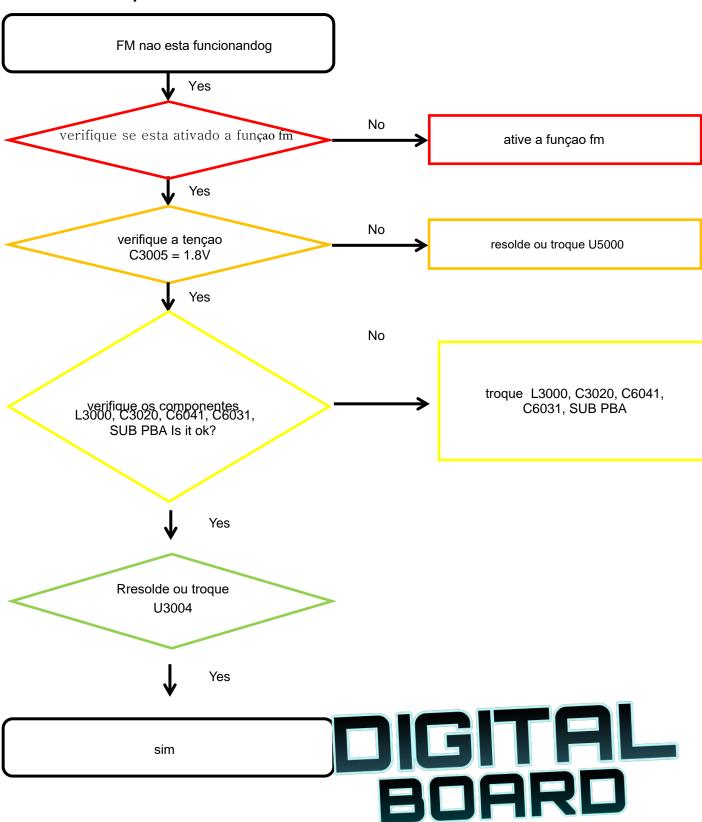




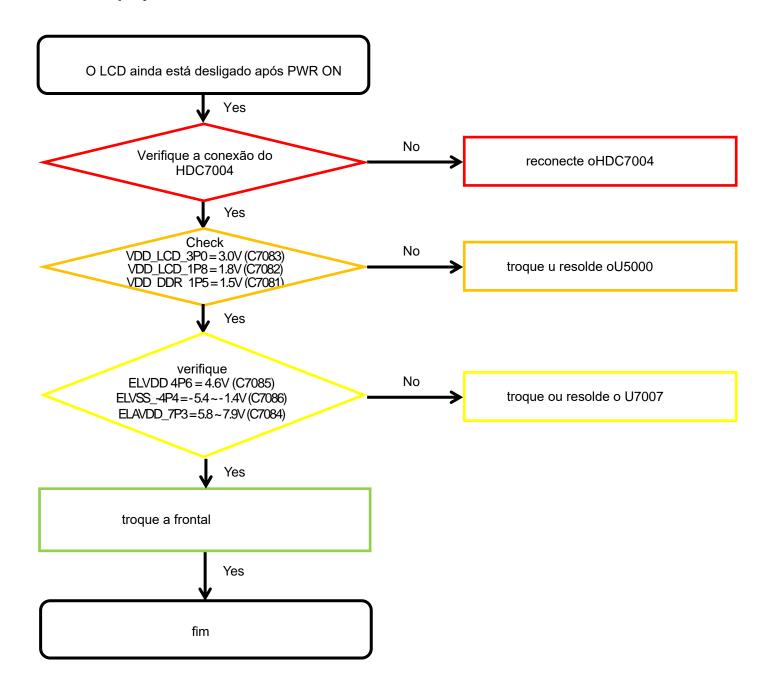
8-4-13. GPS



8-4-15. FM radio part

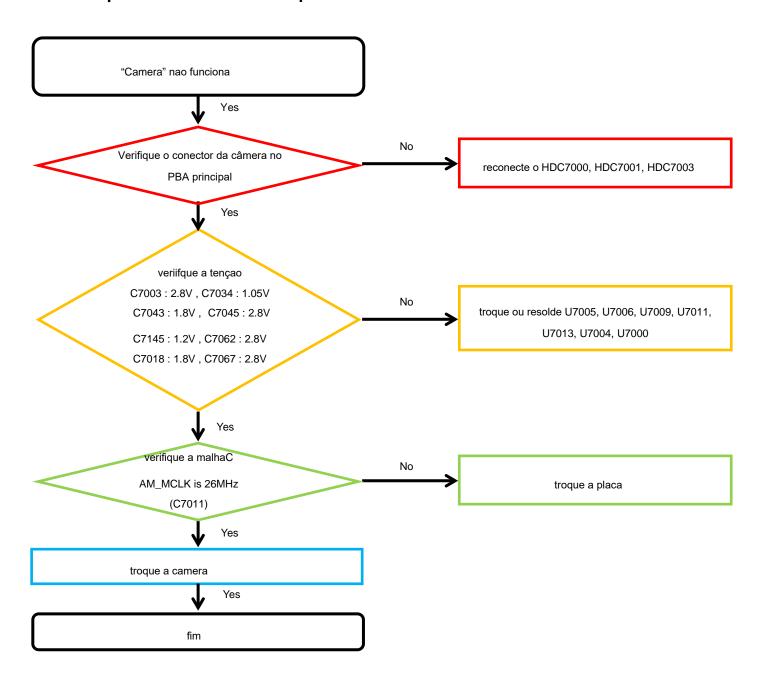


8-4-16. Display/ fontal



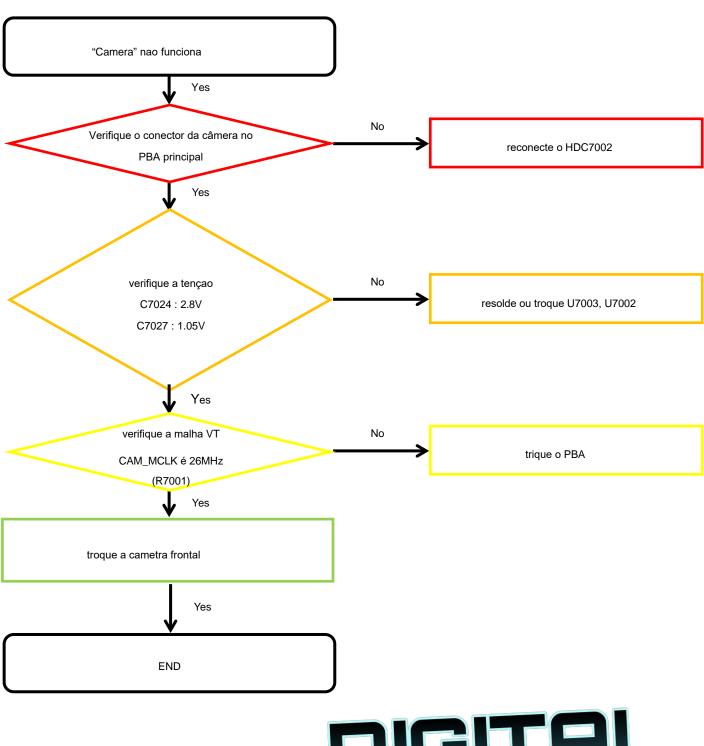


8-4-17. Triple Camera/ cametra tripla



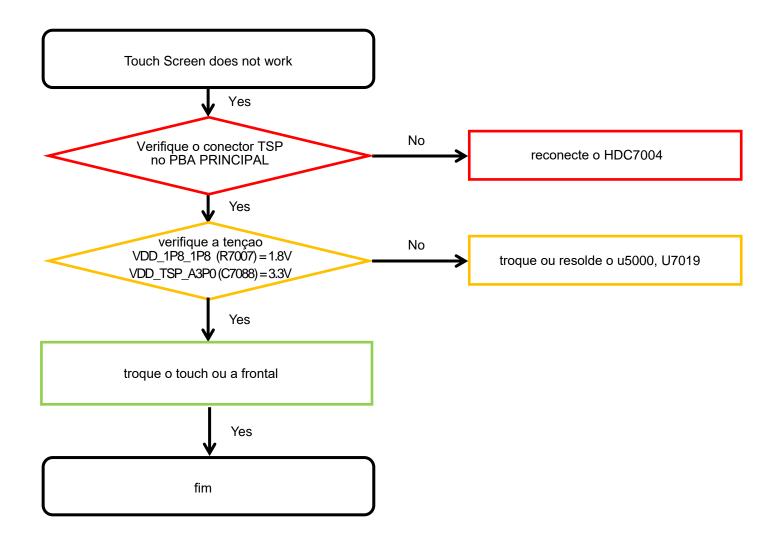


8-4-18. VT CAMERA frontal



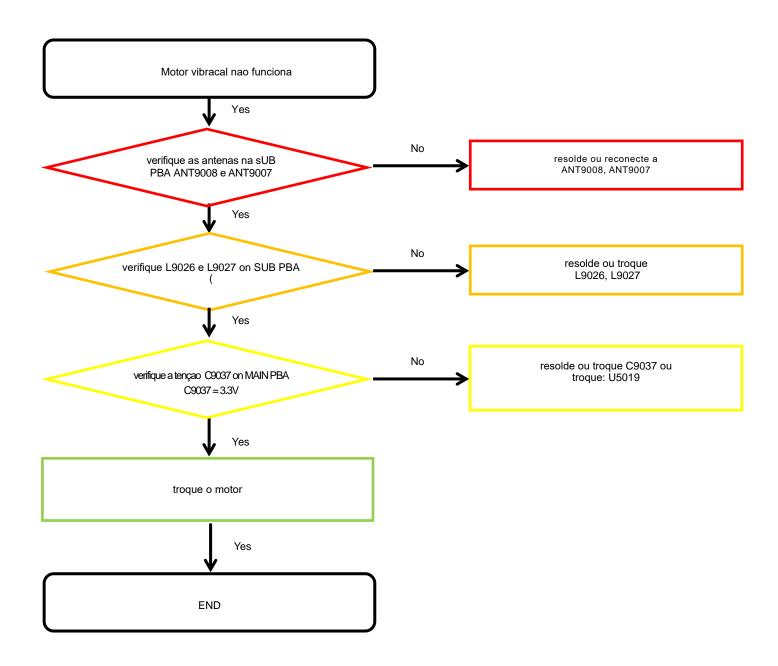


8-4-19. TSP/ touch





8-4-20. Motor/ vibracal





8-5. Service Schematics

- NC Point(Top View)

Ţ	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29
A				XMMC_E MBD_CLK	MBD_RD		VDDQ,M EM	VDD2_M EM		XEINT_19	XEINT_15	XEINT_16	VSS	VDD2_M EM	XEINT_1	VDDQ_M EM		хлтоо	XIDBGSE L	VDD1_M EM	XGPIO21	VDD2_M EM	VDDQ,M EM	XI2C5SCL	XGP1027	XGP1O25			
В			XMMC_C ARD_CLK	VSS	XMMC_E MBD_CM D	XMMC_E MBD_HW RESET	XMMC_E MBD_DS	XMMC_E MBD_D7	VDD1_M EM	XNWRESE T	XEINT_9	XEINT_10	XEINT_17	XEINT_18	XENT_13	XEINT_2	XEINT_0	XJTRSTN	XITDI	XGP9018	XGPIO23	XGP1O6	VSS	XI2C4SCL	XI2C5SD A	XGP1O26	XPWMTO UT_1		
с		VDD2_M EM	XMMC_C ARD_CM D	XMMC_S DIO_CLX	XMMC_S DIO_CM D	XMMC_S DIO_D3	XMMC_E MBD_D6	XMMC_E MBD_D3	XNRESET	XPWRRG TON_CO N		XEINT_8	XEINT_7		XEINT_21	XEINT_20	XEINT_5		хлск	хлмѕ	XGPIO19	XGP1O20	XI2C4SD A	XGPIO7	XGP1024	XUOCTSN	XUORTSN	VDD2_M EM	VSS
D	USB20D M	VDDQ_M EM	XMMC_C ARD_D0	XMMC_S DIO_D0	XMMC_S DIO_D1	XMMC_S DIO_D2	XMMC_E MBD_D0	XMMC_E MBD_D1	XPWRRG TON_SUB	XPSHOLD	XEINT_12	XEINT_11	XEINT_14	XEINT_6	XEINT_4	XEINT_3	XOM_2	XOM_0	XOM_1	XGPIO16	XGPIO15	XGP1022	XGPIOS	XIZC6SD A	XI2C6SCL	XUOTXD	XUORXD	VDDQ_M EM	XWLBT_V LDA_TX0 P
E	USB20D P	VSS	XMMC_C ARD_D3	XMMC_C ARD_D2																						XPWMTO UT_0	XWLBT_W LMR_SPD Y	VSS	XWLBT_V LDA_TXX N
,	IUSB30D ID_REXT 1	XUSB30D RD_SS_RX 2_N	VSS	XUSB20T XRTUNE XUSB30D	XMMC_C ARD_D1	VDDQ18 33_MMC_ CARD VDDP18	XMMC_E MBD_D4	XMMC_E MBD_D2		XPWRRG TON_G3D	XSPEEDY, PMIC	XPWRRG TON_MAI N	VSS	XRTCXTI	XRTCXTO	XUART_D EBUG_TX D XUART D	XCLKREQ	XCLKOUT 2	XI2C2SD A	XIZCZSCL	XGP100	XGP9O2	XI2C3SD A	XI2C1SD A	XI2C1SCL	XWLBT_W LSL_SPDY XWLBT B	VSS	XWLBT_W LDA_TXIP	XWLBT_I LDA_TXI
G R	D_SS_RX 1_N (USB30D	RD_SS_RX 1_P XUSBBOD	RD_SS_RX 2_P XUSB30D	RD_SS_TX 1_P XUSB300	AVDD33	MMC_CA RD	MMC_EM BD_SDIO	MMC_EM BD_SDIO	VSS	VSS	VSS	LL_DORP HY0	PLL_DDR PHY0	VSS	XDRAMO_ ZQ	EBUG_RX D	XEINT_22	XCLKOUT	XGPIO1	XGP9017	XOCP_CP UCL0	XI2C3SCL	XI2C0SD A	XBOOTL DO	AVDD08	TMR_SPD Y AVDD12	XWLBT_B TSL_SPDY AVDD18	VDDQ_M EM XWLBT W	VDD1_N EM
н	2_N	RD_SS_TX 2_P	RD_SS_TX 1_N	RD_REXT 2	USB20_P HY	VDD09_U S820_PHY	VSS AVDD18	VDD_FSY S	VDDQ06_ DRAM0	VDDQ05_ DRAM0	VDD_MIF	VDD_MIF	VDD_MIF	VDDQ06, DRAM0	VDDQ05_ DRAM0 VDD CP	VDD18_R TC	XEINT_23	VDD08_A LIVE VDD018	VSS	VDD08_A LIVE	VSS VDDO18	XGPIO4	XIZCOSCL	VDDQ18_ WLBT	WLBT_WP LL	WLBT_WP LL AVDD18	WLBT_WP LL XWLBT W	IAD_RXQ P	LAD_RXX N
J	EM	VDD1_M EM XADCOAL	XADCOAL	XADCIAI N,0 XADCIAI	AVDD18_	AVDD18_ PLL_USB AVDD33	USB20_P HY VDD09_P	VDD08_F SYS_PPC	VDD_G3 D VDD_G3		VDD_INT. M	DRAMO_ CLK	VDD_INT	TNLOOV	UCL2_PP C AVDD18	VDD_CP UCL2 VDD_CP	VDDJNT	ALIVE_EX TO VDD_CP	VDD_CP	VSS VDD_CP	ALIVE_EX TO VDD_CP	VDD_WB G VDD_WB		VDD_W8 G VDD08_T	AVDD18_	WLBT_DA C AVDD08	LAD_VRE FP AVDD12	XWLBT_W LAD_RXIP	XWLBT_V LAD_RXII VDD2_N
K 1	N.5	N.3 VDD2.M	N_1 XADCOAI	N,2 XADCOAL	USB30_P HY	USB30_P HY VDD09_U	L_USB	VDD_FSY	D VDD.G3		VDD_G3 D VDD_G3		VDD_G3 D VDD.G3	VDD_INT	PLL_MER GED1 VDD09_P	UCL2 VDD.CP	VSS VDD09_P	UCL2 AVDD18_	UCLO VDD_CP	UCLO VDD.CP	UCL0_M	G G		CXO	WLBT_AD C	WLBT_DA C0 AVDD18_	WLBT_AD C VDDO18	VSS ZO0 ME	EM TOTAL
L Í	N,7	EM	N_6	N_4	AVDD18_	SB30_PHY AVDD33_	VSS VDD09 U	S AVDD18	D VDD G3		D_M VDD G3		D VDD G3	TNLOOV	LL_MERG ED1	UCL2	LL_MERG ED0	PLL_MER GED0	UCLO VDD CP	UCLO VDD CP	VSS VDD CP	VDD_INT	VSS VDD08 A	AVDD08	AVDD08_	TCXO XGNSS S	GNSS L	M XGNSS A	M XGNSS,
M D	VSS KCII_MC	XGPIO9	XGPIO14 XCI2_MC	XGPIO3	USB30_P HY	USB30_P HY VDD08_G	SB30_PHY	TS_G3D VDD_INT_	D VDD INT	VDD_G3	D	VSS VDD_G3	D	VDDJNT		VDD_MIF	VSS VDD_MIF	TALCON	UCT0	UCL0 VDD_CP	UCLQ_M VDD_CP	G VDD_WB	LIVE AVDD08_	ADC_GNS S AVDD12_ GPLL GN	GPLL_GN SS	PEEDY VDD08_D	NA_EN AVDD18_ ADC_GNS	DC_VIN.I	Q XGNSS_0
N .	LK GSPHSI2	LK XISPHSI2	LK XGPIOR	XGPIO10	XGPIO12	PADC XGPIO11	GPADC	M VDD INT	VSS	D VDD_G3	VDD_G3	D VDD_G3	AVDD18_	VDD_INT	VSS	VDD_MIF	VDD MIE	VDD_INT_		UCL0 VDD_CP	UCLO_M	G AVDD18_	MPLL_CP	SS AVDD18_ GPLL GN	AVDD12_	UL_GNSS	S VSS	XCP_ADC	EXT XCP_AD
R	CZSCL GSPHSI2	C3SCL XISPHSI2	XISPHSI2	XGP1O29	AGPIO12	VDDQ18_	XGPIO28	VDD_INT		D VDD INT	D_M VSS	D AVDOIN	PLL_G3D VDD09_P				VDD08,A	M AVDD18_ TS_CPUC	VDD_CP	UCL0 VDD_CP	VSS	MPLL_CP VDD08_A	MPLL_CP AVDD08_	SS AVDD18_	ADCO_CP	DC0_CP XCP_ADC	XCP_ADC	O_P_IN XCP_ADC	O_P_JP XCP_AD
T	C2SDA VSS	XISPHSI2 COSCL	C3SDA XISPHSI2 C1SDA	XISPHSI2 C1SCL	AVDD18_ MIPLDP	EXT1 AVDD18_ MIPLDP	AVDD12 MIPI_DP	VDD_INT	VDD_INT_ M	VDDJNT			VDD_CP UCL1_PP				VDD_INT_	LO	VDD_CP	vss	VDD_CP	LIVE	ADCO_CP AVDD18_ ADC1_CP	ADC0_CP AVDD08_ ADC1_CP	AVDD12_ ADC1 CP	O_D_QP XCP_ADC	O_D_QN XCP_ADC	O_P_QP XCP_ADC	O_P_QN VSS
U	MIPLDS	XMIPLDS 14L_DO_P	VSS	XV_TES	HY	HY AVDD09_ MIPI	HY VSS	VDD_INT	VSS	VDDJNT	VDD_CP UCL1		C VDD_CP UCL1	VSS	VDD_CP UCL1	VSS	VDD_CP		VDD_CP		VDD_CP		AVDD12_ ADC2_CP	VREFP_A DC1_CP	ADC1_CP	O_D_IP XCP_ADC 1_D_QP	O_D_IN XCP_ADC 1_D_QN	1,P,IP XCP,ADC 1,P,QP	XCP_AD
v s	MIPLDS 4L_D1_N	XMIPLDS I4L_D1_P	XMIPL,DS I4L,CLK_ N	XMIPLOS 14LCLK_P	XMIPL DS I4L_VREG _OP4V	AVDD09_ MIPI	VSS	VDD_INT		TNLOOV	VDD_CP UCL1		VDO_CP UCL1		VDD_CP UCL1		VDD_CP		AVDD18_ BPLL_CP		VDD_CP		AVDD18_ ADC2_CP	AVDD08_ ADC2_CP	VREFP_A DC2_CP	XCP_ADC 1_D_IN	XCP_ADC 1_D_IP	VSS	XCP_AD 1_P_QN
w k	MIPL DS 4L D2 N /DDQ_M	XMIPL DS I4L D2 P VDD2_M EM	XMIPLDS I4LD3_N VSS	XMIPL DS IAL_D3_P VSS	XMIPLCS	AVDD09_ MIPI AVDD09_ MIPI	VSS	VDD_INT		VDD_INT_ VDD_INT_ M	VDD_CP UCL1 VSS	VSS VDD_INT	VDD_CP UCL1 VSS	VSS TML_DDV	VDD_CP UCL1 VSS		VDD_CP		VDD08_B PLL_CP VDD_CP		VDD_CP		AVDD18_ DAC1_CP AVDD18_	AVDD08_ DAC1_CP AVDD08_ DAC0_CP	VSS	XCP_ADC 2_D_QP XCP_ADC		VDD2_M EM XCP_ADC	VSS XCP_AD
AA X	EM OMEPLES 2LD1_N	XMIPLCS IZL_D1_P	XMIPLCS IZLCLX_P	XMIPLCS 12L_D0_N	IZL_DO_P	VSS	VDDJNT	VDD_INT_		VDD_MIF		VDO_INT	AVDD18_ OTP	XOTPVPP	VDD08_O		VDD_CP		VDD08_A LIVE_CP		VDD_CP	VDD_CP	AVDD18_ ETDAC_C	AVDD08_ ETDAC_C		2_D_IP VSS	2_D_IN VSS	XCP_ADC 2_P_QP	XCP_AD 2_P_QN
AB	/DD1_M EM	XMEPLCS 12L_CLK_ N	VSS	XMIPLCS 14LA_DO_ N	XMIPLCS I4LA_DO_ P	TMLOOV	VDDQ18, AUD	VSS	VDDQ06_ DRAM1	VDDQ06, DRAM1	VDD_MIF	VDDQ06, DRAM1	VDDQ06_ DRAM1	VDD_MIF	VDDJNT	VDDQ18_ EXT2	VDD_CP		VDDQ18 30_USIM1 CP	VDDQ18 30_USIM2 CP	XCP_EXT_ LNA_ON4	XCP_EXT_ LNA_ONO	XCP_EXT_ LNA_ON1	VSS	VDDQ18_ RFINTF_C P	XCP_DAC 1_QP	XCP_DAC 1_QN	VDDQ_M EM	VDD1_N EM
AC 3	MIPLCS 4LA_D1_ N	XMIPLCS IALA_D1_ P	XIMEPE_CS I4LA_CLK_ N	XMIPLCS IALA_CLK_ P		XAUD J2S 3_SDO	XAUD_I2S 3_BCLK	VDD09_P LL_AUD	VDD09D_ PLL_AUD	VSS	VDD09_P LL_DDRP HY1	VDDQ06, DRAM1, CLK	AVDD18_ PLL_DDR PHY1	XDRAM1, ZQ	XSPI1CLK	VSS	VDDQ18_ ALV_GPI OPERI_CP	VDDP18_ USIM1_C P	VDDQ18 30_USIM0 _CP	VDDQ18_ ALV_GPI OPERI_CP	VDDQ18_ RFINTF_C P	XCP_RFIC 0_RESET	XCP_ANT _SW2	XCP_AIT_ SW0		XCP_DAC	XCP_DAC 1,IN	XCP_DAC 0_QP	XCP_DA 0_QN
AD I	MIPLCS 4LA_D2_ N	XMIPLCS I4LA_D2_ P	XMIPI_CS I4LB_DO_ P		XAUD_F M_SPDY_ M	XAUD J2S 3_SDI	XAUD_I2S 3_WS	XAUD_I2S 1_SDI	XAUD_I2S 0_MCLK	AVDD18_ PLL_AUD	XSP10MIS O	XSPIOMO SI	XSPI1MIS O	XSPI1CSN	XSPI1MO SI	XCP_GPI O7	XCP_USI M1_CLK	XCP_USI M1_RSTN	XCP_USI M1_DATA	VDDP18_ USIM2_C P	XCP_GPI O5	XCP_TX0_ GSM_POL AR	XCP_AIT_ SW1	XCP_AIT_ SW2	XCP_AIT_ SW3	VSS	XCP_ETD ACO_N	XCP_DAC 0_JP	XCP_DA 0_IN
AE I	MIPLCS 4LA_D3_ N	XMIPLCS IALA_D3_ P	XMIPI_CS I4LB_DO_ N	XMIPLCS IALB_D1_ P																						VSS	XCP_ETD AC0_P	XCP_ETD ACl_P	XCP_ETI AC1_N
AF V	/DDQ_M EM	VSS	XMIPLCS I4LB_D1_ N	XMIPLCS IALB_D2_ P	XMIPLCS I4LC_DO_ P	14LC_D3_ N	XMIPI_CS 14LC_D3_ P	XAUD_IZS Z_WS	XAUD_I2S 0_BCLK	XAUD_IZS 0_WS	XSP10CLK	XSP10CSN	CLK_CTS	XISPUSIO_ SCLO_MO SI_TXD	XCP_RF_ ON0	XCP_USI MO_RSTN	XCP_USI MO_DATA	XCP_USI M0_CLK	VSS	XCP_GPI O4	XCP_MIPI _RFFE3_S DATA	XCP_MIPI _RFFE3_S CLK	XCP_EXT_ LNA_ON9	XCP_EXT_ LNA_ON1 0	XCP_EXT_ LNA_ON7	XCP_ANT _SW3	VSS	VDD2_M EM	VDDQ_N EM
AG		VDD2_M EM	XMIPLCS I4LB_CLX_ P	XMIPLCS I4LB_D2_ N	XMIPLCS I4LC_DO_ N	XMIPLCS I4LC_D2_ P	XMIPLCS 14LC_CLX P	VSS	XAUD_I2S 0_SDI	XAUD_IZS 0_SDO	XUSIZ_SC L1_SPICL K_CTS	XUSIZ_SC LO_MOSI_ TXD	SDA0_MI SO_RXD	XISPUSIO_ SDA1_NS S_RTS	XCP_GPI O6	XCP_GPI O1	XCP_GPI O_ALV3	XCP_GPI O_ALV4	XCP_GPI O_ALV1	XCP_GPI O3	XCP_MIPI _RFFE2_S CLK	XCP_MIPI _RFFE0_S DATA	XCP_MIPI _RFFE0_S CLX	XCP_EXT_ LNA_ON1 1	XCP_EXT_ LNA_ONZ	XCP_ANT _SW6	XCP_ANT _SW4	XCP_ANT _SW0	VSS
АН			XMIPLCS I4LB_CLX_ N	XMIPLCS I4LB_D3_ P XMIPLCS	VSS VMIDLOS	XMIPLCS I4LC_D2_ N XMIPLCS	XMIPLCS 34LC_CLX N	XAUD_I2S 2_SDX	XAUD_I2S 2_BCLK	VSS	XUSI2_SD A1_NSS_R TS XUSI2_SD	XISPUSII, SDAI,NS S_RTS XISPUSII	XISPUSI1_ SDA0_MI SO_RXD	XCP_GPI O2	XCP_GPI O0	VSS	XCP_GPI O_ALV0	XCP_GPI O_ALV2	XCP_TCX O_AFC	XCP_MIPI _RFFE1_S CLK	XCP_MIPI _RFFE2_S DATA XCP_MIPI	VSS	XCP_EXT_ LNA_ON8	XCP_EXT_ LNA_ON6	XCP_EXT_ LNA_ON3	XCP_ANT _SW5	XCP_ANT _SW1		
AJ	VSS	VSS	VSS	IALB_D3_ N	IALC_D1_ N	IALC_D1_ P	VDDQ_M EM	VDD2_M EM	VSS	VDD1_M EM	A0_MISO _RXD	SCLO_MO SL_TXD	SCLL_SPI CLK_CTS	VDDQ_M EM	XCP_UAR T1_TXD	XCP_UAR T1_RXD	VDD2_M EM	VSS	XCP_RFIC 0_SPEEDY	VSS	RFFE1_S DATA	VDD1_M EM	VDDQ_M EM	XCP_EXT_ LNA_ONS	XCP_ANT _SW7	VDDQ_M EM	VSS	VSS	22.V

