

# SM-J250G\_REV0.8

2017. 10. 19

**SHEET 1 : RF PART**

**SHEET 2 : TRCV,DIVERSITY,CONNECTYBITY**

**SHEET 3 : BASEBAND(MSM8917-4)**

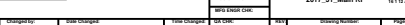
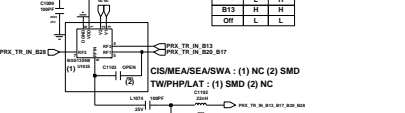
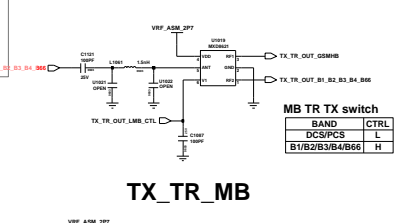
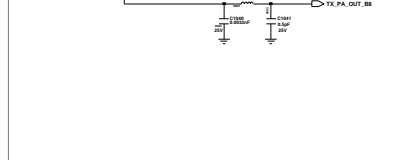
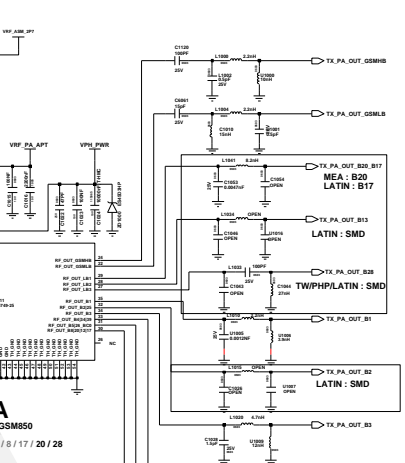
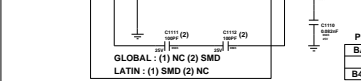
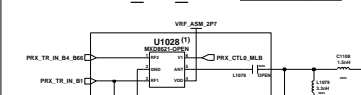
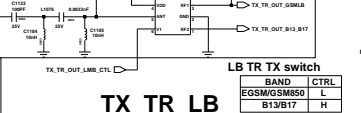
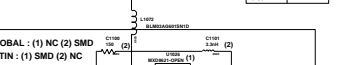
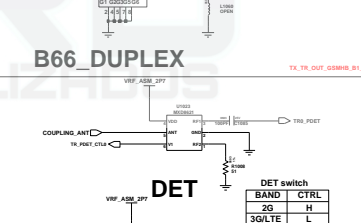
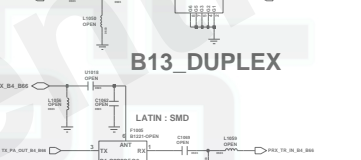
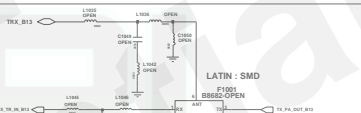
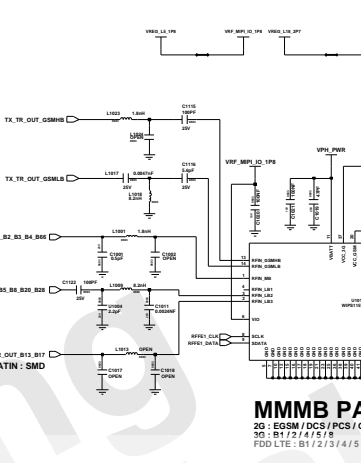
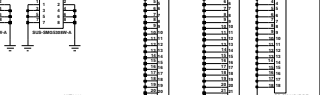
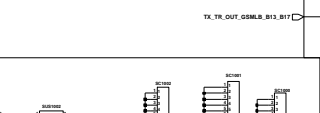
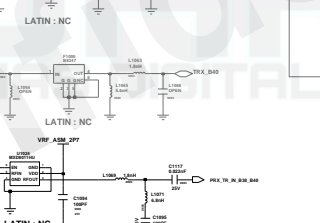
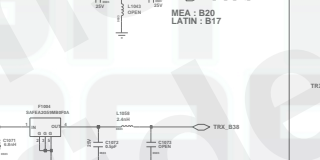
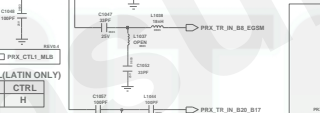
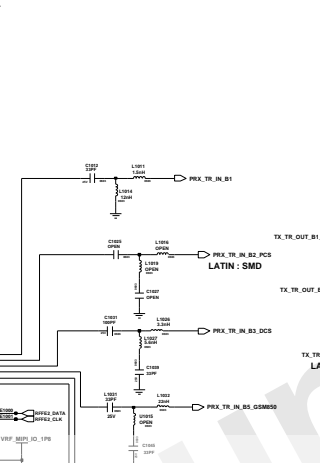
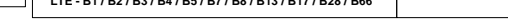
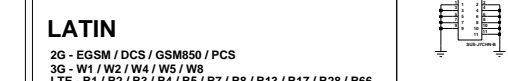
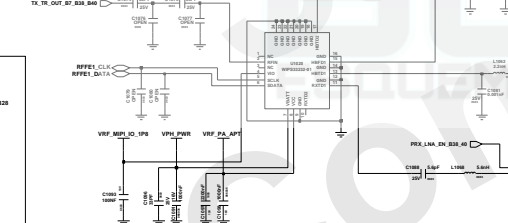
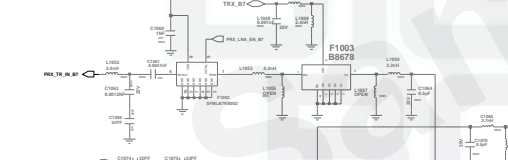
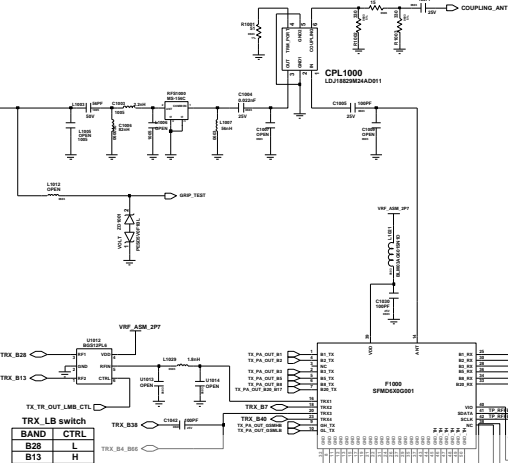
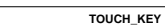
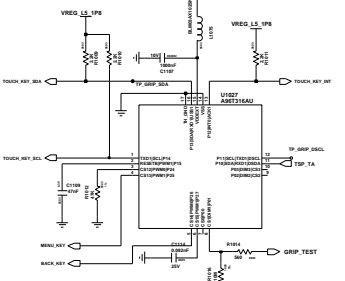
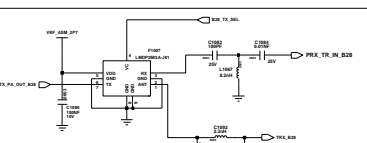
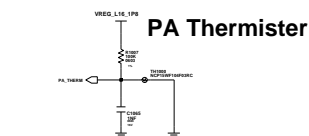
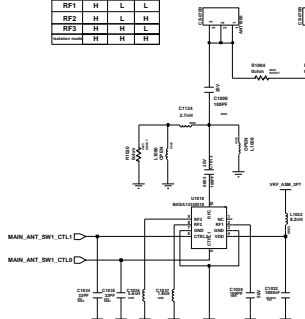
**SHEET 4 : PMIC(PM8937), MUIC(SM5703A),INTERFACE**

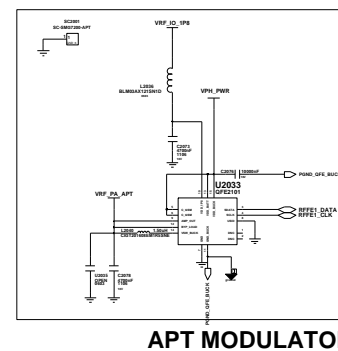
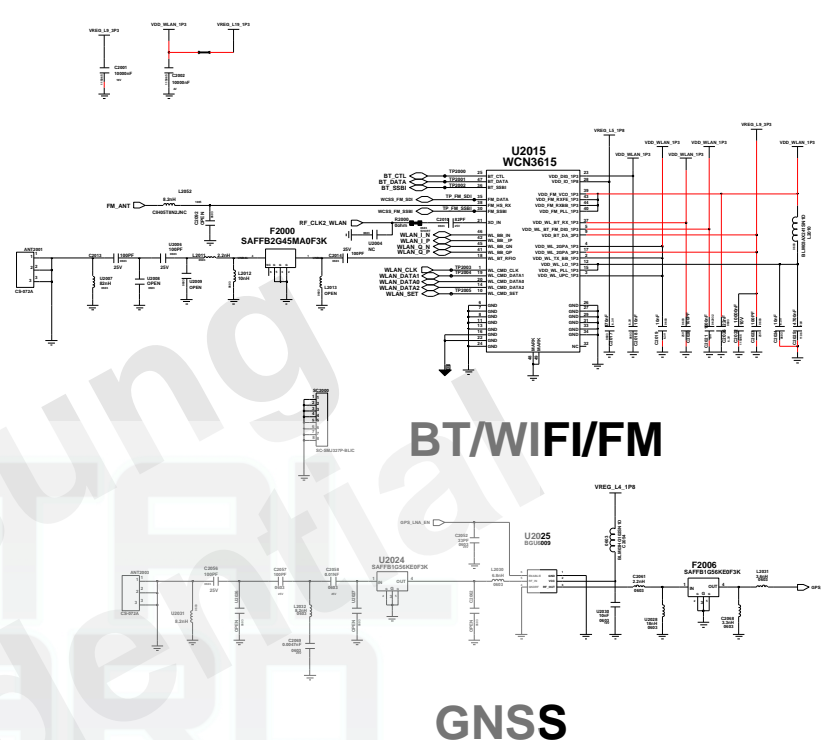
**SHEET 5 : MEMORY,AUDIO**

**SHEET 6 : CAMERA, DISPLAY, SENSOR,SOCKET,KEY**



MODE	VDD	CTL1	CTL2
RF1	H	L	L
RF2	H	L	H
RF3	H	H	L
Initiation mode	H	H	H





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## UCP3001-1 MSM8917-4

## UCP3001-2 MSM8917-4

### PCB JTAG CON

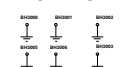
### PCB JTAG

### PCB H/W Revision

HW REV	HW REV	HW REV	HW REV	HW REV	HW REV
REV0.0	L	L	L	L	L
REV0.1	L	L	L	L	L
REV0.2	L	L	L	L	L
REV0.3	L	L	L	L	L
REV0.4	L	L	L	L	L
REV0.5	L	L	L	L	L
REV0.6	L	L	L	L	L
REV0.7	L	L	L	L	L

2D BARCODE

### BOTH HOLE

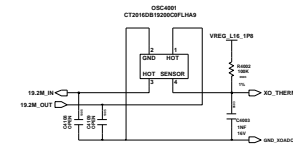
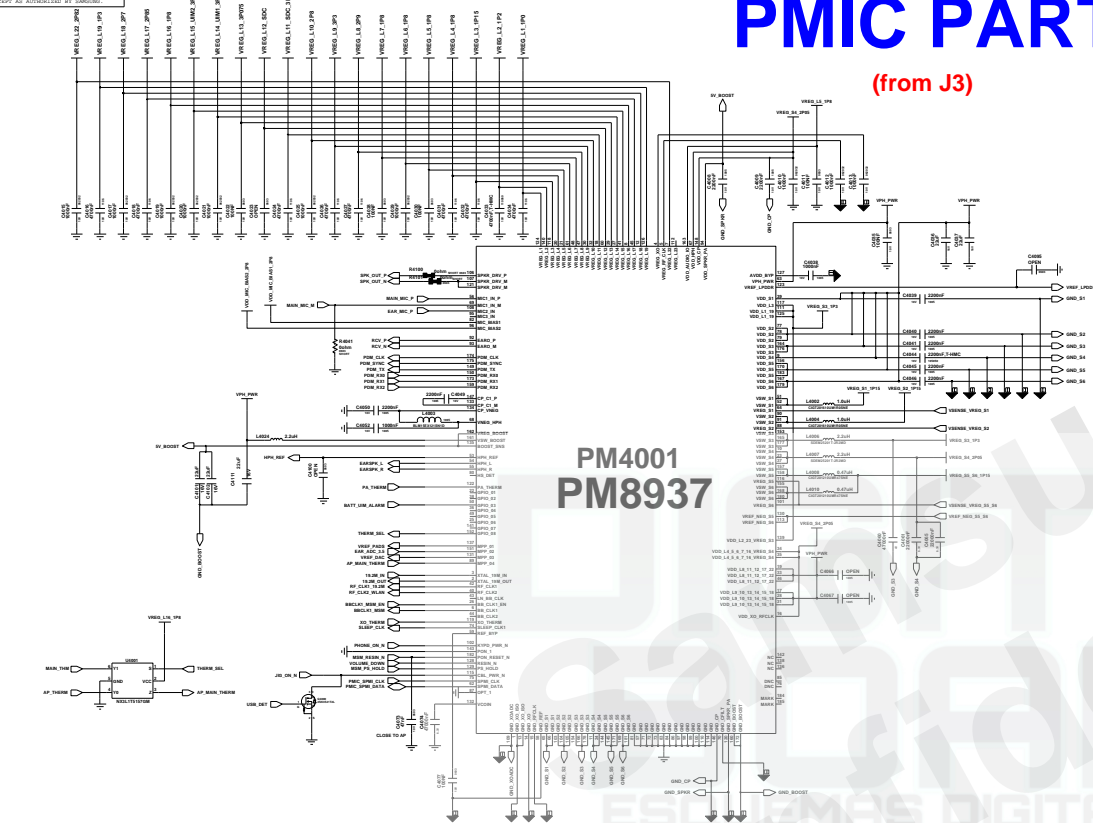


Part No.	2017_J1 BASEBAND
Rev	0.0
Date	
By	
Check	

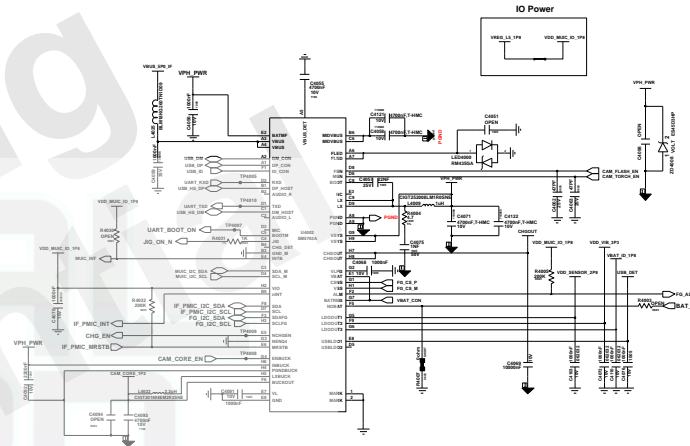
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# PMIC PART

(from J3)



19.2MHz OSC (from ELITE)

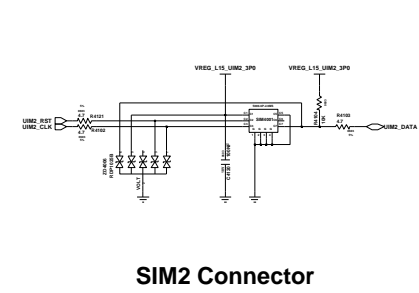
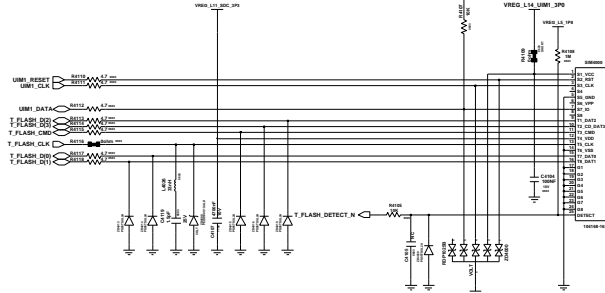
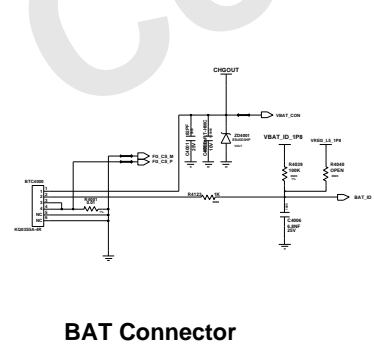
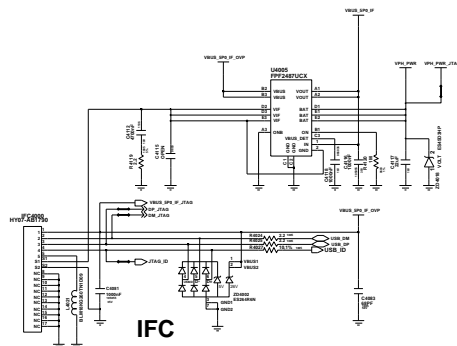


IF PMIC(MUIC+CHG+FG+BUCK+LDO) (from J3)

## USER INTERFACE PART

(FROM G532F)

## SIM / SD CARD PART



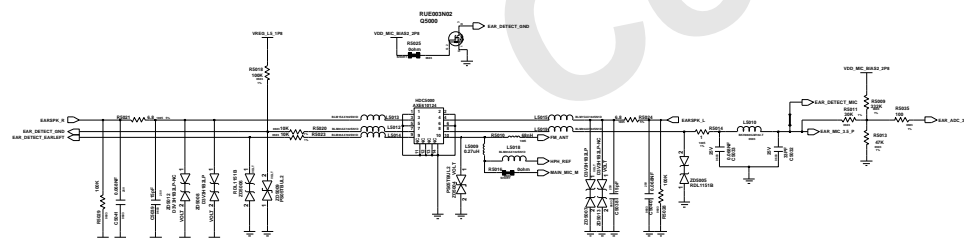
Engineer	PMIC / USER INTERFACE
Designer	
Checker	
DATE	2017_J1_PMIC
REV	
Drawing Number	



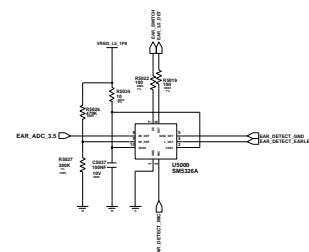
**MEMORY(16GB eMMC + 12Gb LPDDR3)**  
(FROM : J3)



## EARJACK

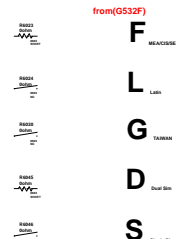
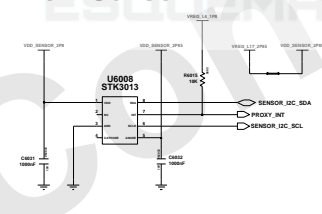
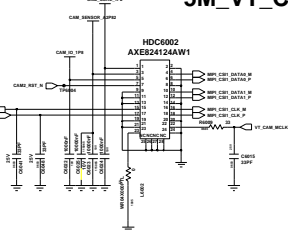


### E/P Anti Pop up IC



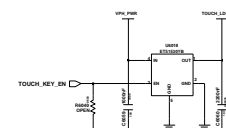
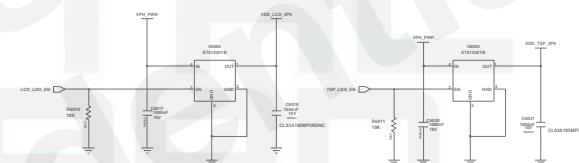
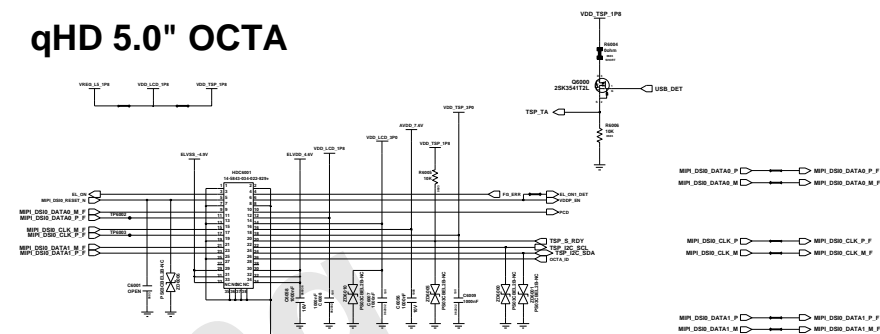
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## CAMERA PART\_from (G532F)

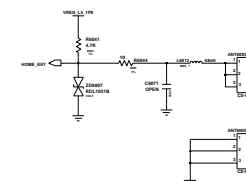
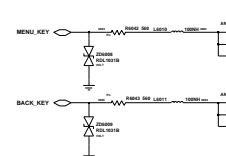
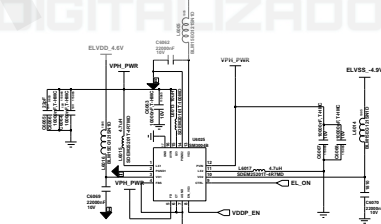


## DISPLAY PART\_From(J200G)

## qHD 5.0" OCTA



## Touch Key LDO



## KEYS\_from(G532F)



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