


SYSTEM 4.0
04-S28-2014
04/09/15

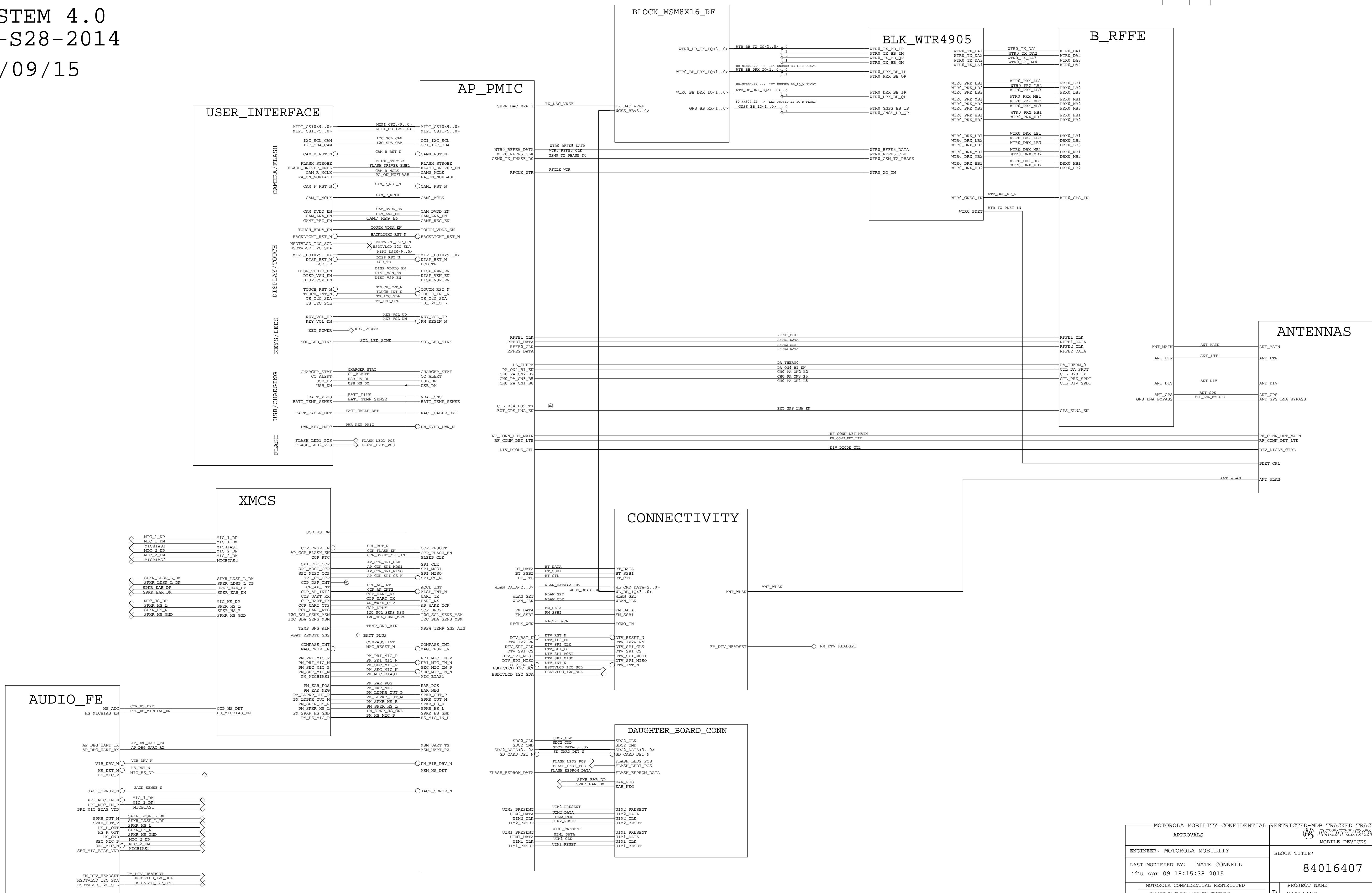
D

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B

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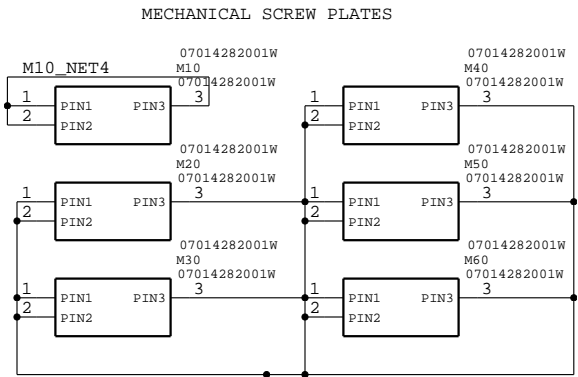
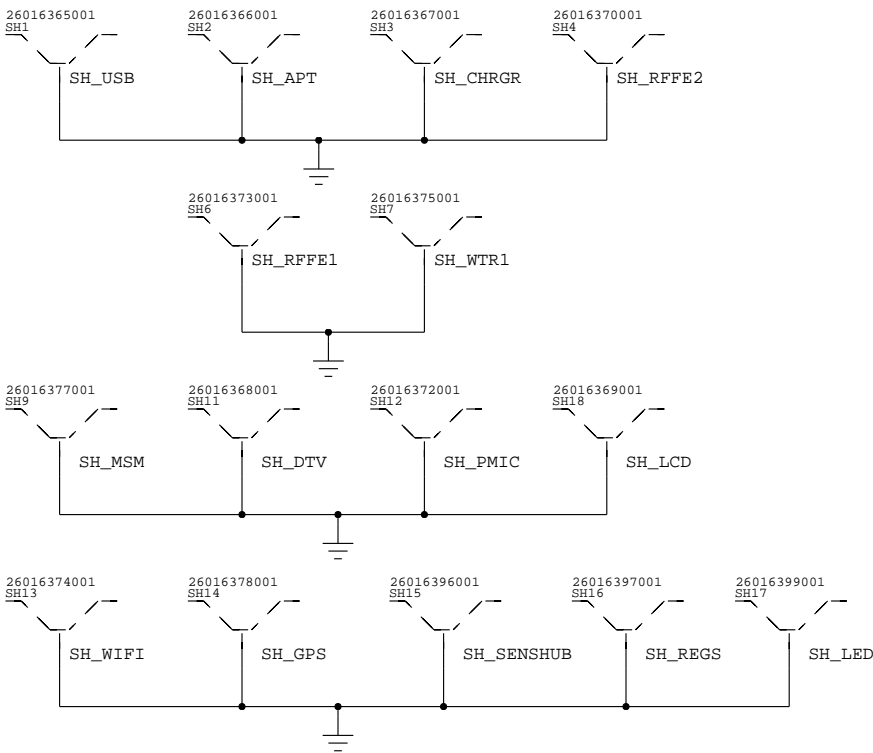
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APPROVALS			
ENGINEER: MOTOROLA MOBILITY			
LAST MODIFIED BY: NATE CONNELL Thu Apr 09 18:15:38 2015			
BLOCK TITLE:		84016407	
MOTOROLA CONFIDENTIAL RESTRICTED		REV	
THE DRAWING ON THIS PRINT AND INFORMATION THEREON ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCLOSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT.		01	
© COPYRIGHT, MOTOROLA, THIS DOCUMENT IS UNCLASSIFIED BY: [redacted] DATE: 11/19/2013		D PROJECT NAME 84016407.cpm	
NOT IMPACT FORFEITURE		CAD FILE NAME: [redacted]	
		PAGE 1 OF 47	



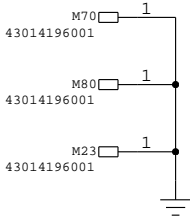
CALLISTO A ME/SHIELDS

CALLISTO REFERENCE DESIGNATOR RANGES BY BLOCK

- 000 - 499: ANTENNA
- 500 - 599: USER INPUT
- 600 - 799: POWER
- 1000-2999:
- 1600-1699: MEMORY
- 2000-2999: PM8916
- 3000-3199: WTR4905
- 4000-4499: CONNECTIVITY
- 4500-4999: AUDIO FE
- 5300-5399: DISPLAY
- 5500-5999: CAMERA
- 6000-6999: XMCS
- 7000-7799, 8000-8020: RFFE
- 7800-7899:
- 9000-9999: PRODUCT-SPECIFIC (FILTERING, 100PF CAPS)



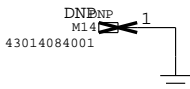
MECHANICAL BUSHINGS



MECHANICAL GND FINGERS




MECHANICAL STANDOFF




DNP UNTIL CORRECT PART IS AVAILABLE

MOTOROLA MOBILITY CONFIDENTIAL RESTRICTED-MDB TRACKED TRACKEDANDBLOCKED


APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER :		BLOCK TITLE :	
LAST MODIFIED BY : Wed Apr 15 15:46:44 2015		84016407	
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		B 84016407.cpm	
		REV 01	
		CAD FILE NAME :	
		PAGE 2 OF 47	

REF 000-499

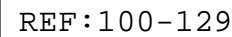
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LDJ18829M24AC002

ANT_MAIN  3 INPUT

4 COUPLING TERM_P

MAIN_CPL 

2 5



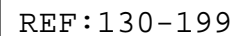
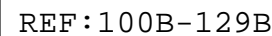
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ANT_LTE 3 INPUT

PDET_CPL 4 COUPLING TERM

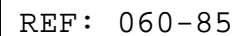
GND 1 GND 2


2 5



The PCB layout for the GPS module includes the following components and connections:

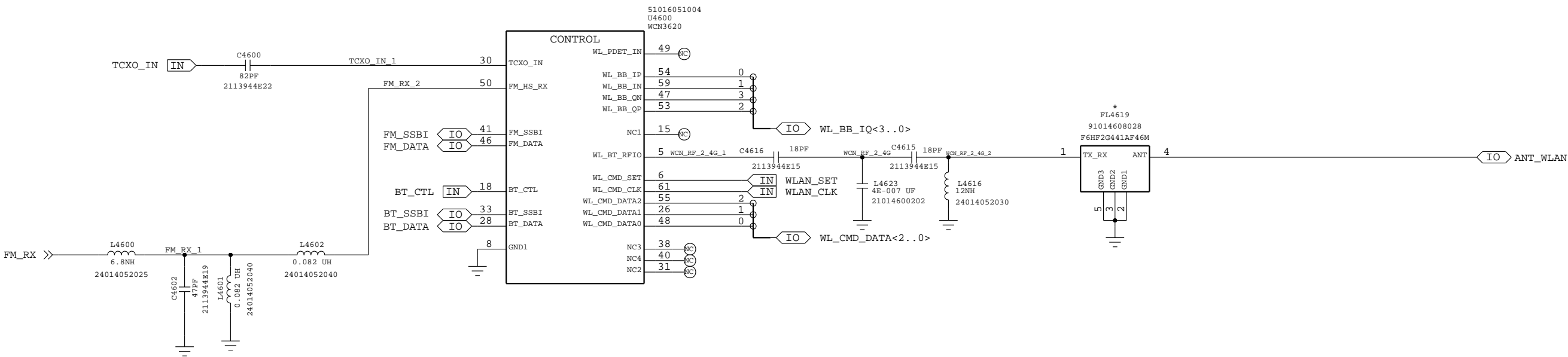
- GPS Module:** Labeled "GPS_MODULE" at the top left.
- GPS Module Pinout:**
 - 1: GND
 - 2: GPS_RF_CONN_2
 - 3: GND
 - 4: GND
 - 5: OUT
 - 6: GND
- GPS Module Connections:**
 - Pin 2 is connected to GPS_RF_CONN_2.
 - Pin 5 is connected to OUT.
 - Pin 6 is connected to GND.
- GPS Module Components:**
 - L081: 3PF
 - L082: 6.8NH
 - L080: 4.3NH
 - C082: 1PF
 - M080: GPS_ANT 390140
 - VR47: VR47
- GPS Module Labels:**
 - DNP (Do Not Populate) labels are present for L081, L082, L080, C082, and M080.
 - 06014025001W is labeled near the GPS module.
 - 06014025001W is labeled near the GPS module.
 - 06014025001W is labeled near the GPS module.
- GPS Module Connections:**
 - GPS_RF_CONN_2 is connected to GPS_RF_CONN_3.
 - GPS_RF_CONN_3 is connected to GPS_ANT 390140.
 - GPS_ANT 390140 is connected to VR47.
 - VR47 is connected to GND.
- GPS Module Labels:**
 - DNP (Do Not Populate) labels are present for L081, L082, L080, C082, and M080.
 - 06014025001W is labeled near the GPS module.
 - 06014025001W is labeled near the GPS module.
 - 06014025001W is labeled near the GPS module.




DRAWING NUMBER: 84		 MOTOROLA	
DRAWN BY:	DATE:	TITLE:	REV 01
LEAD ENGINEER:	DATE:		SIZE B
ASSY NUMBER:		PCB FILE NAME:	PAGE 3 OF 47

WCN3620 RF/CTRL

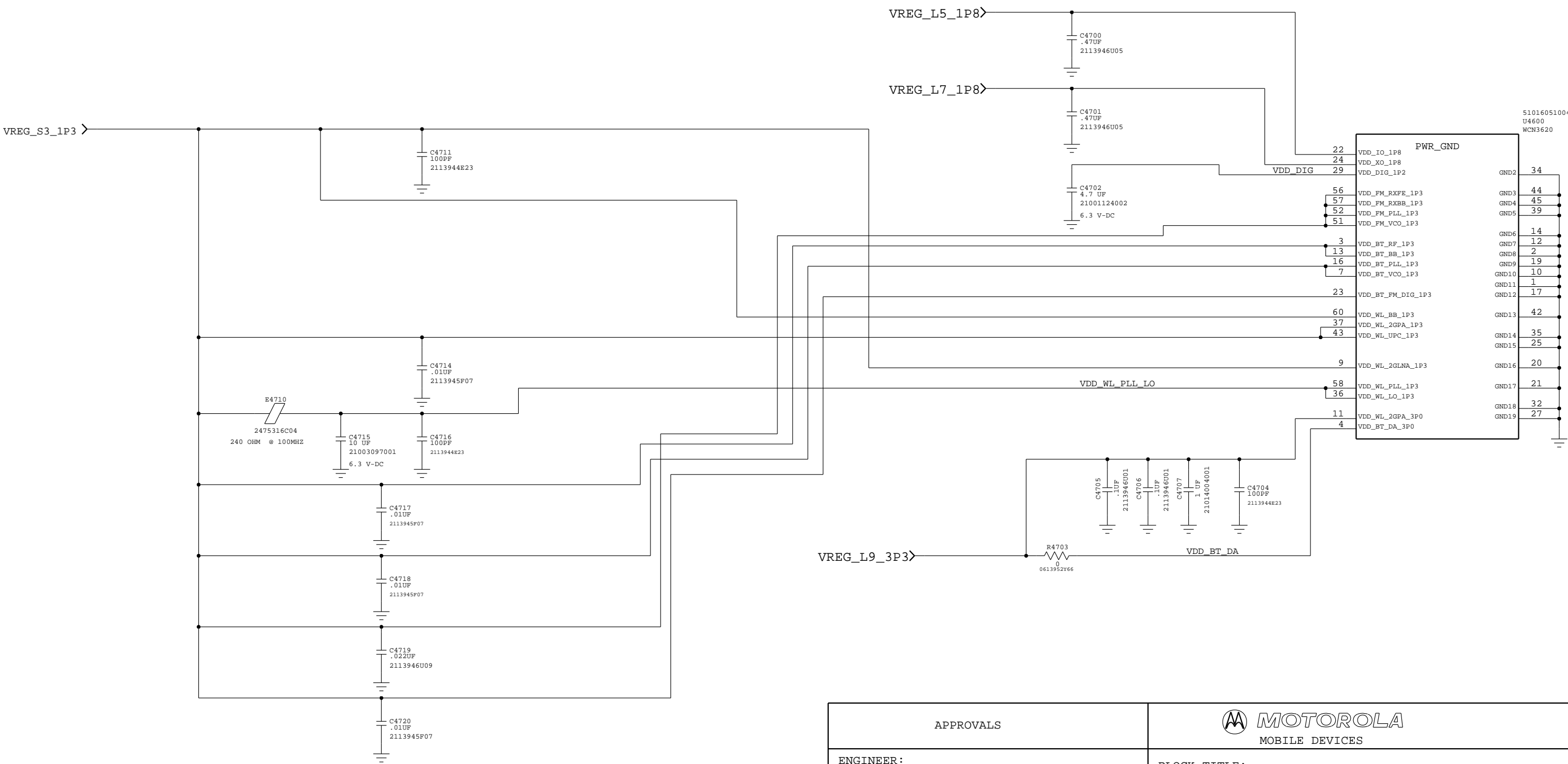
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BASED ON 80-WL006-44 REV C

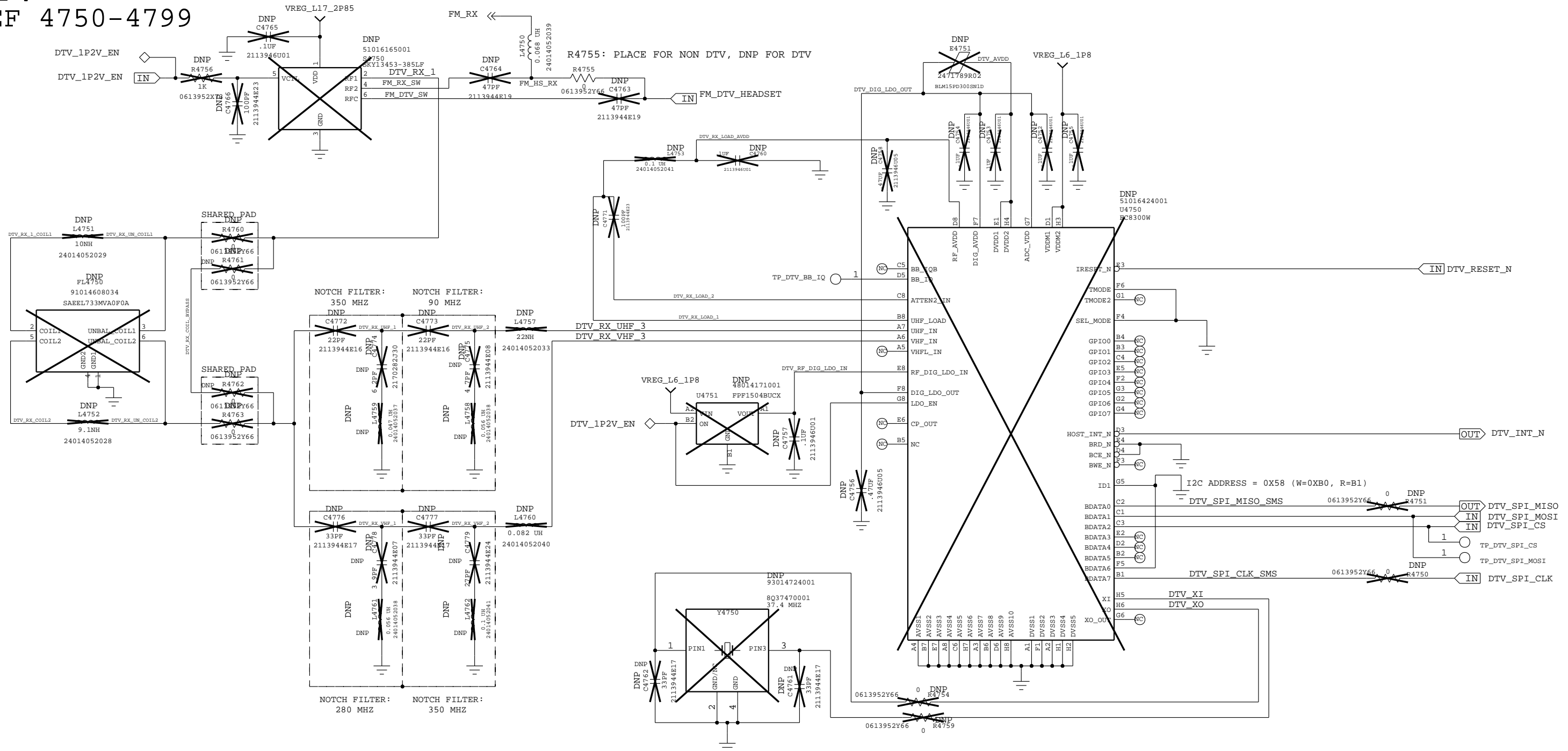
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LAST MODIFIED BY: FROEHLING Wed Apr 15 15:46:54 2015		connectivity	
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		CAD FILE NAME : PAGE 4 OF 47	


WCN3620 POWER
REF 4700-4749



BASED ON 80-WL006-44 REV C

APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER :		BLOCK TITLE :	
LAST MODIFIED BY: FROEHLING Wed Apr 15 15:46:55 2015		connectivity	
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		REV 01	
CAD FILE NAME :			PAGE 5 OF 47

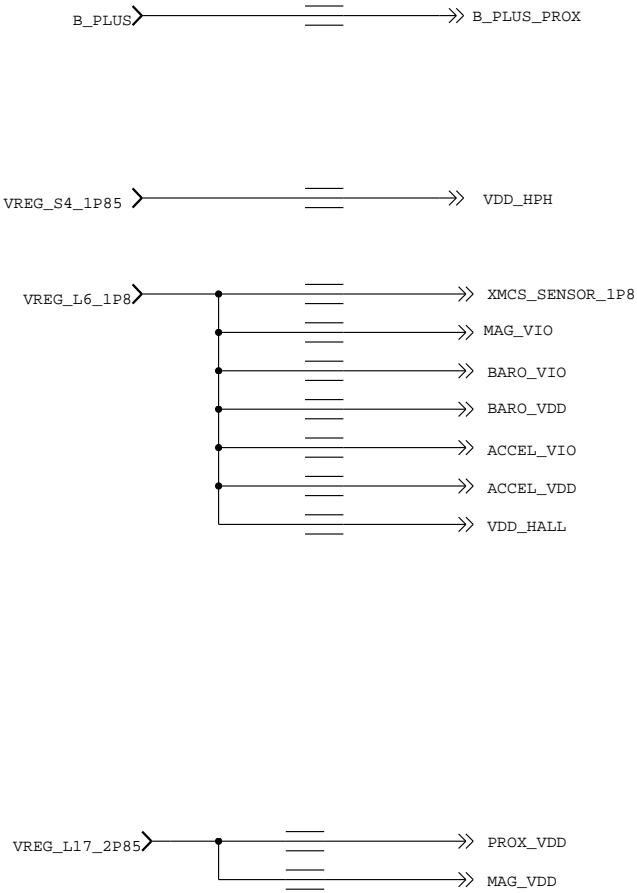
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		CAD FILE NAME:	PAGE 6 OF 47

XMCS: POWER

REF 6000-6999

SUPPLIES

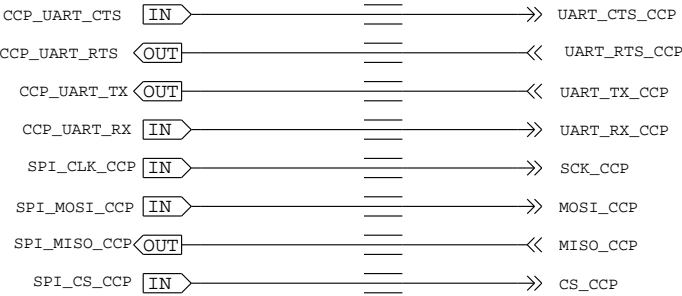


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LAST MODIFIED BY: NATE CONNELL Thu Apr 09 18:13:56 2015				
MOTOROLA CONFIDENTIAL RESTRICTED		B	PROJECT NAME	REV
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		CAD FILE NAME:		PAGE 7 OF 47

XMCS: PORTS

REF 6000-6999

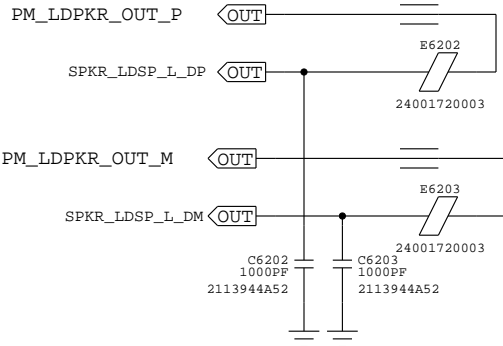
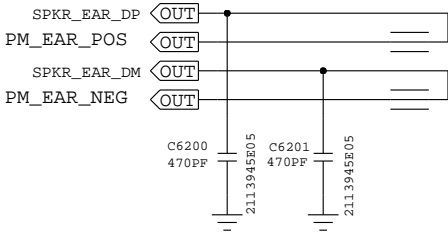
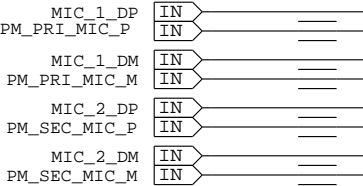
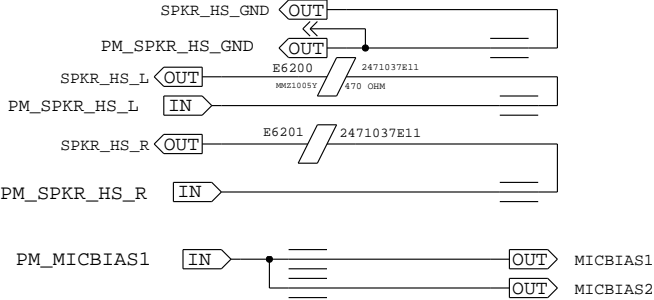
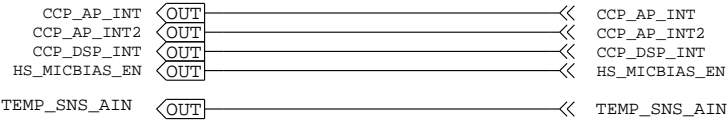
INTERFACES




INPUTS



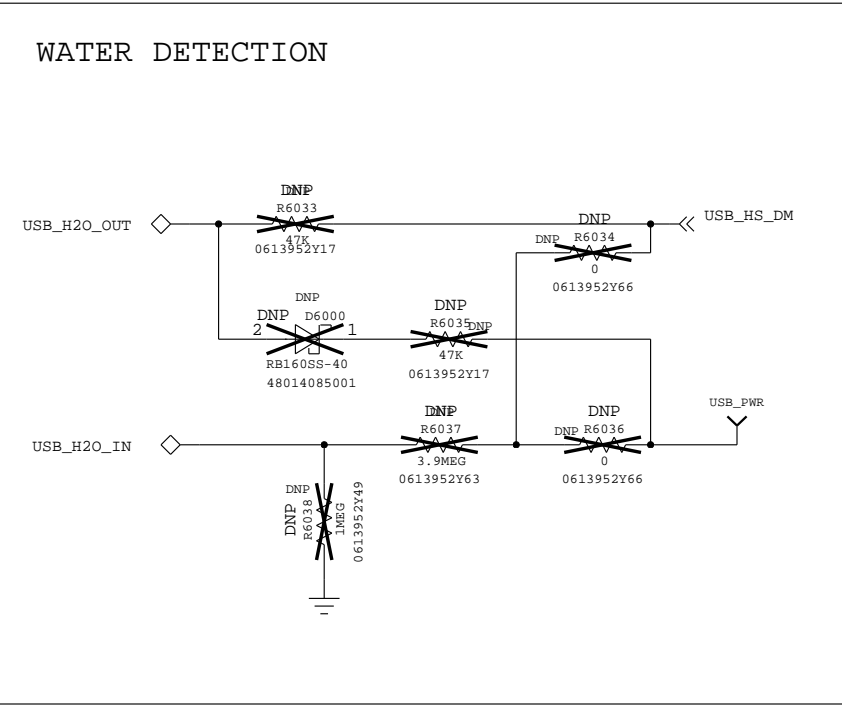
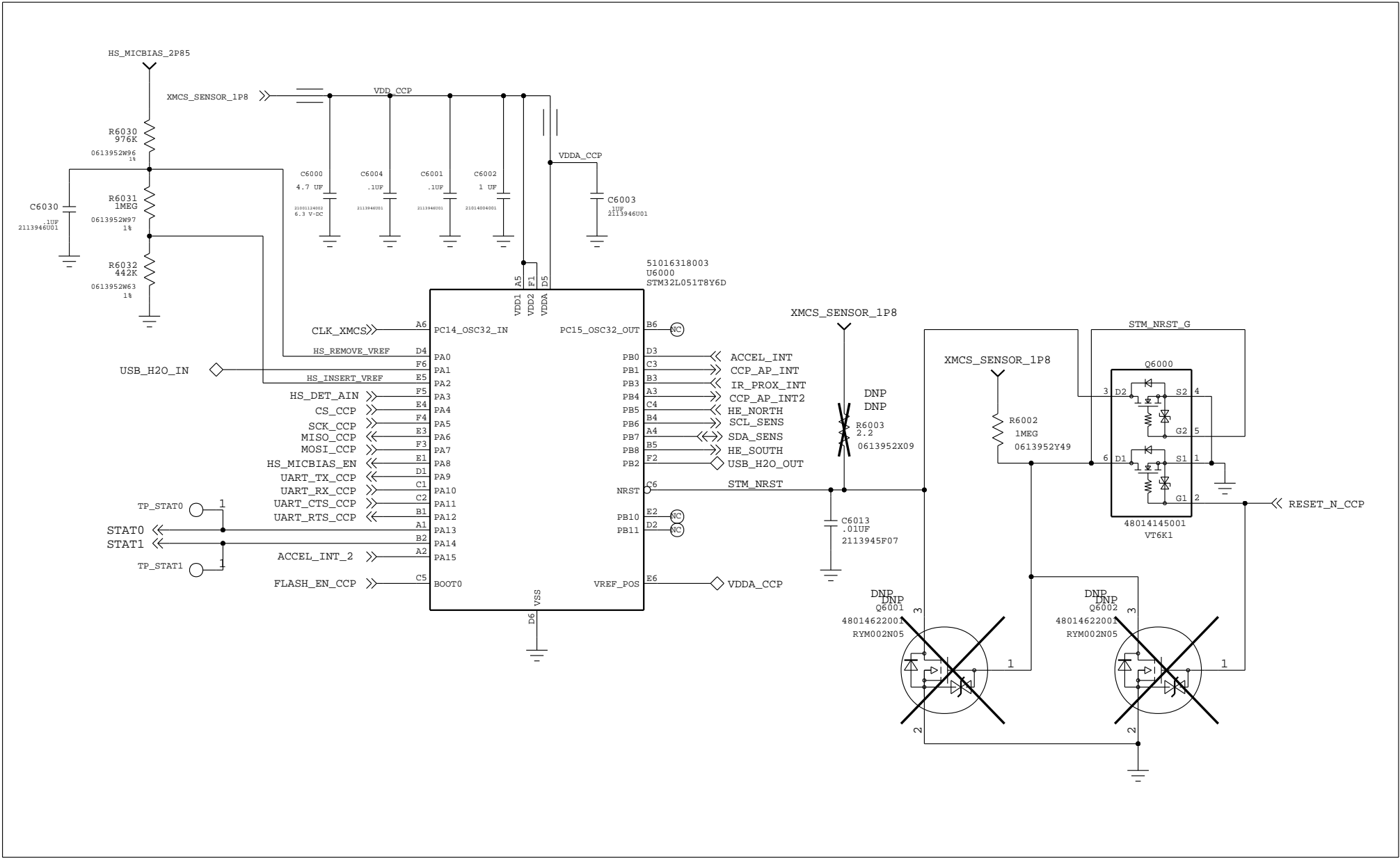
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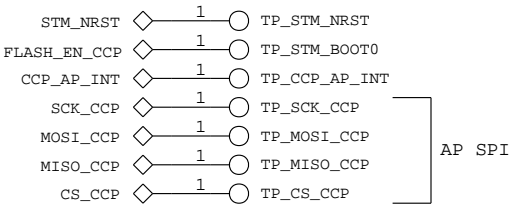
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ENGINEER: RAUL LOPEZ		BLOCK TITLE:	
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		PAGE 8 OF 47	


XMCS: CCP

REF: 6000-6049



TESTPOINTS



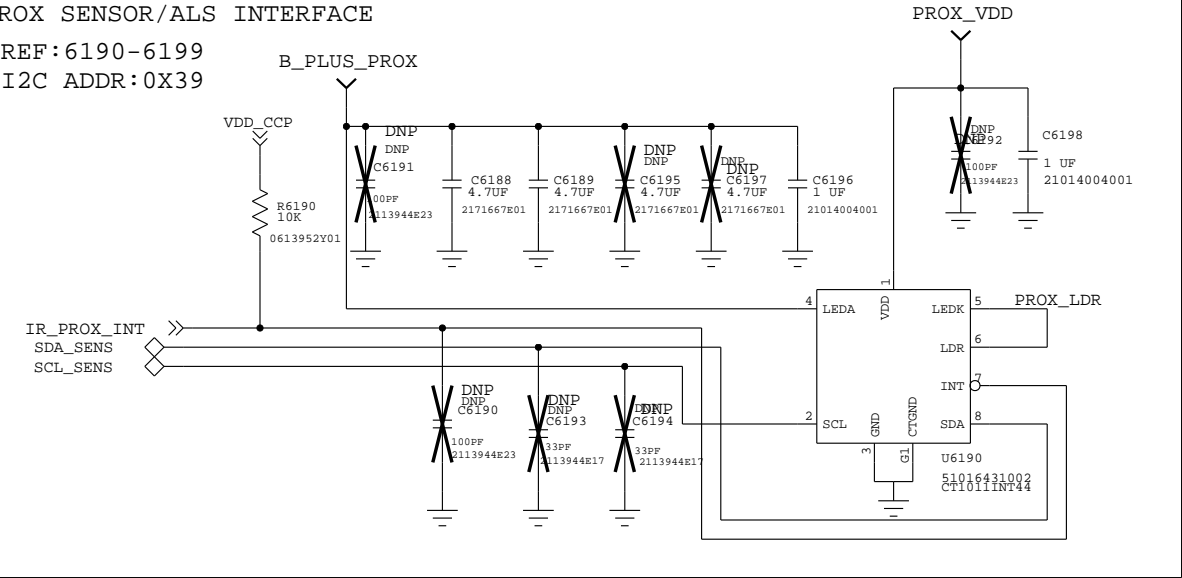
APPROVALS		 MOTOROLA MOBILE DEVICES	
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SENSORS

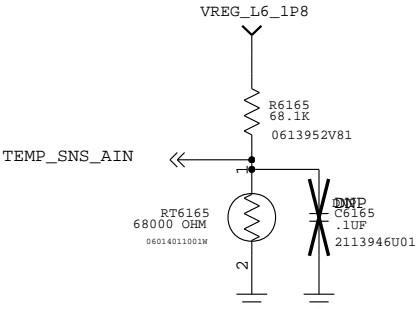
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PROX SENSOR/ALS INTERFACE

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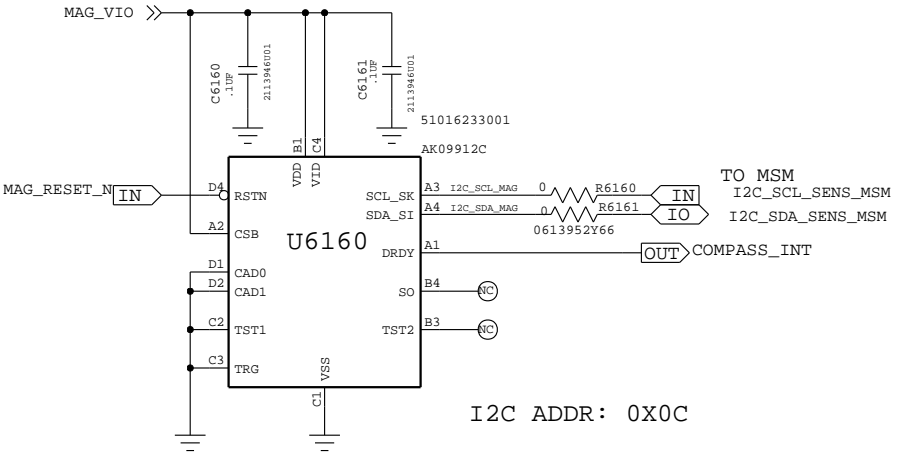


BOARD TEMP SENSOR



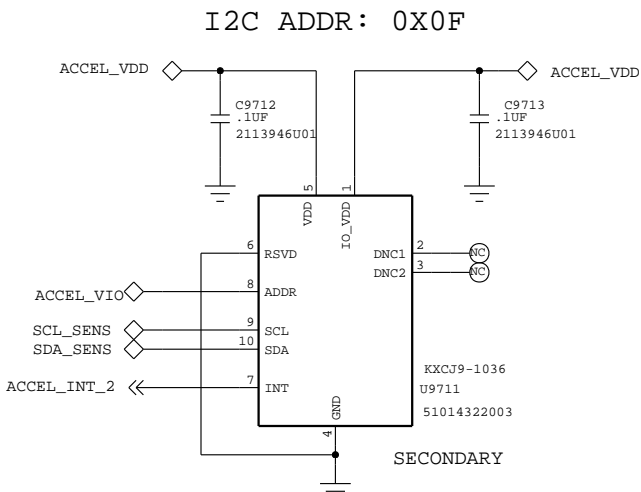
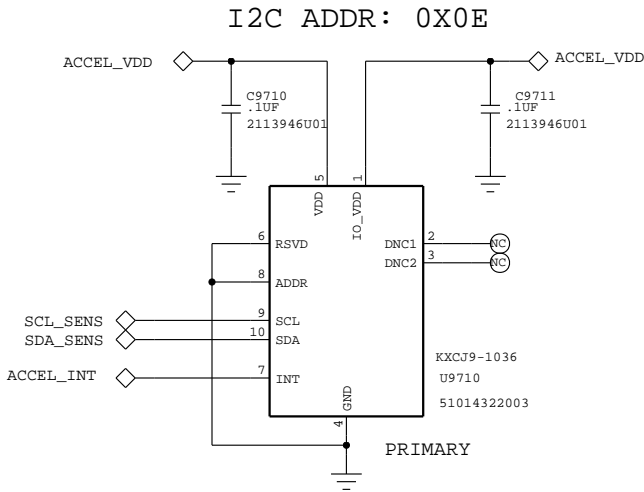
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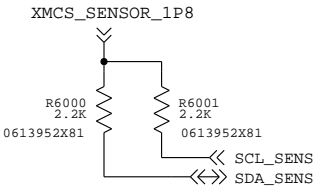
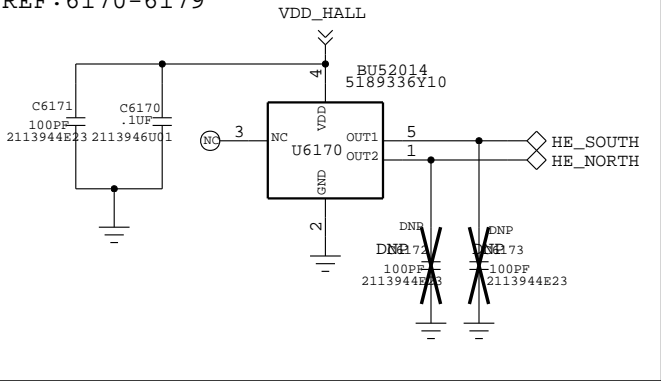
ACCELEROMETER

REF:9710-9719



HALL EFFECT

REF:6170-6179



JOHN GORSICA/NATE CONNELL
APPROVALS

ENGINEER: C HERNANDEZ

LAST MODIFIED BY: C HERNANDEZ
Wed Apr 15 15:46:57 2015

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MOTOROLA
MOBILE DEVICES

BLOCK TITLE:

xmcs

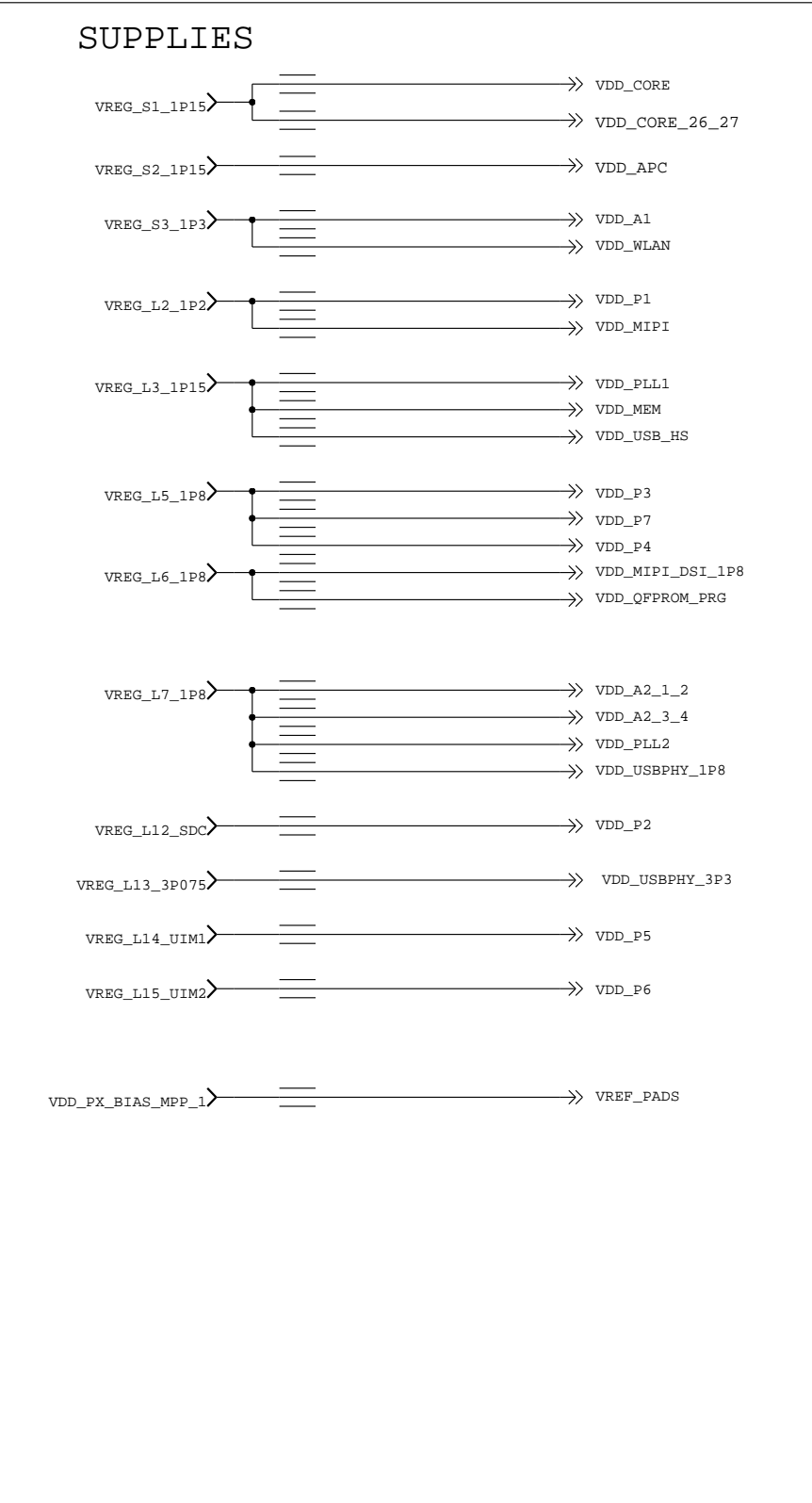
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CAD FILE NAME:
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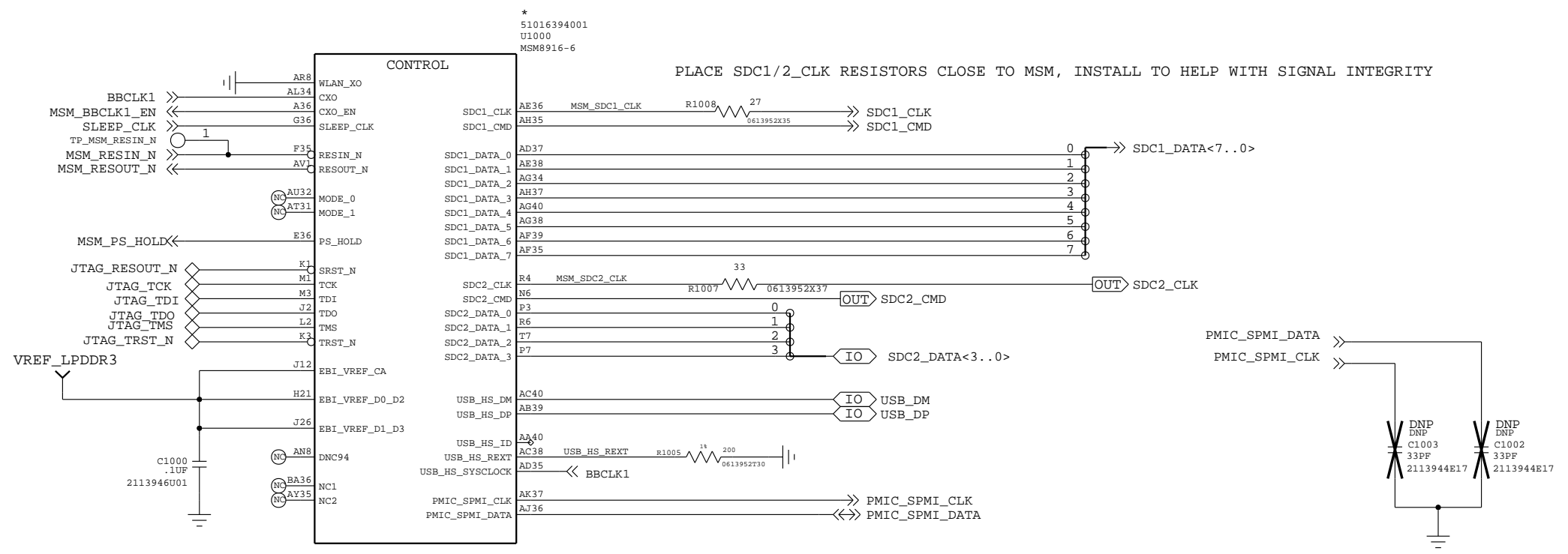
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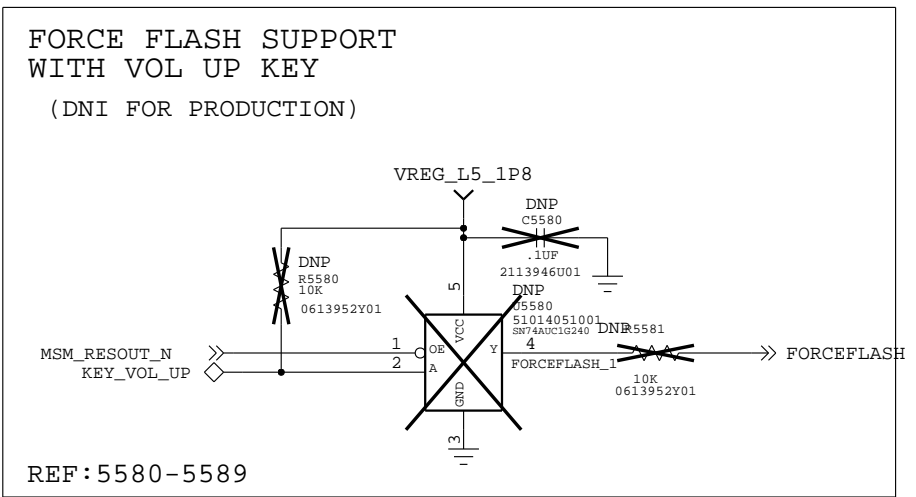
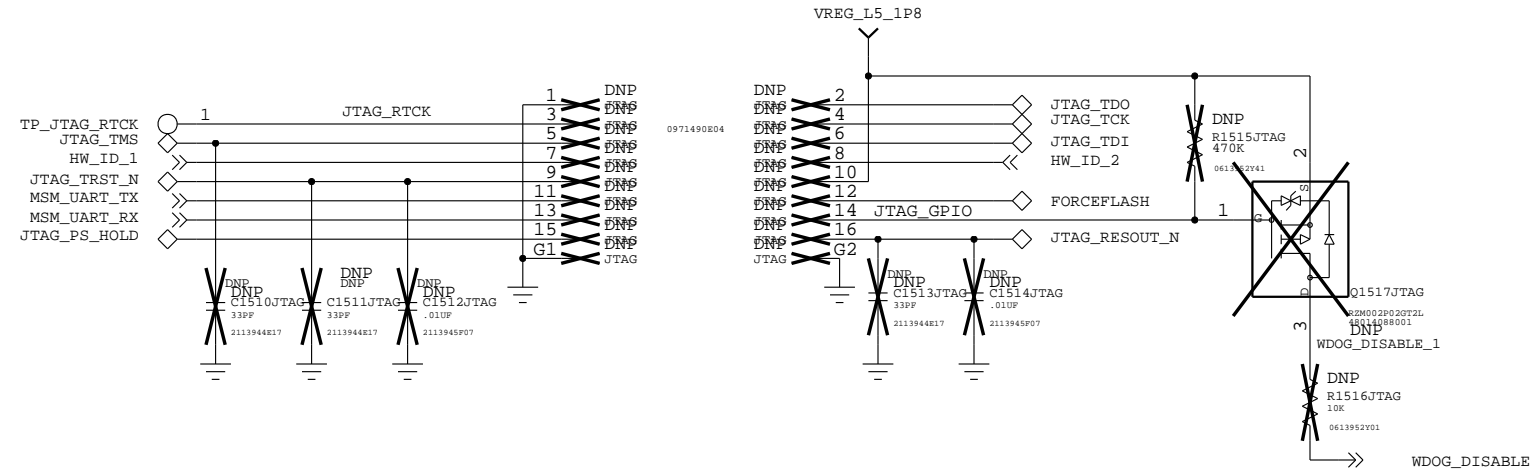


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LAST MODIFIED BY: NATE CONNELL Thu Apr 09 18:13:45 2015		ap_pmic	
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		REV 01	
		CAD FILE NAME:	
		PAGE 11 OF 47	

MSM: CTRL
REF 1000-1099

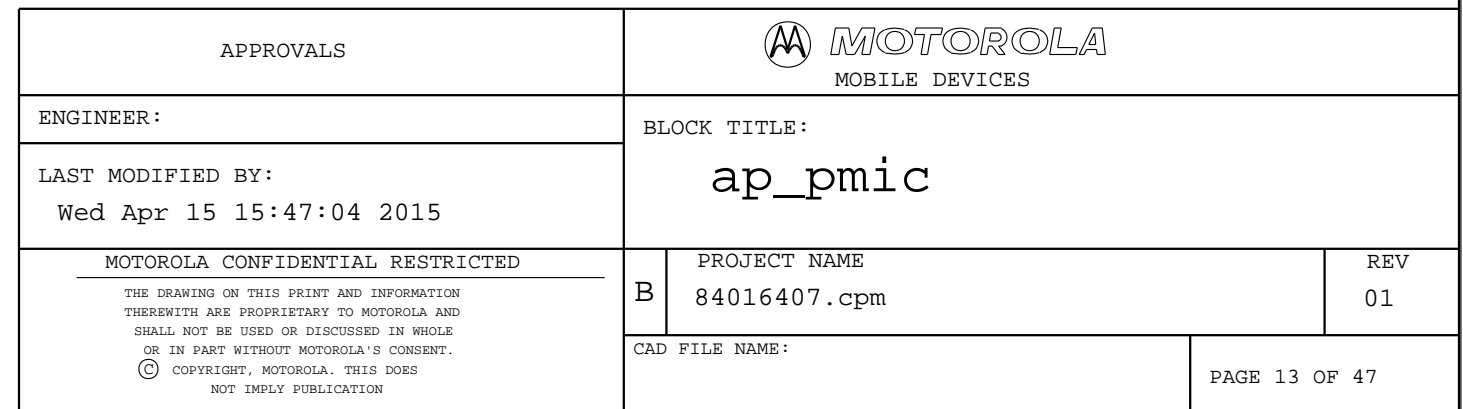


JTAG:MINI



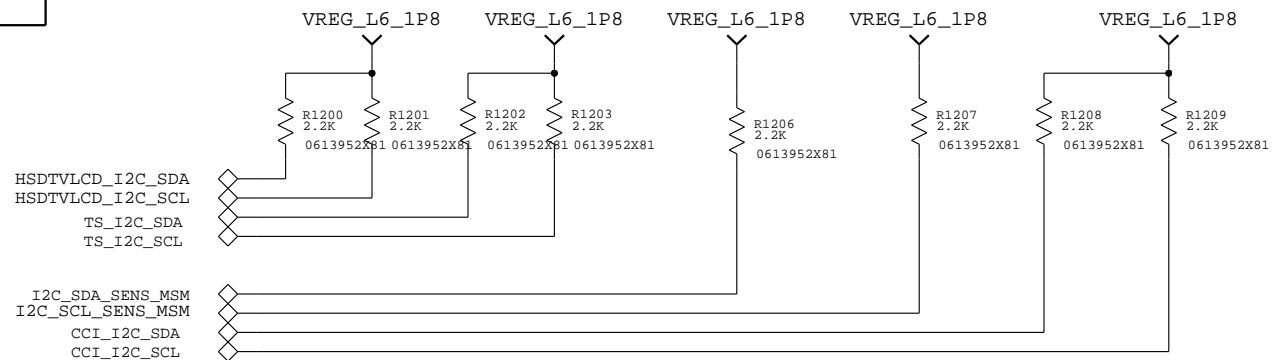
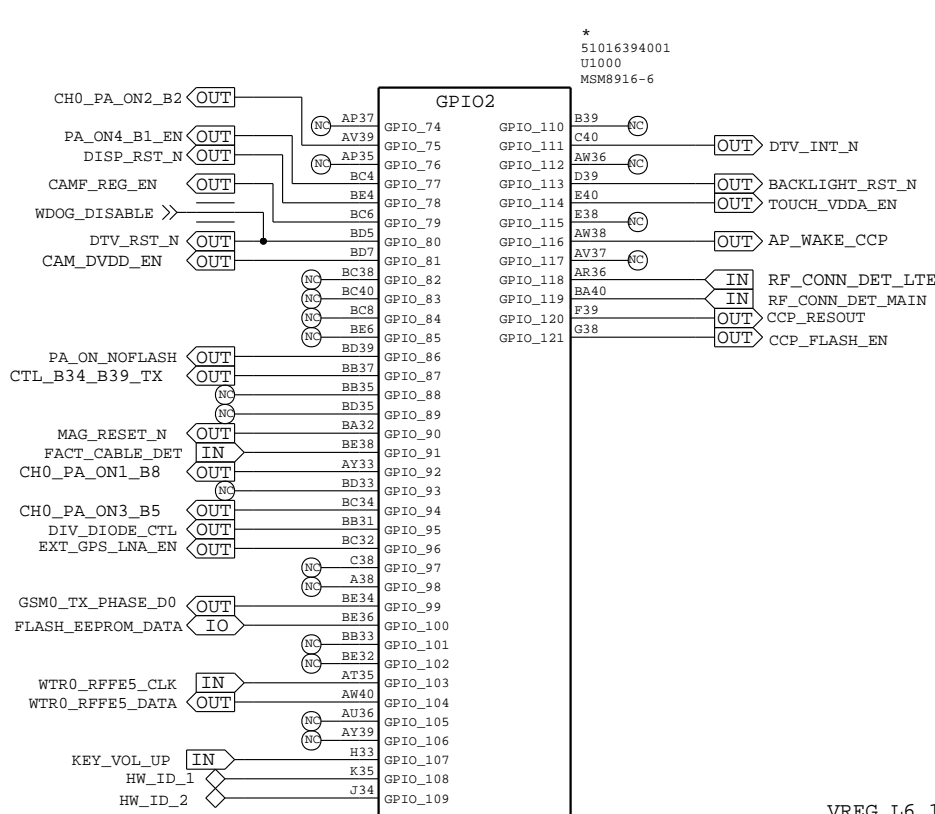
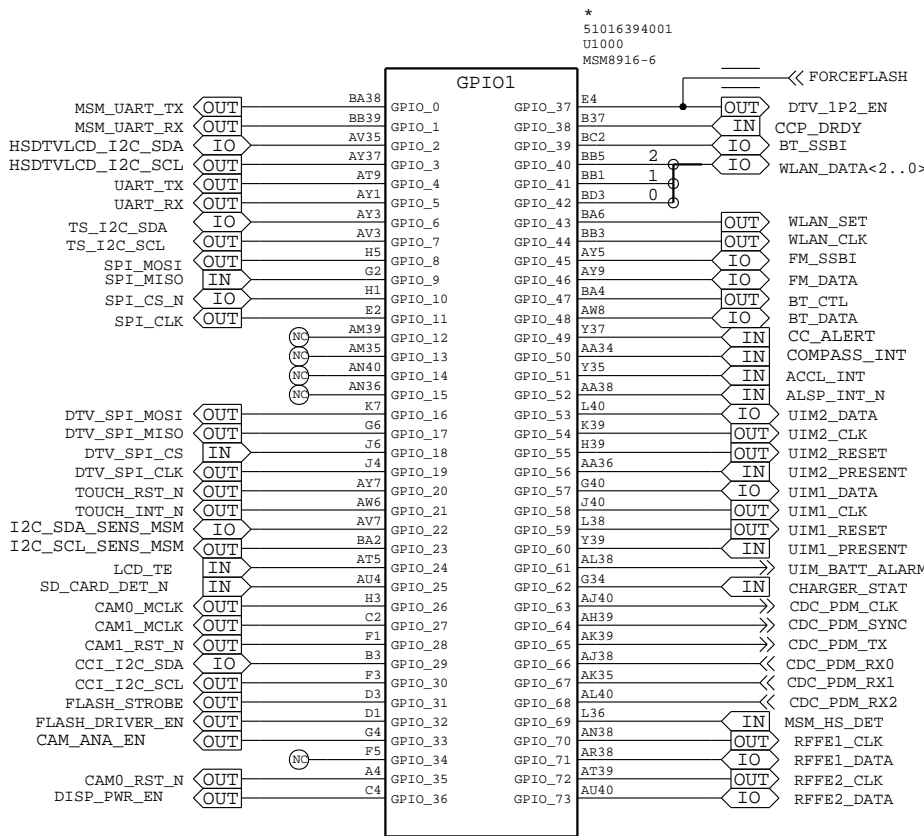
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		CAD FILE NAME : PAGE 12 OF 47	

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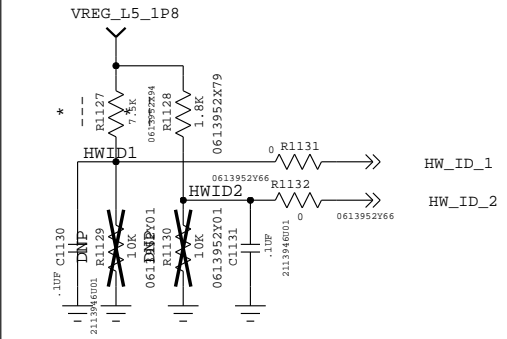
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REF 1200-1299



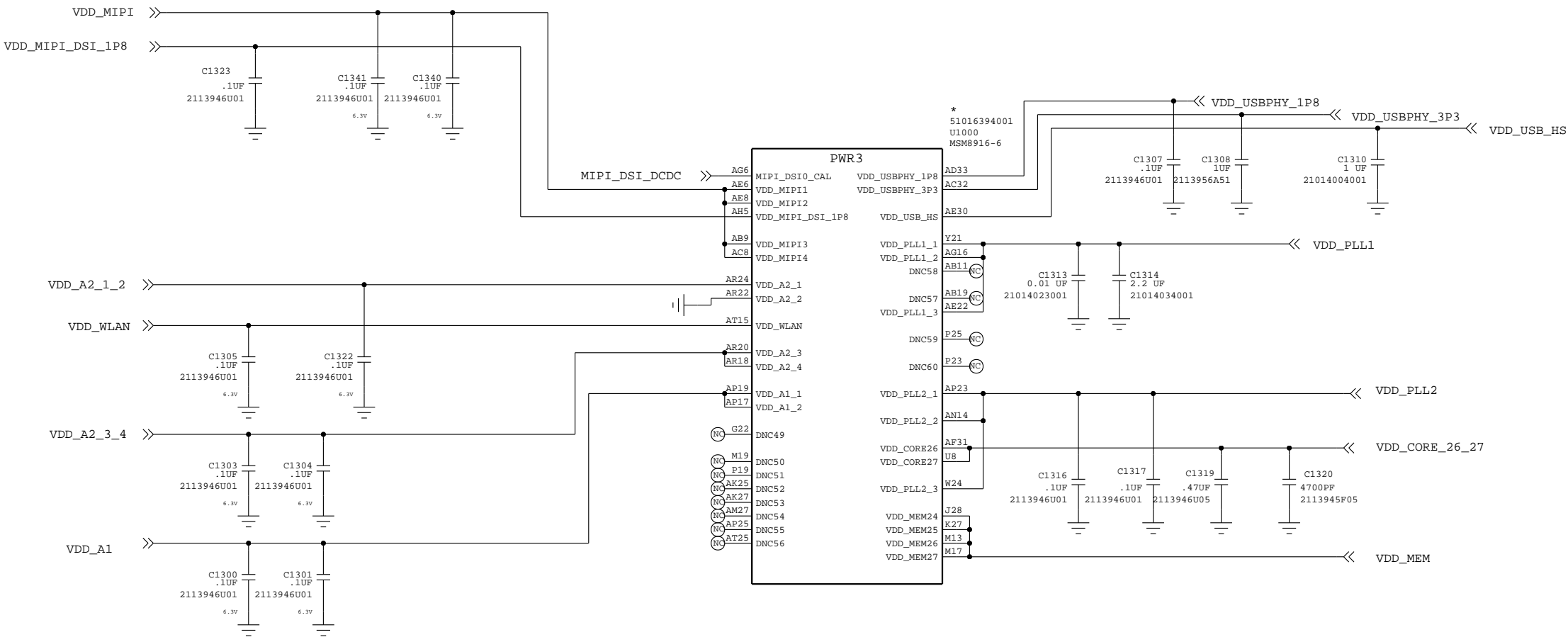
MOT BOARD ID


FOR RUNTIME REVISION INFORMATION, REFER TO:
<https://sites.google.com/a/motorola.com/runtime-product-ids>



MSM: PWR1

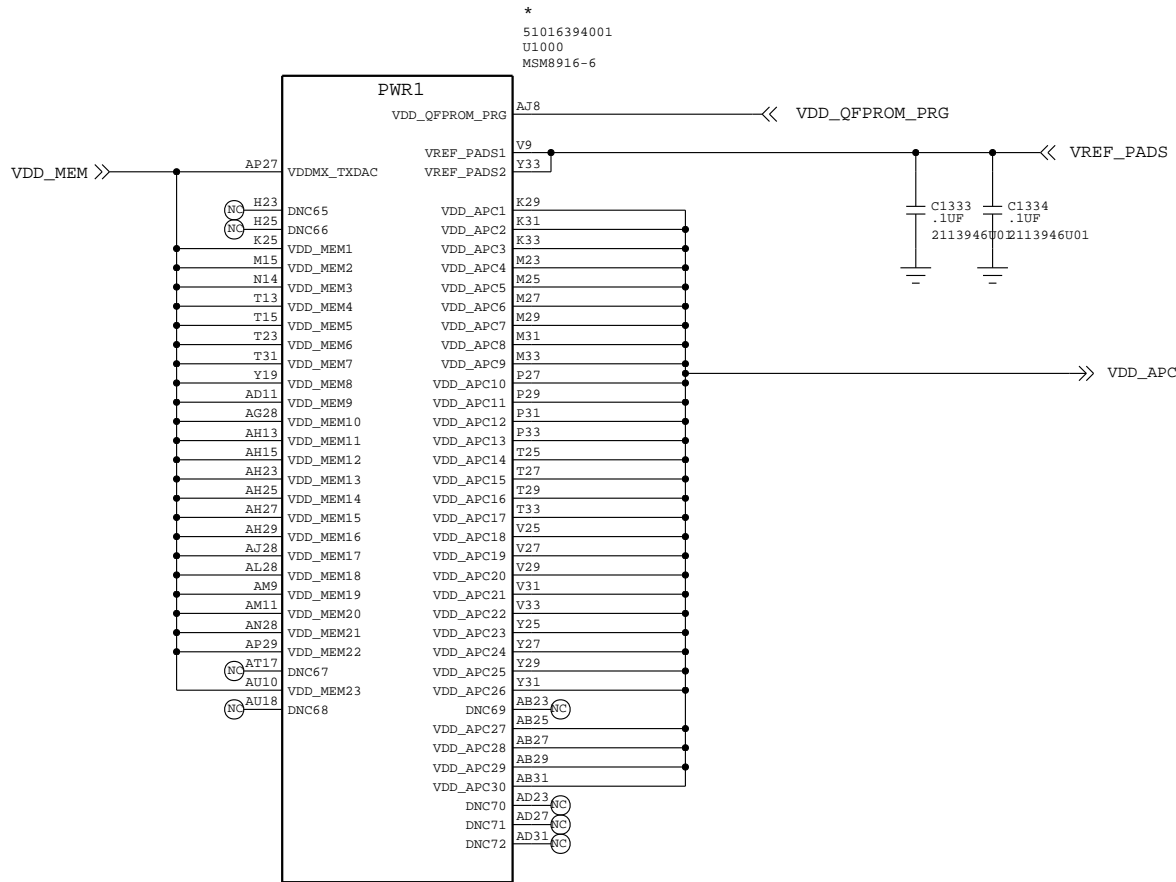
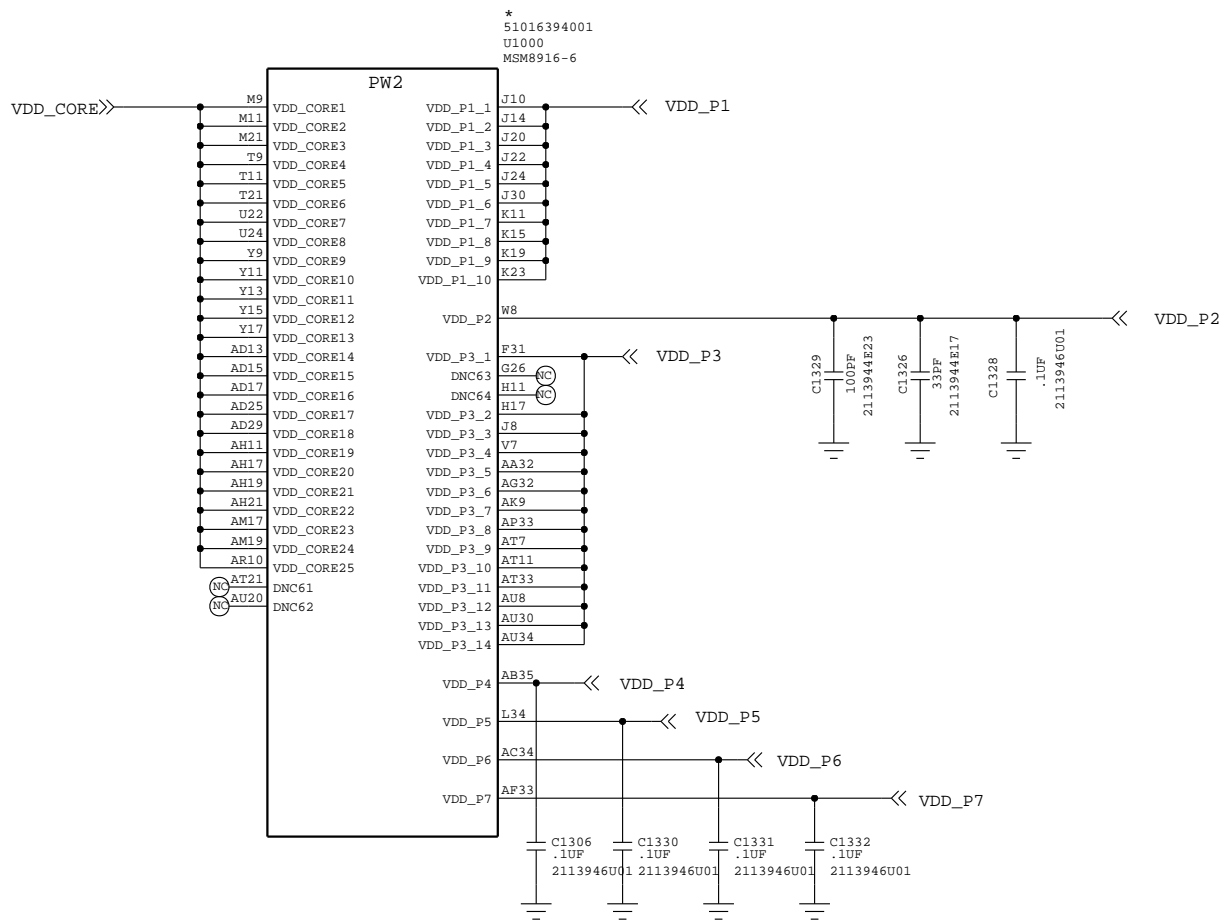
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APPROVALS		 MOTOROLA MOBILE DEVICES	
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		REV 01 PAGE 15 OF 47	

MSM: PWR2

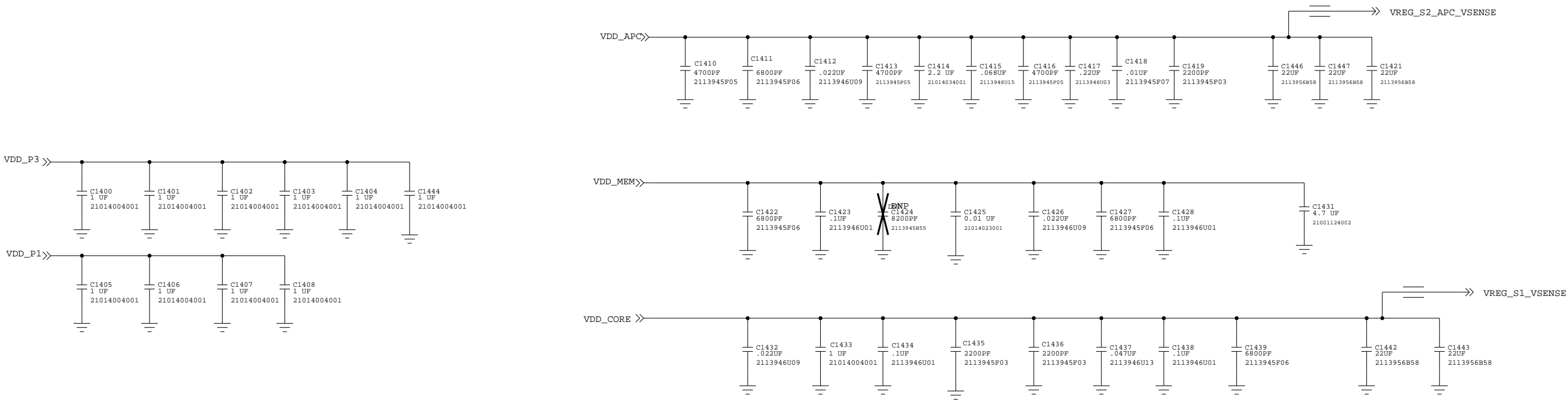
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APPROVALS		 MOTOROLA MOBILE DEVICES	
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		84016407.cpm	
		REV 01	
		PAGE 16 OF 47	

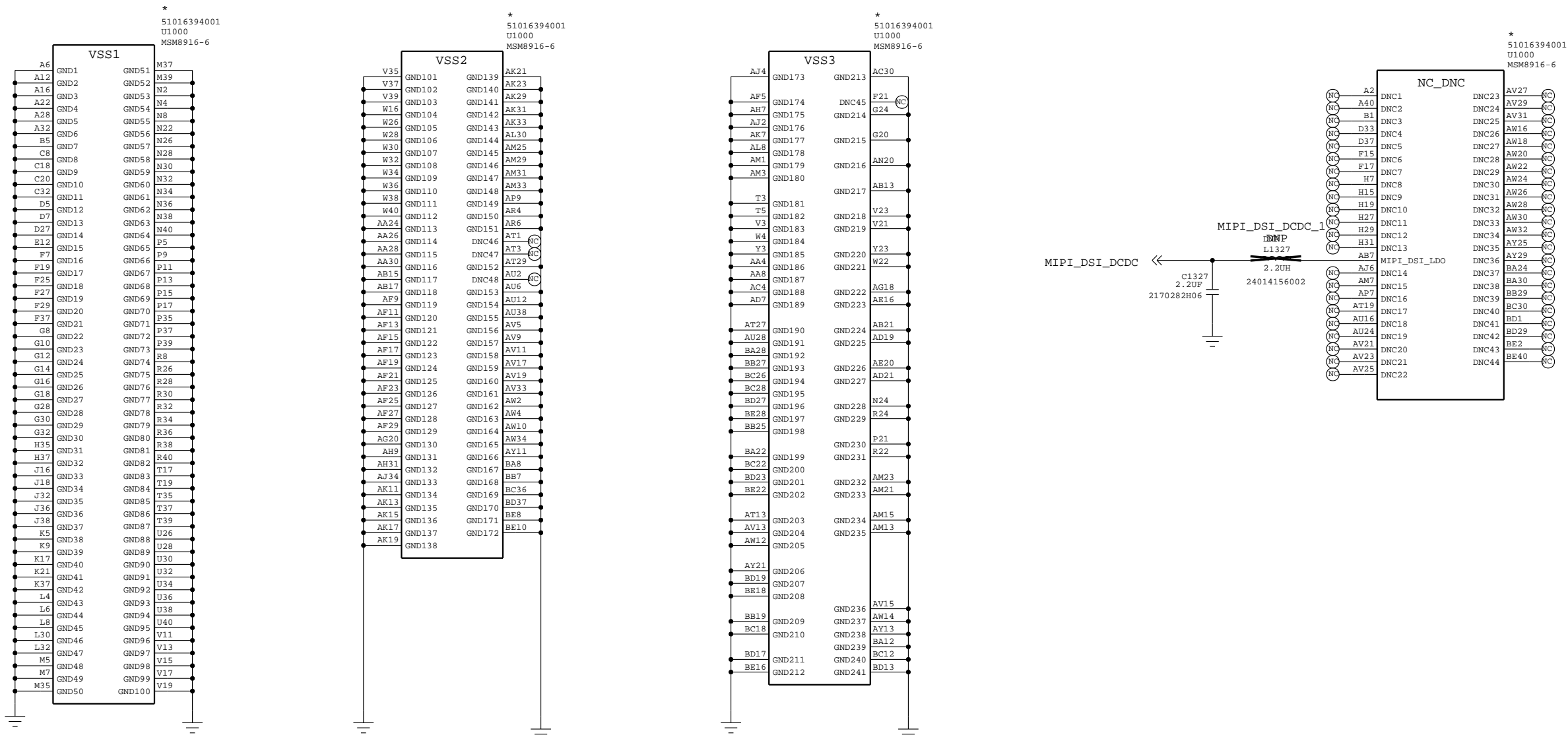
MSM: BYPASS CAPS


REF 1400-1499



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		B 84016407.cpm	01
CAD FILE NAME :			PAGE 17 OF 47

MSM: GROUND



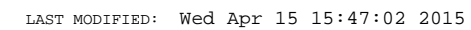
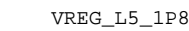
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		REV 01 PAGE 18 OF 47	

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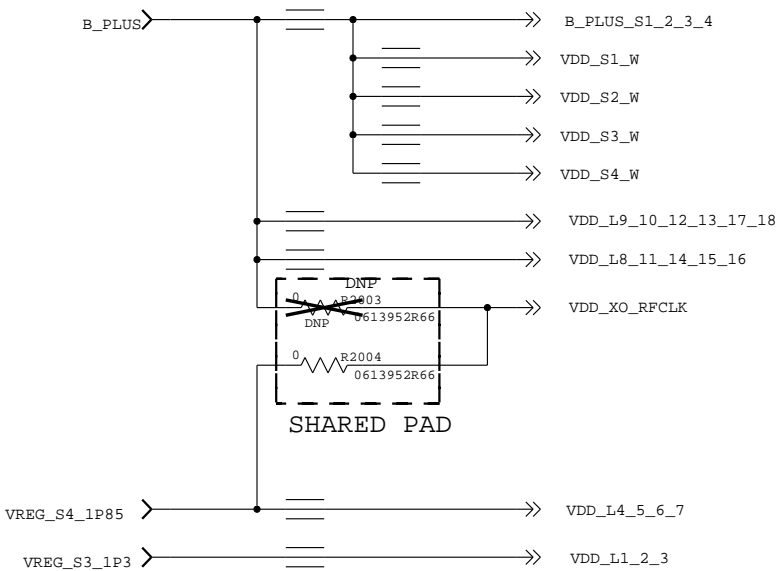


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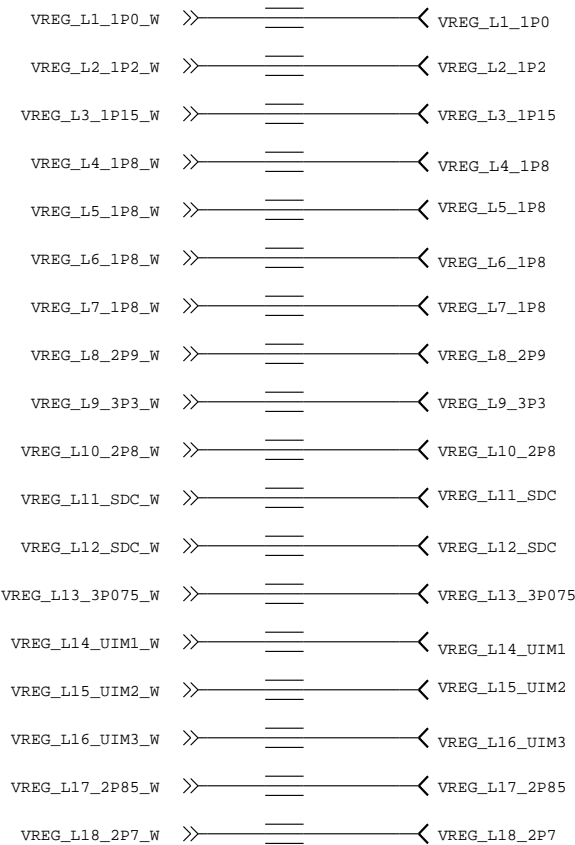
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REF: 2000-2999

SUPPLIES



OUTPUTS



APPROVALS

ENGINEER: NATE CONNELL

LAST MODIFIED BY: NATE CONNELL
Wed Apr 15 15:47:03 2015

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BLOCK TITLE:

ap_pmic

B

PROJECT NAME
84016407.cpm

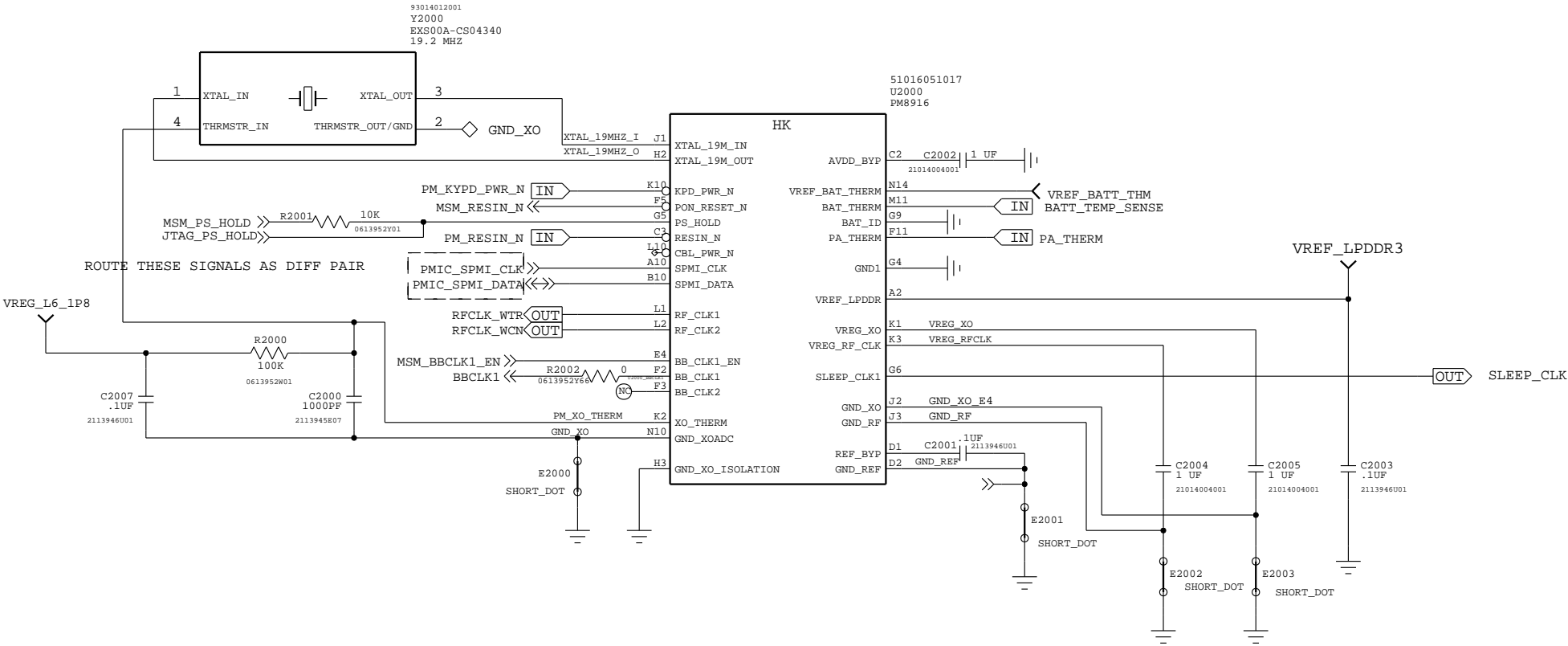
REV
01


CAD FILE NAME:

PAGE 20 OF 47

PM8916: HK

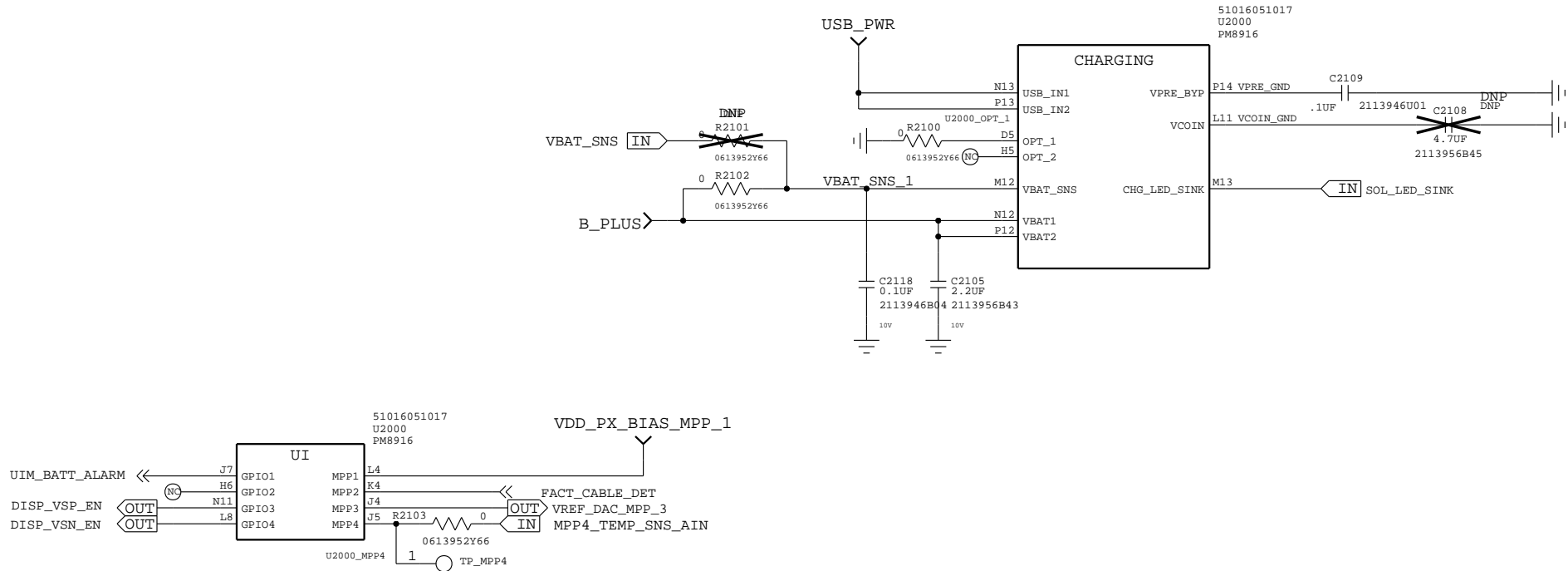
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APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: XIAOYU WANG		BLOCK TITLE:	
LAST MODIFIED BY: MMI Wed Apr 15 15:46:58 2015		ap_pmic	
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		84016407.cpm	
		CAD FILE NAME:	
		PAGE 21 OF 47	

PM8916: CHRG/LED/GPIO

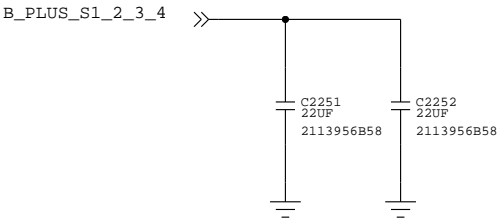
REF: 2000-2999



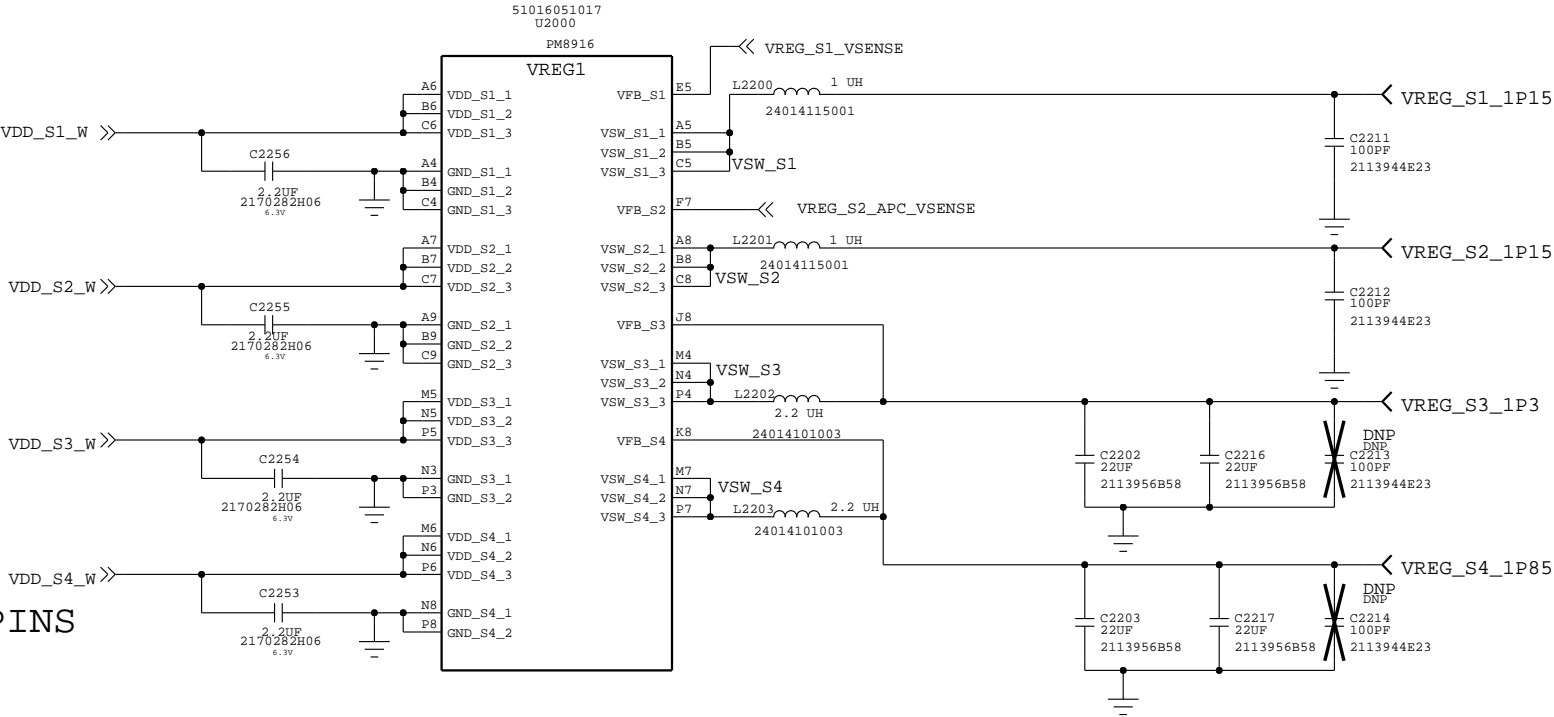
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ENGINEER: MMI		BLOCK TITLE:	
LAST MODIFIED BY: MMI Wed Apr 15 15:47:02 2015		ap_pmic	
MOTOROLA CONFIDENTIAL RESTRICTED <small>THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCUSSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © COPYRIGHT, MOTOROLA. THIS DOES NOT IMPLY PUBLICATION</small>		PROJECT NAME	
		84016407.cpm	
		REV 01	
		CAD FILE NAME:	
		PAGE 22 OF 47	


PM8916: SMPS

REF: 2000-2999



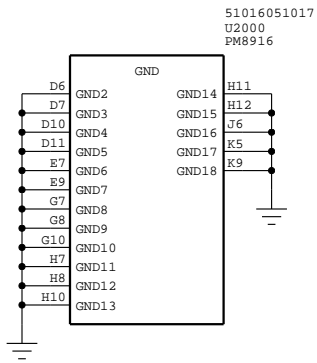
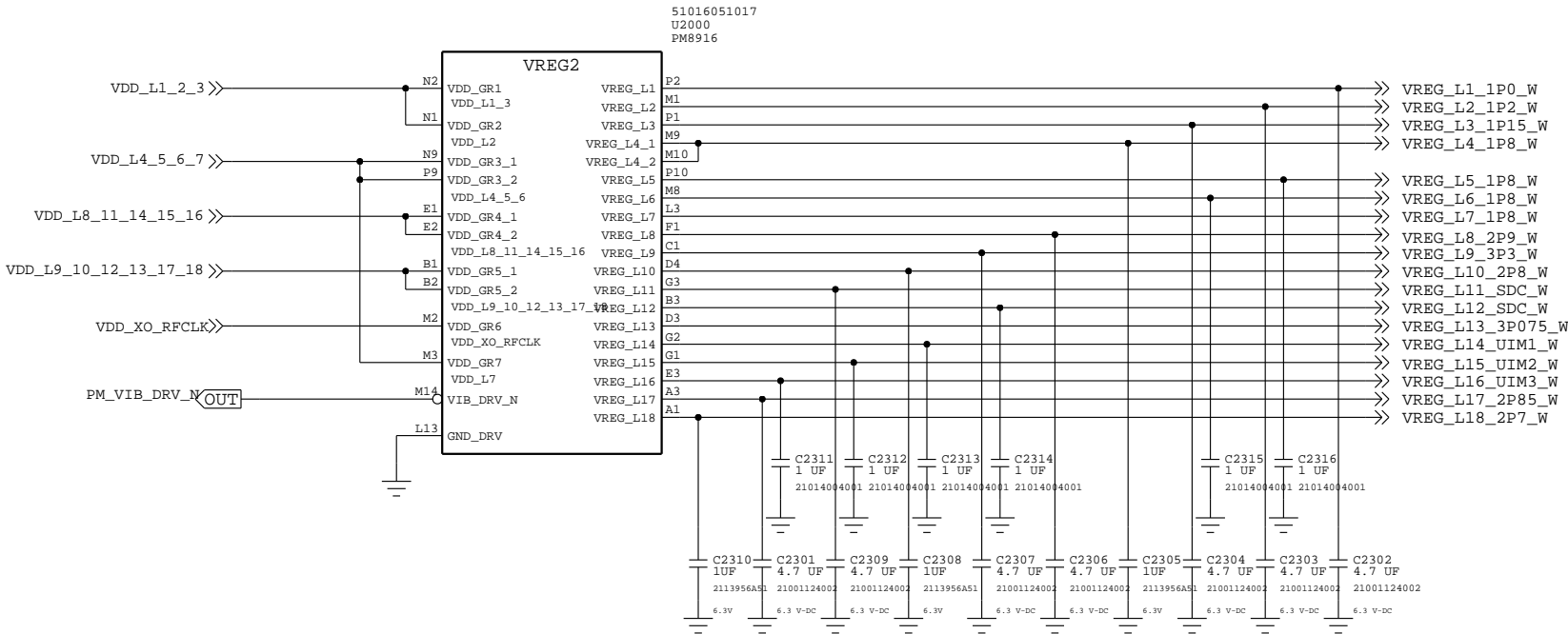
NOTE: PLACE INPUT CAPS CLOSE TO RESPECTIVE VIN PINS
EACH CAP GND SHOULD BE STAR-ROUTED



APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: XIAOYU WANG		BLOCK TITLE:	
LAST MODIFIED BY: MMI Wed Apr 15 15:47:03 2015		ap_pmic	
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		84016407.cpm	
		REV 01	
		CAD FILE NAME:	
		PAGE 23 OF 47	

PM8916: VREGS

REF: 2000-2999




PSEUDO CAPLESS LDOS

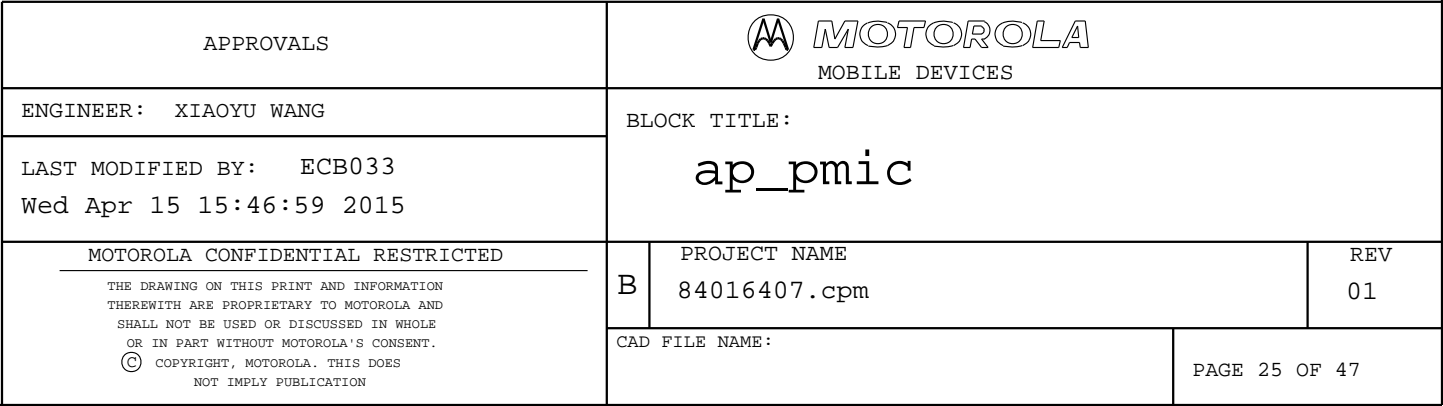
L4, L5, L6, L7, L8, L9, L10, L11, L12, L13, L14, L15, L16, L17, L18

L8, L9, L11, L17 EACH SHALL HAVE TOTAL EFFECTIVE OUTPUT CAPACITANCE OF 4.7UF
REST OF THE LDOS SHALL HAVE TOTAL EFFECTIVE OUTPUT CAPACITANCE OF 1.0UF

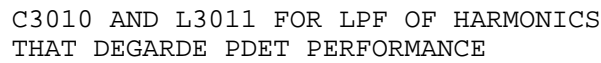
NOTE: IF A DECOUPLING CAP OF A PSEUDO CAPLESS LDO
DOES NOT ADD TO 1UF ON THE LOAD SIDE, INSTALL THE CAP
CLOSE TO THE PMIC

APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: XIAOYU WANG		BLOCK TITLE:	
LAST MODIFIED BY: MMI Wed Apr 15 15:47:03 2015		ap_pmic	
MOTOROLA CONFIDENTIAL RESTRICTED <small>THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCUSSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © COPYRIGHT, MOTOROLA. THIS DOES NOT IMPLY PUBLICATION</small>		B	PROJECT NAME 84016407.cpm
		CAD FILE NAME:	
		REV 01	
		PAGE 24 OF 47	

REF 2000-2999

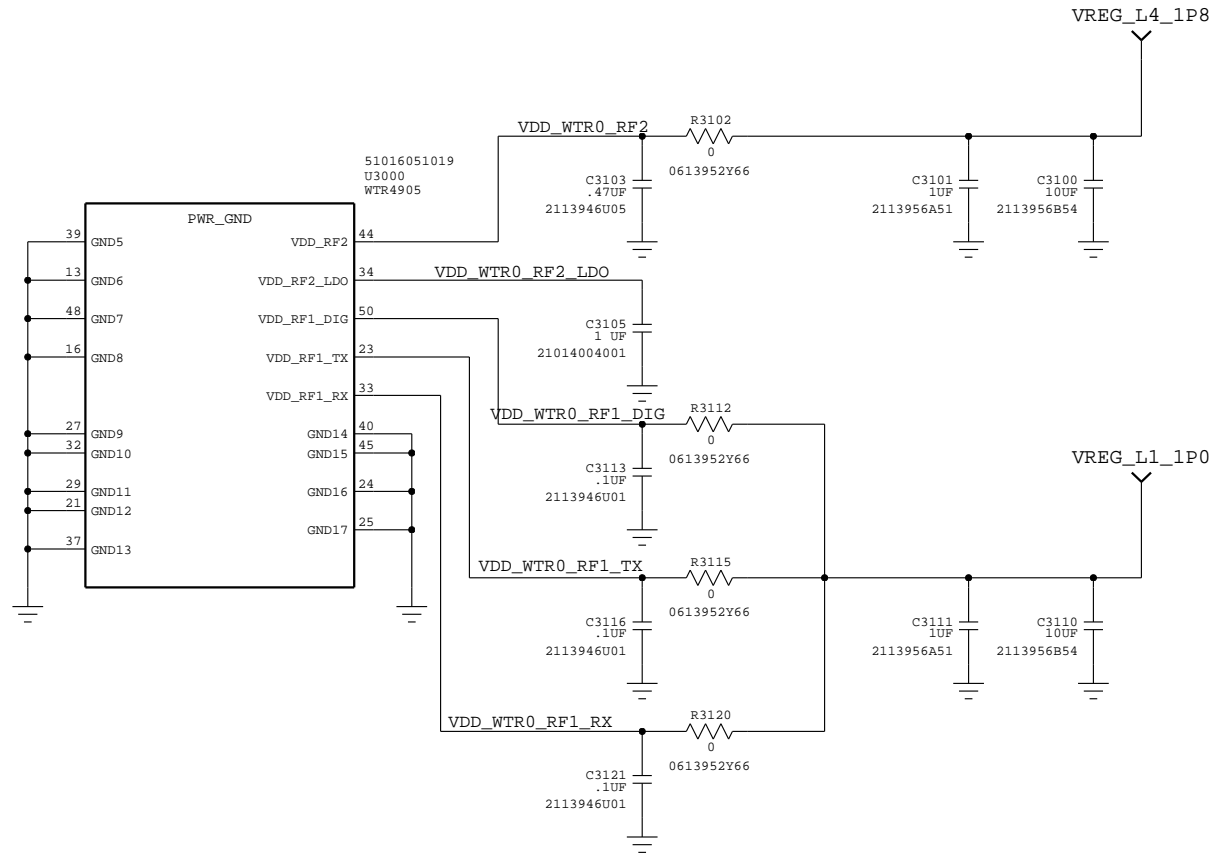


REF 3000-3099




WTR4905 POWER

REF 3100-3199



MOTOROLA MOBILITY CONFIDENTIAL RESTRICTED-MDB TRACKED TRACKEDANDBLOCKED

APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: MMI		BLOCK TITLE:	
LAST MODIFIED BY: MMI Wed Apr 15 15:47:06 2015		block_wtr4905	
MOTOROLA CONFIDENTIAL RESTRICTED <small>THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCUSSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © COPYRIGHT, MOTOROLA. THIS DOES NOT IMPLY PUBLICATION</small>		B	PROJECT NAME 84016407.cpm
		CAD FILE NAME:	
		REV 01	
		PAGE 27 OF 47	

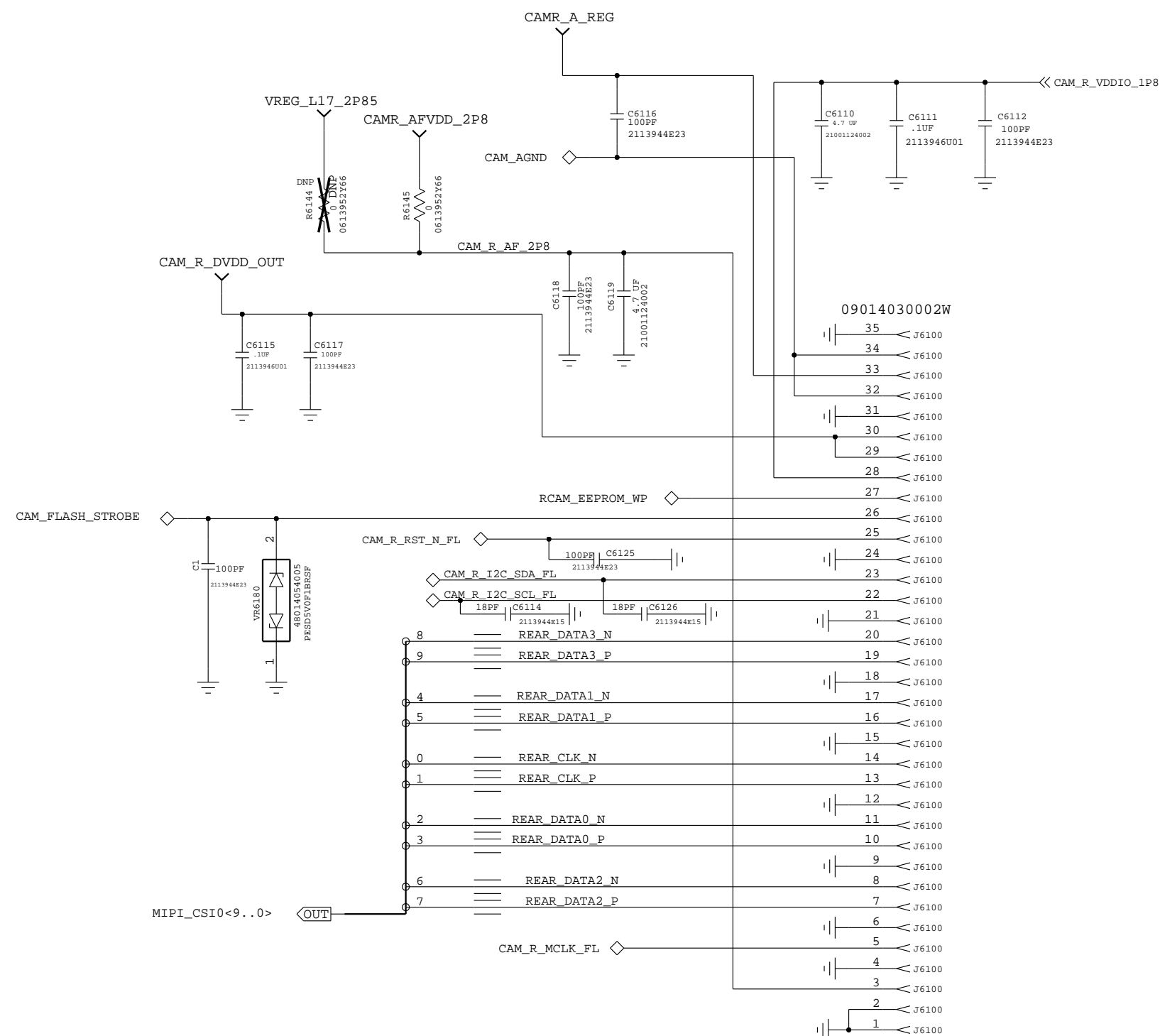
BASED ON 80-NL713-44 REV. A

REAR CAMERA
REF: 6100-6149

2		1		
REVISIONS				
REV	ECN	DESCRIPTION	MADE BY	APPROVED

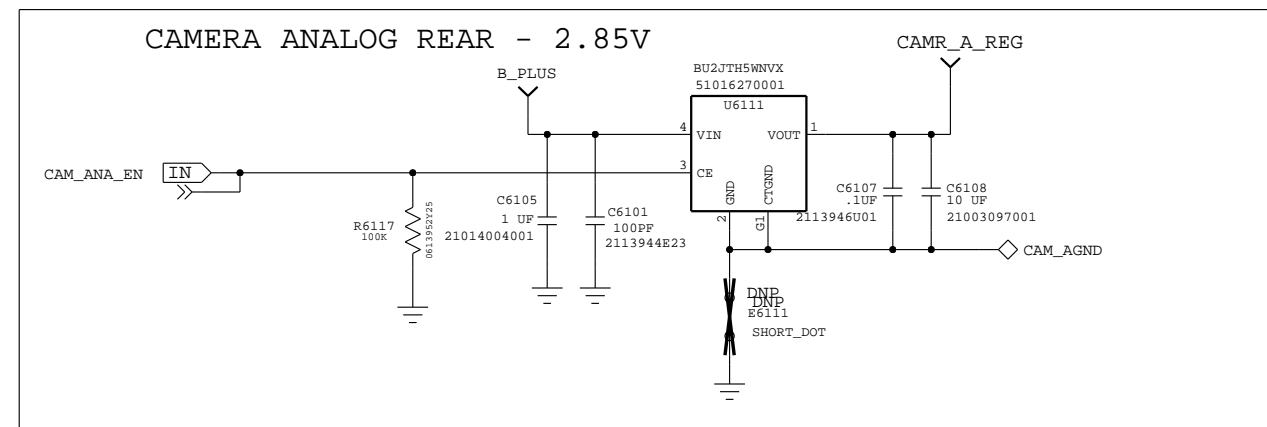
IMAGER FLEX CONNECTOR

I2C ADDR: 0X.. (0X..)

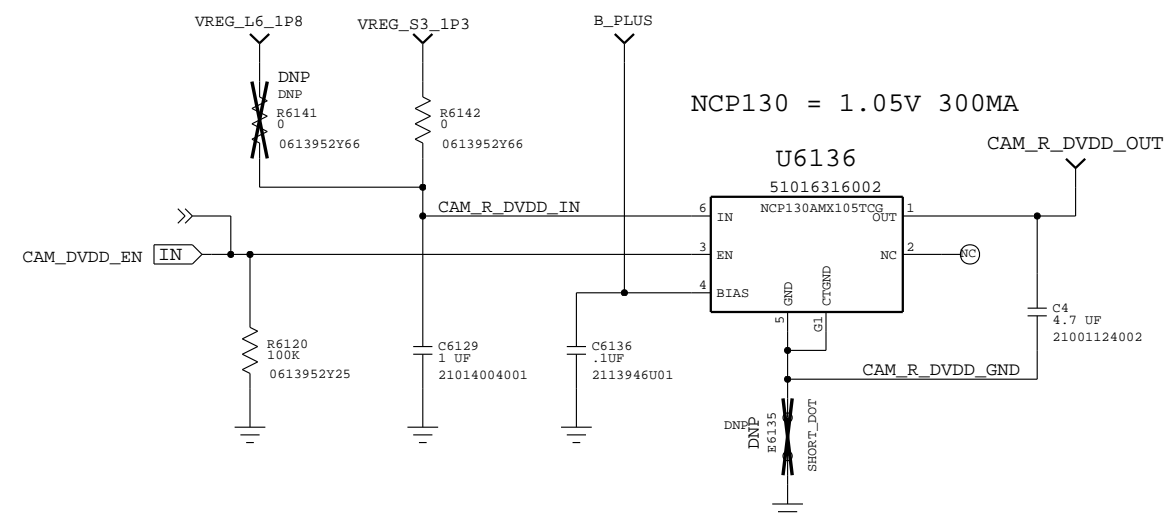


REF: 6100-6110

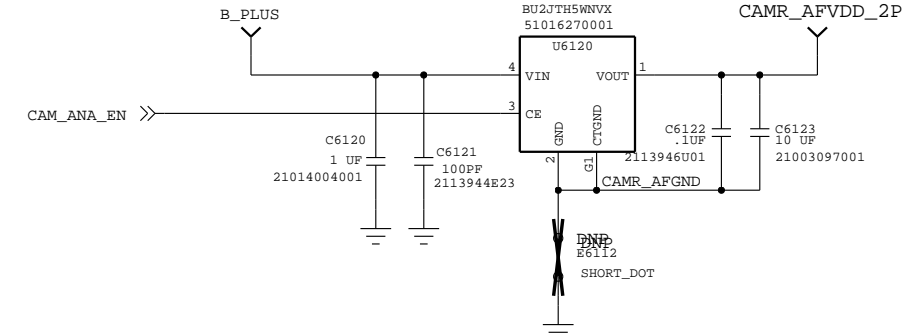
POWER REGULATORS



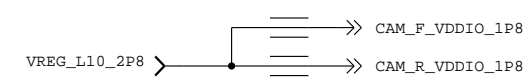
CAMERA CORE REAR - 1.05V



CAMERA AUTO FOCUS - 2.85V

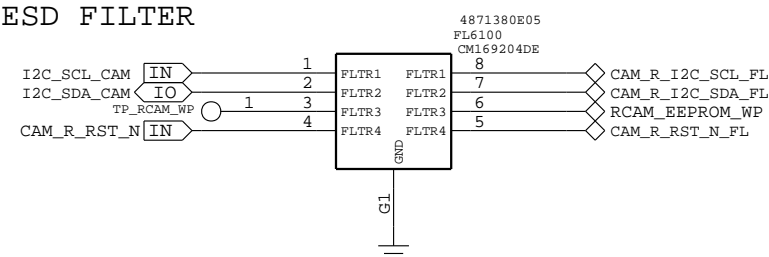


CAMERA VDDIO - 1.8V

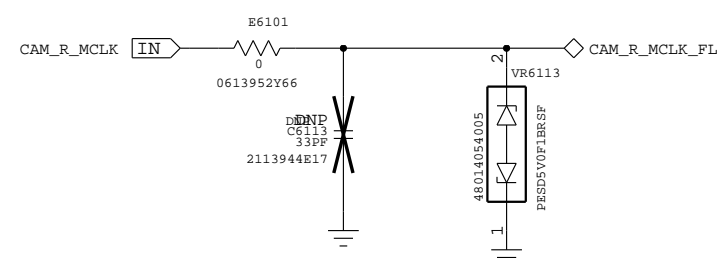


REF: 6110-6120

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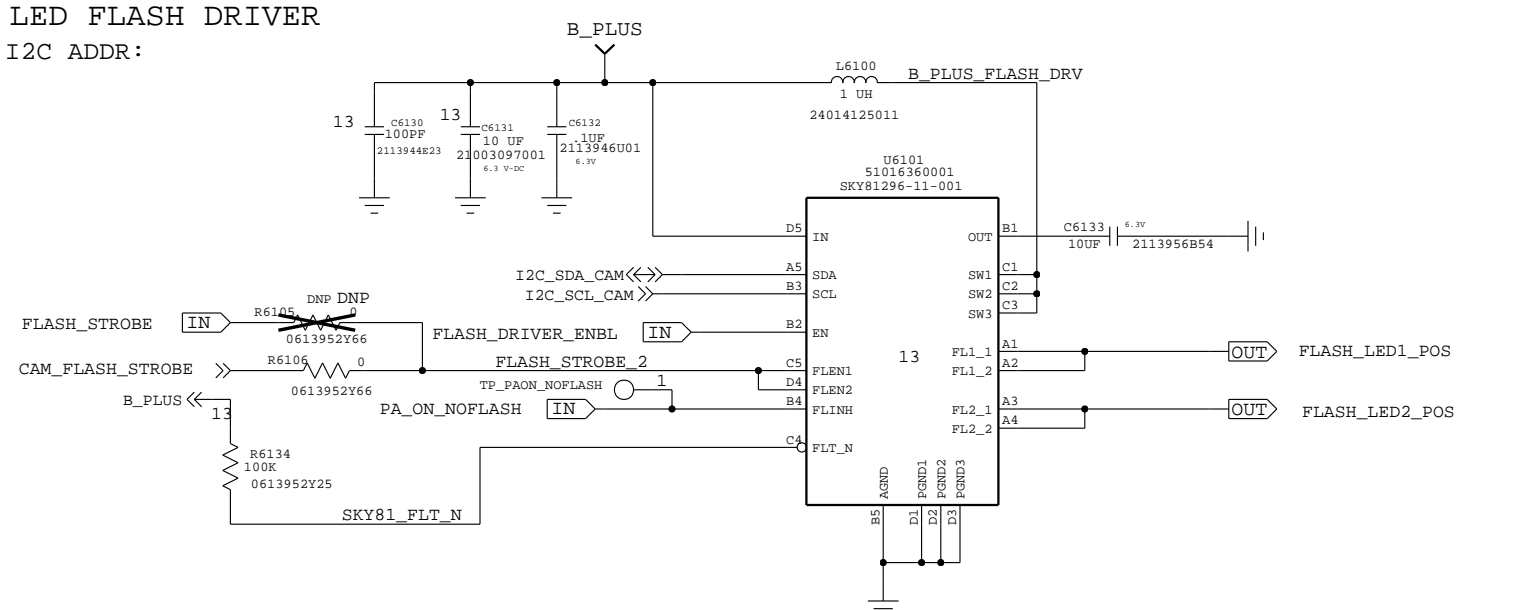


PLACE R6101 AND C6113 NEAR MSM

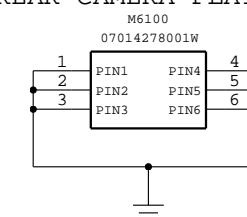


LED FLASH DRIVER


I2C ADDR:



REAR CAMERA PLATE



LAST MODIFIED: Wed Apr 15 15:47:07 2015

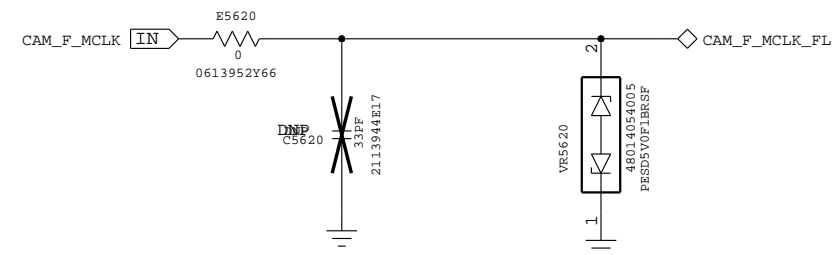
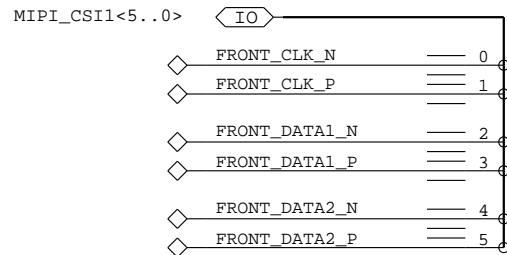
DRAWING NUMBER: 84		 MOTOROLA	
DRAWN BY:	DATE:	TITLE:	REV 01
LEAD ENGINEER:	DATE:		SIZE C
ASSY NUMBER:		PCB FILE NAME:	PAGE 28 OF 47

MOTOROLA CONFIDENTIAL RESTRICTED

PAGE 28 OF 47

A

8	7	6	5	4	3	2	1
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A

8	7	6	5	4	3	2	1
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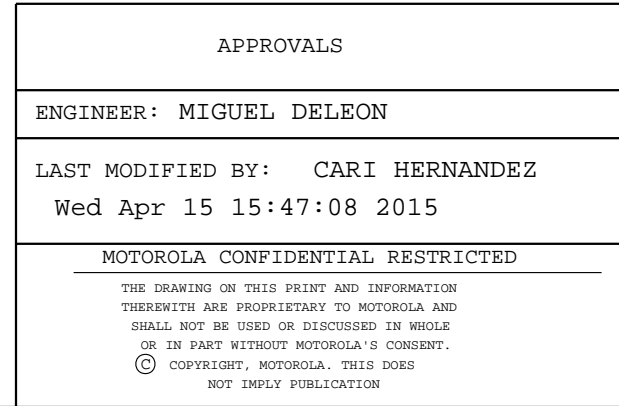
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C

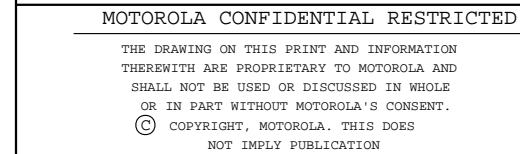


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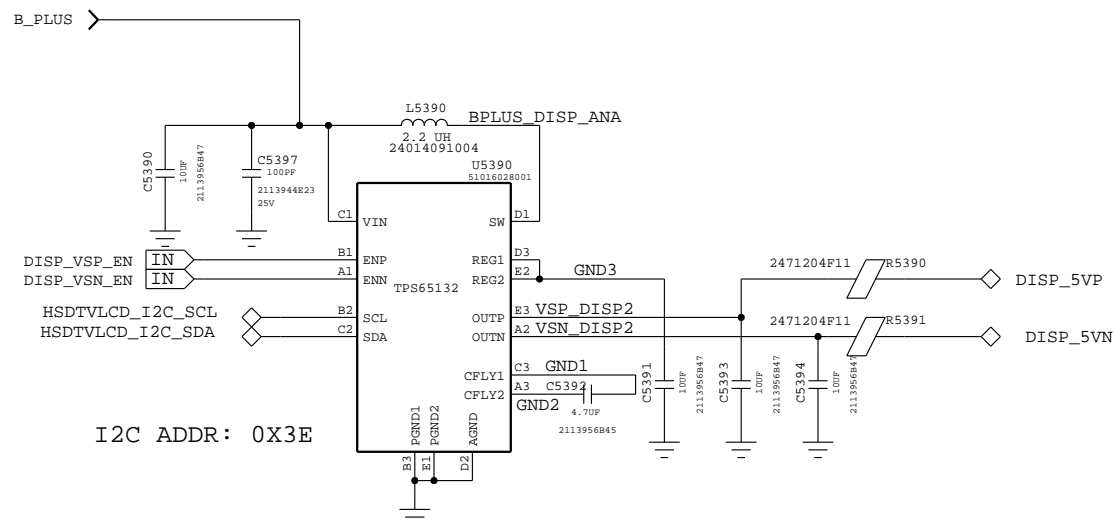
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ENGINEER: MIGUEL DELEON
LAST MODIFIED BY: CARI HERNANDEZ Wed Apr 15 15:47:08 2015



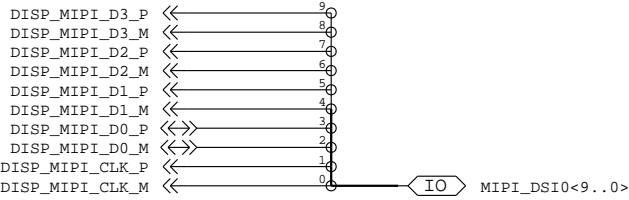
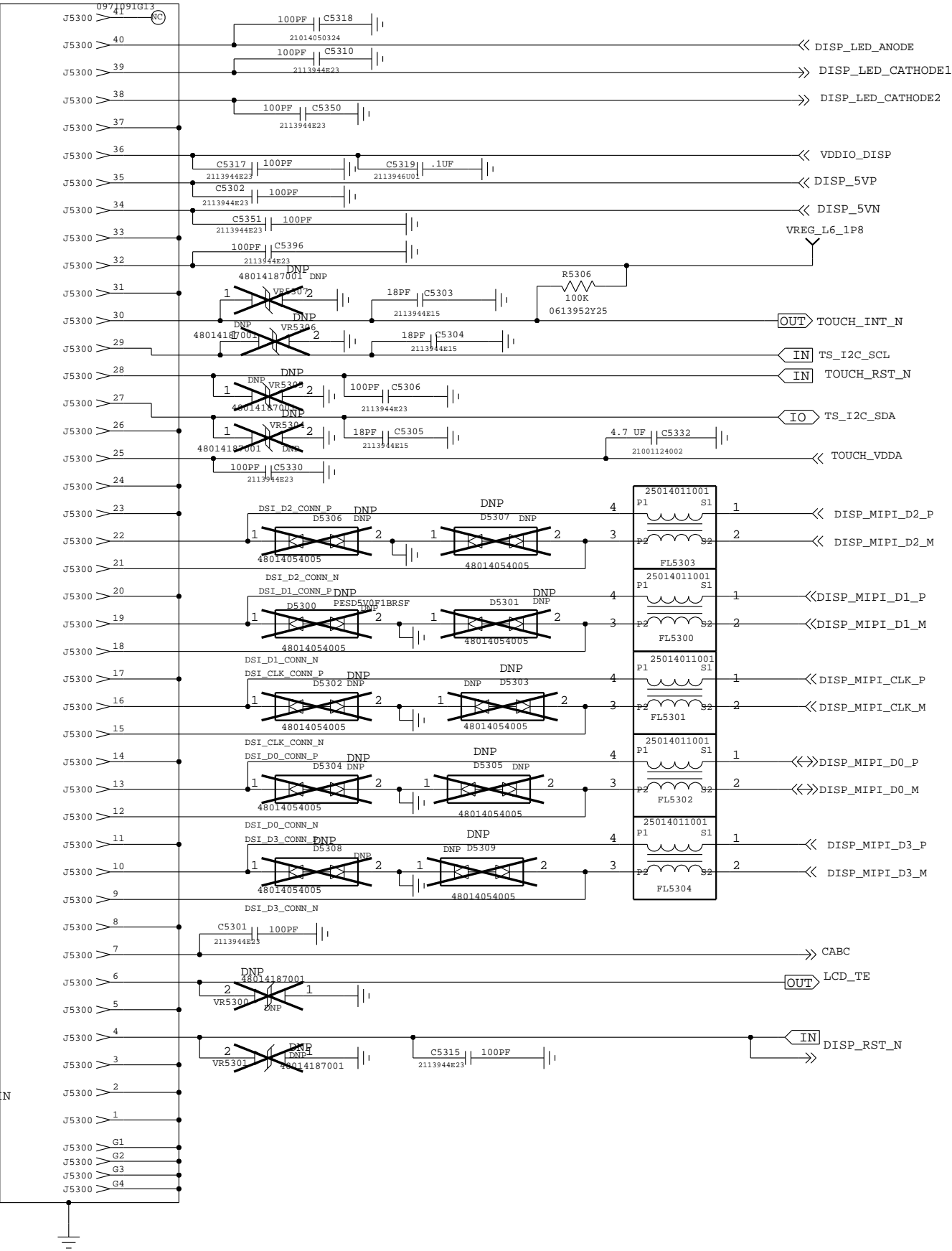
BLOCK TITLE:	
user_interface	
B	PROJECT NAME 84016407.cpm
	REV 01
CAD FILE NAME:	
PAGE 30 OF 47	




DISPLAY CONNECTOR

REF: 5300-5399

REVISIONS				
REV	ECN	DESCRIPTION	MADE BY	APPROVED



LAST MODIFIED: Wed Apr 15 15:47:10 2015				
DRAWING NUMBER: 84		 MOTOROLA		
DRAWN BY: MIGUEL DELEON		DATE: 		REV 01
LEAD ENGINEER: MIGUEL DELEON		DATE: 		SIZE B
ASSY NUMBER:		PCB FILE NAME:		PAGE 31 OF 47

A

SOL + NOTIFICATION LED

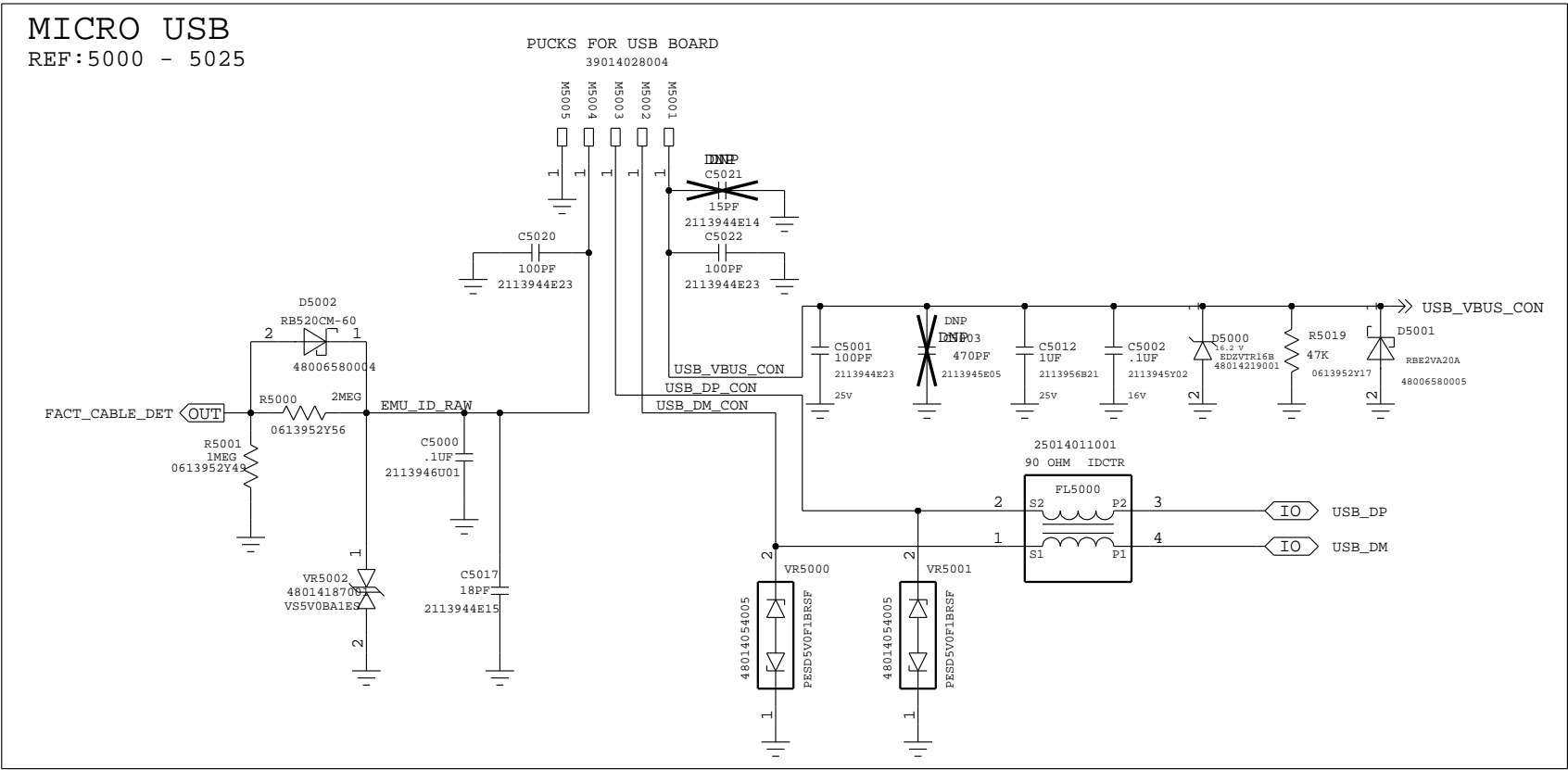
The schematic diagram illustrates the electrical connections for a SOL + NOTIFICATION LED. The circuit is powered by a B_PLUS supply. A MOSFET (D5401A, MT16-219A-T2D-AS1T1S2-3T, 48014022002) is used as a switch, controlled by a signal from the SOL_LED_SINK input (labeled IN). The MOSFET's drain is connected to the B_PLUS supply, and its source is connected to the LED (48014187001, VR5575, VS5V0BA1ES). A resistor (R5403A, 0613952Y66) is connected between the B_PLUS supply and the MOSFET's gate. A capacitor (C5401A, 100PF, 2113944E23) is connected between the MOSFET's gate and ground. The LED is labeled 'WHITE'. The circuit is also connected to a ground symbol.

The image displays three circuit diagrams, each for a different signal: KEY_VOLDN2, KEY_VOLUP2, and KEY_POWER2. Each circuit starts with a switch (S5570, S5571, and S5572 respectively) controlled by a 40014033001 signal. The switches have terminals A, B, and two ground connections (GND1, GND2, GND3). The output of each switch is connected to a diode (VR5573, VR5574, and VR5572 respectively) and a diode stack (VR5576, VR5577, and VR5578 respectively). The diodes are 48014187001 (VS5V0BA1ES) and the diode stacks are 48014054005 (PESD5V0F1BR5F). The output of the diode stack is connected to an inductor (L5570, L5571, and L5572 respectively) with a value of 0.082 UH and a part number 24014052040. The inductor's output is connected to the final output signal (KEY_VOL_DN, KEY_VOL_UP, and KEY_POWER) and a terminal labeled TP_PWR_KEY TSPM1_27.

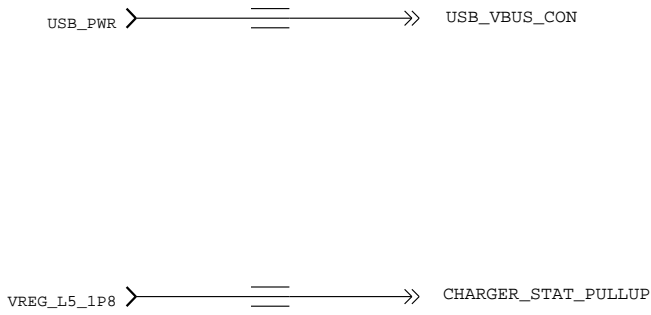
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USB_BATTERY

REF:5000 - 5299

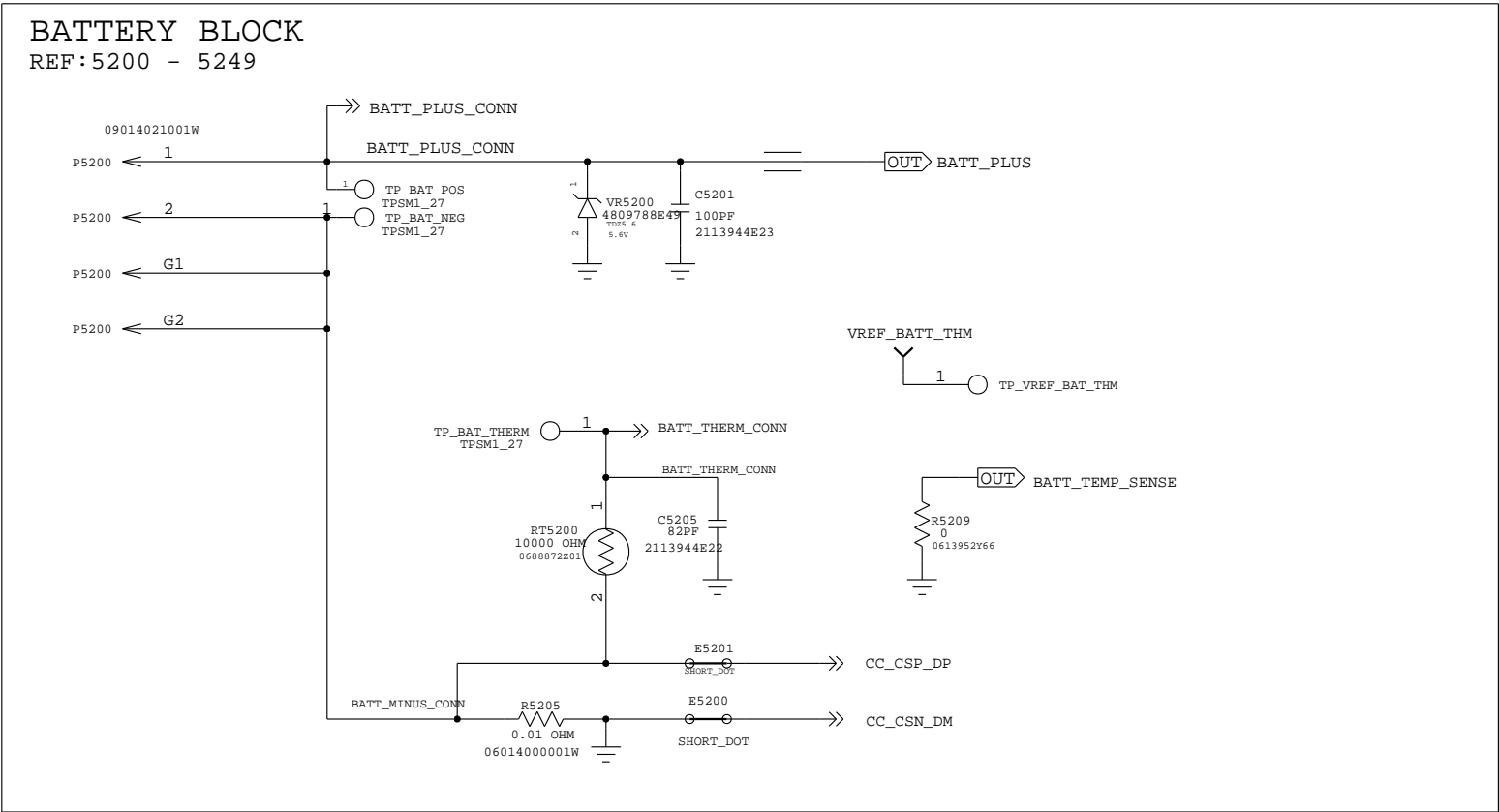
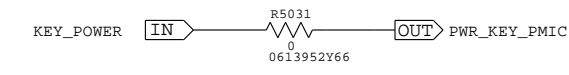


SUPPLIES



KUNG POW BATTERY DISCONNECT

REMOVED



APPROVALS		MOTOROLA MOBILE DEVICES	
ENGINEER: XIAOYU WANG		BLOCK TITLE:	
LAST MODIFIED BY: NATE CONNELL Wed Apr 15 15:47:09 2015		user_interface	
MOTOROLA CONFIDENTIAL RESTRICTED <small>THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCUSSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © COPYRIGHT, MOTOROLA. THIS DOES NOT IMPLY PUBLICATION</small>		PROJECT NAME	
		84016407.cpm	
		REV 01	
		CAD FILE NAME:	
		PAGE 33 OF 47	

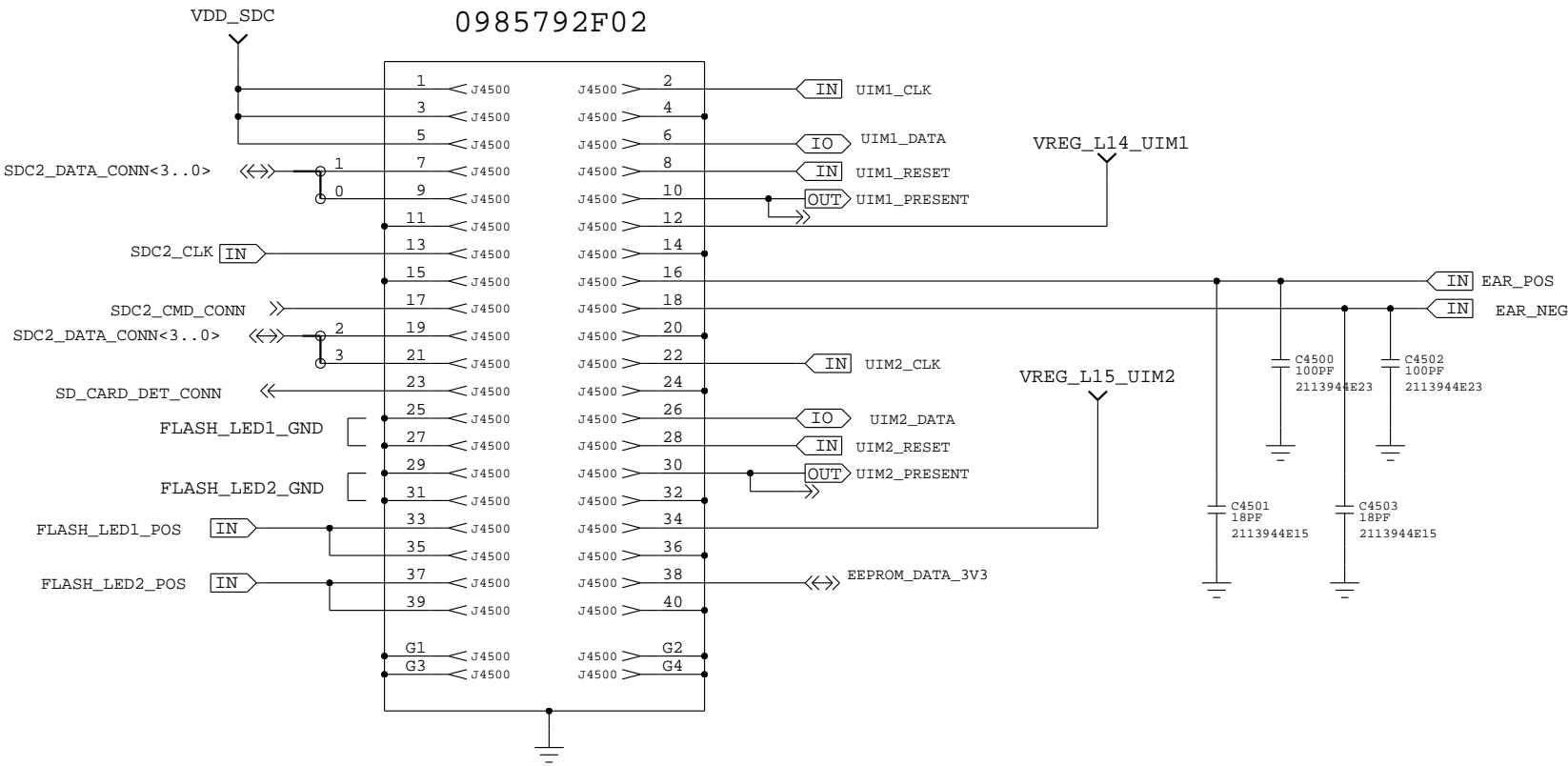
D



2

DAUGHTER BOARD CONNECTOR

REF 4500-4599

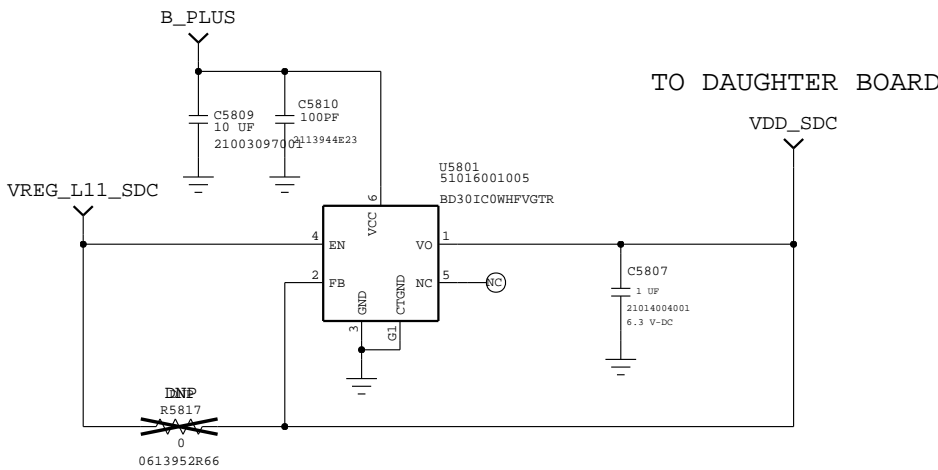


MOTOROLA MOBILITY CONFIDENTIAL RESTRICTED-MDB TRACKED TRACKEDANDBLOCKED

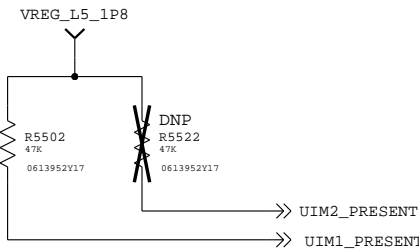
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ENGINEER: CARI HERNANDEZ		BLOCK TITLE:	
LAST MODIFIED BY: CARI HERNANDEZ Wed Apr 15 15:47:11 2015		daughter_board_conn	
MOTOROLA CONFIDENTIAL RESTRICTED <small>THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCUSSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © COPYRIGHT, MOTOROLA. THIS DOES NOT IMPLY PUBLICATION</small>		PROJECT NAME	REV
		B 84016407.cpm	01
CAD FILE NAME:			PAGE 35 OF 47

SIM/SD/FLASH CARD SUPPORT

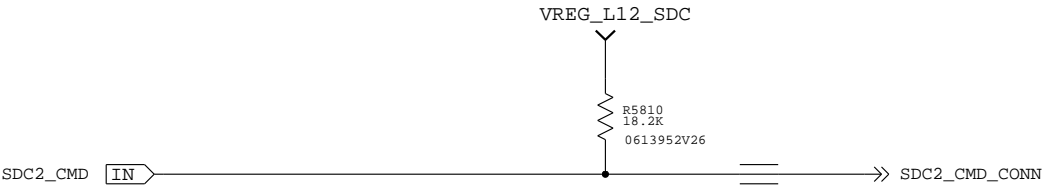
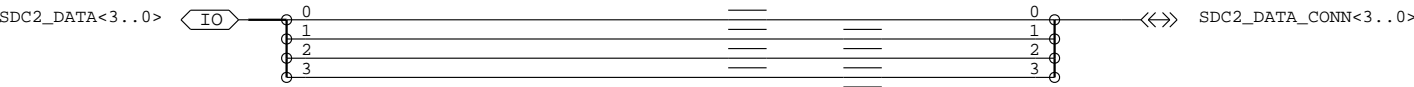
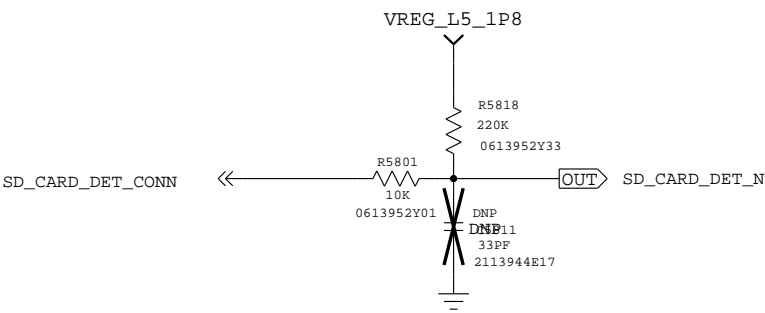
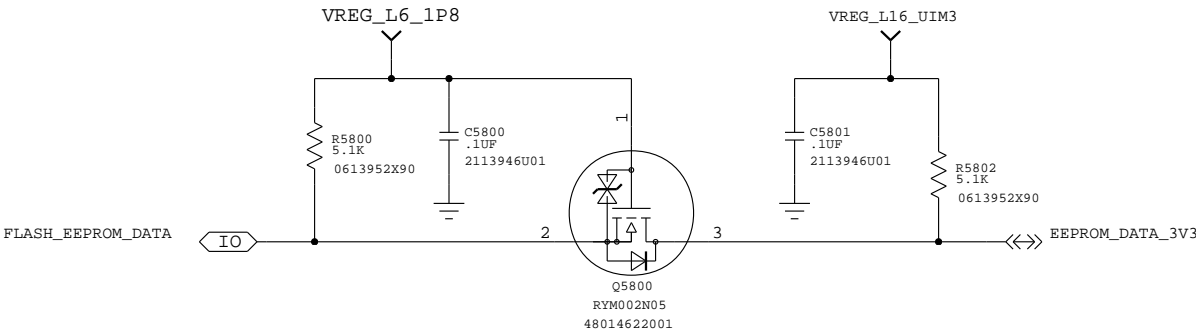
SD CARD POWER SUPPLY




PULLUPS FOR SIM DETECT



LEVEL SHIFT FOR FLASH LED EEPROM DATA

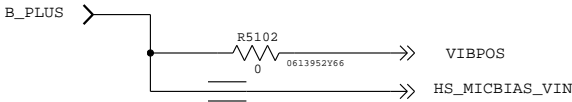
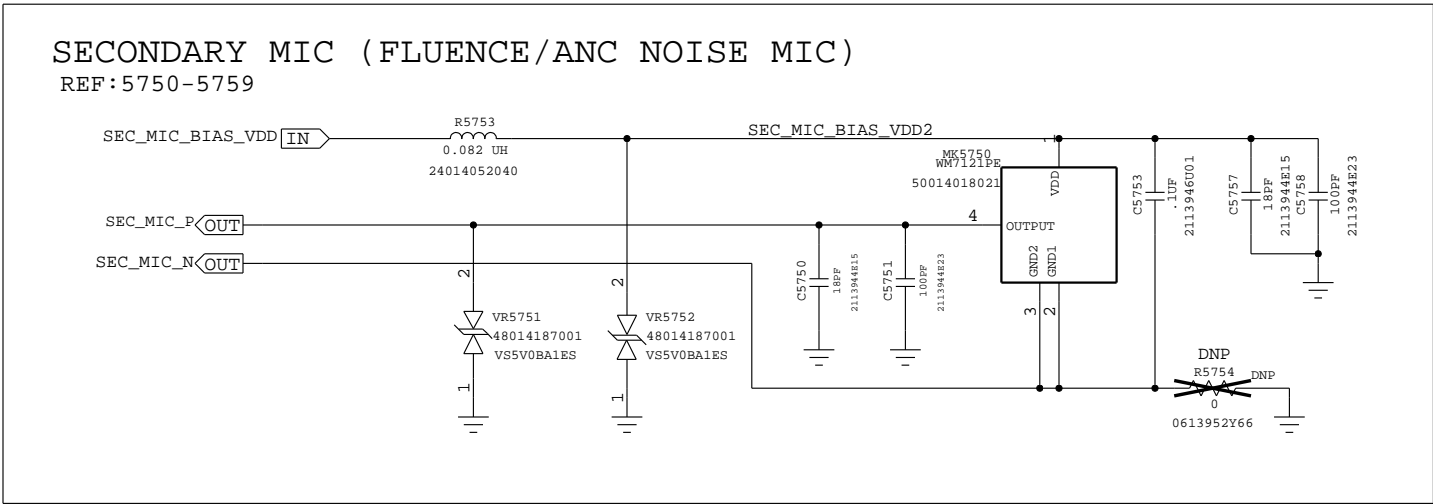
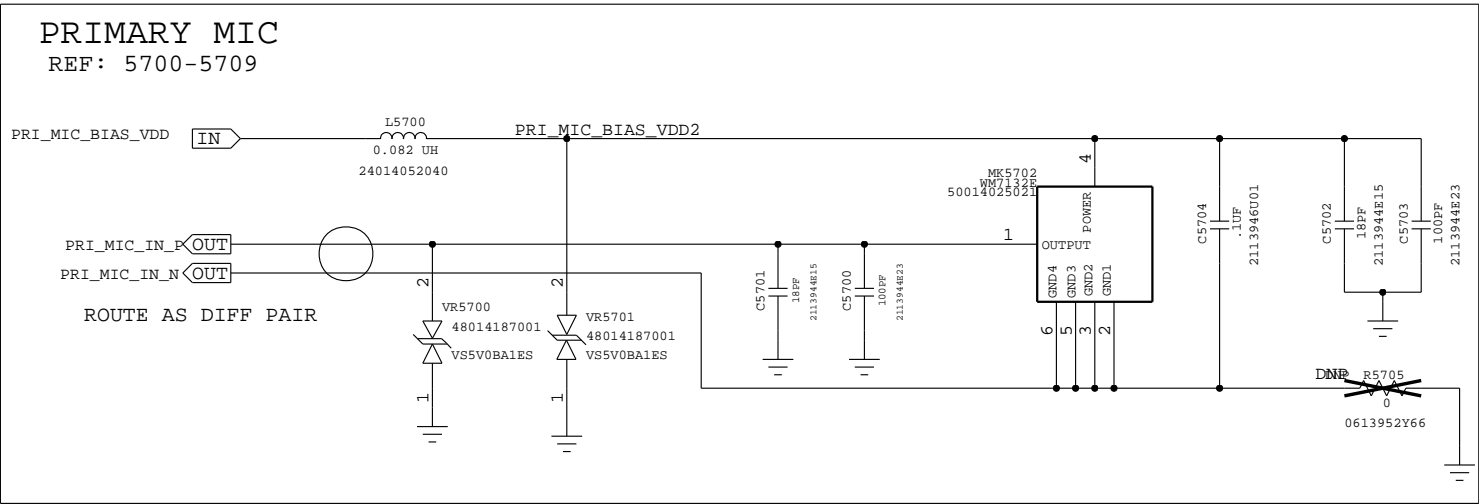



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LAST MODIFIED BY: CARLOS TORRES Wed Apr 15 15:47:11 2015		daughter_board_conn	
MOTOROLA CONFIDENTIAL RESTRICTED <small>THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCUSSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © COPYRIGHT, MOTOROLA. THIS DOES NOT IMPLY PUBLICATION</small>		PROJECT NAME	
		B 84016407.cpm	
		REV 01	
		CAD FILE NAME:	
		PAGE 36 OF 47	

MICS

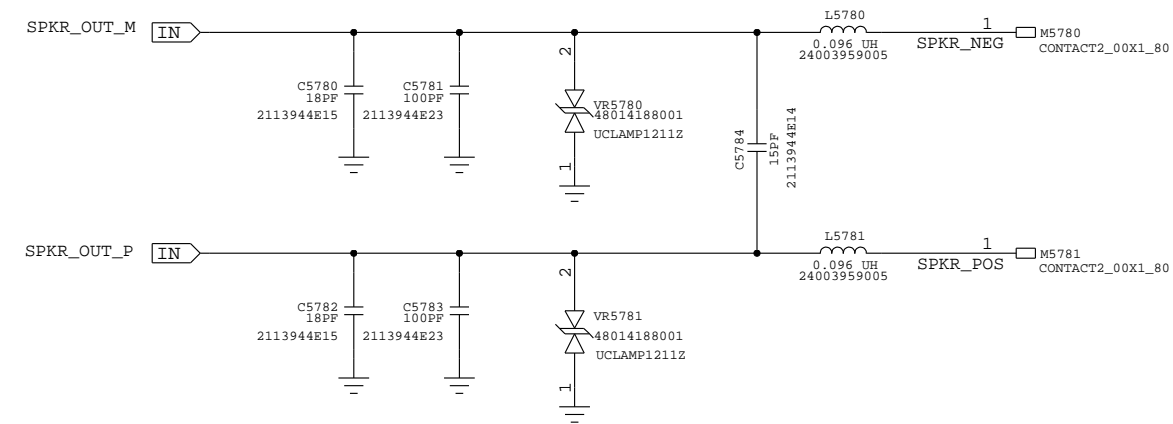
REF: 5700-5799



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ENGINEER: DENNIS BUDNICK		BLOCK TITLE:	
LAST MODIFIED BY: NATE CONNELL Wed Apr 15 15:47:12 2015		audio_fe	
MOTOROLA CONFIDENTIAL RESTRICTED <small>THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCUSSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © COPYRIGHT, MOTOROLA. THIS DOES NOT IMPLY PUBLICATION</small>		PROJECT NAME	
		B 84016407.cpm	
		REV 01	
		CAD FILE NAME:	
		PAGE 37 OF 47	

SPEAKER_VIB

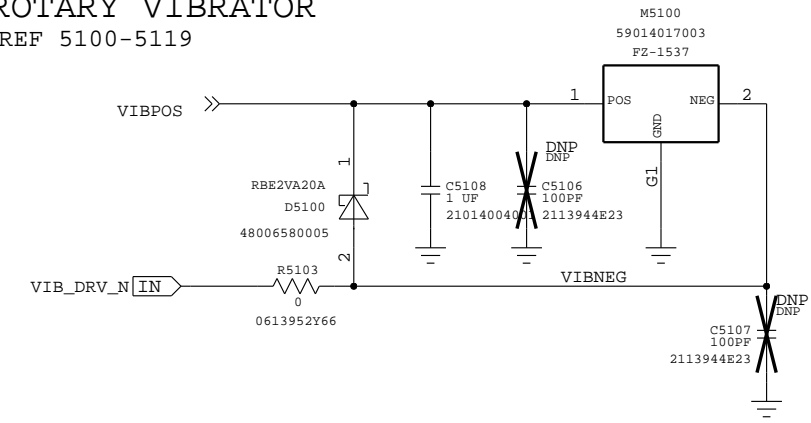
LOUDSPEAKER
REF:5780-5789



EARPIECE TRANSDUCER
REF:5780-5789

EARPIECE MOVED TO DAUGHTER CARD

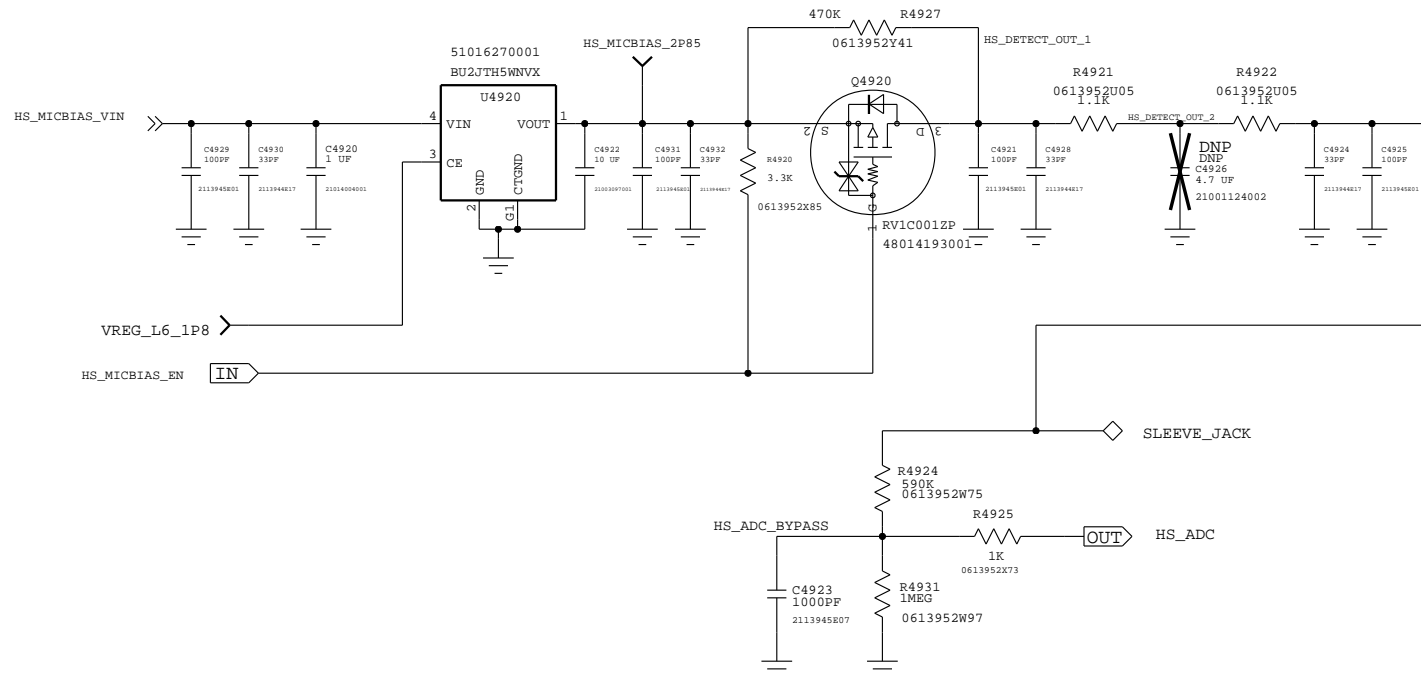
ROTARY VIBRATOR
REF 5100-5119



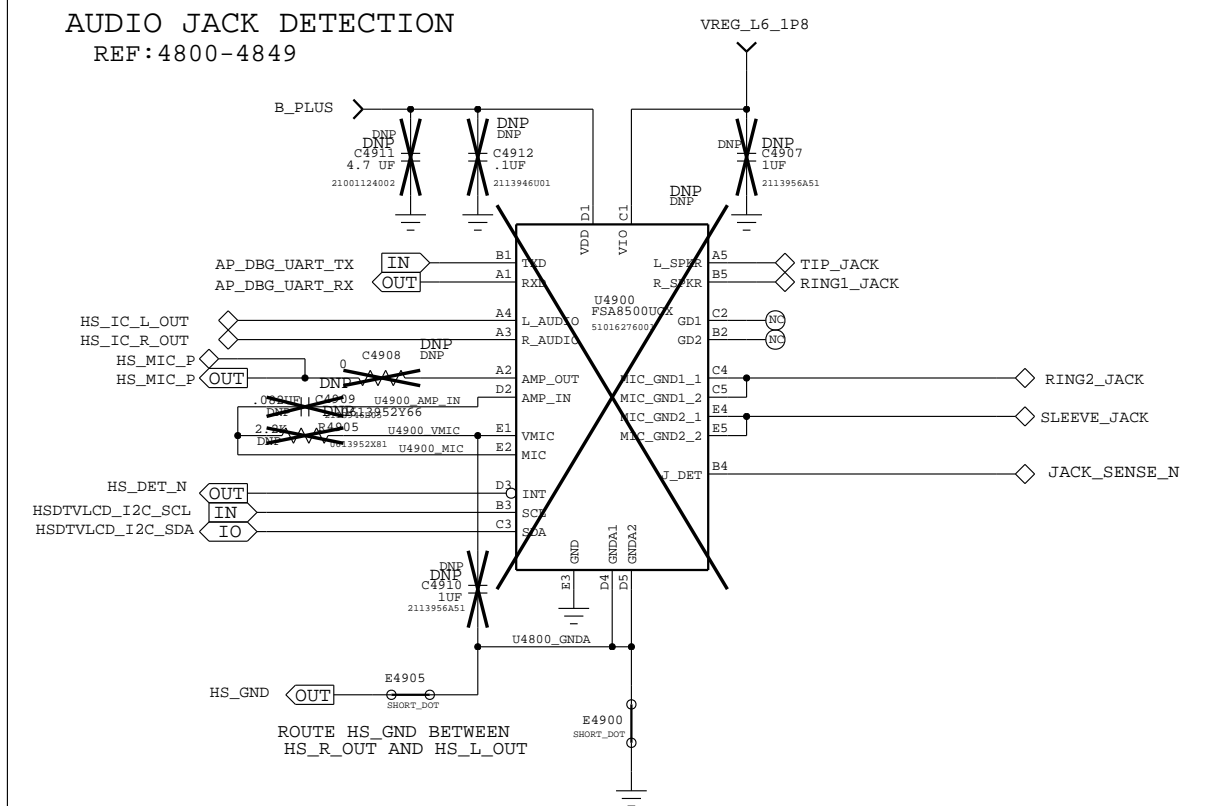
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ENGINEER: DENNIS BUDNICK		BLOCK TITLE:	
LAST MODIFIED BY: TIM MILLER Wed Apr 15 15:47:13 2015		audio_fe	
MOTOROLA CONFIDENTIAL RESTRICTED <small>THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCUSSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © COPYRIGHT, MOTOROLA. THIS DOES NOT IMPLY PUBLICATION</small>		PROJECT NAME	
		84016407.cpm	
		REV 01	
		CAD FILE NAME:	
		PAGE 38 OF 47	

3.5MM HEADSET

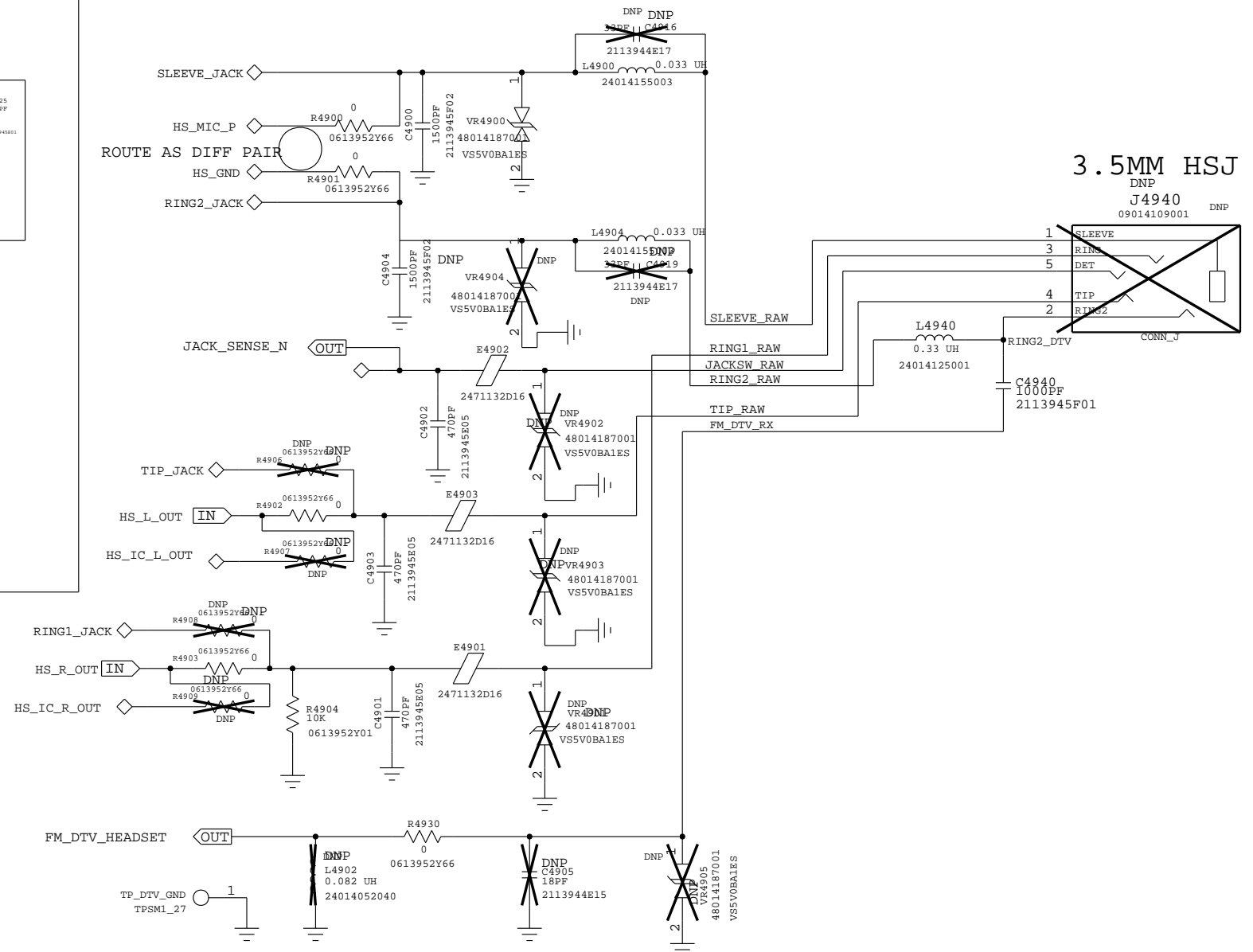
SENSOR HUB HEADSET DETECTION CIRCUIT




AUDIO JACK DETECTION
REF:4800-4849



REF: 4900-4949



APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: MMI	BLOCK TITLE: <div style="font-size: 2em; font-family: monospace;">audio_fe</div>		
LAST MODIFIED BY: MMI Wed Apr 15 15:47:12 2015			
MOTOROLA CONFIDENTIAL RESTRICTED <hr/> <small> THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCUSSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © COPYRIGHT, MOTOROLA. THIS DOES NOT IMPLY PUBLICATION </small>		PROJECT NAME B 84016407.cpm	REV 01
		CAD FILE NAME:	PAGE 39 OF 47

D



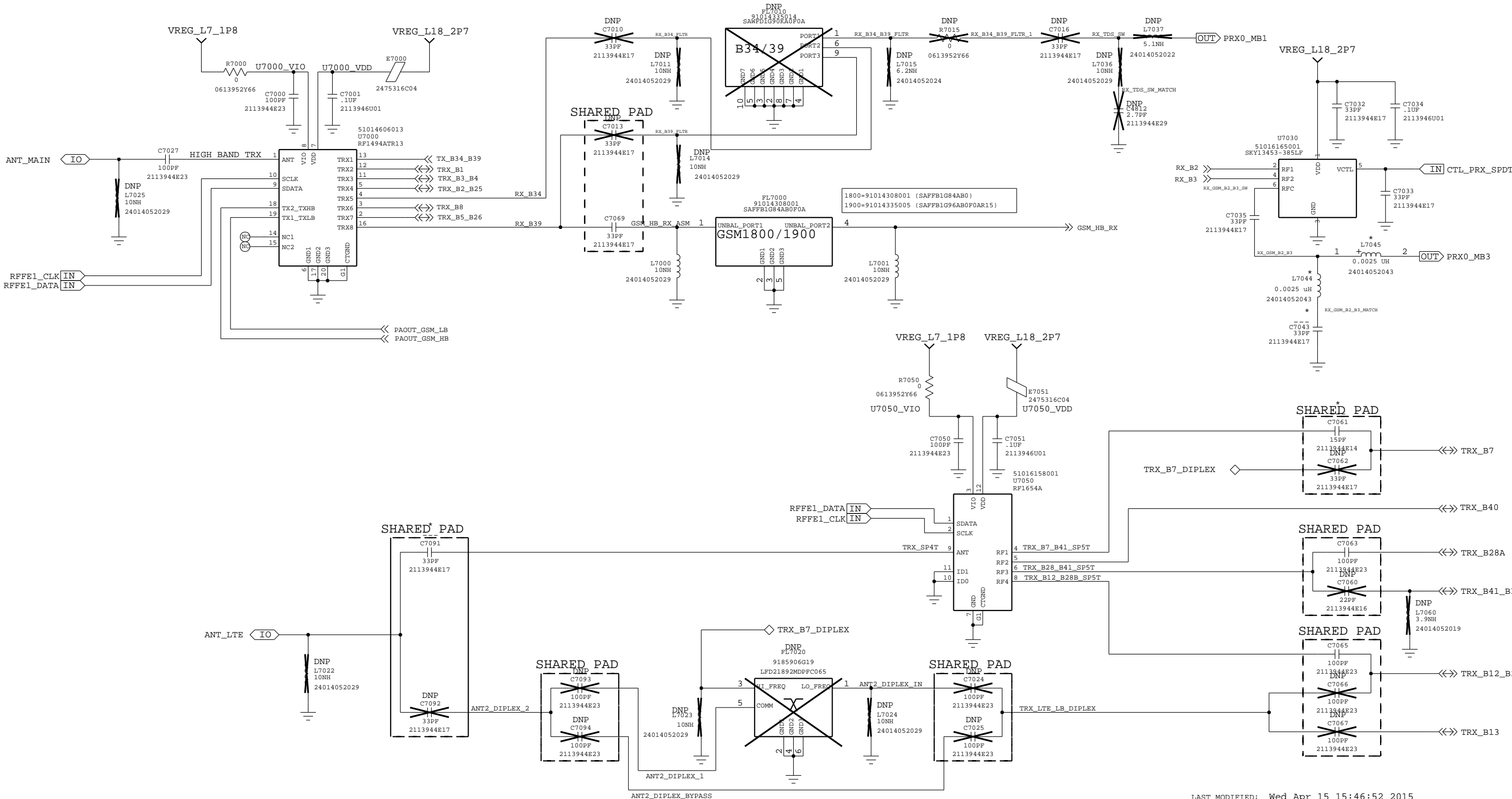
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A


RFFE:SP10T+SP4T

REF 7000-7099


REVISIONS				
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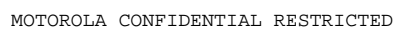


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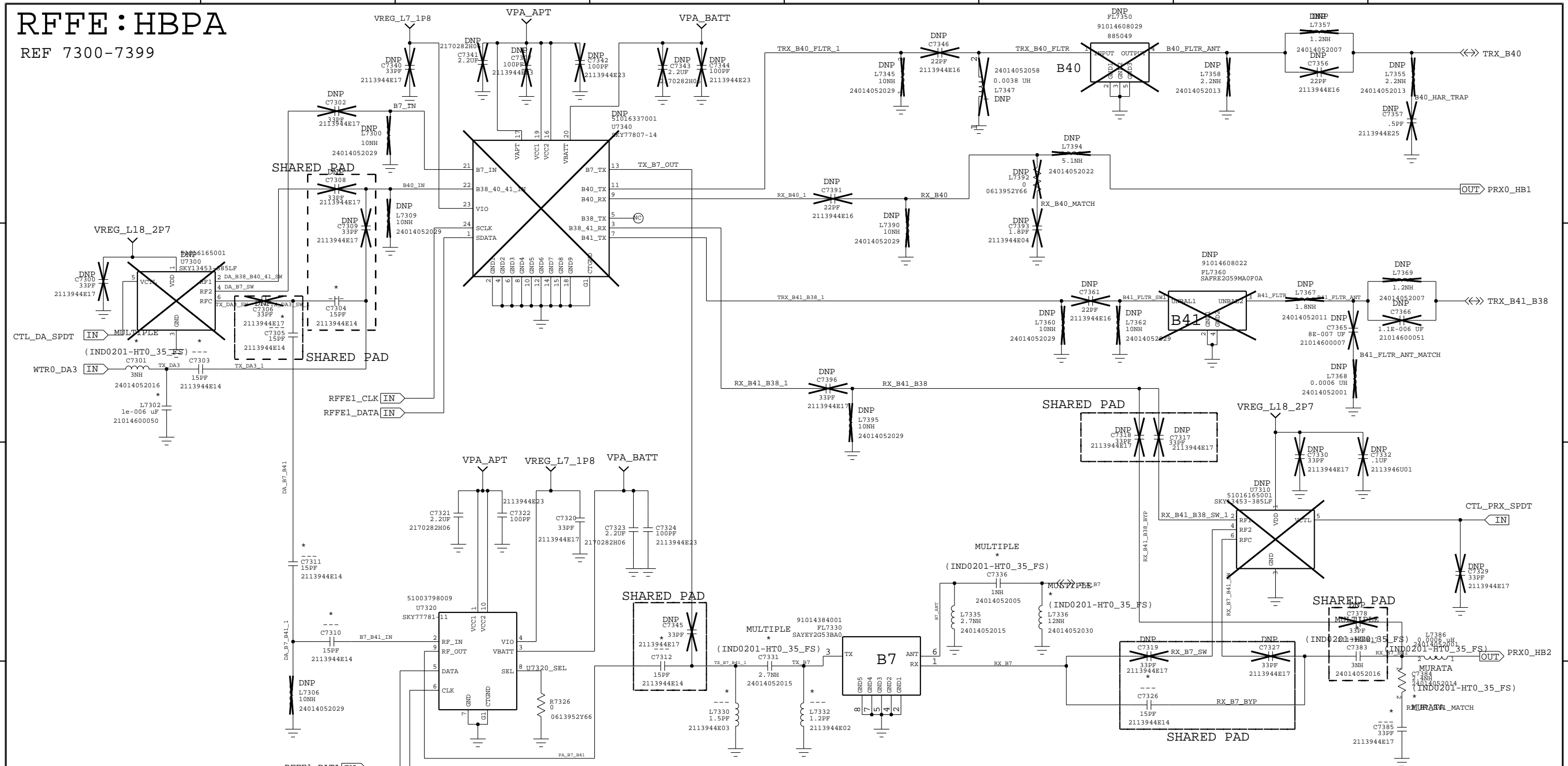
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DRAWN BY:	DATE:	TITLE:		REV 01
LEAD ENGINEER:	DATE:			SIZE B
ASSY NUMBER:	PCB FILE NAME:		PAGE 41 OF 47	


MOTOROLA CONFIDENTIAL RESTRICTED

LAST MODIFIED: Wed Apr 15 15:46:51 2015			
DRAWING NUMBER: 84		 MOTOROLA	
DRAWN BY:	DATE:	TITLE:	REV 01
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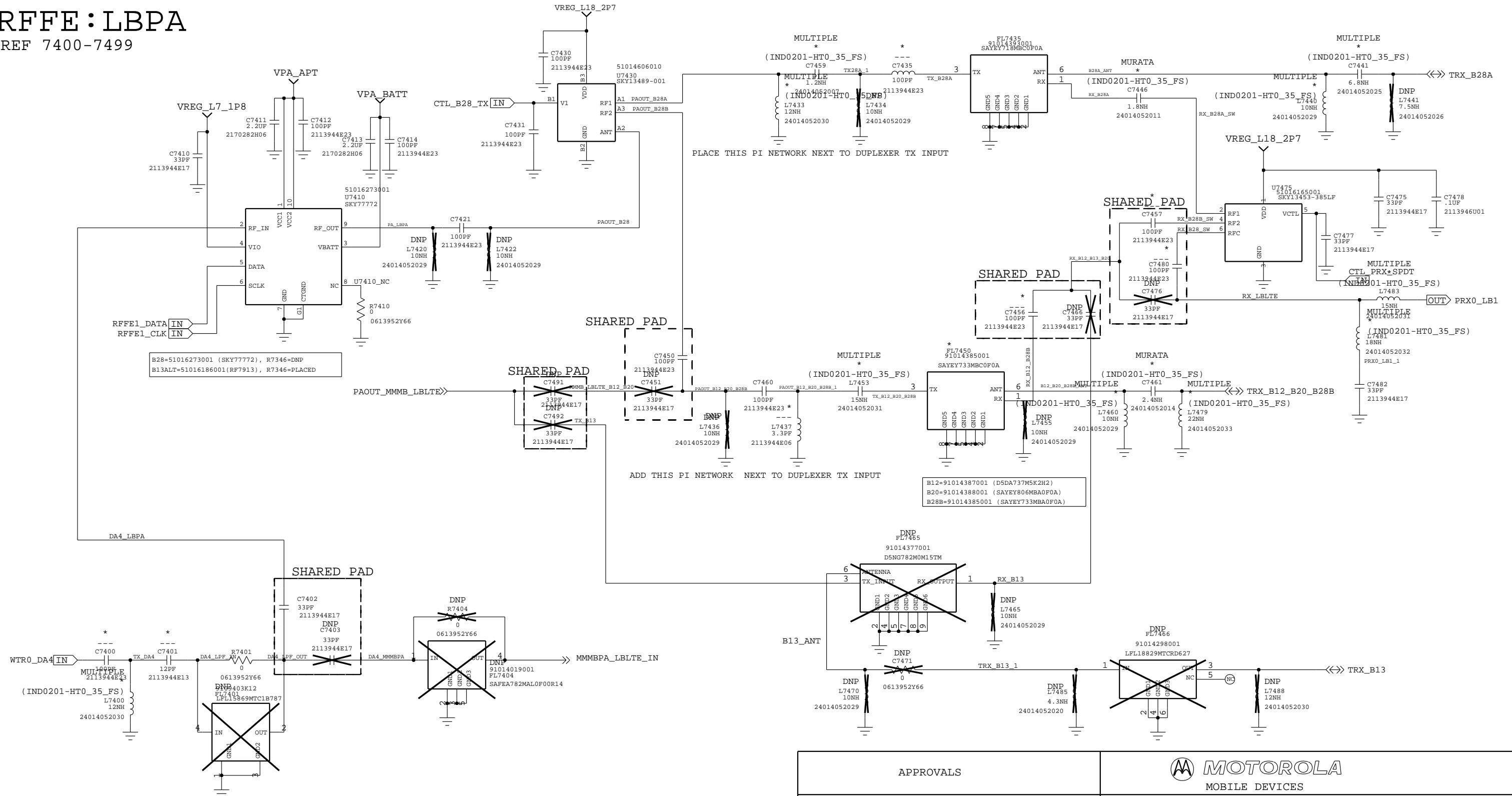



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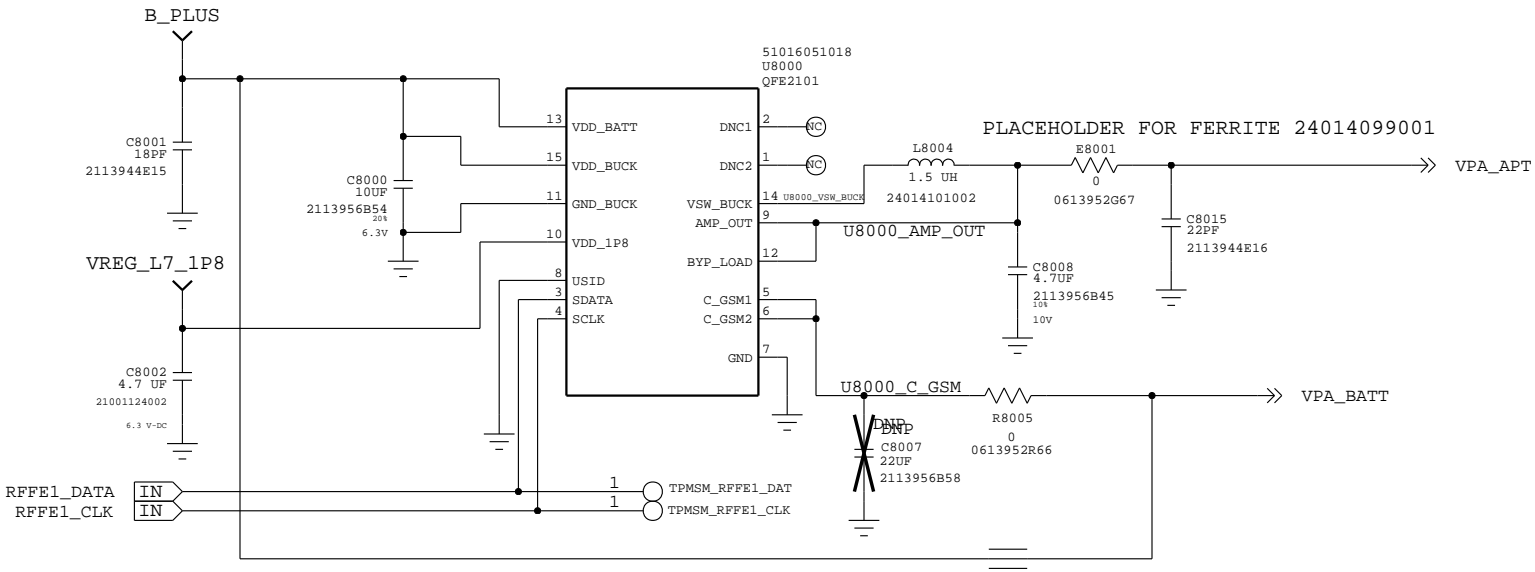
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MOTOROLA CONFIDENTIAL RESTRICTED <hr/> THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCUSSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © COPYRIGHT, MOTOROLA. THIS DOES NOT IMPLY PUBLICATION		PROJECT NAME B 84016407.cpm	REV 01
		CAD FILE NAME:	PAGE 43 OF 47

RFFE : LBPA
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APPROVALS		 MOTOROLA MOBILE DEVICES	
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		CAD FILE NAME:	PAGE 44 OF 47

RFFE:QFE2101
REF 8000-8020



BYPASS OPTION FOR VPA_VBATT
DNP APPROPRIATELY

APPROVALS		 MOTOROLA MOBILE DEVICES	
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		B 84016407.cpm	
		CAD FILE NAME:	
		PAGE 45 OF 47	
		REV 01	

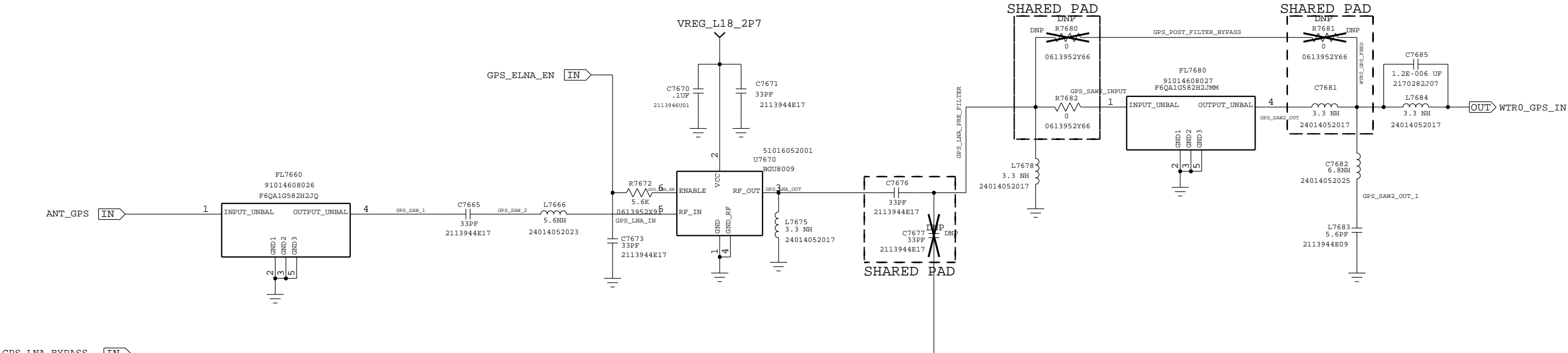
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RFFE: GPS

REF 7650-7699

GPS



APPROVALS		 MOTOROLA MOBILE DEVICES	
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LAST MODIFIED BY: MMI Wed Apr 13 09:45:48 2013		block_rffe	
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			REV 01 PAGE 47 OF 47