JUDO REFERENCE DESIGNATOR RANGES BY BLOCK

000 - 499 :AP_PMIC

500 - 999 : PWOER

1000 -1499 :AUDIO_FE

1500-1999:CONNECTIVITY_BB

2000-2499:MULTIMEDIA

2500-2999:XMCS

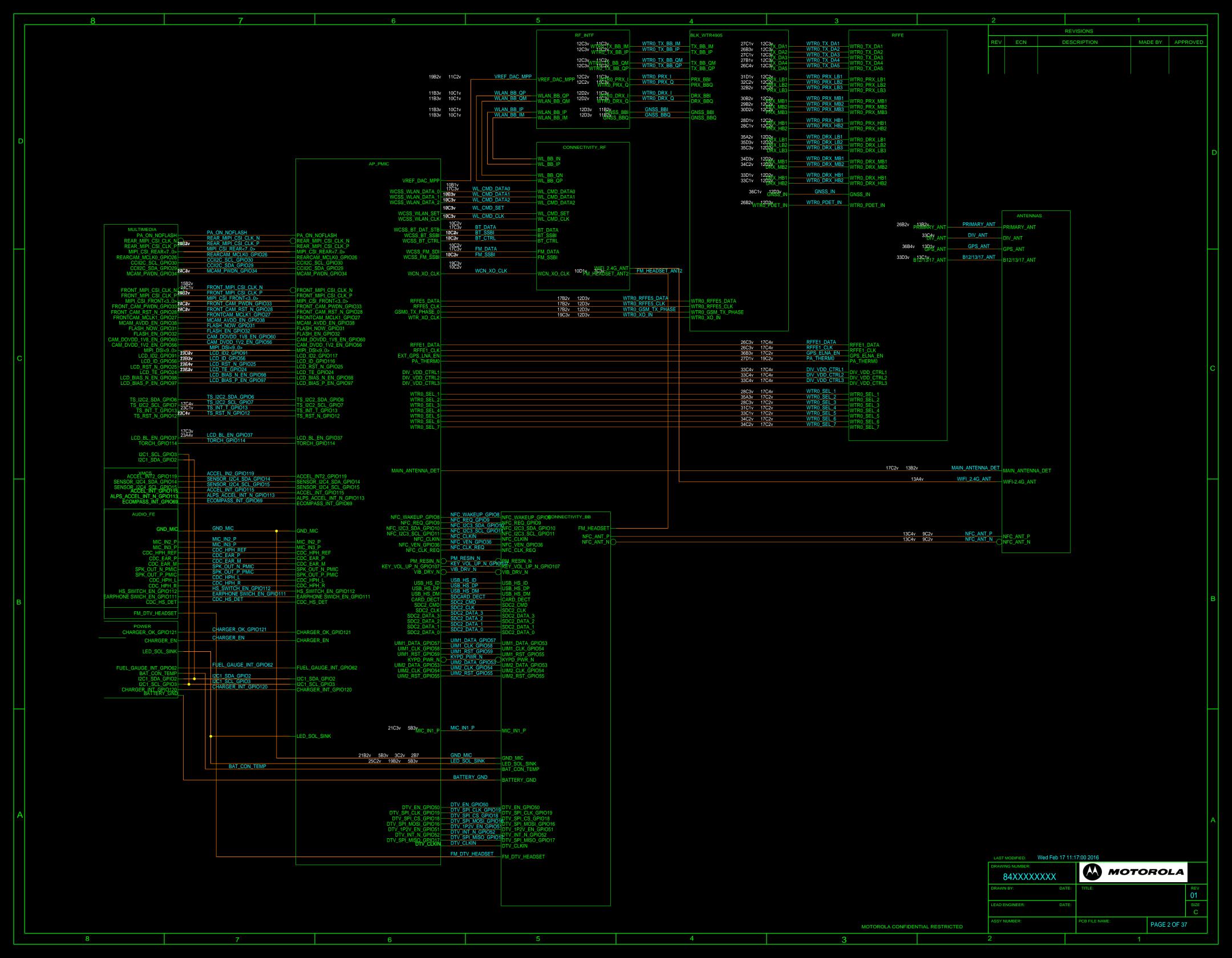
5000-5499:RF INTF

5500-5999:BLK WTR4905

6000-7999:B RFFE

8000-8499:ANTENNAS

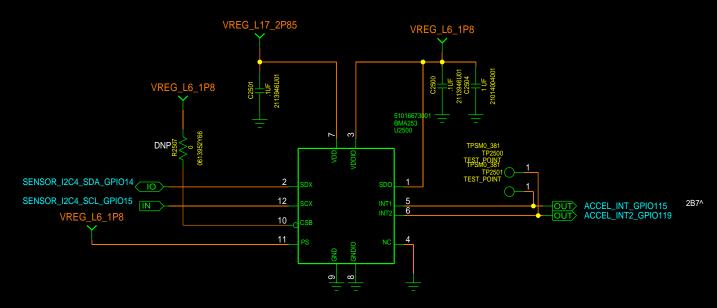
8500-8999:CONNECTIVITY_RF



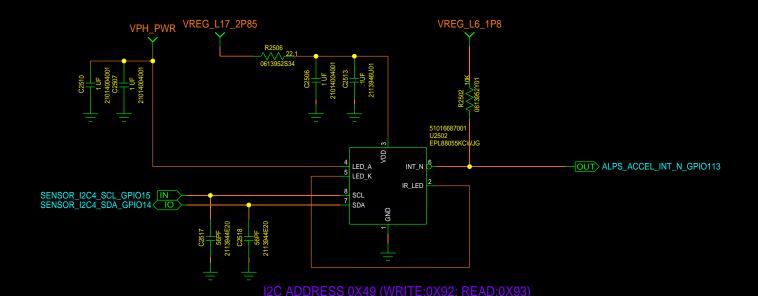
REVISIONS

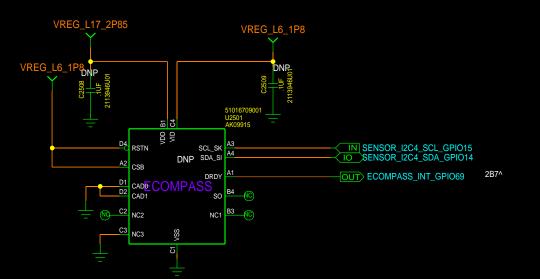
REV ECN DESCRIPTION MADE BY APPROV

REF:2500-2600

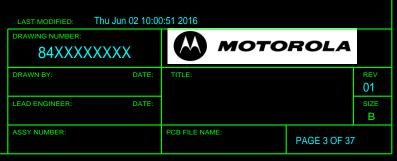


3-AXIS ACCELERATION SENSOR 2C ADDRESS 0X19(WRITE:0X32, READ 0X33)





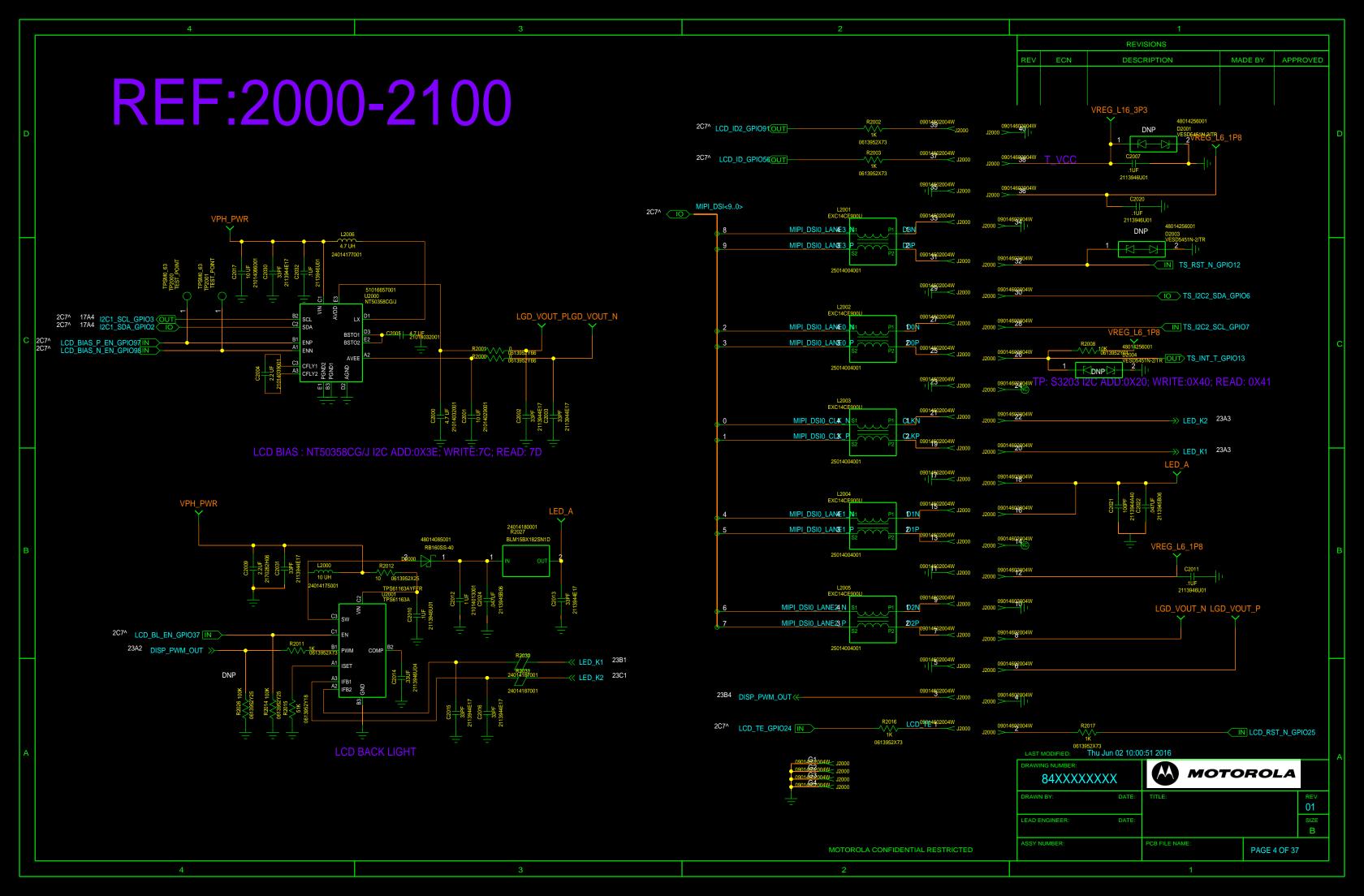
I2C ADDRESS 0X0C(WRITE:0X18, READ 0X19)

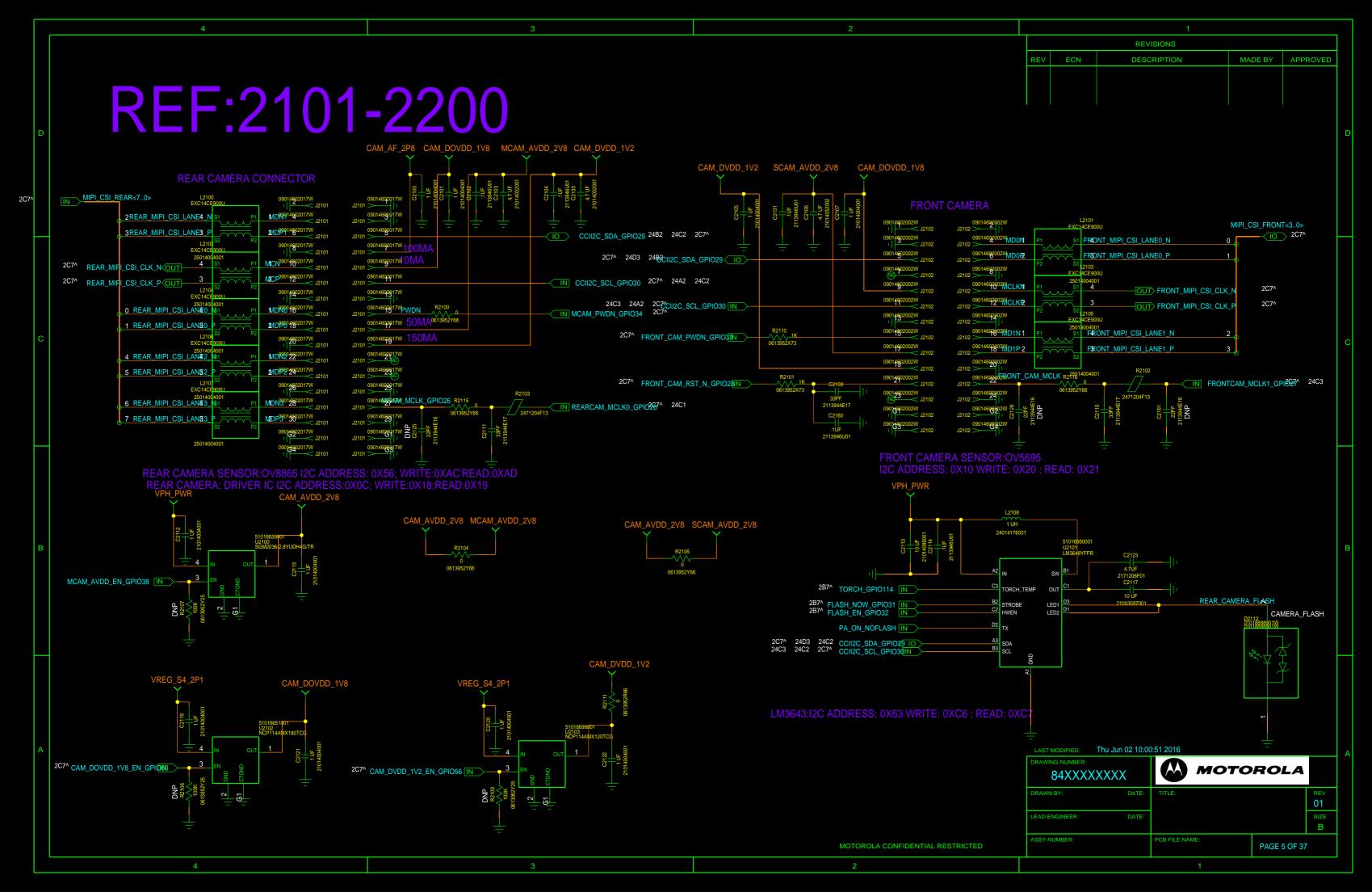


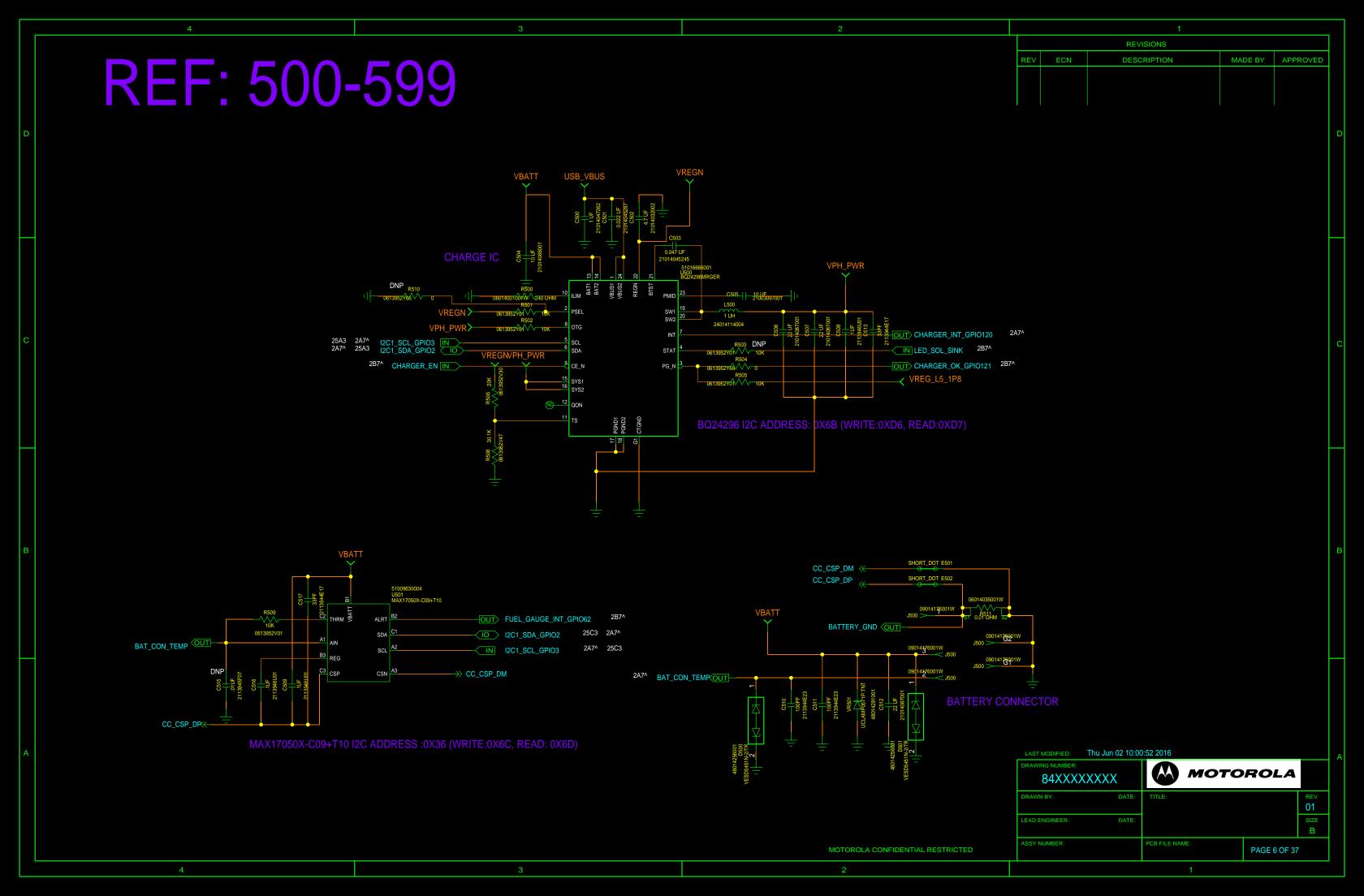
MOTOROLA CONFIDENTIAL RESTRICTED

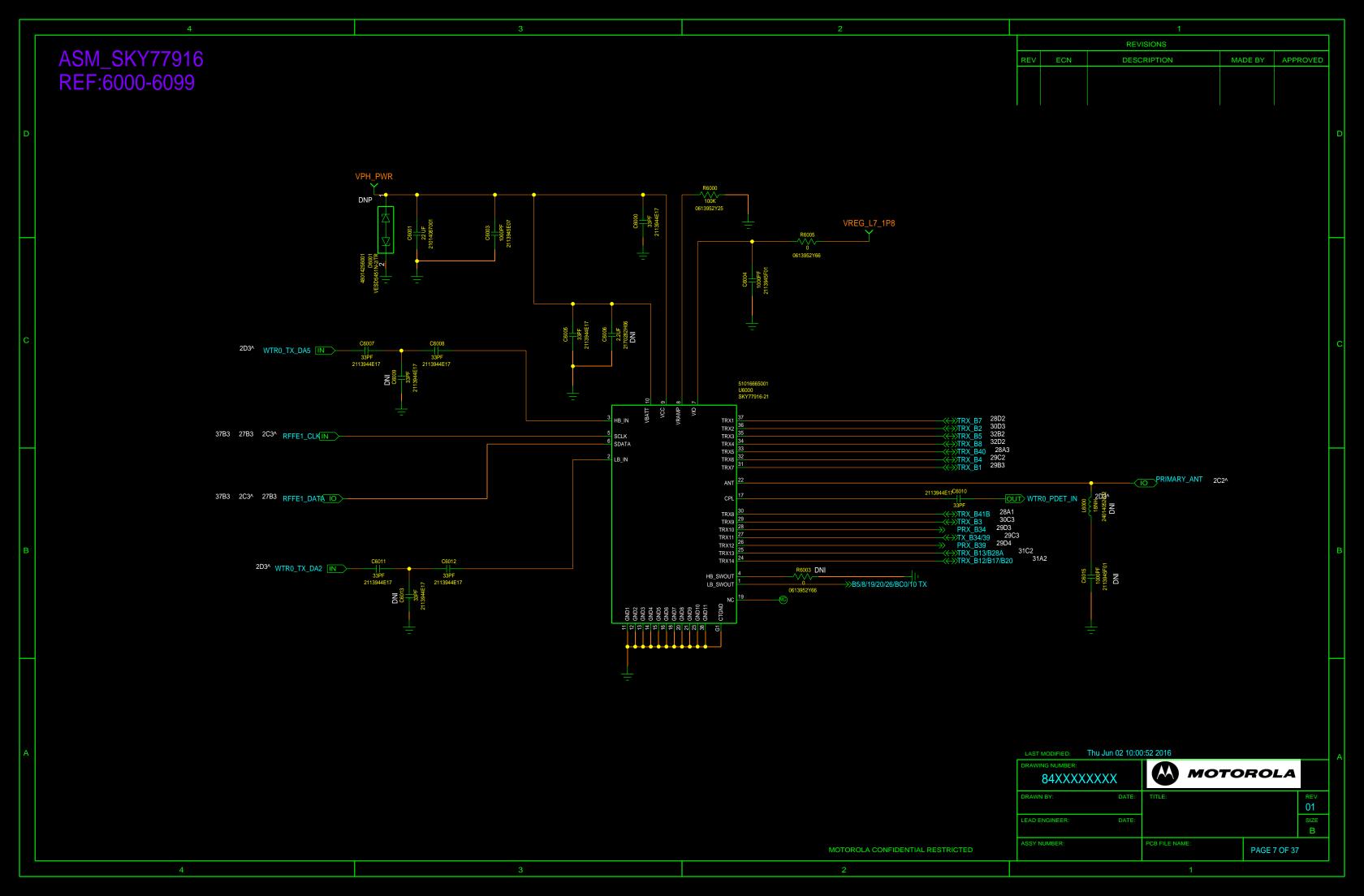
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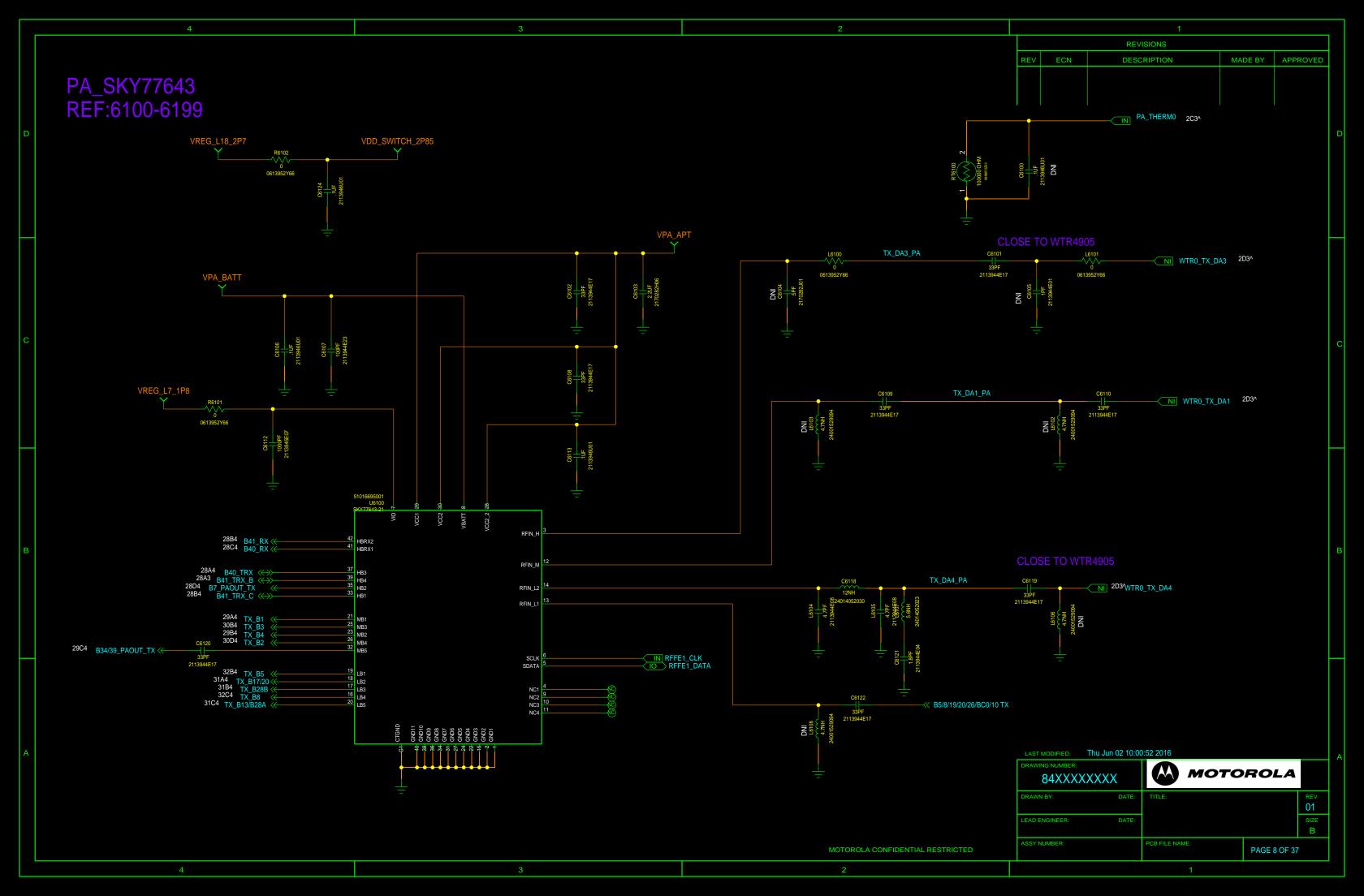
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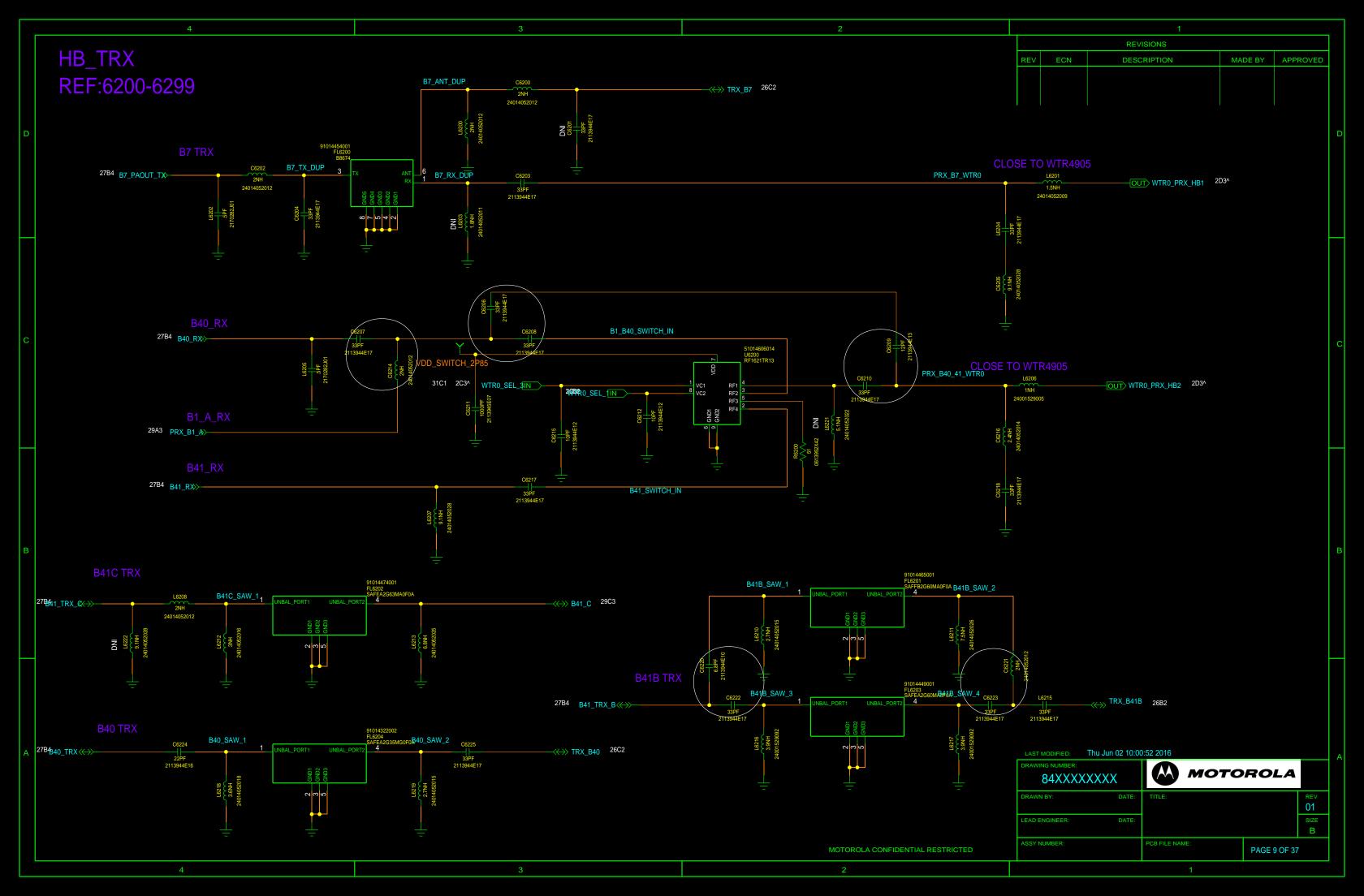


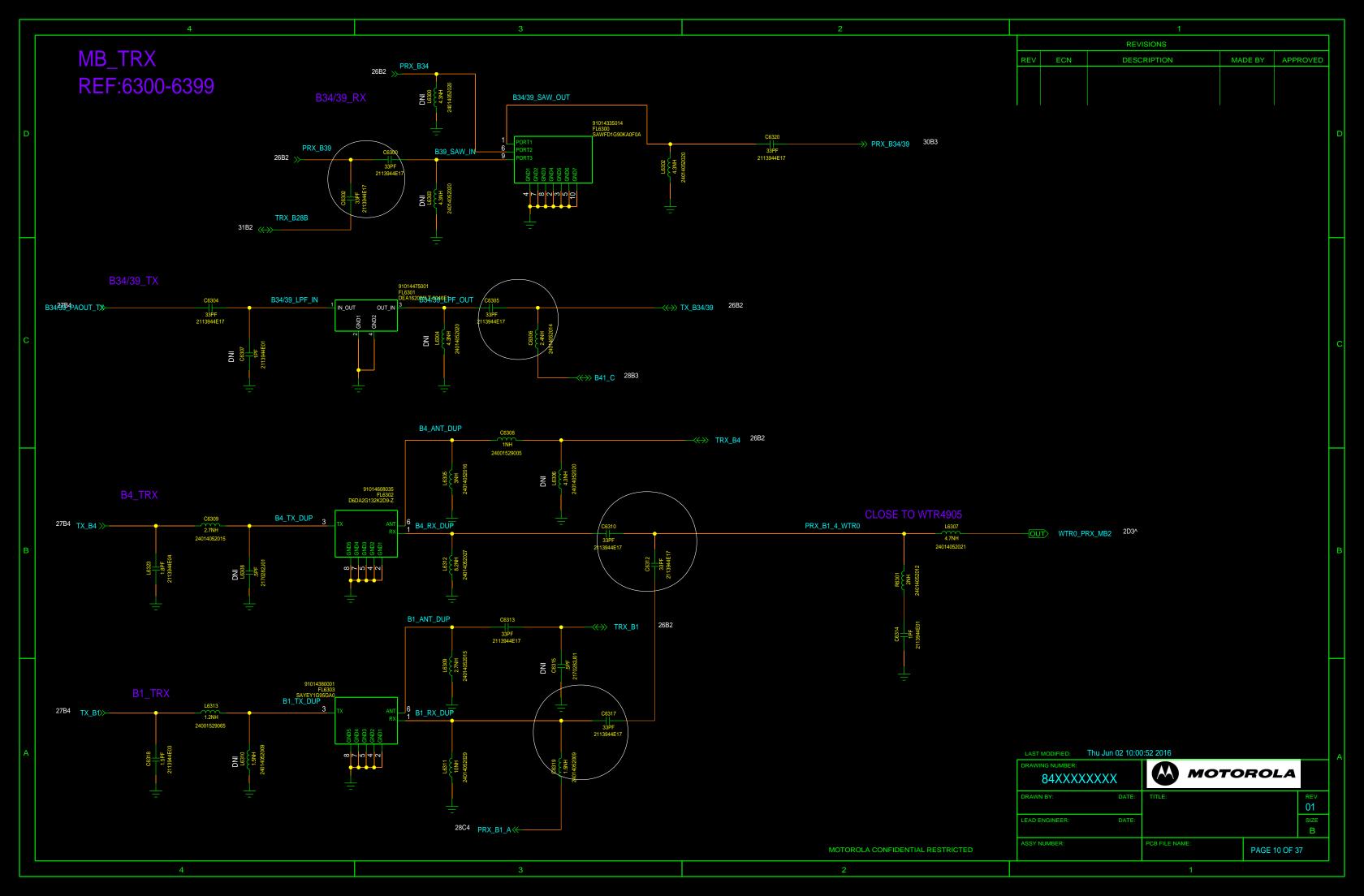


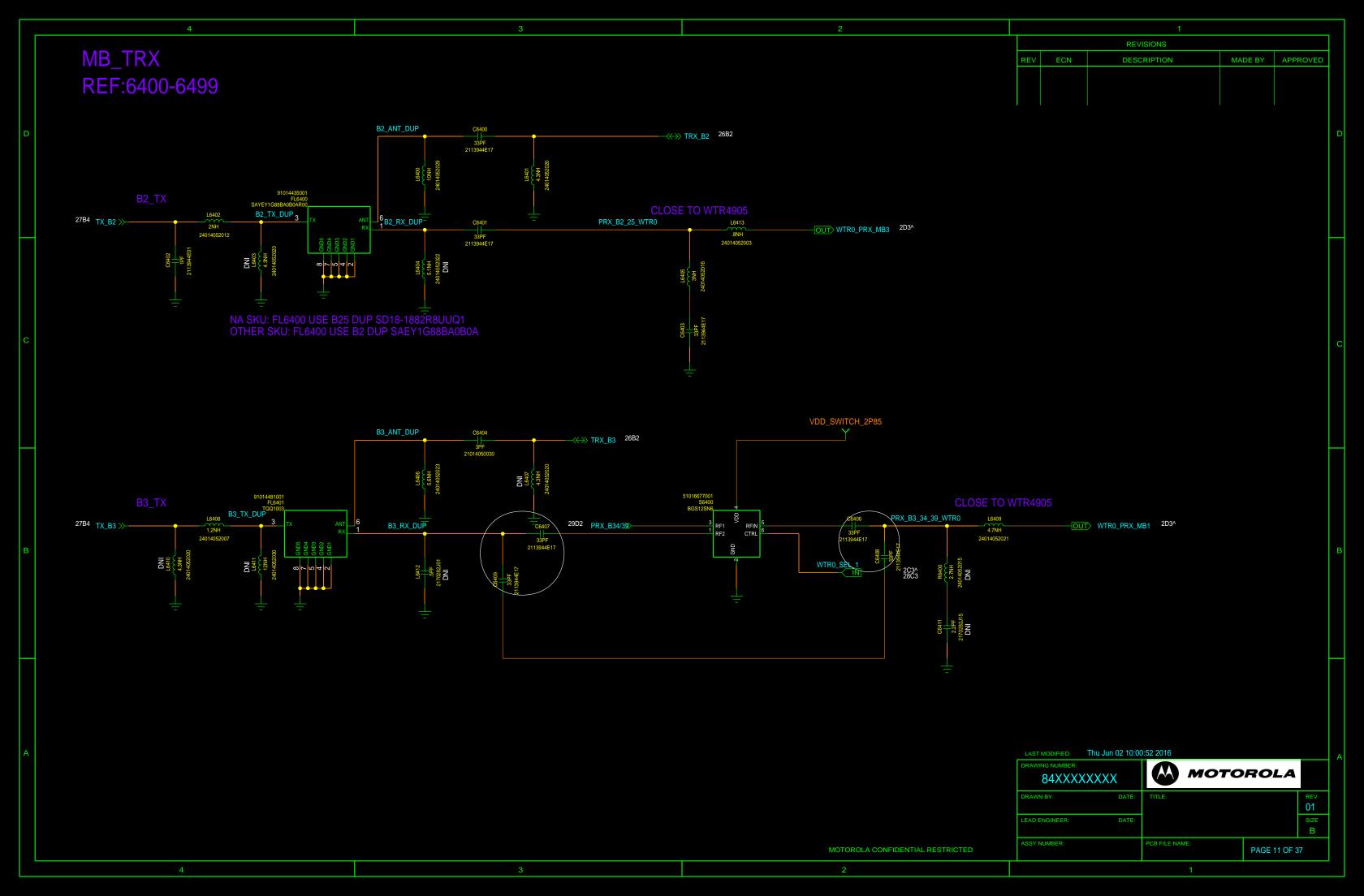


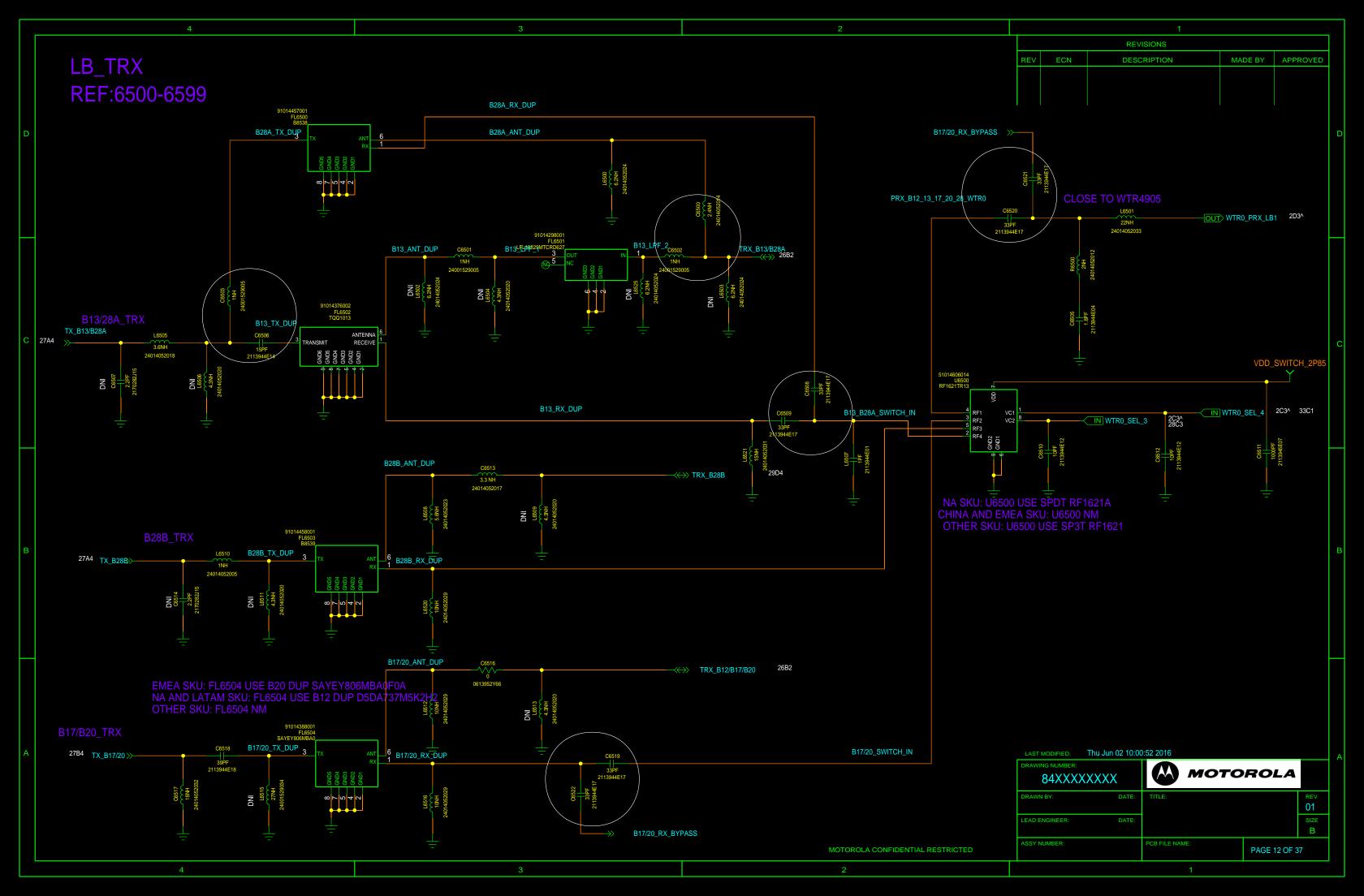


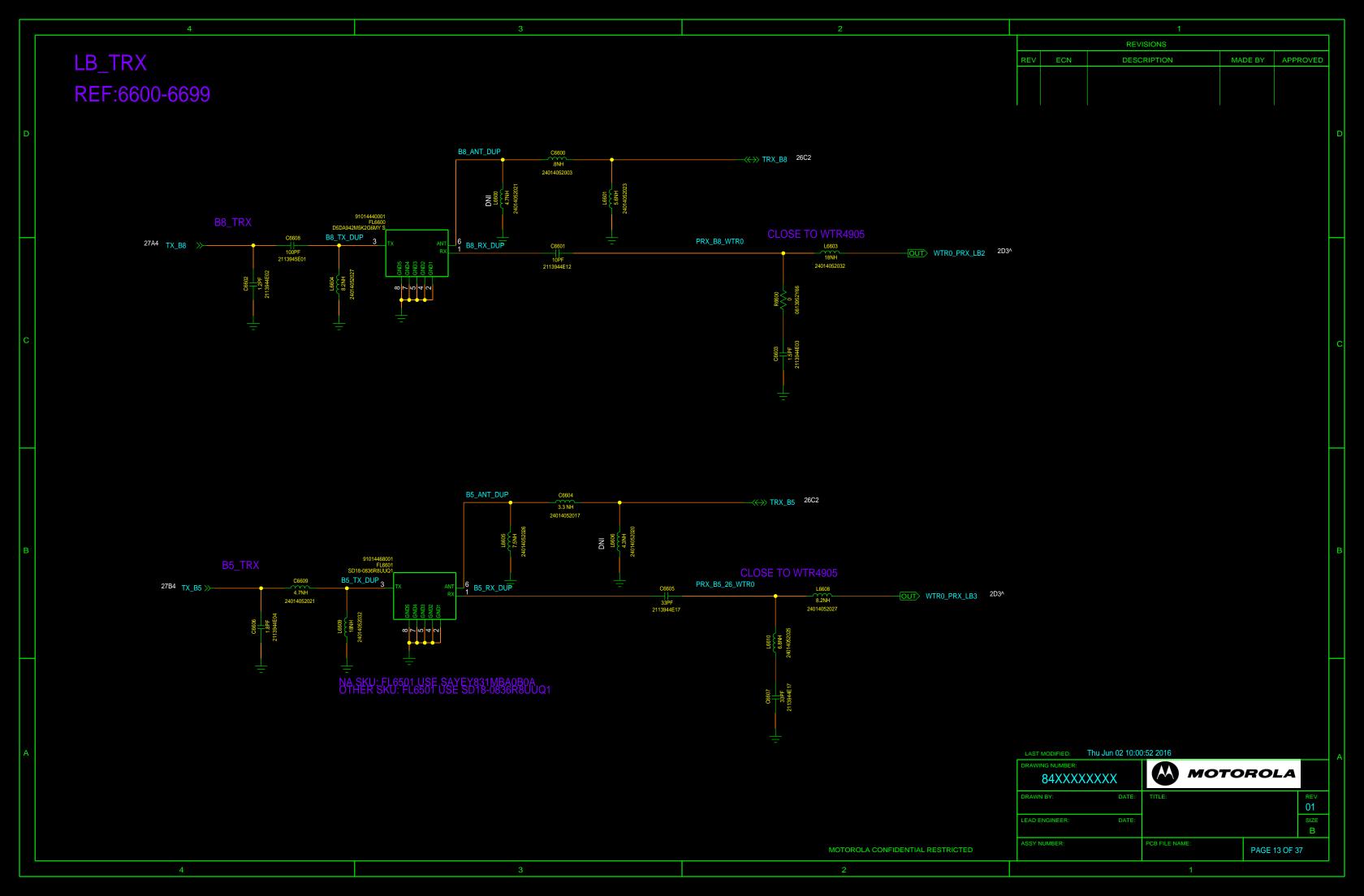


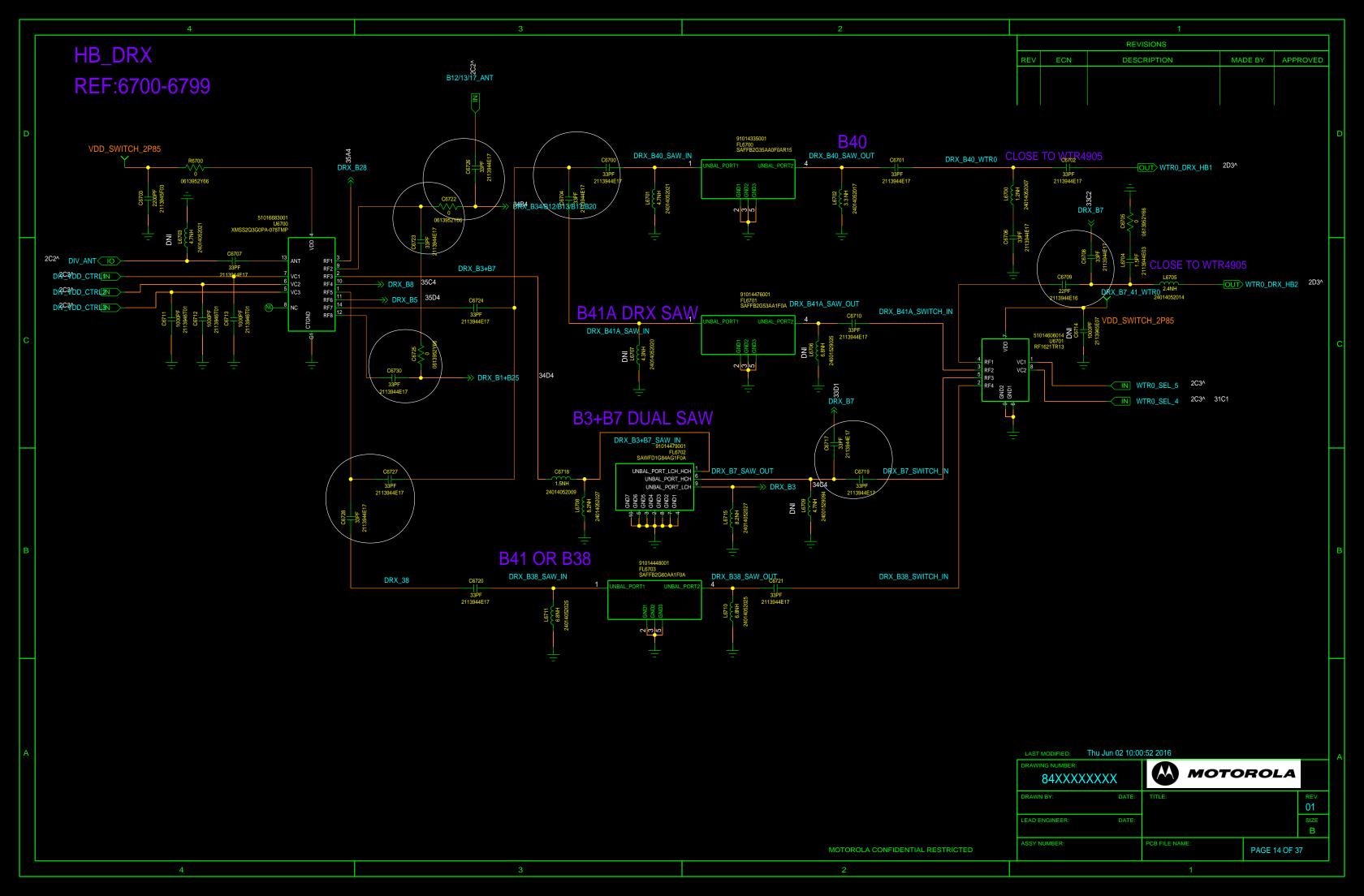


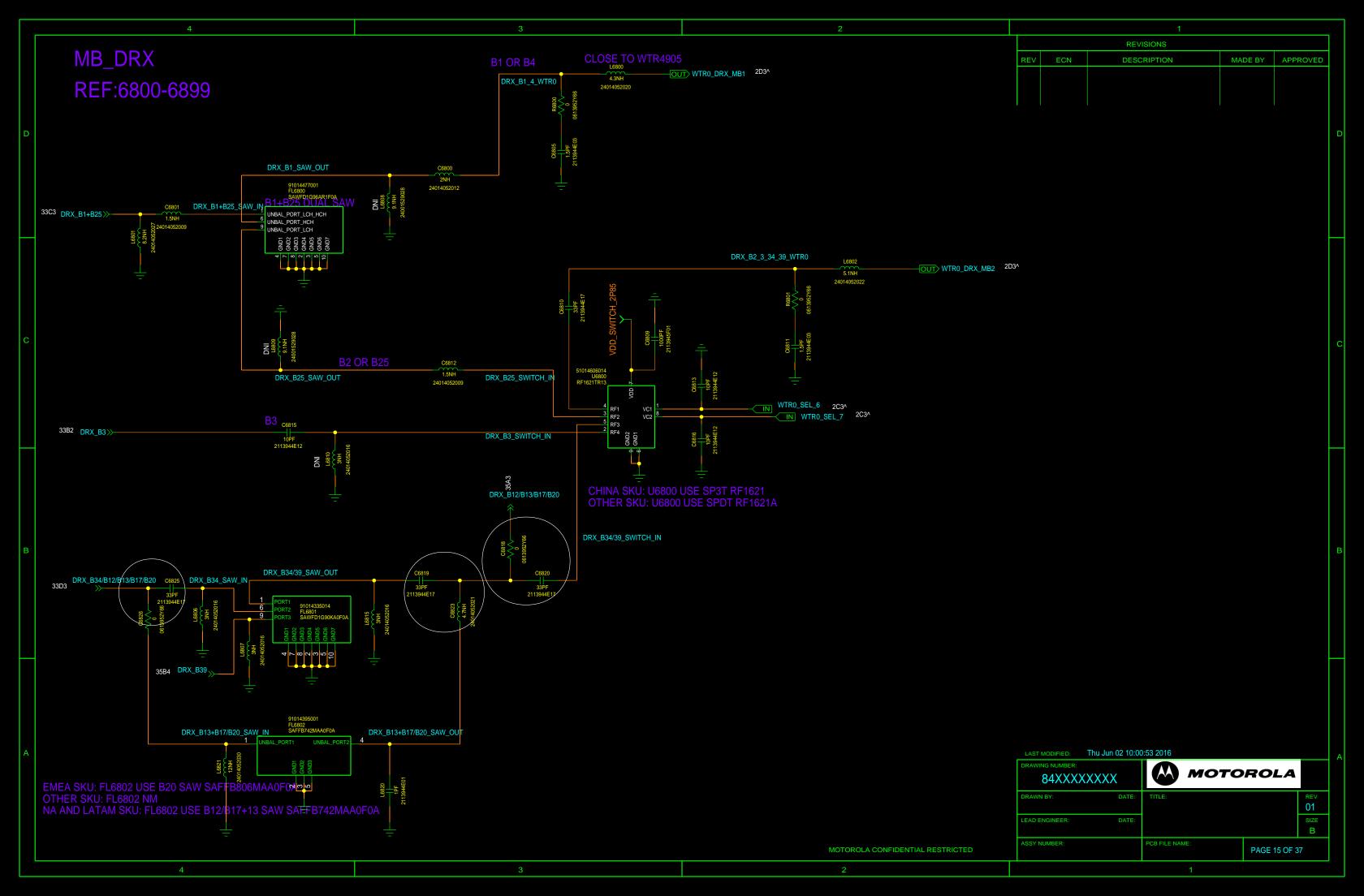


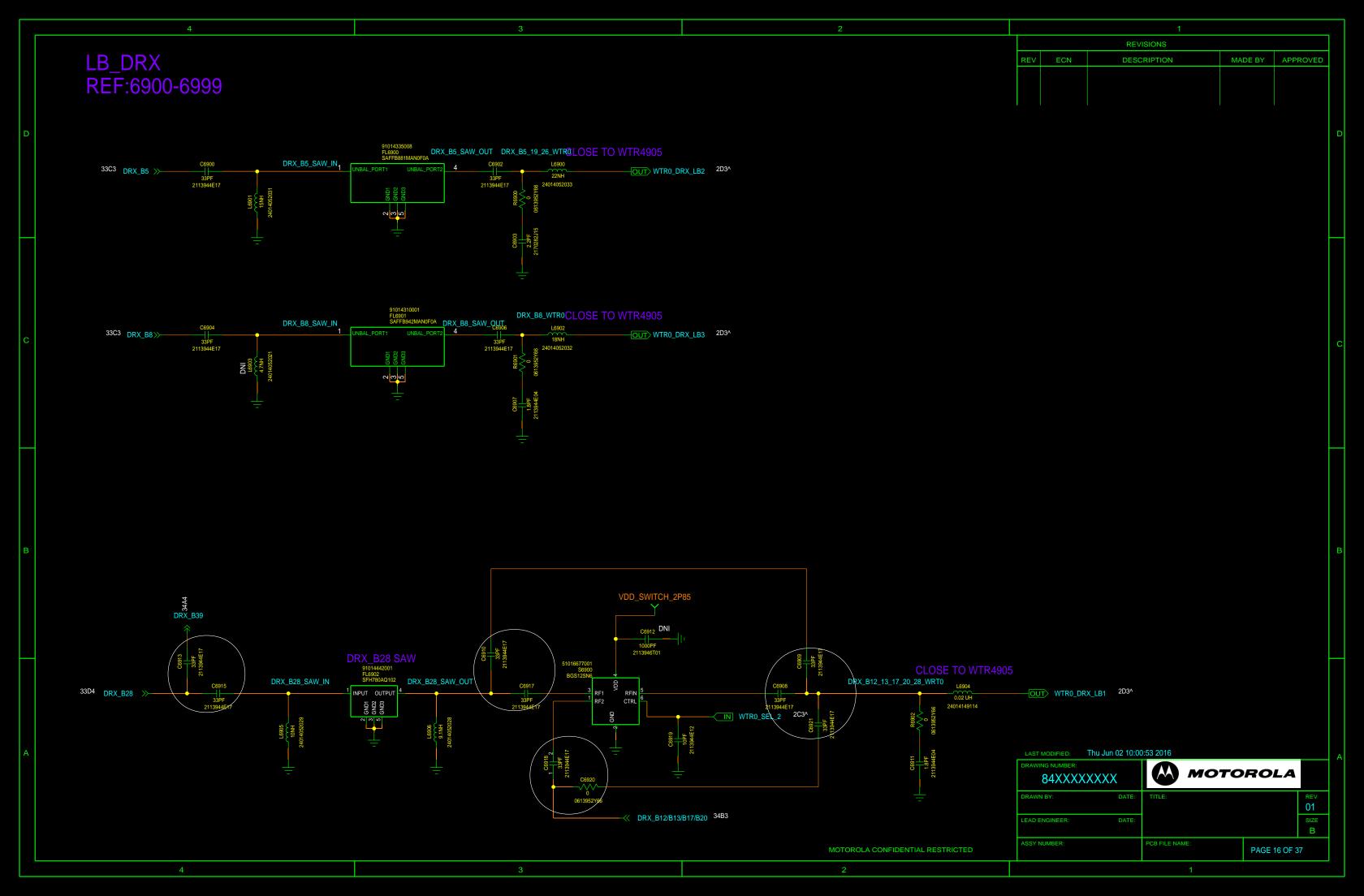


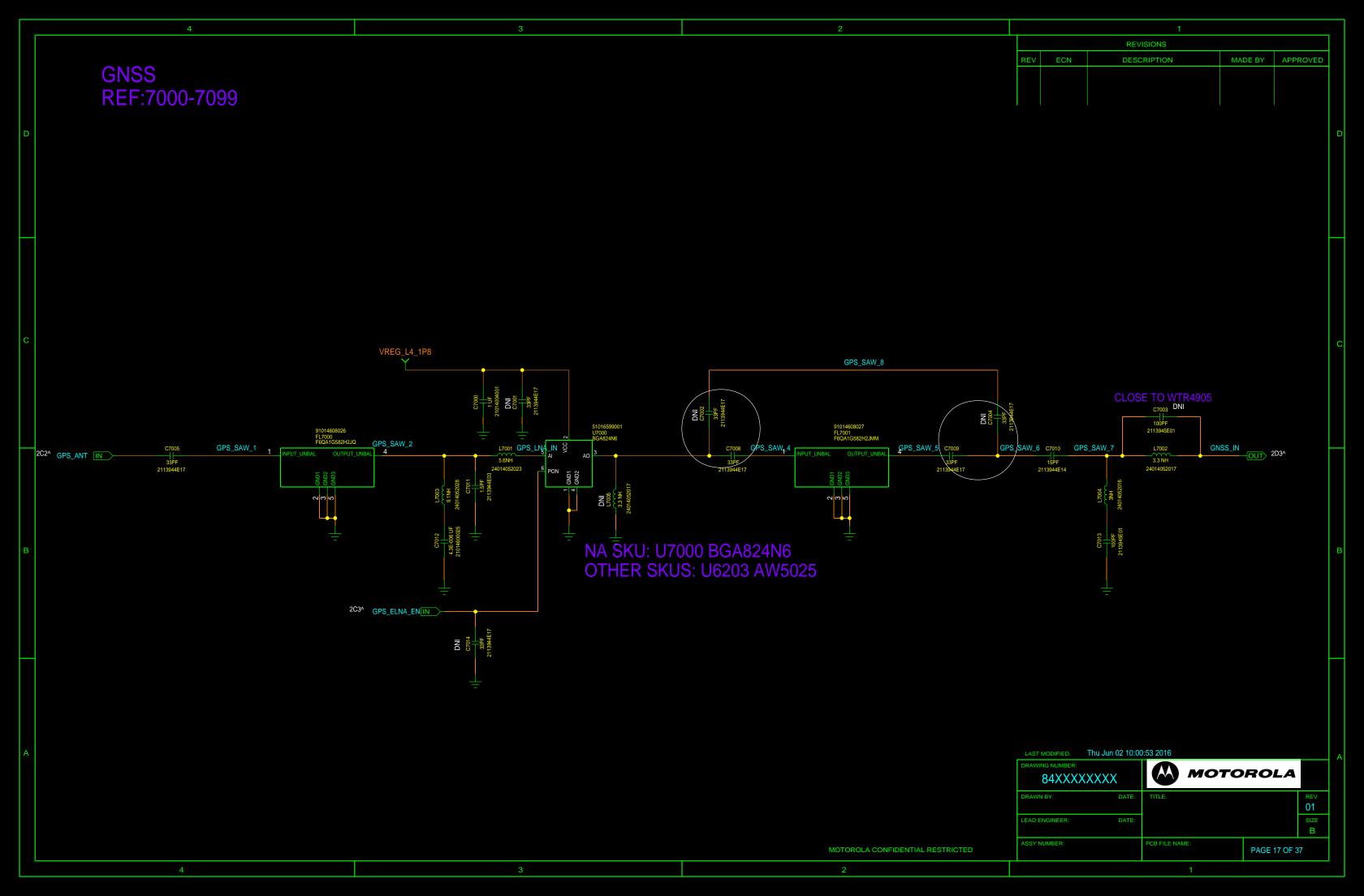


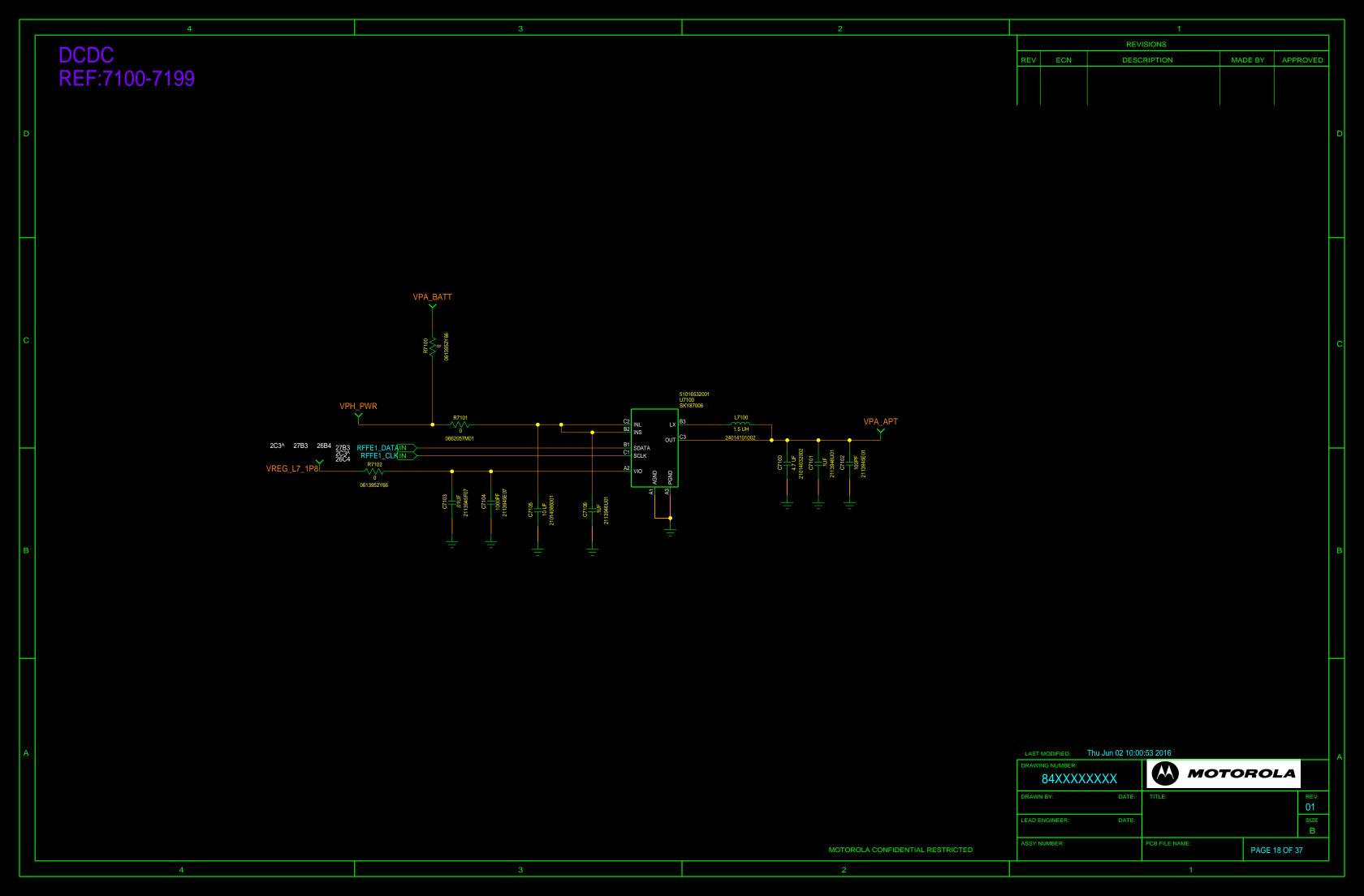


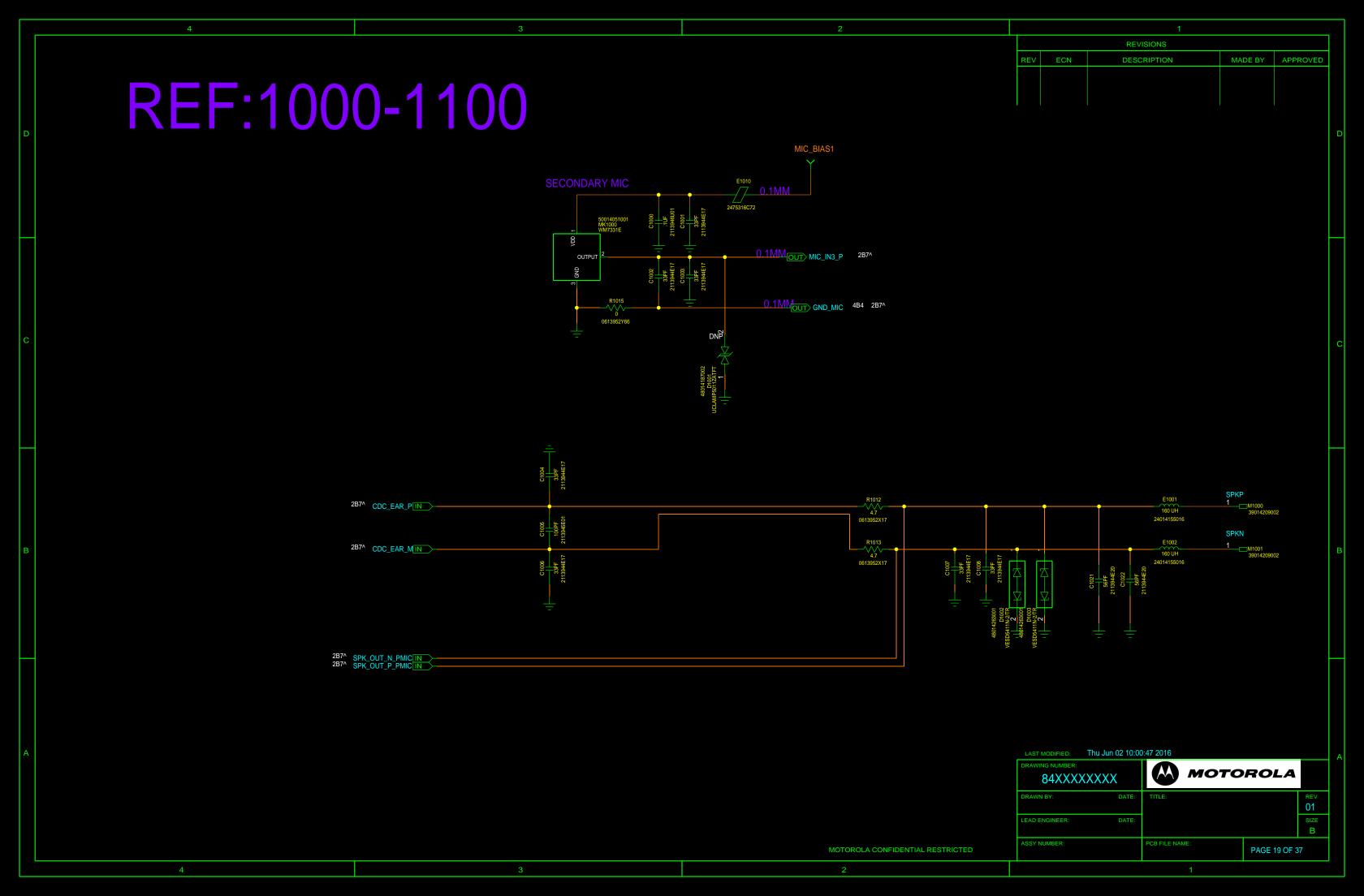


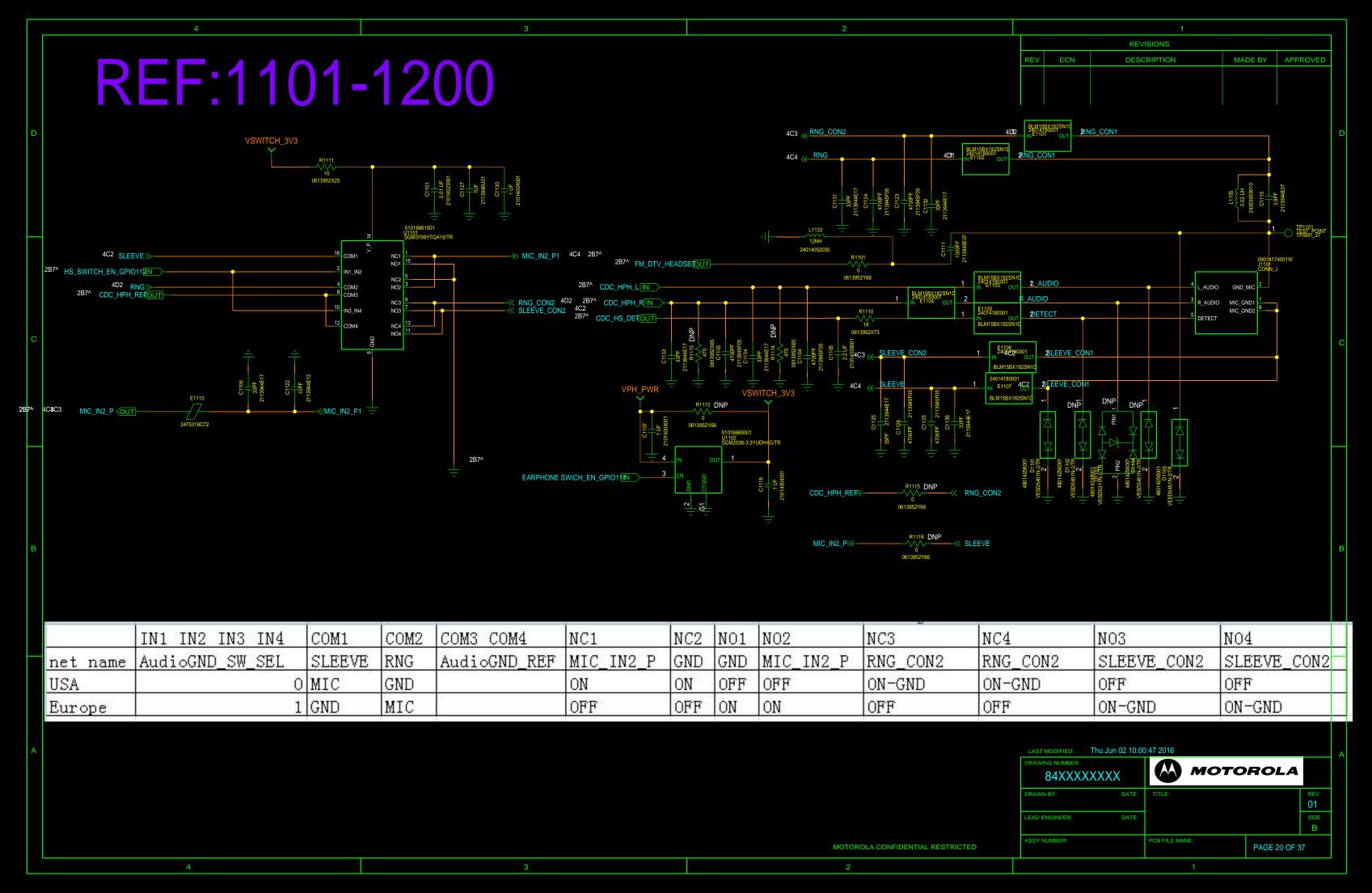




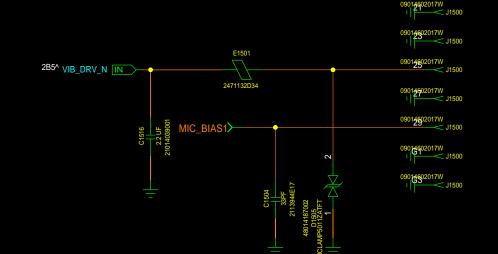


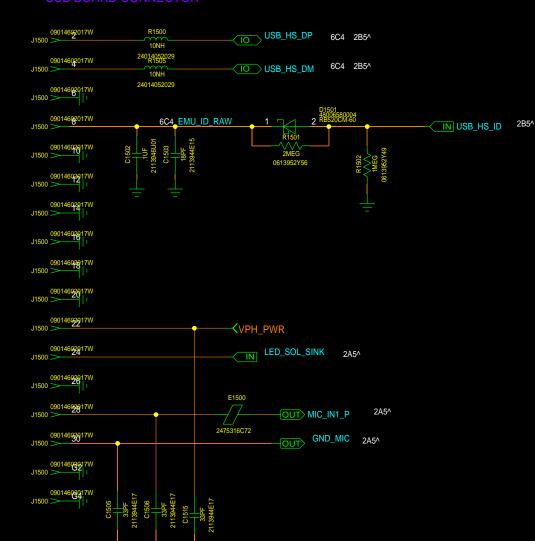






REF:1500-1599 USB_HS_DP 6C4 2B5^ USB_HS_DM 6C4 2B5^ IN USB_HS_ID 2B5^ 09014**6**02017W 09014602017W J1500 09014602017W J1500





LAST MODIFIED: Thu Jun 02 10:00:48 2016 MOTOROLA 84XXXXXXXX ASSY NUMBER: CB FILE NAME: PAGE 21 OF 37

MOTOROLA CONFIDENTIAL RESTRICTED

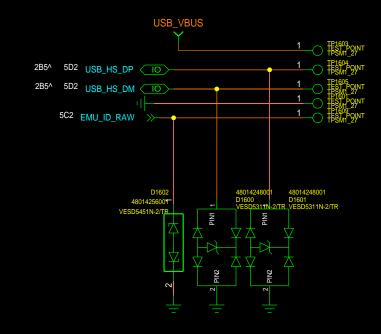
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REV	ECN	DESCRIPTION	MADE BY	APPROVED		

FACTORY TEST POINT FOR BATTER)

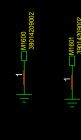


FACTORY TEST POINT FOR USB



FACTORY TEST POINT FOR SIM





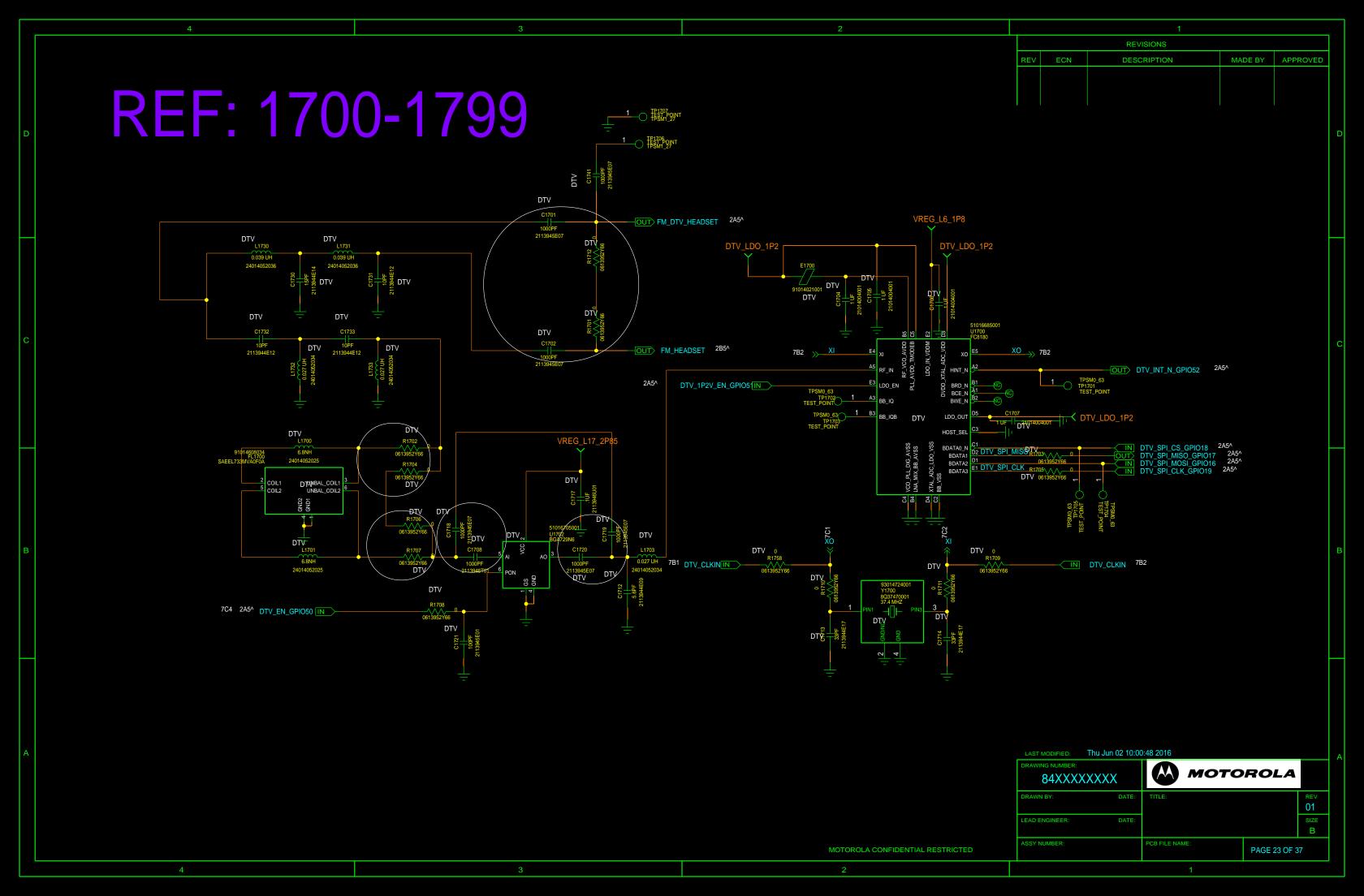


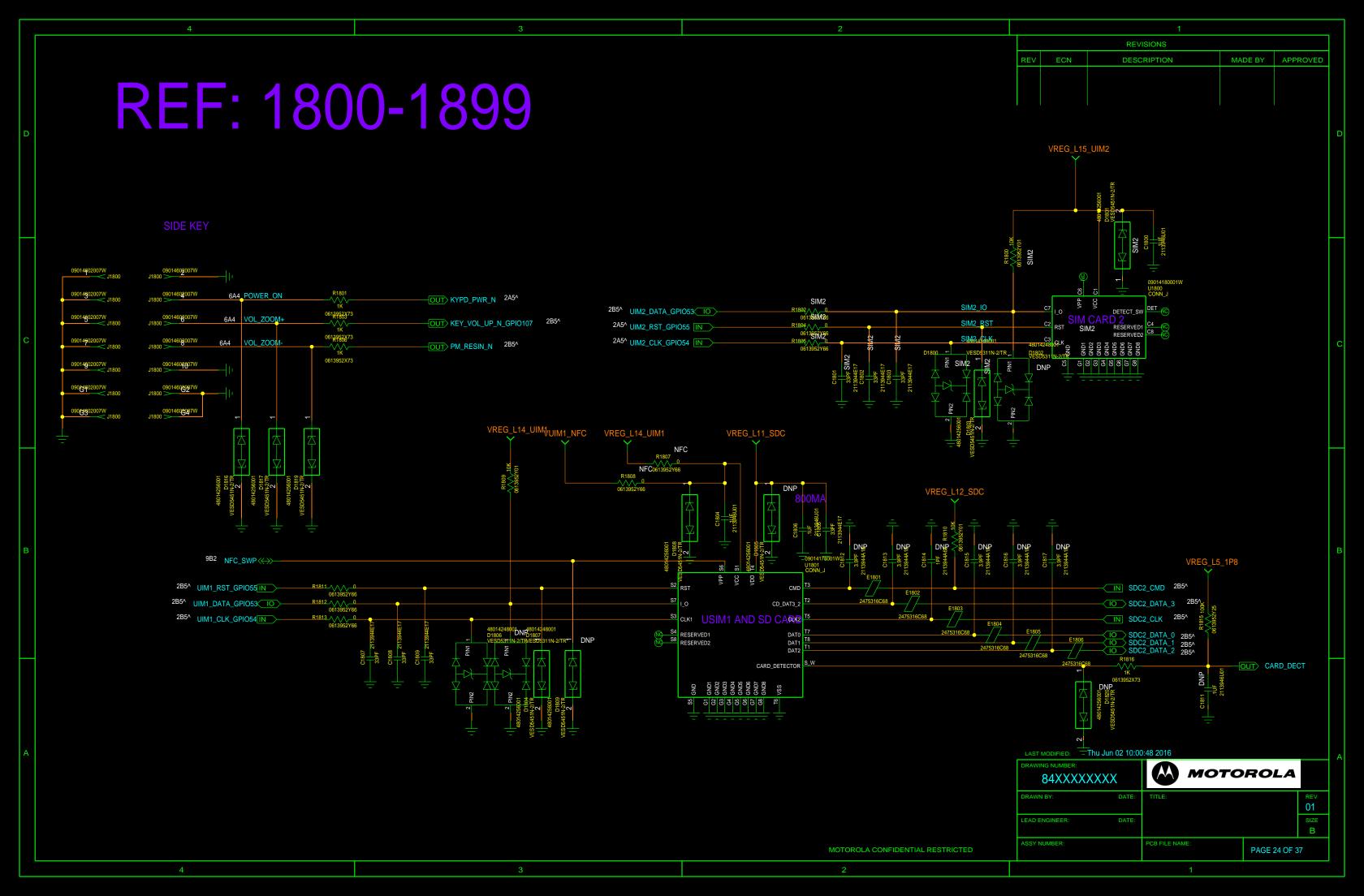
LAST MODIFIED: Thu Jun 02 10:00:48 2016

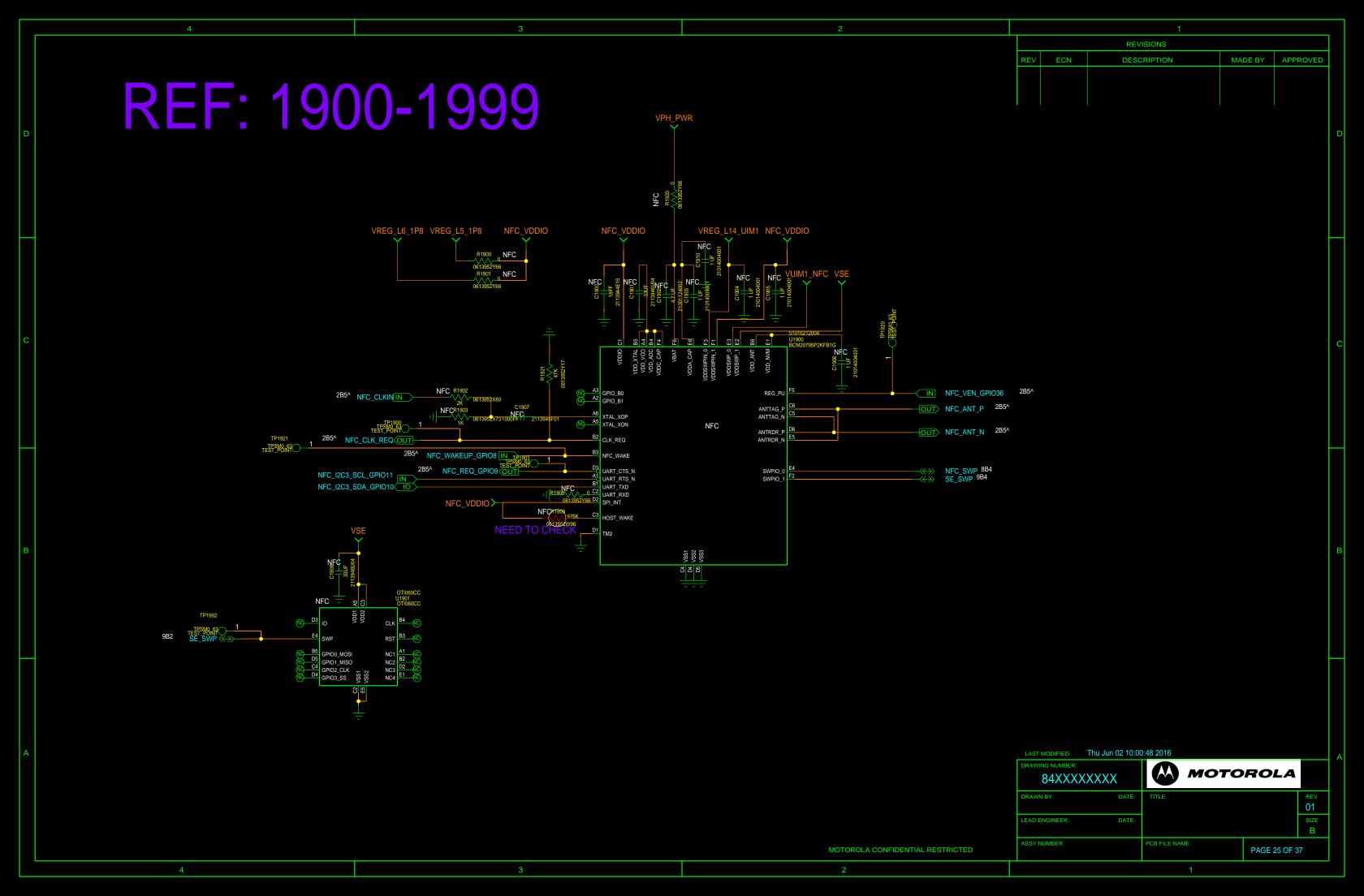
84XXXXX	XXX	W MOTOROLA		
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LEAD ENGINEER:	DATE:		SIZE	
			В	
ASSY NUMBER:		PCB FILE NAME:	PAGE 22 OF 37	

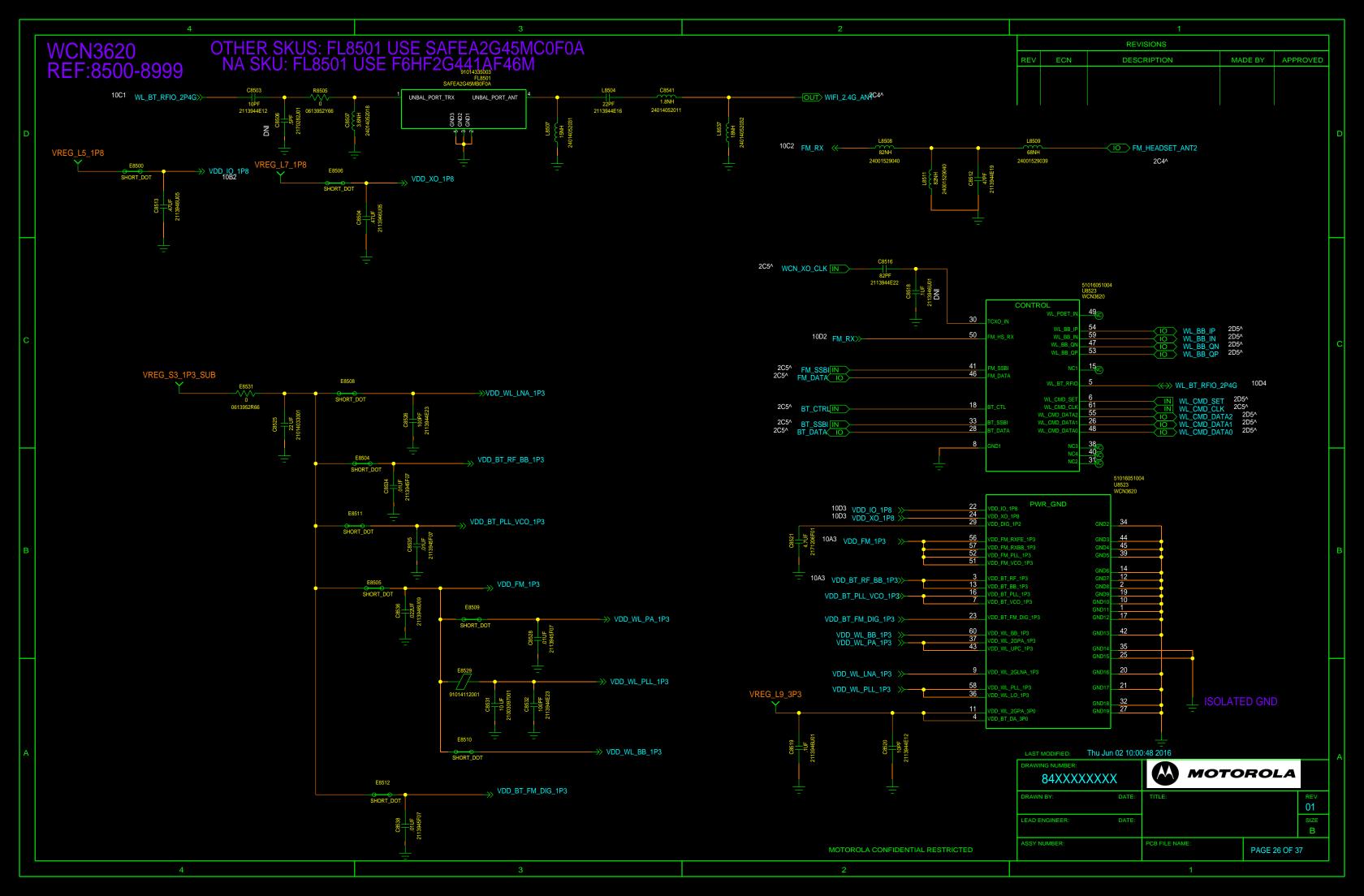
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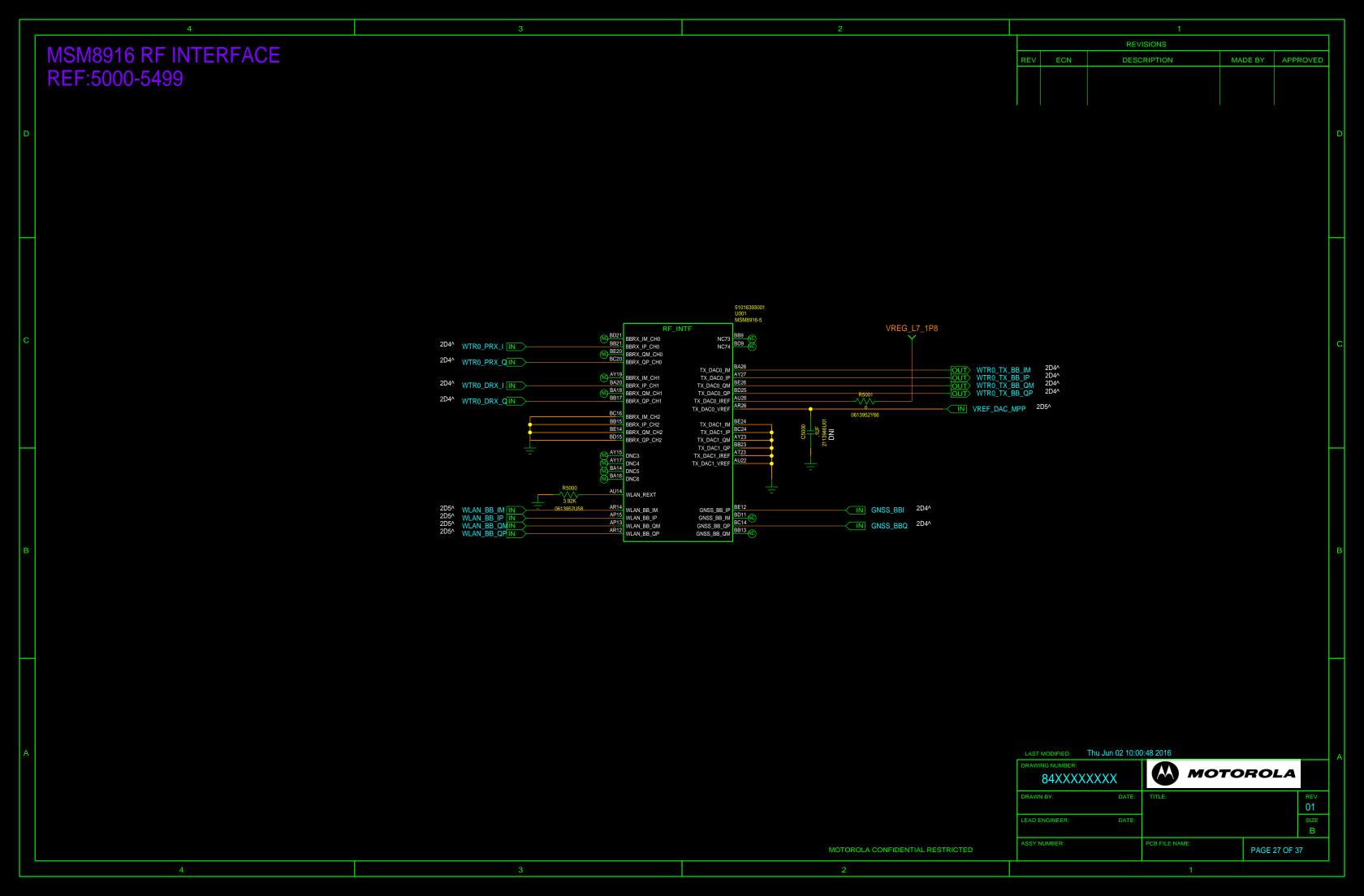
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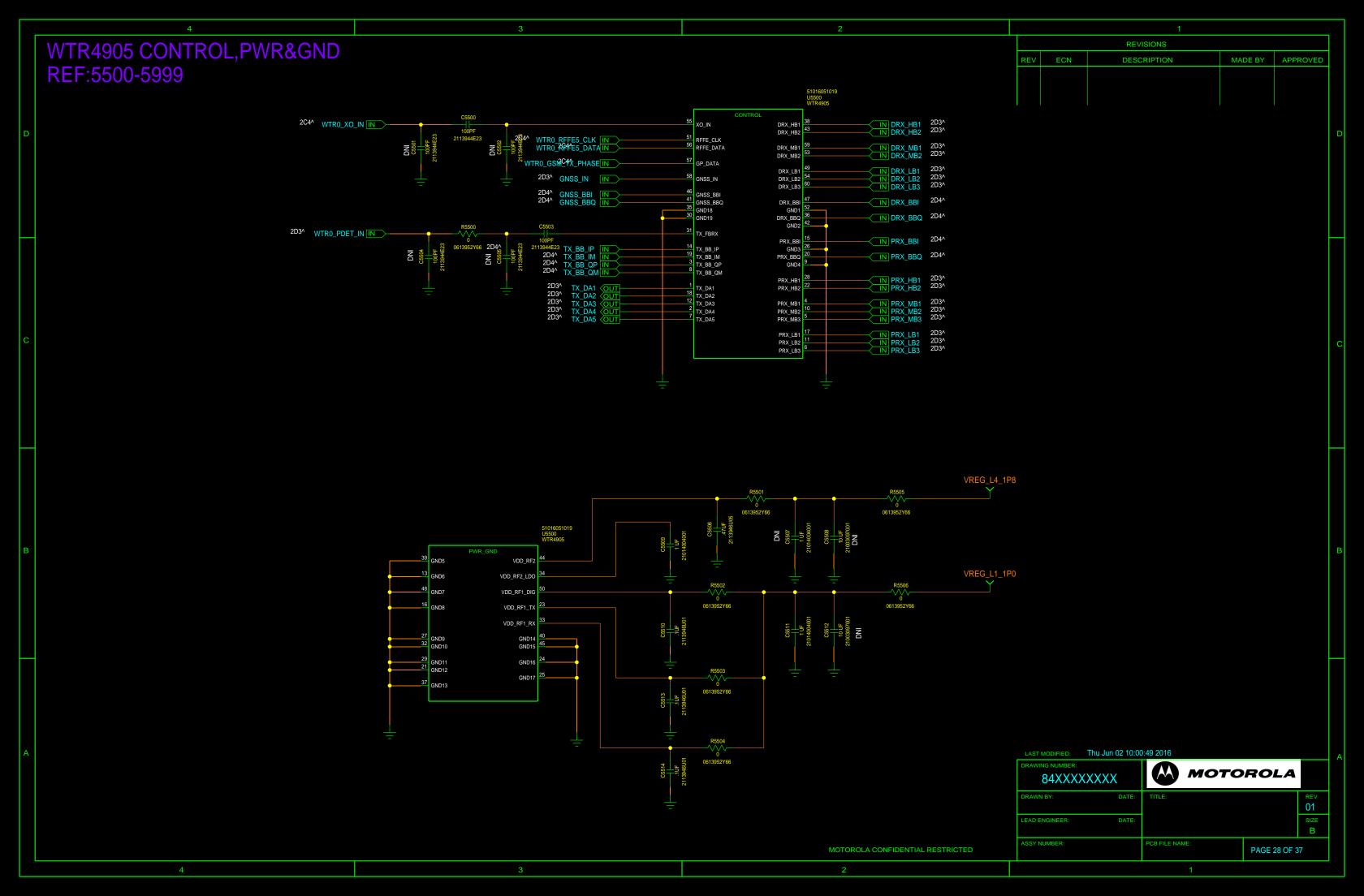


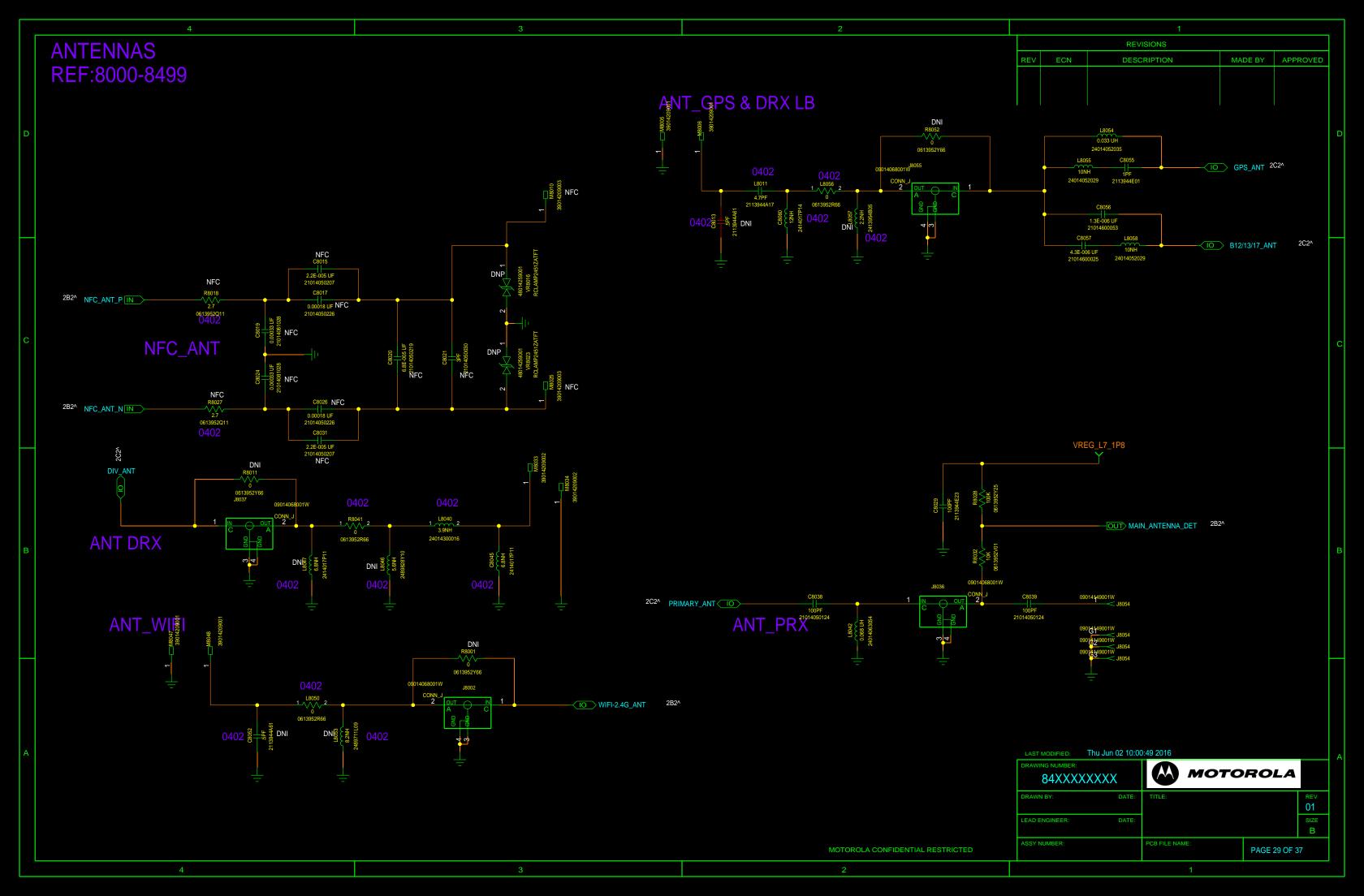


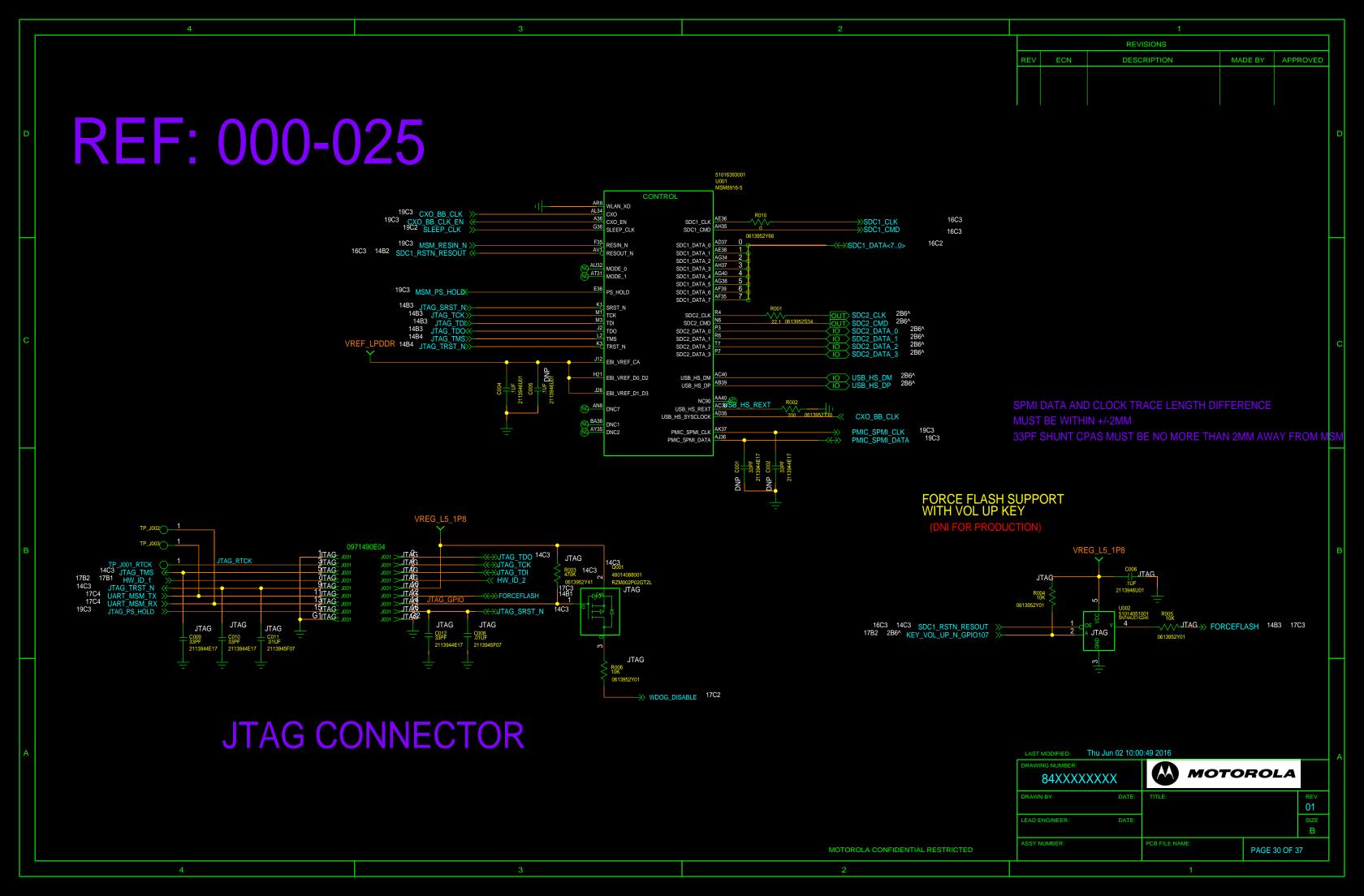












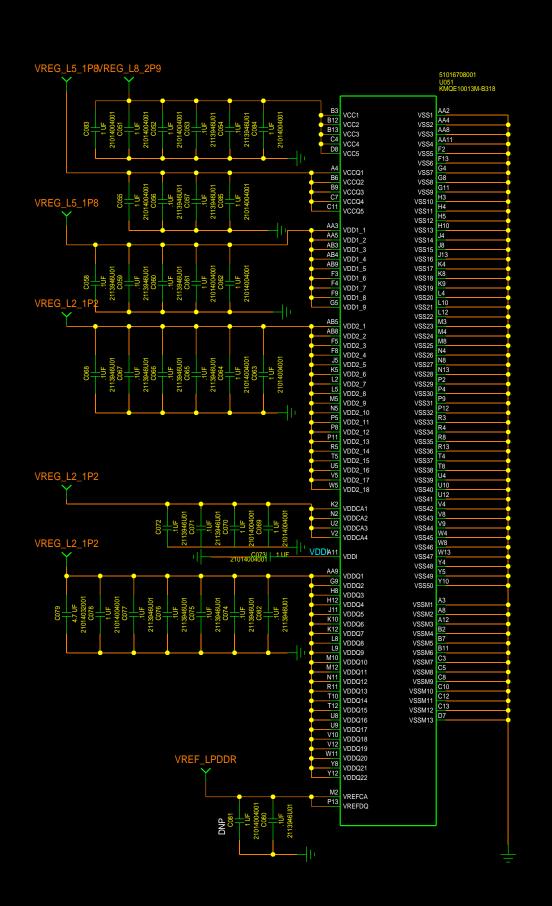
REF:026-050 EBI_CAL_R026 B7 EBI_CS0_N EBI_CS1_N 16C3 EBI_CS0_N</br>
16C3 EBI_CS1_N</br> NC89 H13 240 OHM 06014001001 EBI_CAL_REXT 16C3 EBI_CKB < ——<

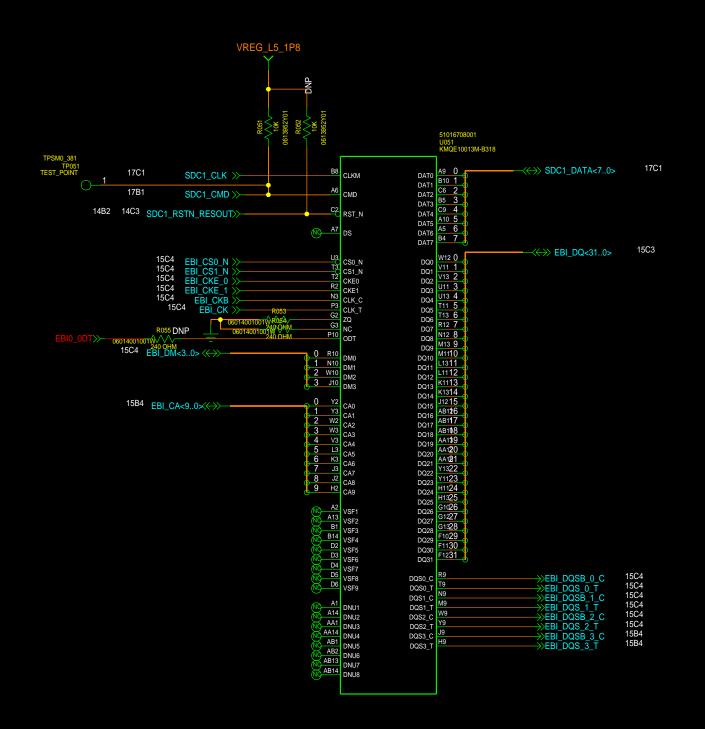
⇒> EBI_DQ<31..0> 16C2 EBI_CK EBI_CKE_0 EBI_CKE_1 16C3 EBI_DM<3..0> <<>> F23 EBI_DM_0 EBI_DM_1 2C7^ MIPI_CSI_REAR<7..0> MIPI DSI0 CLK N 0
MIPI DSI0 CLK P 1
MIPI DSI0 LANE0 N 2
MIPI DSI0 LANE0 P 3
MIPI DSI0 LANE1 N 4
MIPI DSI0 LANE1 P 4
MIPI DSI0 LANE2 N 6
MIPI DSI0 LANE2 P 7
MIPI DSI0 LANE3 N 8
MIPI DSI0 LANE3 P 9 MIPL DSIO_CLK_N AH1
MIPL DSIO_CLK_P AF1
MIPL DSIO_LNO_N AF2
MIPL DSIO_LN1_D AF3
MIPL DSIO_LN1_D AF3
MIPL DSIO_LN1_P AH3
MIPL DSIO_LN2_N AG4
MIPL DSIO_LN2_D AG4
MIPL DSIO_LN3_D AK1
MIPL DSIO_LN3_D AK1 EBI DM 3 MIPI DSI0 CLK N B25 C24 EBI_DQS_0B EBI_DQS_0 W6 MIPI_CSI0_LN0_P
W6 MIPI_CSI0_CLK_N
W1PI_CSI0_CLK_P 16B2 EBI_DQSB_0_C << 16B2 EBI_DQS_0_T << REAR_MIPI_CSI_CLK_NOUT-REAR_MIPI_CSI_CLK_ROUT-2 REAR_MIPI_CSI_LANE1_N U2 3 REAR_MIPI_CSI_LANE1_P T1 4 REAR_MIPI_CSI_LANE2_N W2 5 REAR_MIPI_CSI_LANE2_N V1 6 REAR_MIPI_CSI_LANE3_N AA6 7 REAR_MIPI_CSI_LANE3_P Y5 MIPI_CSIO_LN3_N MIPI_ D25 EBI_DQS_1B EBI_DQS_1 16B2 EBI_DQSB_1_C << 16B2 EBI_DQS_1_T << D19 E18 EBI_DQS_2B EBI_DQS_2 16B2 EBI_DQSB_2_C << 16B2 EBI_DQS_2_T << 2C7^ MIPI_CSI_FRONT<3..0> NC79 AN2 NC80 AK3 NC81 AL4 NC83 AK5 NC84 AN4 NC85 AN5 NC86 AM5 NC86 AM5 NC86 AM5 NC88 AM6 NC87 NC88 D31 EBI_DQS_3B 16B2 EBI_DQSB_3_C \(\leftarrow \) 16B2 EBI_DQS_3_T \(\leftarrow \) FRONT_MIPI_CSI_CLK_NOUT 16C3 EBI_CA<9..0> <<>> MIPI_DSI<9..0> 10 2C7^ FRONT_MIPI_CSI_CLK_POUT EBI_CA_1 EBI_CA_2 4 E6 5 B13 6 C12 7 D13 8 B1 CA_6 EBI_CA_6 EBI_CA_7 LAST MODIFIED: Thu Jun 02 10:00:50 2016 **MOTOROLA** 84XXXXXXXX SSY NUMBER: PAGE 31 OF 37 MOTOROLA CONFIDENTIAL RESTRICTED

REVISIONS

EV ECN DESCRIPTION MADE BY APPROVED

REF:051-100





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Density	1st Source	2nd Source	Comment
16GB EMMC+2GB LPDDR3	KMQE10013M-B318(Samsung)	H9TQ17ABJTMCUR-KUM(Hynix)	QC have not verified KMQE10013M-B318 yet, need to submit a simulation case to QC
16GB EMMC+1GB LPDDR3	KMFE10012M-B214(Samsung)	H9TQ17A8GTMCUR-KUM(Hynix)	QC have not verified KMFE10012M-B214 yet, need to submit a simulation case to QC
8GB EMMC+1GB LPDDR3	KMFNX0012M-B214(Samsung)	H9TQ64A8GTMCUR-KUM(Hynix)	QC have not verified KMFNX0012M-B214 yet, need to submit a simulation case to QC

LAST MODIFIED: Thu Jun 02 10:00:50 2016

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LEAD ENGINEER: DATE:

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ASSY NUMBER:

PCB FILE NAME:

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REF:101-125 **FORCEFLASH** SHORT_DOT 14A3 WDOG_DISABLE(UART_MSM_TX LCD_BL_EN_GPIO37 GPIO_37 B37 GPIO_38 BC2 GPIO_49 BB5 GPIO_41 BB1 GPIO_41 BB3 GPIO_43 BB3 GPIO_44 AY5 GPIO_46 AY9 GPIO_46 AY9 GPIO_47 AW8 GPIO_49 Y37 GPIO_50 A344 GPIO_50 A344 GPIO_50 Y35 GPIO_110 GPIO_1112 GPIO_112 GPIO_113 GPIO_114 GPIO_115 GPIO_116 GPIO_117 GPIO_117 GPIO_119 GPIO_119 GPIO_120 GP MCAM_AVDD_EN_GPIO38 WCSS_BT_SSBI UART_MSM_RX I2C1_SDA_GPIO2< EARPHONE SWICH_EN_GPIO111
HS_SWITCH_EN_GPIO112
ALPS_ACCEL_INT_N_GPIO113
TORCH_GPIO114 2C6^A WTR0_SEL_2 2C6^A WTR0_SEL_3 2C6^A WTR0_SEL_4 2C6^A WTR0_SEL_5 WCSS_WLAN_DATA_2 WCSS_WLAN_DATA_1 WCSS_WLAN_DATA_0 TS_I2C2_SDA_GPI06 TS_I2C2_SCL_GPI07 NFC_WAKEUP_GPI08 < WCSS_WLAN_SET WCSS_WLAN_CLK 2C6^ ACCEL_INT_GPIO115 LCD_ID_GPIO116 LCD_ID2_GPIO117 NFC_REQ_GPI09 NFC_I2C3_SDA_GPI010 NFC_I2C3_SCL_GPI011 TS_RST_N_GPI012 TS_INT_T_GPI013_C WCSS_FM_SDI WCSS_BT_CTRL HW ID 3 CARD_DECT > ACCEL_INT2_GPIO119 > CHARGER_INT_GPIO120 WCSS_BT_DAT_STB DIV VDD CTRL1 DIV_VDD_CTRL2
PA ON NOFLASH CHARGER_OK_GPIO121 DTV_EN_GPIO50 IS_INI_T_GPIO13.C SENSOR_I2C4_SDA_GPIO14 SENSOR_I2C4_SCL_GPIO15.C DTV_SPI_MOSI_GPIO16.C DTV_SPI_MISO_GPIO17.I GPIO_50 | V35 | GPIO_51 | V35 | GPIO_52 | A338 | GPIO_53 | K39 | H39 | GPIO_55 | A436 | GPIO_55 | A436 | GPIO_56 | GPIO_58 | L38 | GPIO_60 | A138 | GPIO_61 | GPIO_58 | A439 | GPIO_66 | A438 | GPIO_67 | A140 | GPIO_71 | A139 | GPIO_71 | A739 | GPIO_73 | A139 | G DTV_LINT_N_GPIO51 DTV_INT_N_GPIO52 UIM2_DATA_GPIO53
UIM2_CLK_GPIO54 USB_HS_ID IN CAM_DVDD_1V2_EN_GPIO56
UIM1_DATA_GPIO57 DTV_SPI_CLK_GPIO19 > UIM1_CLK_GPIO58 > UIM1_RST_GPIO59 > CAM_DOVDD_1V8_EN_GPIO60 DIV_VDD_CTRL3 © 2C6^ EXT_GPS_LNA_EN © LCD_BIAS_P_EN_GPI097 © UIM_BATT_ALM_GPIO61

FUEL_GAUGE_INT_GPIO62

CDC_PDM_CLK_GPIO63

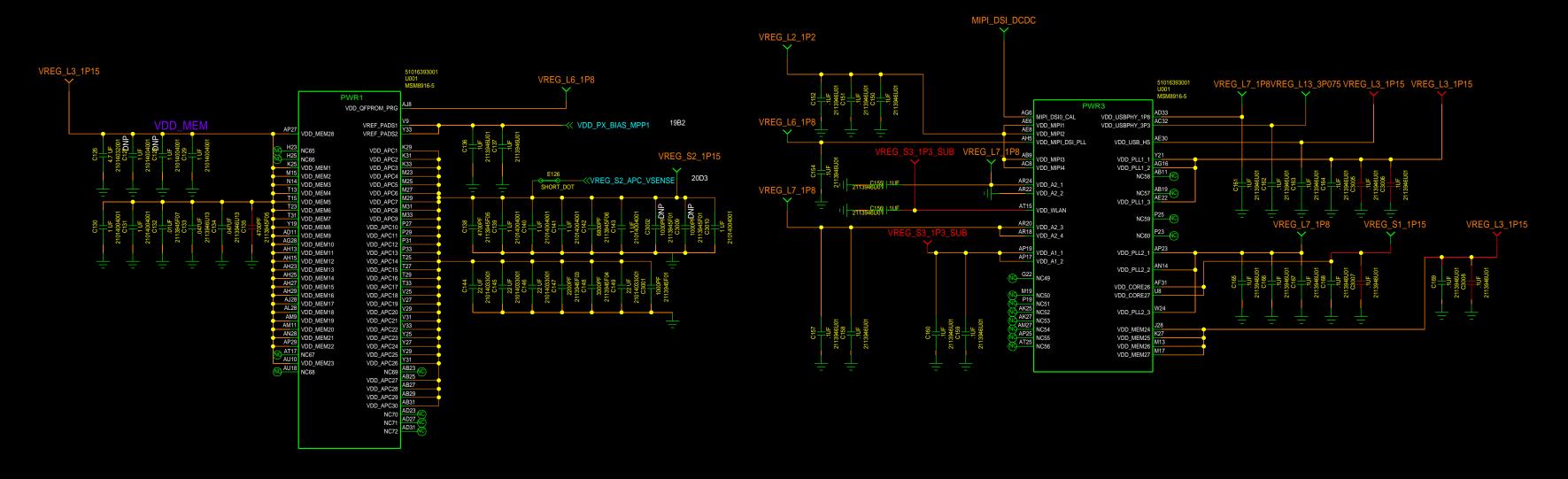
CDC_PDM_SYNC_GPIO64

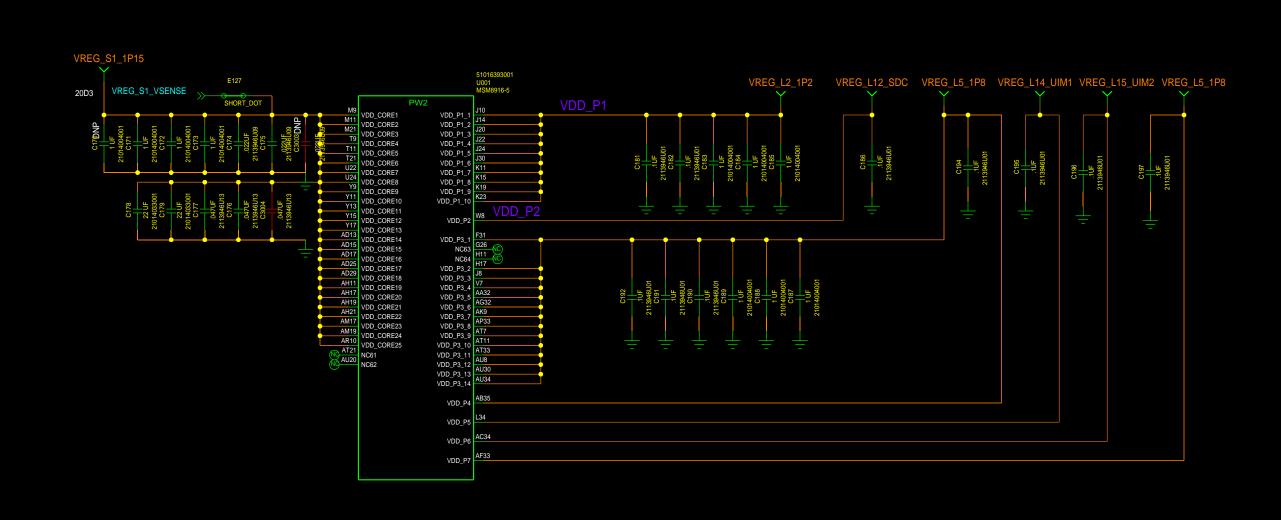
CDC_PDM_TX_GPIO65 LCD_TE_GPI024 OU LCD_RST_N_GPI025 OU REARCAM_MCLK0_GPI026 OU LCD_BIAS_N_EN_GPI098 2C6^ GSM0_TX_PHASE_0 2B6^2C6^ MAIN_ANTENNA_DET HW ID 3 FRONTCAM MCLK1 GPIO27 MAIN_ANTENNA_DET CCII2C_SDA_GPIO29 CCII2C_SCL_GPIO30 CDC_PDM_RX0_GPIO66 CDC_PDM_RX1_GPIO67 2C6[^] RFFE5_DATA IO FLASH_EN_GPIO32 FRONT_CAM_PWDN_GPIO33 ECOMPASS_INT_GPIO69 > RFFE1_CLK KEY_VOL_UP_N_GPIO107 IN 14B4 17B1 HW_ID_1 >> 14B3 17B1 HW_ID_2 >> NFC_VEN_GPIO36 OUT DNP VREG_L5_1P8 VREG_L6_1P8 R115 HW_ID_2 ⇒ HW ID 1 DNP VREG L5 1P8 VREG L6 1P8 17C4 12C1_SDA_GPIO2 10 17C4 12C1_SCL_GPIO3 0UT CCII2C_SDA_GPIO29 (IO > NFC_I2C3_SDA_GPIO(0 IO)-SENSOR_I2C4_SDA_GPIO14 IO TS_I2C2_SDA_GPIO6 NFC_I2C3_SCL_GPIO(DUT) SENSOR_I2C4_SCL_GPIO15O TS_I2C2_SCL_GPI07 OUT-CCII2C_SCL_GPIO30 OUT Thu Jun 02 10:00:50 2016 FRONT AND REAR CAMERA AND FLASH DRIVER MOTOROLA 84XXXXXXXX MOTOROLA CONFIDENTIAL RESTRICTED PAGE 33 OF 37

REVISIONS

REV ECN DESCRIPTION MADE BY APPROVED

REF:126-200 REF:3001-3050







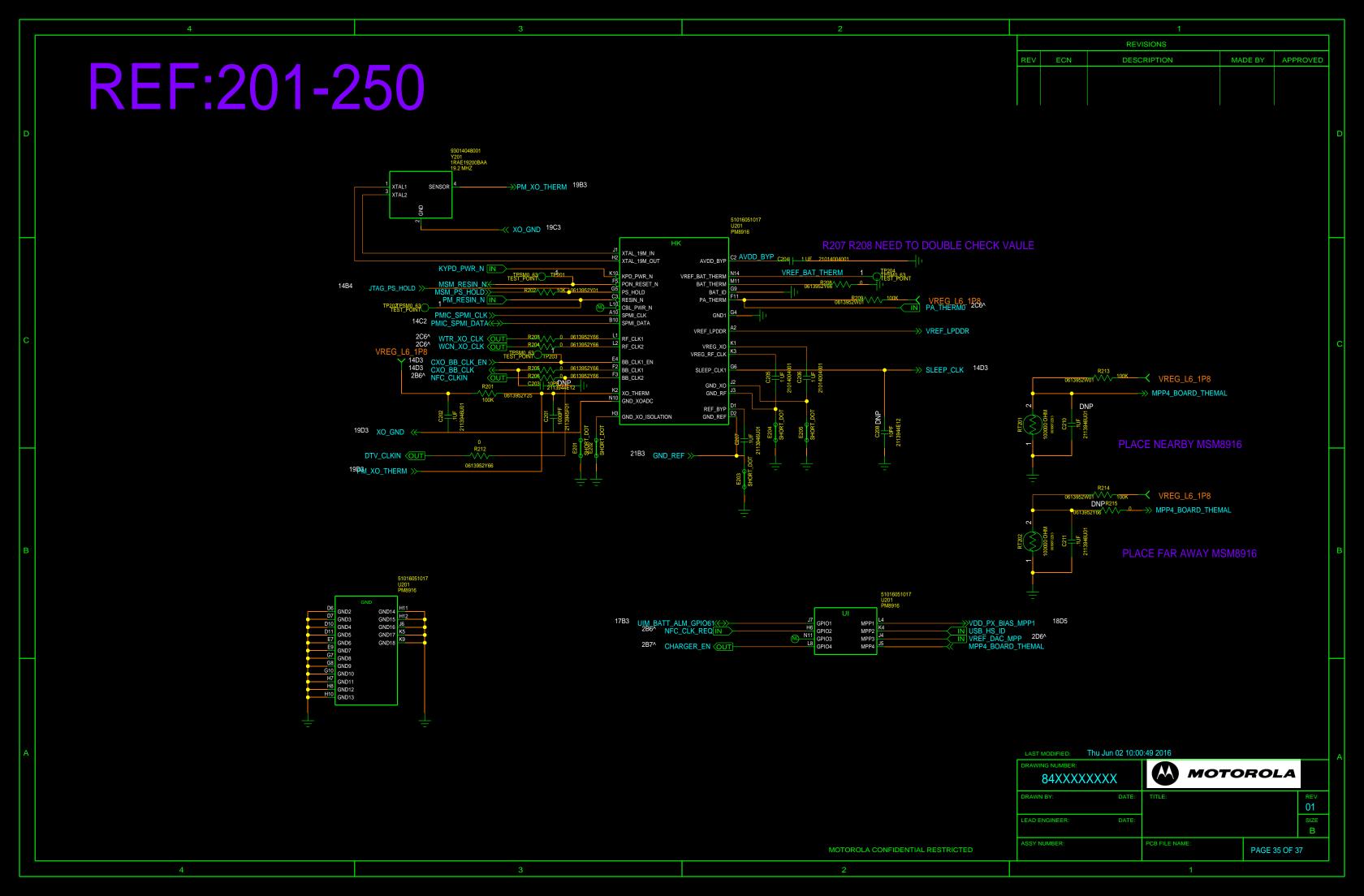
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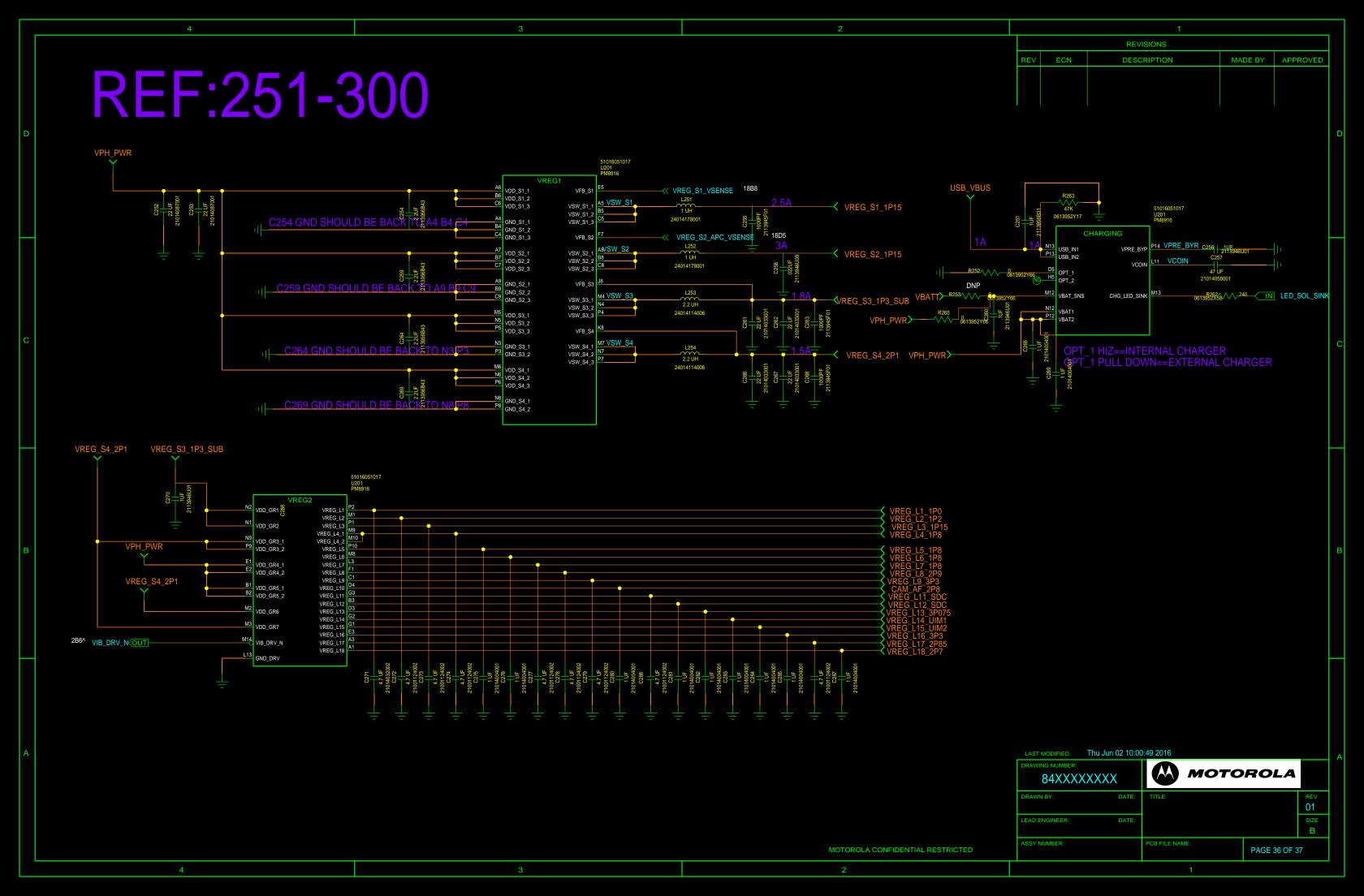
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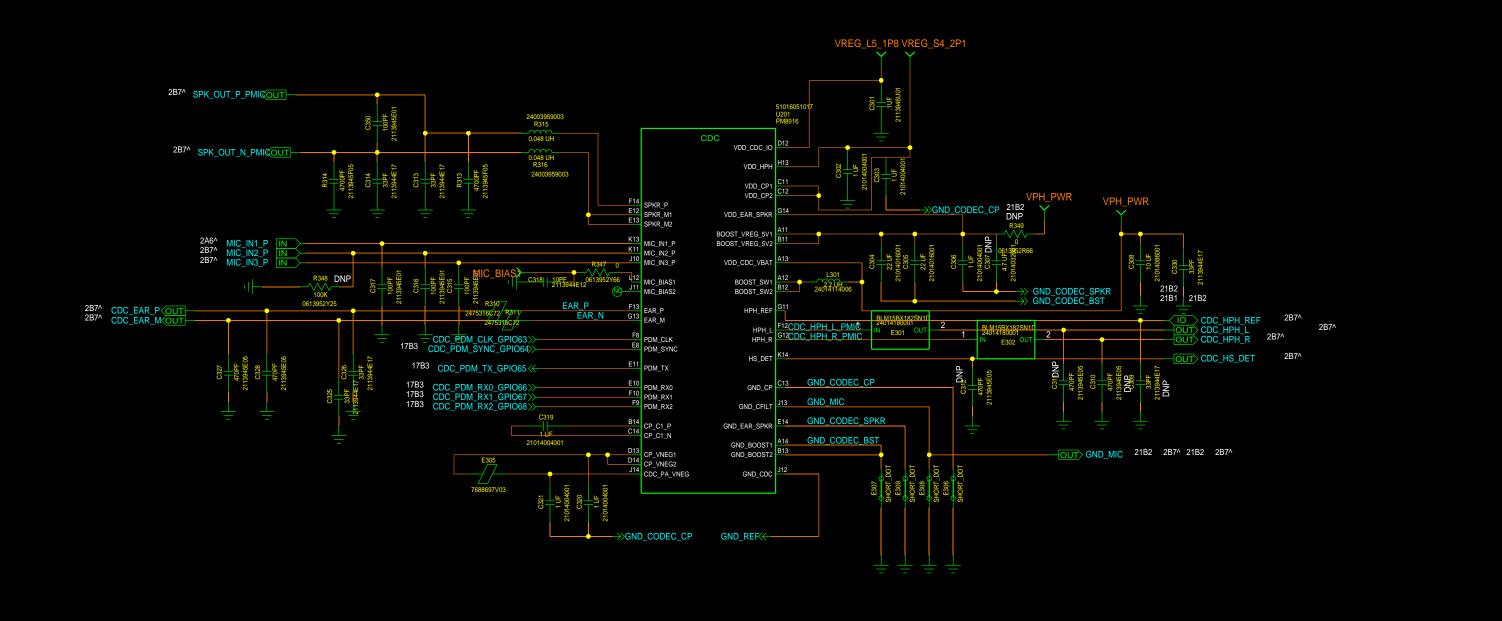


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LEAD ENGINEER: DATE:

ASSY NUMBER:

PCB FILE NAME:

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