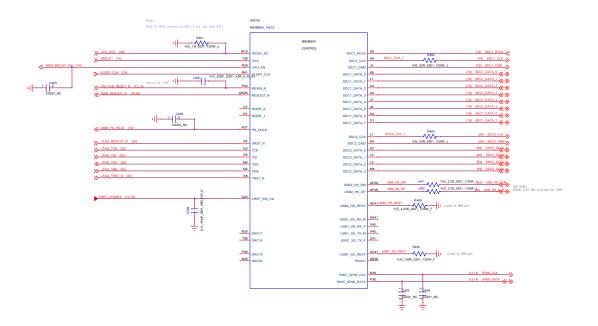
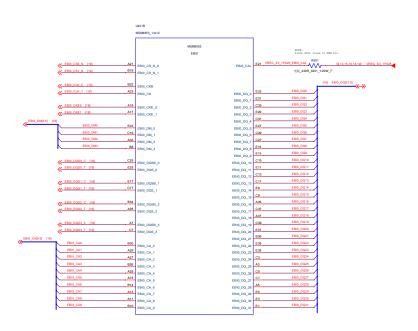
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MSM8953 Control

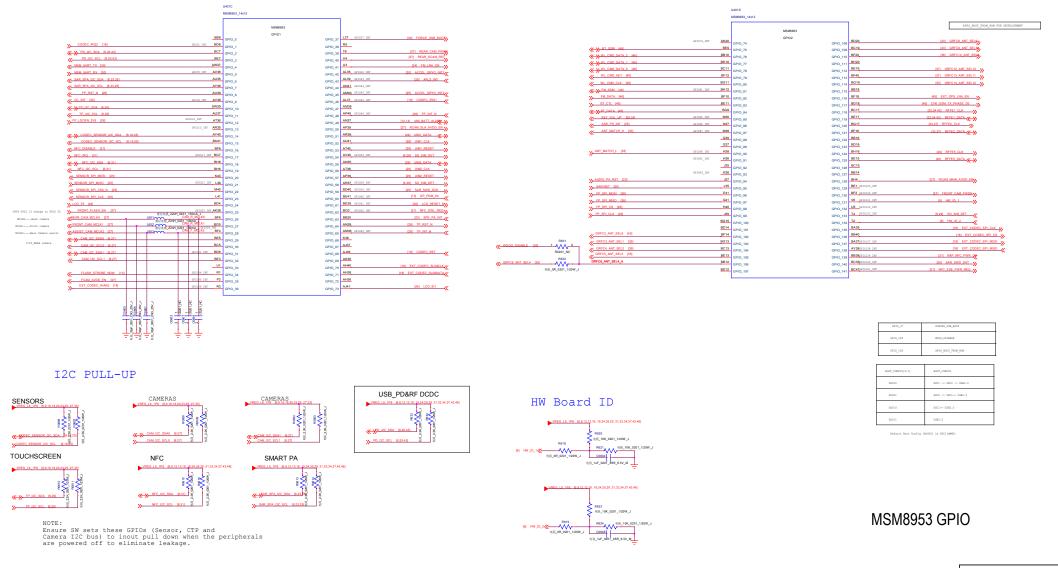




MSM8953 EBI



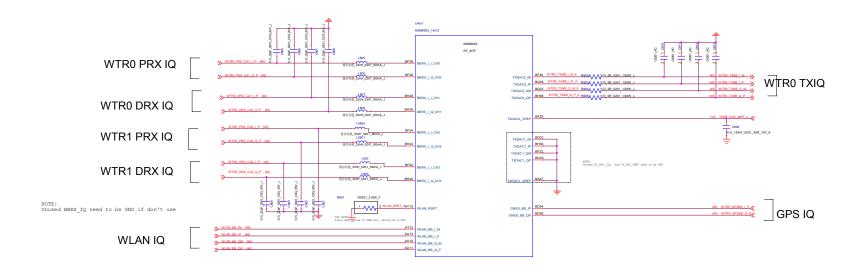
NOTE: Asterisks (*) indicate modem power management (MPM) wake-up pins





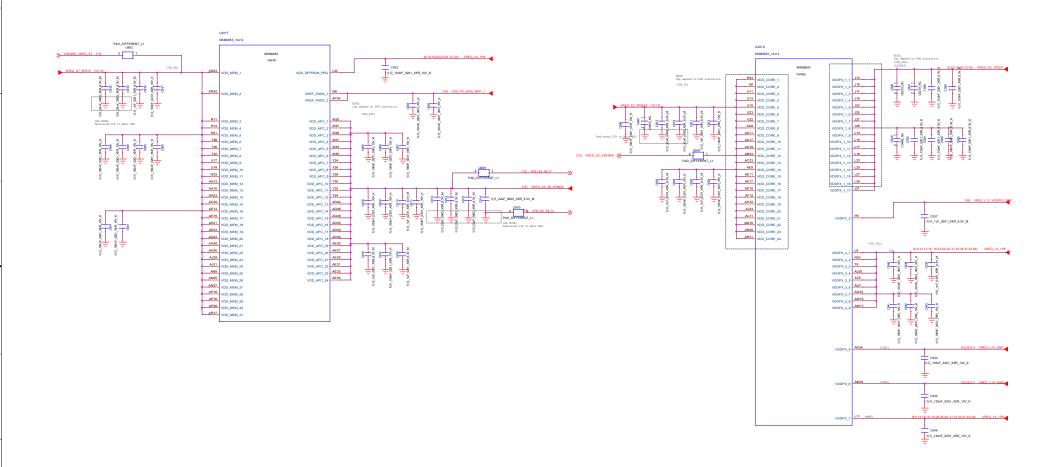
MSM8953 MIPI





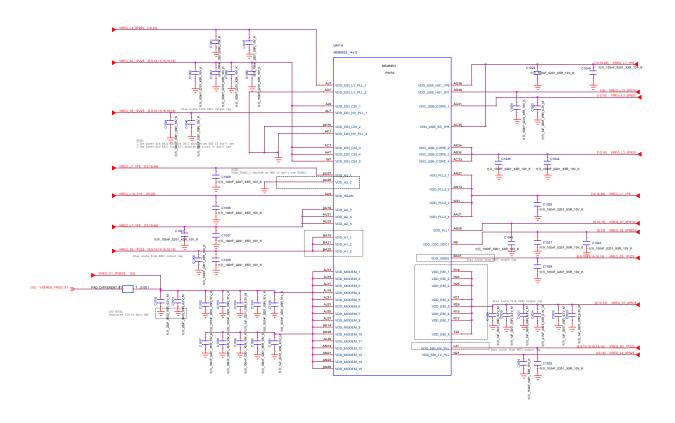
MSM8953 RF





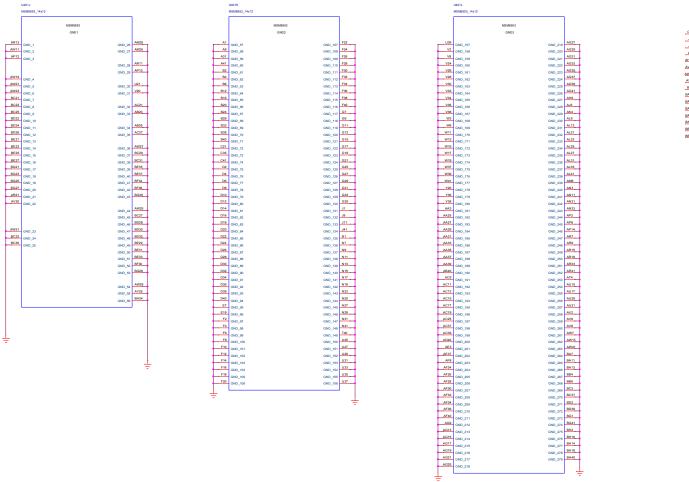
MSM8953 POWER1

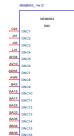




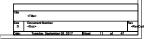
MSM8953 POWER2



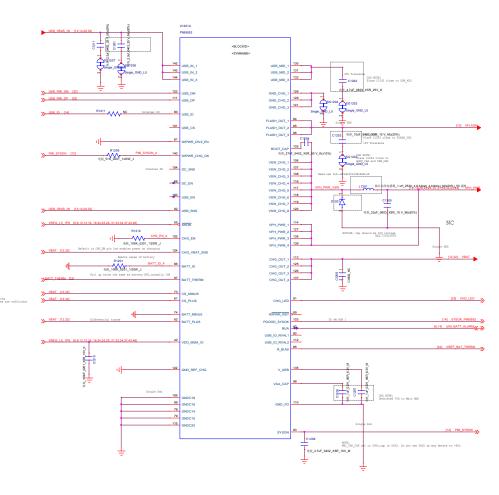




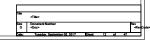
MSM8953 GND

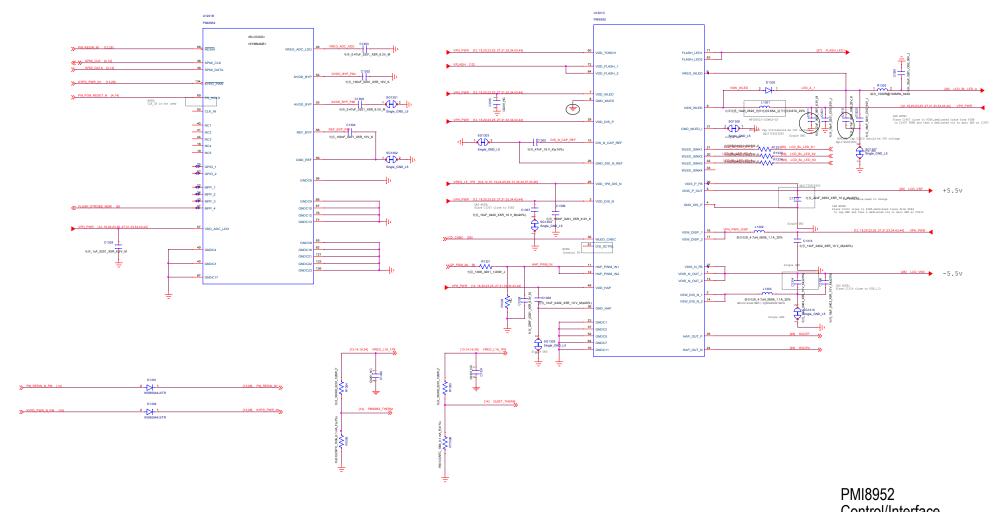


PMI PIN	connection on device without Wipower	connection on device with Wipower	
CRG_OK	Pull up to VIO_CAP with 51kohn	Stark PSU CRG_DK	
DVI2_EN	Pull down to GND with Sohm	Stark PRU DIVZ_EN	
	No Connect For parallel charging: Connects to SMG EN pin Do NOT pull up to VEO CAP	10Wohm pull down to gnd For parallel charging: Cornects to SMG EN pin. Do NOT pull up to VED CAP	



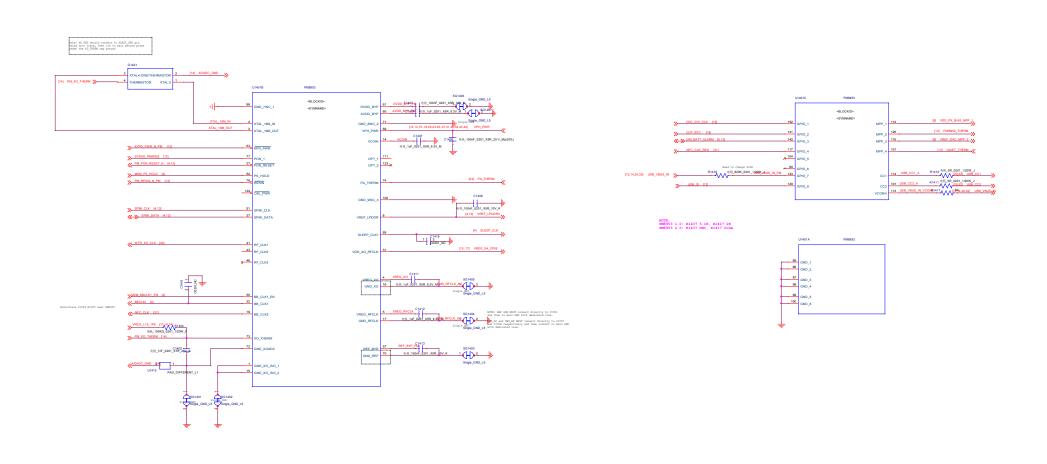
PMI8952 Charger





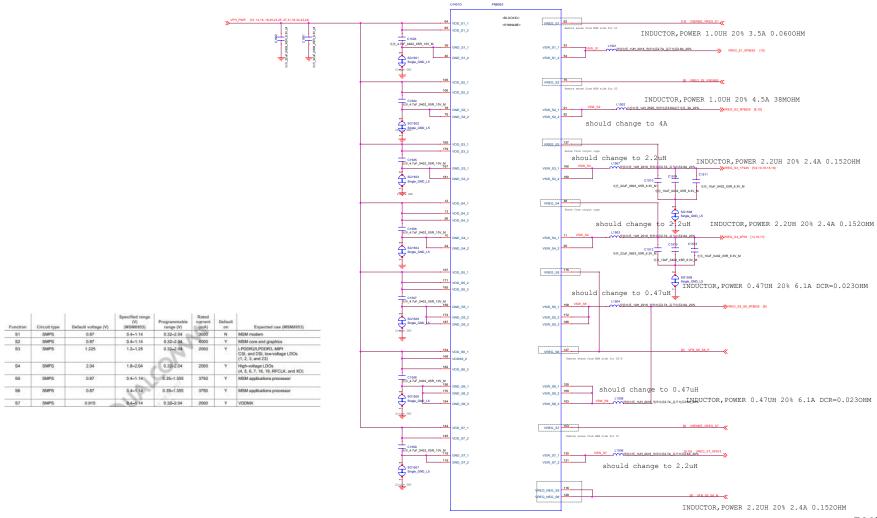
Control/Interface



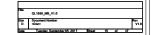


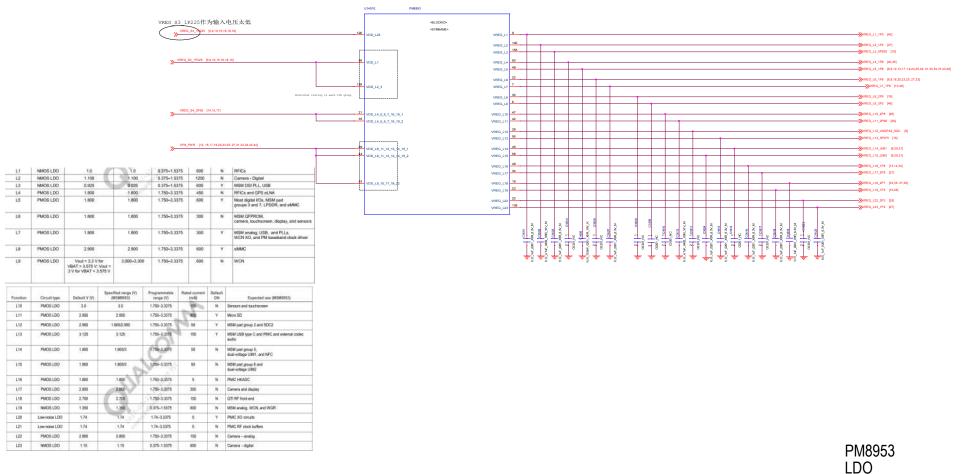
PM8953 Control/Interface

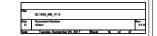


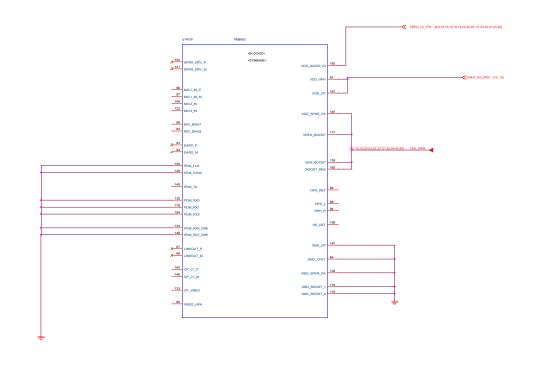


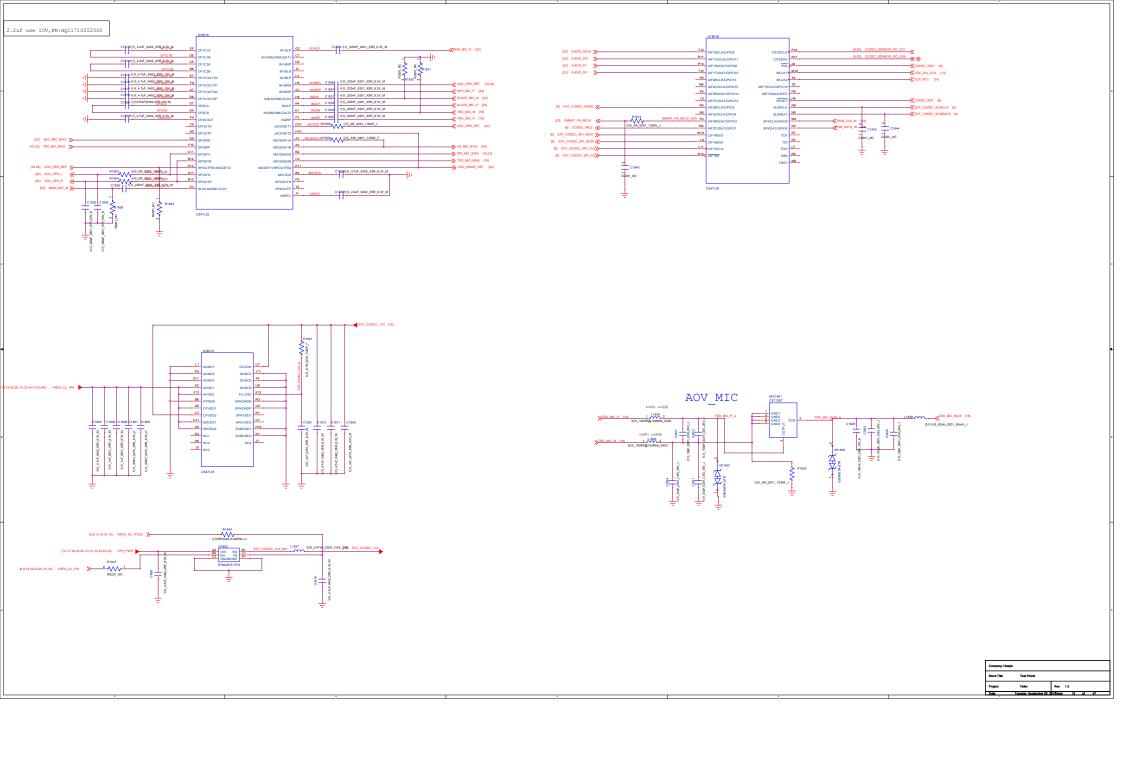
PM8953 SMPS

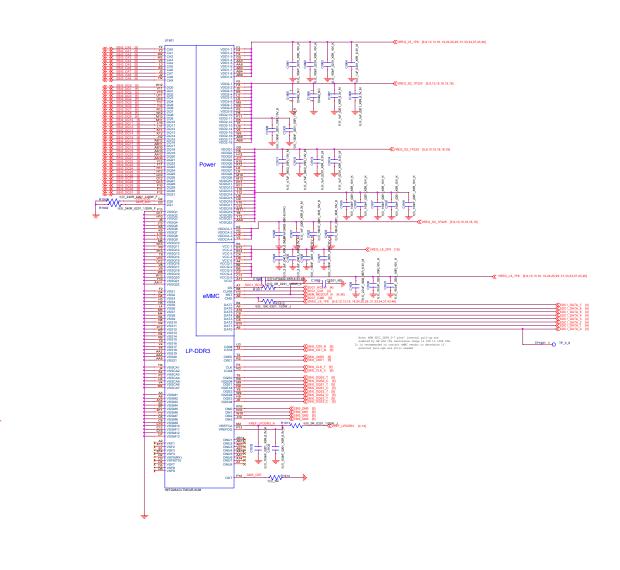




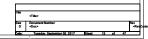


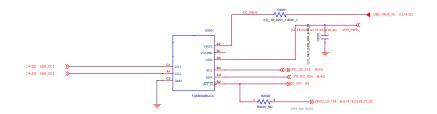


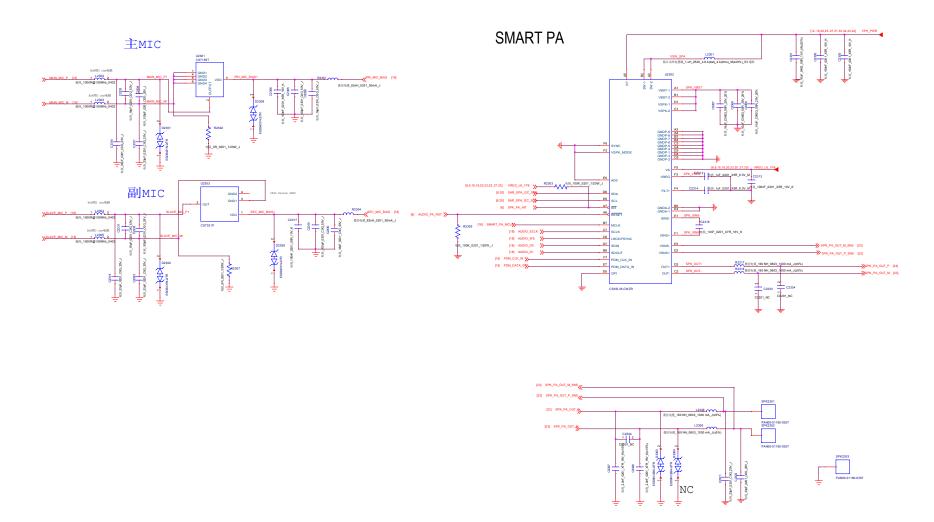




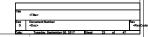
MEMORY(LPDDR3+EMMC V5.1)

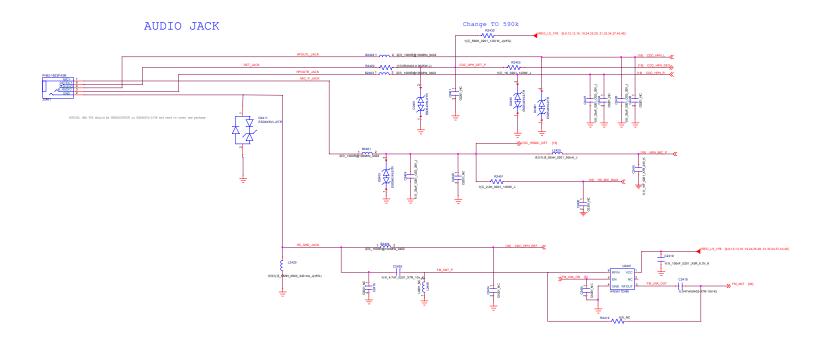






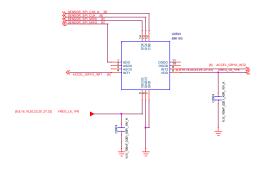
RCV_MIC_Headphone



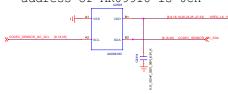


ACCELEROMETER+GYRO

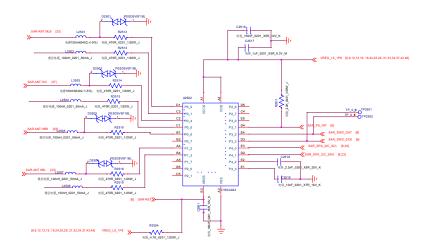
I2C ADDRESS 0X68 0b1101000



Magnetic Sensor address of AK09918 is OCh

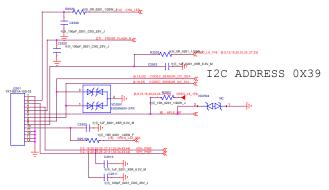


address of AK09918 is OCh



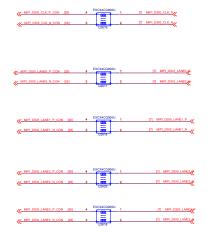
ALS/P SENSOR+Front Flash+Charge LED CONN

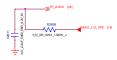
LED change to A-SP194W1D-C01-4T-AR2.4

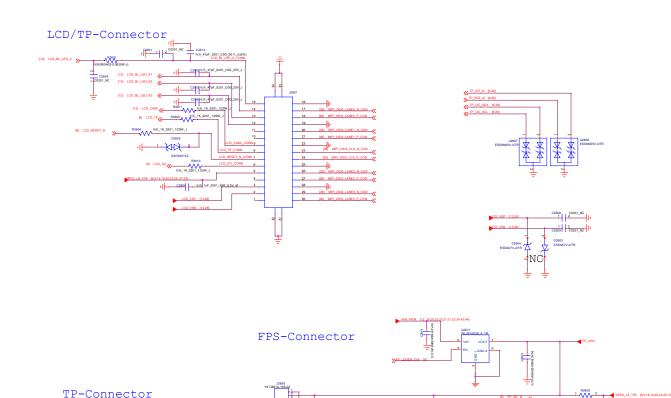




LCD MIPI Common Mode Filter

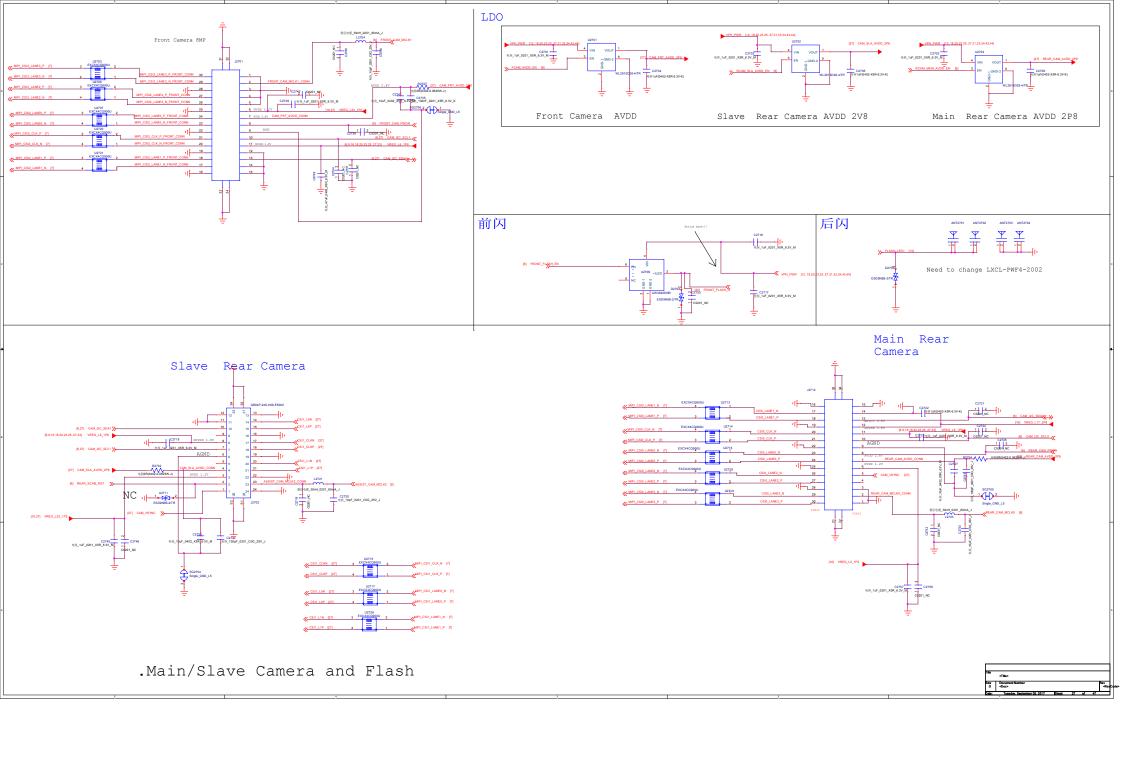




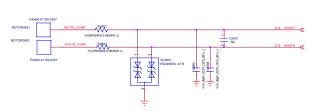


LDO should be WL2810D18-4/TR

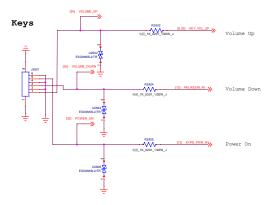


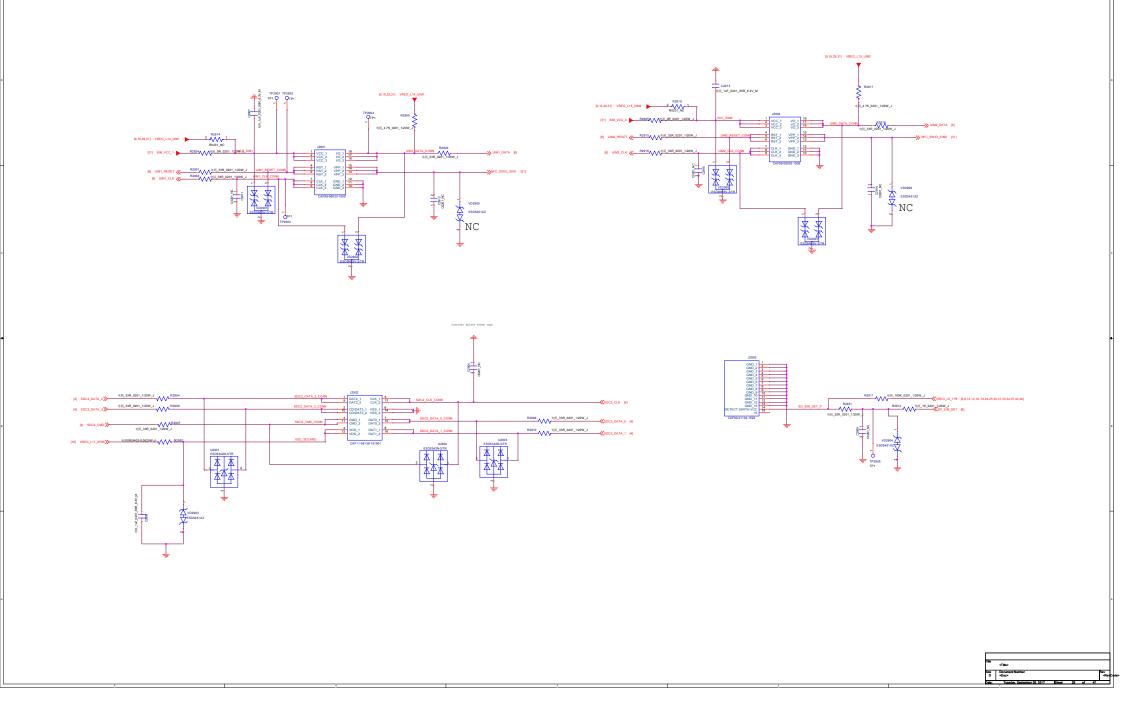


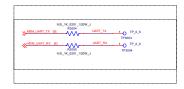
音量键&开关机键



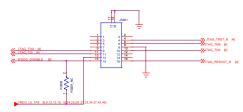
MOTOR



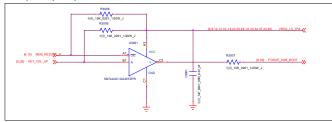


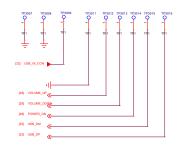


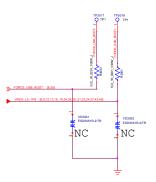
JTAG CONNECTOR



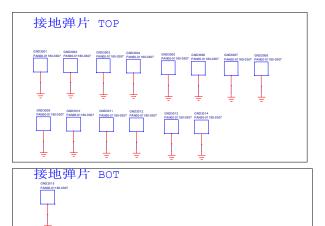
power key +volup to enter 9008 mode







PCBA AUTOMATIC TEST point



TP/GND/Shielding



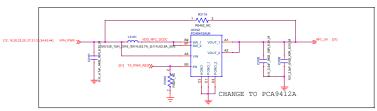


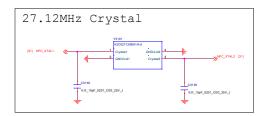


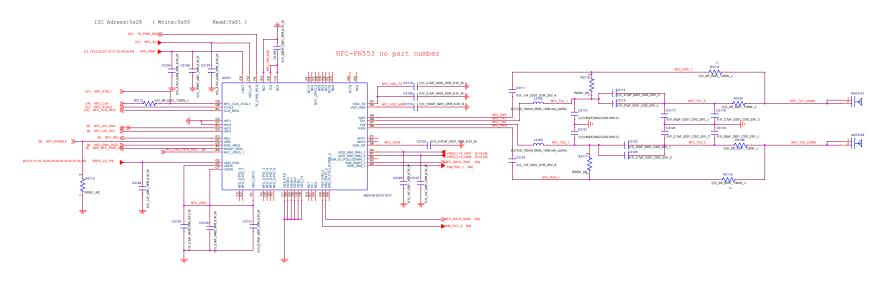
NQ330 PIN-TO-PIN NQ310(PN553)

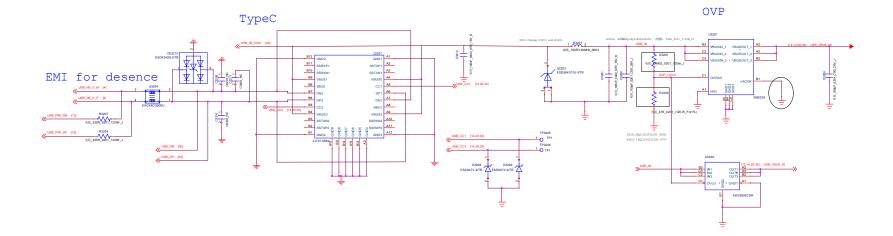
The purpose of this chapter is to propose several application schematics for: NQ310 and NQ330 chips in VFBGA package

DCDC POWER FOR NFC

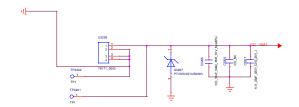




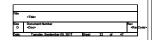


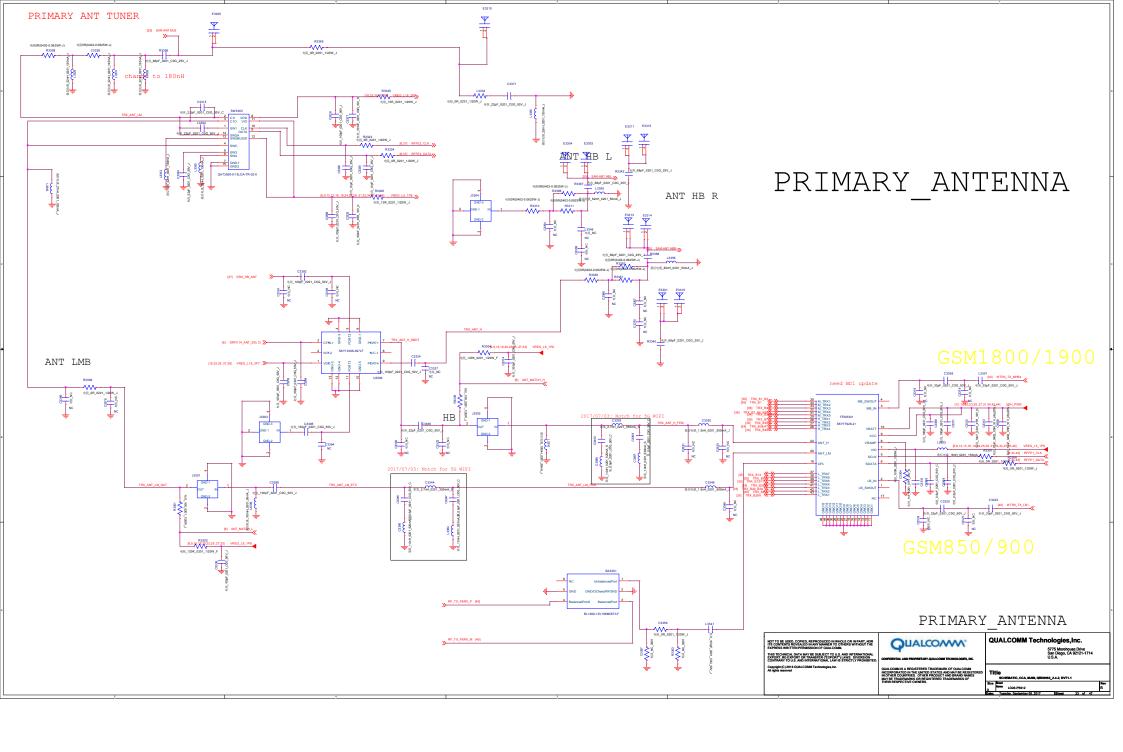


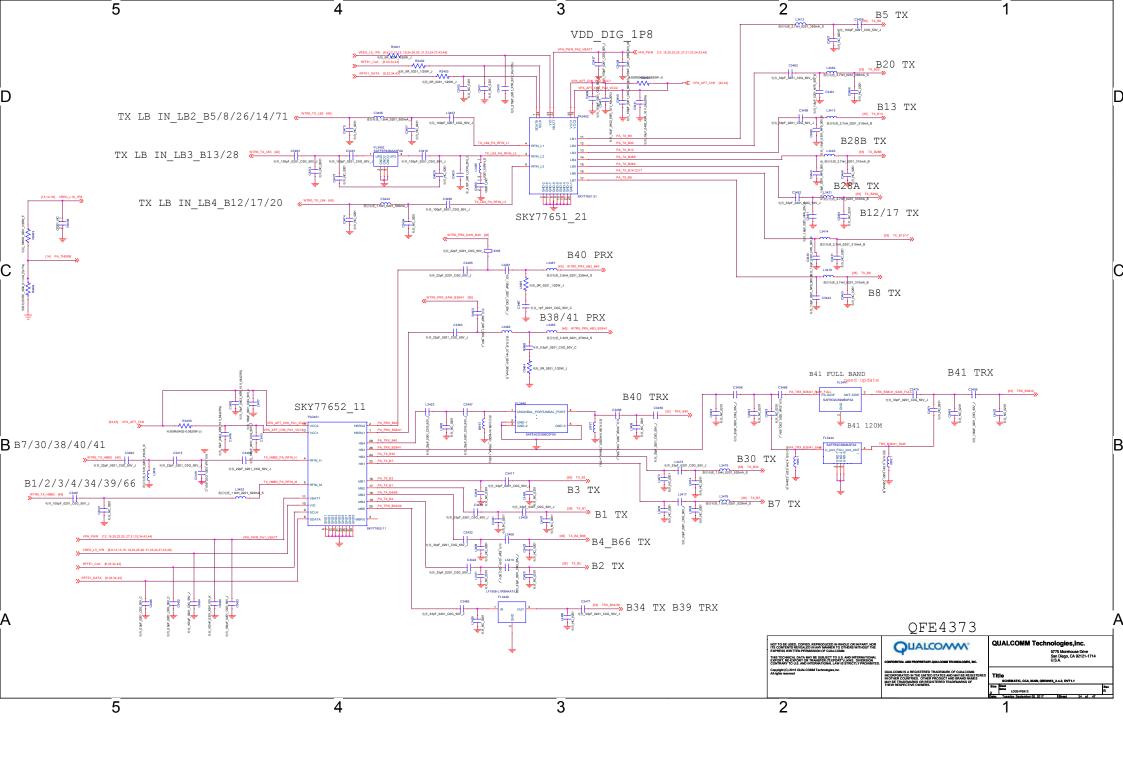
Battery Connector

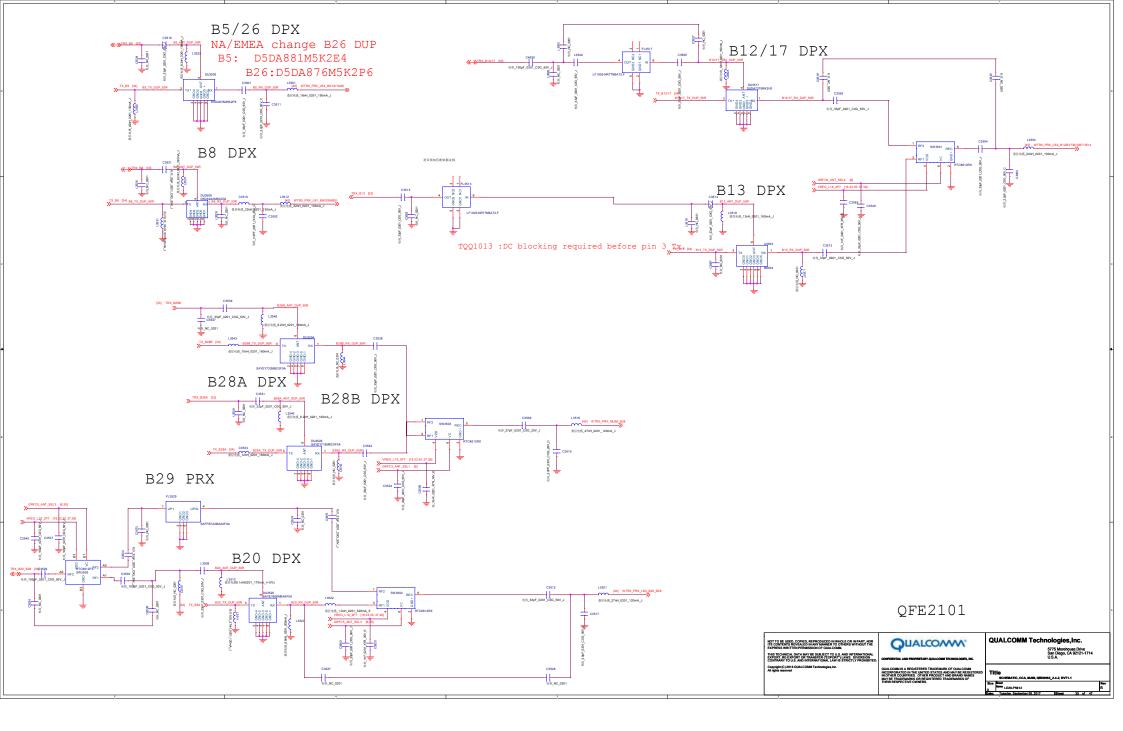


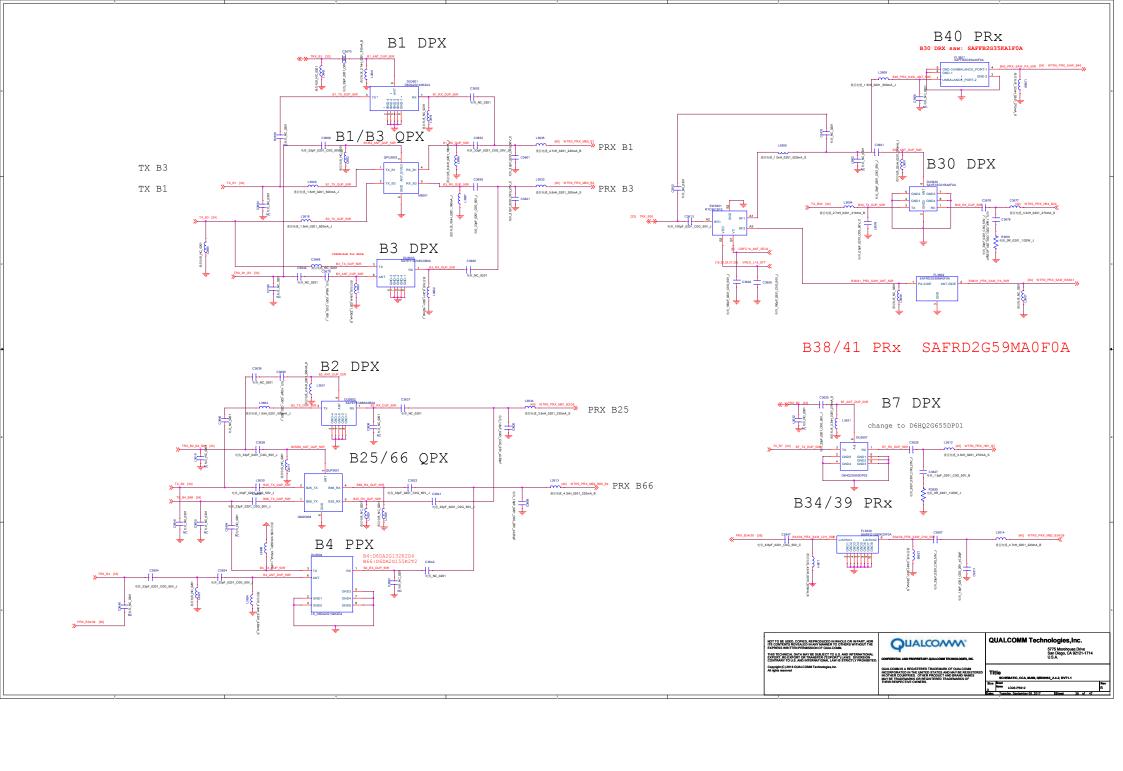


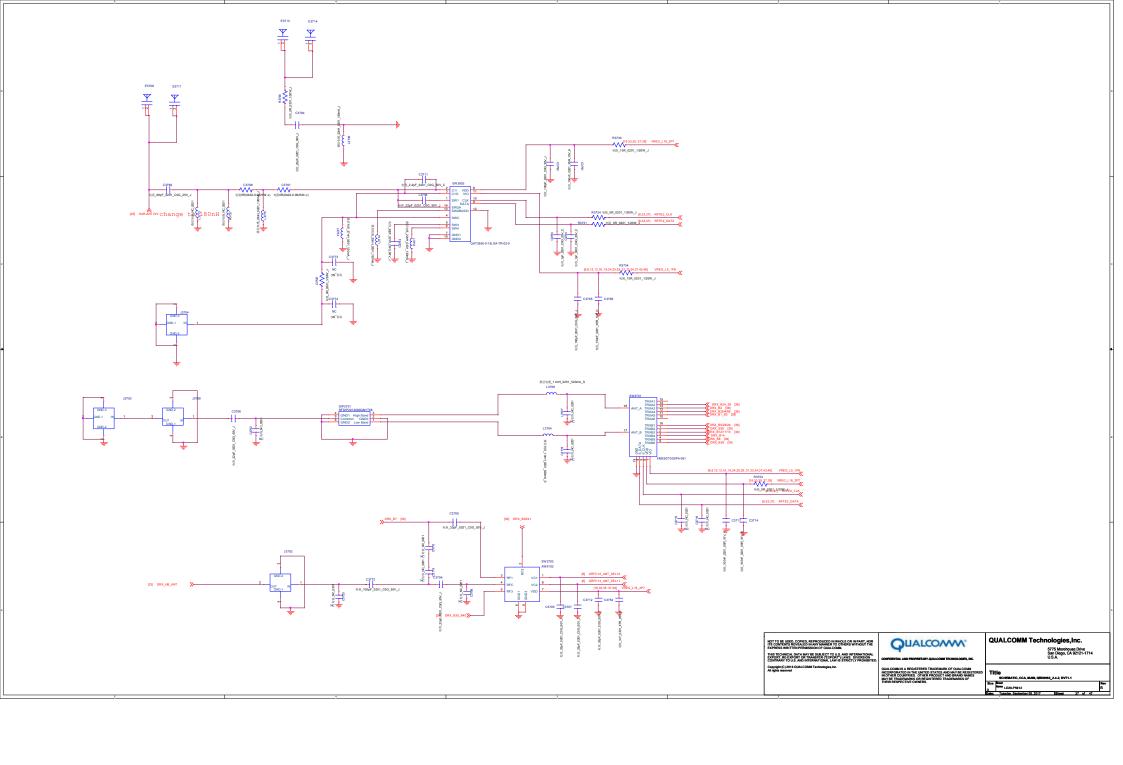


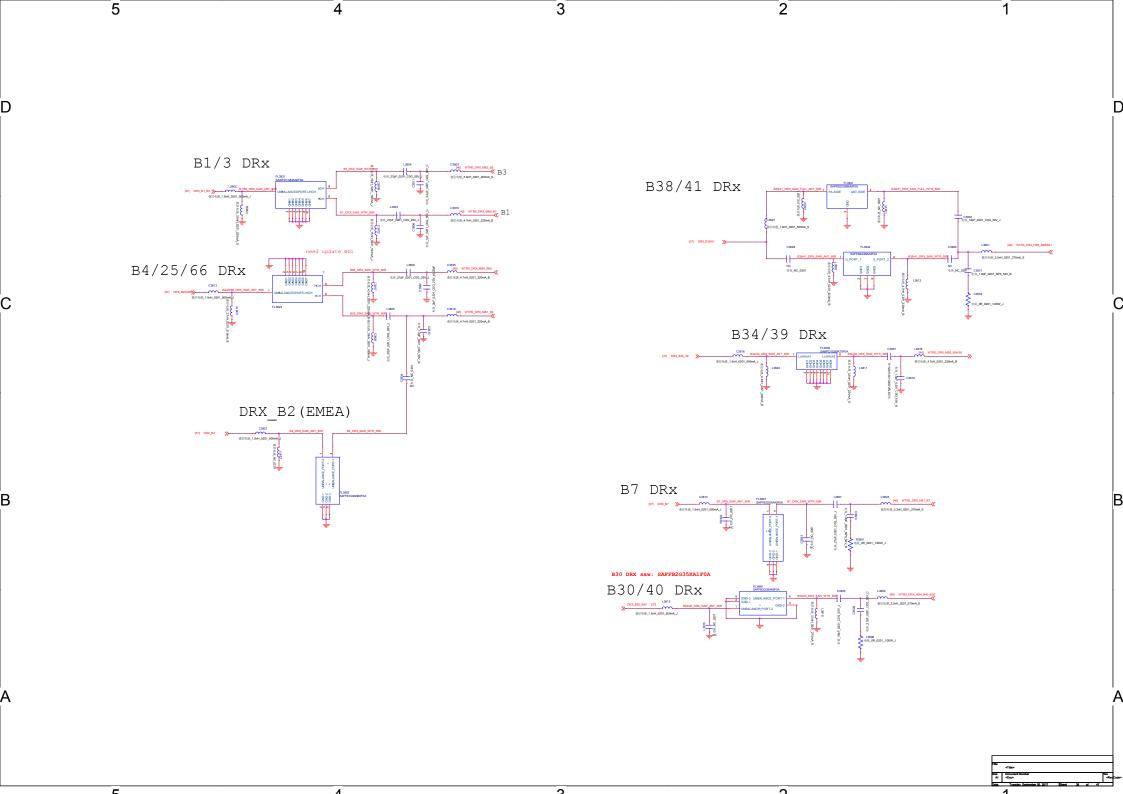






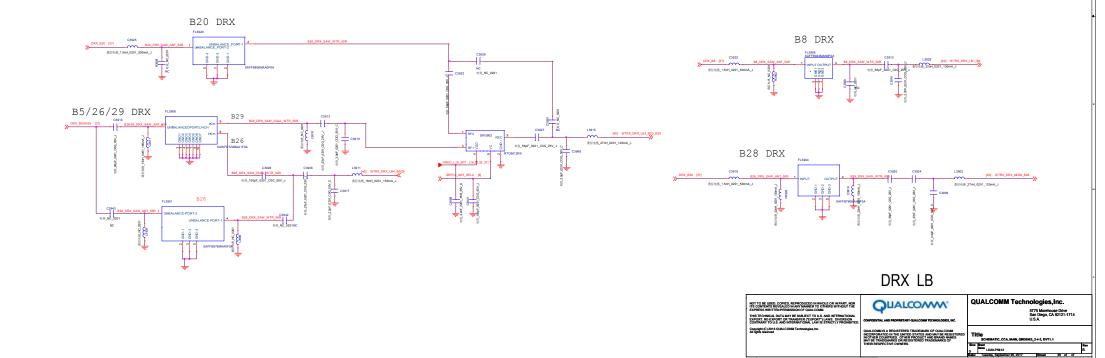


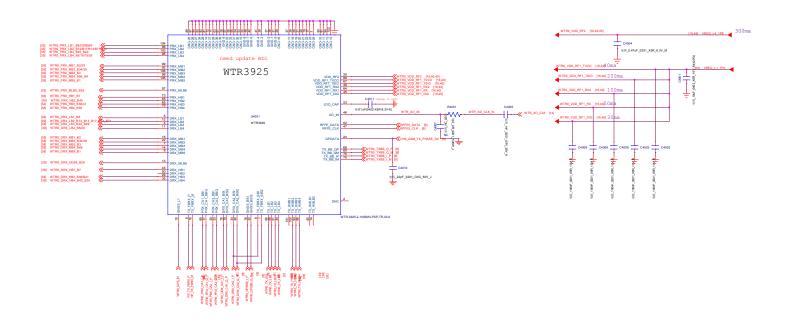




B12/17/13 DRX



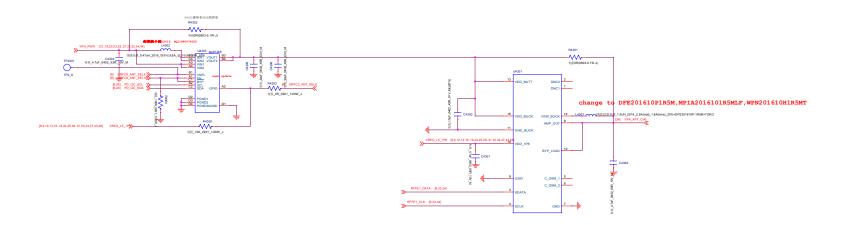




WTRO_PORT	0
Load	1

WTR 2965_0



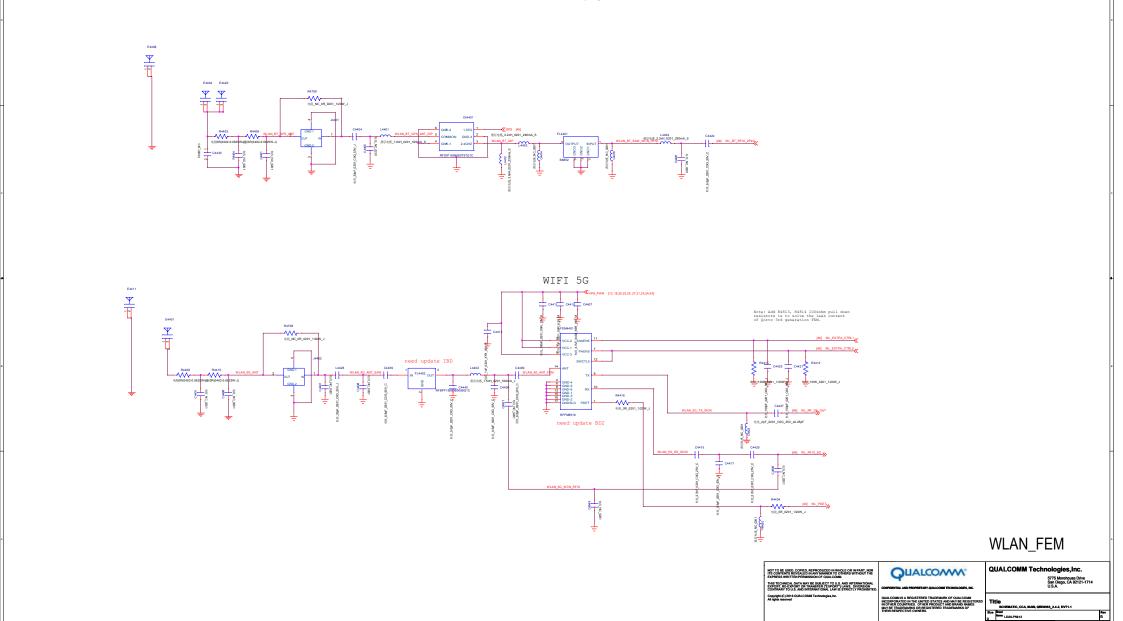


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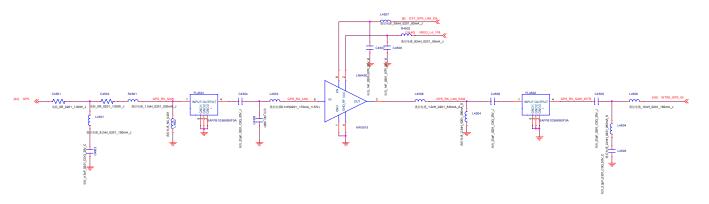
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976 Monthouse Drive
San Diego, CA 92121-1714
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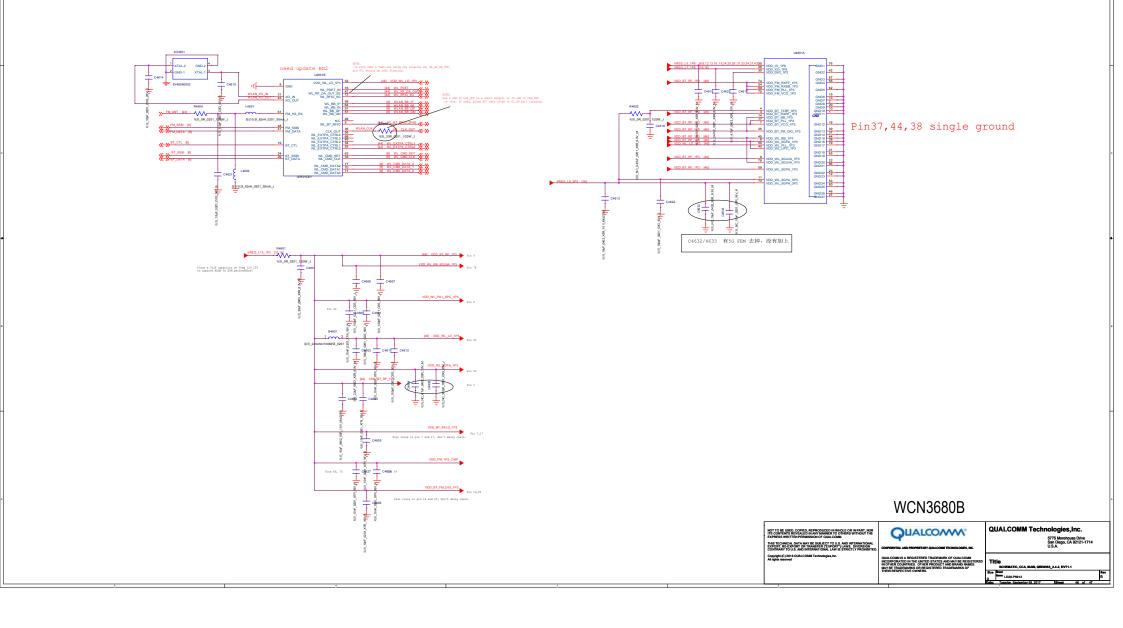




WLAN_FEM



WCN3660B



	MS	M8953 GPIC	Configuration Fo	r QRD89	53
GPIO_0	NFC_SPI_ESE_MOSI	GPIO_41		GPIO_82	FM_DATA
GPIO_1	NFC_SPI_ESE_MISO	GPIO_42	ACCL_INT1	GPIO_83	BT_CTL
GPIO_2	NFC_SPI_ESE_CS_N	GPIO_43	ALSP_INT_N	GPIO_84	BT_DATA
GPIO_3	NFC_SPI_ESE_CLK	GPIO_44	MAG_DRDY_INT	GPIO_85	KEY_VOL_UP_N
GPIO_4	UART_MSM_TX	GPIO_45	GYRO_INT	GPIO_86	
GPIO_5	UART_MSM_RX	GPIO_46	SCAM_D_ELDO_EN	GPIO_87	
GPIO_6		GPIO_47	SENSOR_SPI_CS2_N	GPIO_88	
GPIO_7		GPIO_48	FP_INT_N	GPIO_89	
GPIO_8		GPIO_49	UIM_BATT_ALARM	GPIO_90	
GPIO_9		GPIO_50	WCD_ELDO_EN	GPIO_91	
GPIO_10	TP_I2C_SDA	GPIO_51	UIM1_DATA	GPIO_92	
GPIO_11	TP_I2C_SCL	GPIO_52	UIM1_CLK	GPIO_93	SCAM_ID
GPIO_12		GPIO_53	UIM1_RESET	GPIO_94	
GPIO_13		GPIO_54		GPIO_95	
GPIO_14	SENSOR_I2C_SDA	GPIO_55	UIM2_DATA	GPIO_96	WSA_EN
GPIO_15	SENSOR_I2C_SCL	GPIO_56	UIM2_CLK	GPIO_97	
GPIO_16	NFC_DISABLE	GPIO_57	UIM2_RESET	GPIO_98	
GPIO_17	NFC_IRQ	GPIO_58		GPIO_99	
GPIO_18	NFC_I2C_SDA	GPIO_59		GPIO_100	GRFC0_SEL
GPIO_19	NFC_I2C_SCL	GPIO_60	GP_PDM_A0	GPIO_101	GRFC1_SEL
GPIO_20	SENSOR_SPI_MOSI	GPIO_61	LCD_RST_N	GPIO_102	GRFC2_SEL
GPIO_21	SENSOR_SPI_MISO	GPIO_62	NFC_DWL_REQ	GPIO_103	GRFC3_SEL
GPIO_22	SENSOR_SPI_CSO_N	GPIO_63		GPIO_104	GRFC4_SEL
GPIO_23	SENSOR_SPI_CLK	GPIO_64	TP_RST_N	GPIO_105	GRFC5_SEL
GPIO_24	LCD_TE0	GPIO_65	TP_INT_N	GPIO_106	WDOG_DISABLE
GPIO_25		GPIO_66		GPIO_107	GRFC7_SEL
GPIO_26	CAM_MCLK0	GPIO_67	WCD_RST_N	GPIO_108	GRFC8_SEL
GPIO_27	CAM_MCLK1	GPIO_68		GPIO_109	GRFC9_SEL
GPIO_28		GPIO_69	WCD_MSM_MCLK	GPIO_110	
GPIO_29	CAM_I2C_SDA0	GPIO_70	SLIMBUS_CLK	GPIO_111	
GPIO_30	CAM_I2C_SCL0	GPIO_71	SLIMBUS_DATA0	GPIO_112	
GPIO_31	CAM_I2C_SDA1	GPIO_72		GPIO_113	
GPIO_32	CAM_I2C_SDA1	GPIO_73	WCD_INTR1	GPIO_114	
GPIO_33		GPIO_74	WCD_INTR2	GPIO_115	
GPIO_34	FLASH_STROBE_NOW	GPIO_75	BT_SSBI	GPIO_116	EXT_GPS_LNA_EN
GPIO_35		GPIO_76	WL_CMD_DATA_2	GPIO_117	CHO_GSM_TX_PHASE_D0
GPIO_36		GPIO_77	WL_CMD_DATA_1	GPIO_118	RFFE1_CLK
GPIO_37	FORCE_USB_BOOT	GPIO_78	WL_CMD_DATA_0	GPIO_119	RFFE1_DATA
GPIO_38		GPIO_79	WL_CMD_SET	GPIO_120	RFFE2_CLK
GPIO_39	MCAM_PWD_N	GPIO_80	WL_CMD_CLK	GPIO_121	RFFE2_DATA
GPIO_40	MCAM_RST_N	GPIO_81	FM_SSBI	GPIO_122	RFFE4_CLK

GPIO_123	RFFE4_DATA	GPIO_134	
GPIO_124		GPIO_135	FP_SPI_CLK
GPIO_125		GPIO_136	FP_SPI_CS
GPIO_126	RFFE3_CLK	GPIO_137	FP_SPI_MOSI
GPIO_127	RFFE3_DATA	GPIO_138	FP_SPI_MISO
GPIO_128		GPIO_139	USB_SS_SEL
GPIO_129	SCAM_RST_N	GPIO_140	FP_RST_N
GPIO_130	SCAM_PWD_N	GPIO_141	NFC_ESE_PWR_REQ
GPIO_131			
GPIO_132			
GPIO_133	SDCARD_DET_N		

PMI8952 GPIO/MPP Configuration For QRD8953				
GPIO_1		MPP_1		
GPIO_2		MPP_2	GREEN_LED	
		MPP_3		
		MPP_4	FLASH_STROBE_NOW	

PM8953 GPIO/MPP Configuration For QRD8953					
GPIO_1	CODEC_DIV_CLK	MPP_1	VDD_PX_BIAS_MPP_1		
GPIO_2	NFC_CLK_REQ(SDCARD_DET_N)				
GPIO_3	UIM_BATT_ALARM	MPP_3	VREF_DAC_MPP_3		
GPIO_4		MPP_4	QUIET_THERM		
GPIO_5					
GPIO_6					
GPIO_7	VBUS_USB_IN				
GPIO_8	USB_ID				

GPIO TABLE

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