

Schematic design notice of "12_BB_1" page.

Note 3-1: The de-coupling cap. for REFP (AF18 ball) has to be placed as close to BB as possible.

Note 3-2: To short a 1uF capacitor in the AUXIN ADC input to prevent noise coupling. It should be placed as close to BB as possible. Connect the unused ADC input to GND.

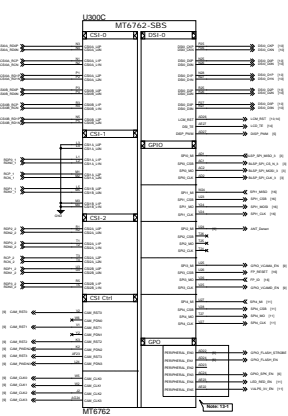
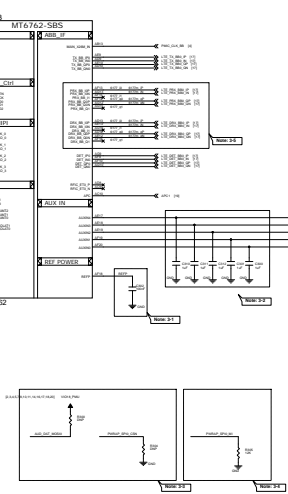
Note 3-3: PWRAP, SPK, CSD, and AUDIO, DATA, and MODE are bonding pins to select which interface will be the JTAG pin out.

USE CASE	PWRAP	SPK	CSD	AUDIO	DATA	MODE	JTAG
1	NC	NC	NC	NC	NC	NC	NC
2	NC	NC	NC	NC	NC	NC	NC
3	NC	NC	NC	NC	NC	NC	NC
4	NC	NC	NC	NC	NC	NC	NC
5	NC	NC	NC	NC	NC	NC	NC
6	NC	NC	NC	NC	NC	NC	NC
7	NC	NC	NC	NC	NC	NC	NC
8	NC	NC	NC	NC	NC	NC	NC
9	NC	NC	NC	NC	NC	NC	NC
10	NC	NC	NC	NC	NC	NC	NC
11	NC	NC	NC	NC	NC	NC	NC
12	NC	NC	NC	NC	NC	NC	NC
13	NC	NC	NC	NC	NC	NC	NC
14	NC	NC	NC	NC	NC	NC	NC
15	NC	NC	NC	NC	NC	NC	NC
16	NC	NC	NC	NC	NC	NC	NC
17	NC	NC	NC	NC	NC	NC	NC
18	NC	NC	NC	NC	NC	NC	NC
19	NC	NC	NC	NC	NC	NC	NC
20	NC	NC	NC	NC	NC	NC	NC
21	NC	NC	NC	NC	NC	NC	NC
22	NC	NC	NC	NC	NC	NC	NC
23	NC	NC	NC	NC	NC	NC	NC
24	NC	NC	NC	NC	NC	NC	NC
25	NC	NC	NC	NC	NC	NC	NC
26	NC	NC	NC	NC	NC	NC	NC
27	NC	NC	NC	NC	NC	NC	NC
28	NC	NC	NC	NC	NC	NC	NC
29	NC	NC	NC	NC	NC	NC	NC
30	NC	NC	NC	NC	NC	NC	NC
31	NC	NC	NC	NC	NC	NC	NC
32	NC	NC	NC	NC	NC	NC	NC
33	NC	NC	NC	NC	NC	NC	NC
34	NC	NC	NC	NC	NC	NC	NC
35	NC	NC	NC	NC	NC	NC	NC
36	NC	NC	NC	NC	NC	NC	NC
37	NC	NC	NC	NC	NC	NC	NC
38	NC	NC	NC	NC	NC	NC	NC
39	NC	NC	NC	NC	NC	NC	NC
40	NC	NC	NC	NC	NC	NC	NC
41	NC	NC	NC	NC	NC	NC	NC
42	NC	NC	NC	NC	NC	NC	NC
43	NC	NC	NC	NC	NC	NC	NC
44	NC	NC	NC	NC	NC	NC	NC
45	NC	NC	NC	NC	NC	NC	NC
46	NC	NC	NC	NC	NC	NC	NC
47	NC	NC	NC	NC	NC	NC	NC
48	NC	NC	NC	NC	NC	NC	NC
49	NC	NC	NC	NC	NC	NC	NC
50	NC	NC	NC	NC	NC	NC	NC
51	NC	NC	NC	NC	NC	NC	NC
52	NC	NC	NC	NC	NC	NC	NC
53	NC	NC	NC	NC	NC	NC	NC
54	NC	NC	NC	NC	NC	NC	NC
55	NC	NC	NC	NC	NC	NC	NC
56	NC	NC	NC	NC	NC	NC	NC
57	NC	NC	NC	NC	NC	NC	NC
58	NC	NC	NC	NC	NC	NC	NC
59	NC	NC	NC	NC	NC	NC	NC
60	NC	NC	NC	NC	NC	NC	NC
61	NC	NC	NC	NC	NC	NC	NC
62	NC	NC	NC	NC	NC	NC	NC
63	NC	NC	NC	NC	NC	NC	NC
64	NC	NC	NC	NC	NC	NC	NC
65	NC	NC	NC	NC	NC	NC	NC
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95	NC	NC	NC	NC	NC	NC	NC
96	NC	NC	NC	NC	NC	NC	NC
97	NC	NC	NC	NC	NC	NC	NC
98	NC	NC	NC	NC	NC	NC	NC
99	NC	NC	NC	NC	NC	NC	NC
100	NC	NC	NC	NC	NC	NC	NC

Note 3-4: PWRAP, SPK, and CSD are DOR pins featuring in bonding.

USE CASE	PWRAP	SPK	CSD	AUDIO	DATA	MODE	JTAG
1	NC	NC	NC	NC	NC	NC	NC
2	NC	NC	NC	NC	NC	NC	NC
3	NC	NC	NC	NC	NC	NC	NC
4	NC	NC	NC	NC	NC	NC	NC
5	NC	NC	NC	NC	NC	NC	NC
6	NC	NC	NC	NC	NC	NC	NC
7	NC	NC	NC	NC	NC	NC	NC
8	NC	NC	NC	NC	NC	NC	NC
9	NC	NC	NC	NC	NC	NC	NC
10	NC	NC	NC	NC	NC	NC	NC
11	NC	NC	NC	NC	NC	NC	NC
12	NC	NC	NC	NC	NC	NC	NC
13	NC	NC	NC	NC	NC	NC	NC
14	NC	NC	NC	NC	NC	NC	NC
15	NC	NC	NC	NC	NC	NC	NC
16	NC	NC	NC	NC	NC	NC	NC
17	NC	NC	NC	NC	NC	NC	NC
18	NC	NC	NC	NC	NC	NC	NC
19	NC	NC	NC	NC	NC	NC	NC
20	NC	NC	NC	NC	NC	NC	NC
21	NC	NC	NC	NC	NC	NC	NC
22	NC	NC	NC	NC	NC	NC	NC
23	NC	NC	NC	NC	NC	NC	NC
24	NC	NC	NC	NC	NC	NC	NC
25	NC	NC	NC	NC	NC	NC	NC
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27	NC	NC	NC	NC	NC	NC	NC
28	NC	NC	NC	NC	NC	NC	NC
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31	NC	NC	NC	NC	NC	NC	NC
32	NC	NC	NC	NC	NC	NC	NC
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35	NC	NC	NC	NC	NC	NC	NC
36	NC	NC	NC	NC	NC	NC	NC
37	NC	NC	NC	NC	NC	NC	NC
38	NC	NC	NC	NC	NC	NC	NC
39	NC	NC	NC	NC	NC	NC	NC
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50	NC	NC	NC	NC	NC	NC	NC
51	NC	NC	NC	NC	NC	NC	NC
52	NC	NC	NC	NC	NC	NC	NC
53	NC	NC	NC	NC	NC	NC	NC
54	NC	NC	NC	NC	NC	NC	NC
55	NC	NC	NC	NC	NC	NC	NC
56	NC	NC	NC	NC	NC	NC	NC
57	NC	NC	NC	NC	NC	NC	NC
58	NC	NC	NC	NC	NC	NC	NC
59	NC	NC	NC	NC	NC	NC	NC
60	NC	NC	NC	NC	NC	NC	NC
61	NC	NC	NC	NC	NC	NC	NC
62	NC	NC	NC	NC	NC	NC	NC
63	NC	NC	NC	NC	NC	NC	NC
64	NC	NC	NC	NC	NC	NC	NC
65	NC	NC	NC	NC	NC	NC	NC
66	NC	NC	NC	NC	NC	NC	NC
67	NC	NC	NC	NC	NC	NC	NC
68	NC	NC	NC	NC	NC	NC	NC
69	NC	NC	NC	NC	NC	NC	NC
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71	NC	NC	NC	NC	NC	NC	NC
72	NC	NC	NC	NC	NC	NC	NC
73	NC	NC	NC	NC	NC	NC	NC
74	NC	NC	NC	NC	NC	NC	NC
75	NC	NC	NC	NC	NC	NC	NC
76	NC	NC	NC	NC	NC	NC	NC
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83	NC	NC	NC	NC	NC	NC	NC
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86	NC	NC	NC	NC	NC	NC	NC
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89	NC	NC	NC	NC	NC	NC	NC
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91	NC	NC	NC	NC	NC	NC	NC
92	NC	NC	NC	NC	NC	NC	NC
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99	NC	NC	NC	NC	NC	NC	NC
100	NC	NC	NC	NC	NC	NC	NC

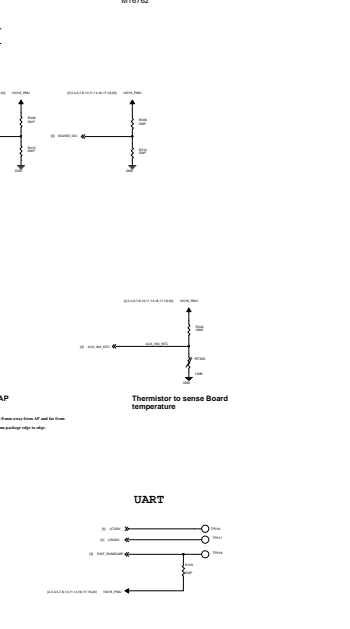
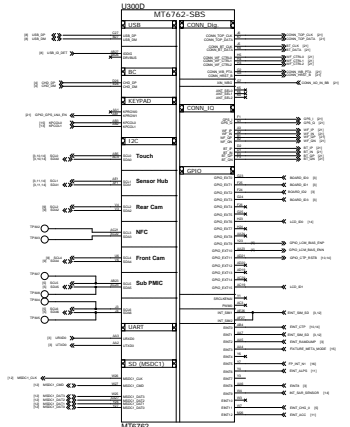
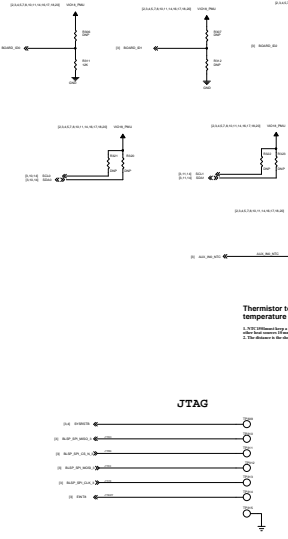
Note 12-5: Please set unused I2 pins to NC.

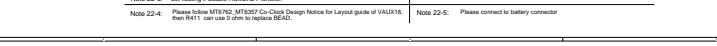


Schematic design notice of "13_BB_2" page.

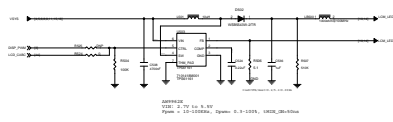
Note 13-1: The enable pin of acoustic or optoelectronic devices (e.g. SPK, AMP, Backlight/Charger, COFOT) is suggested to use PWRAP, CSD or GND.

If use other OPIN as enable pin, suggest to reserve GND1 NC to GND.

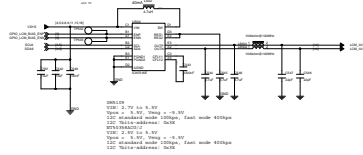




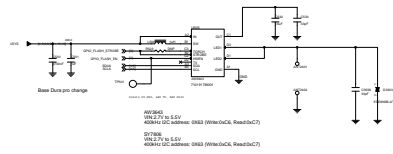
LCM Backlight LED driver



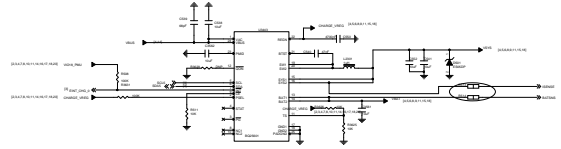
LCD Bias



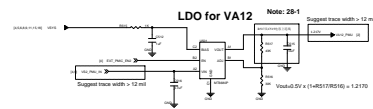
Flash LED 5V Boost



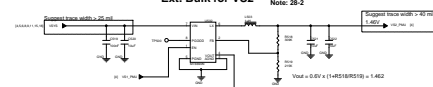
Charger



base MT6762 ref-SCH



base MT6762 ref-SCH



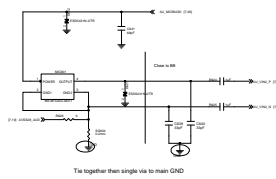
Schematic design notice of "28_POWER_ThirdParty-Power"

Note 28-1: VA12 Layout placement please close to AP

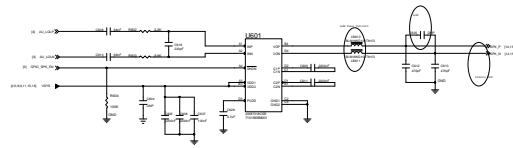
Note 28-2: VS2 Bulk Layout placement please close to PMIC MT6762

Note 28-3: VCN33 LDO Layout placement please close to MT6762

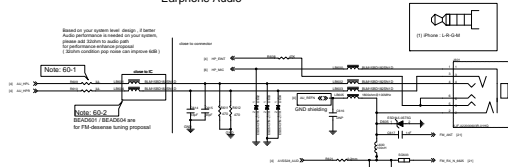
Handset 2nd Microphone



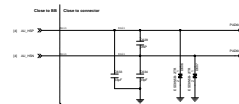
SPEAKER base M3901-P 0626



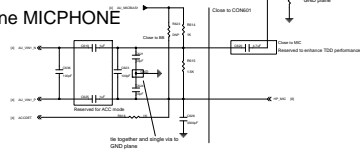
Earphone Audio base M3901-P 0626

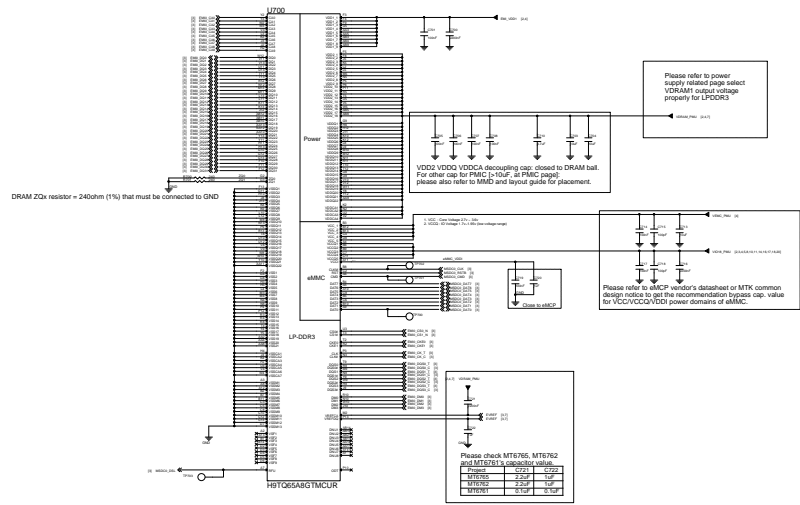


EAR PIECE

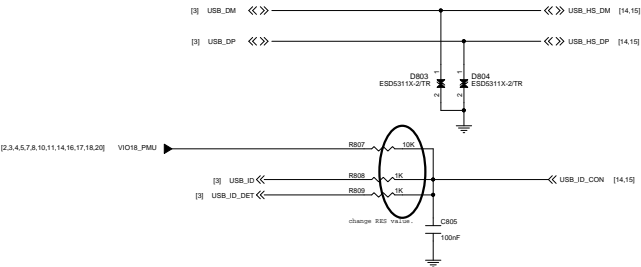
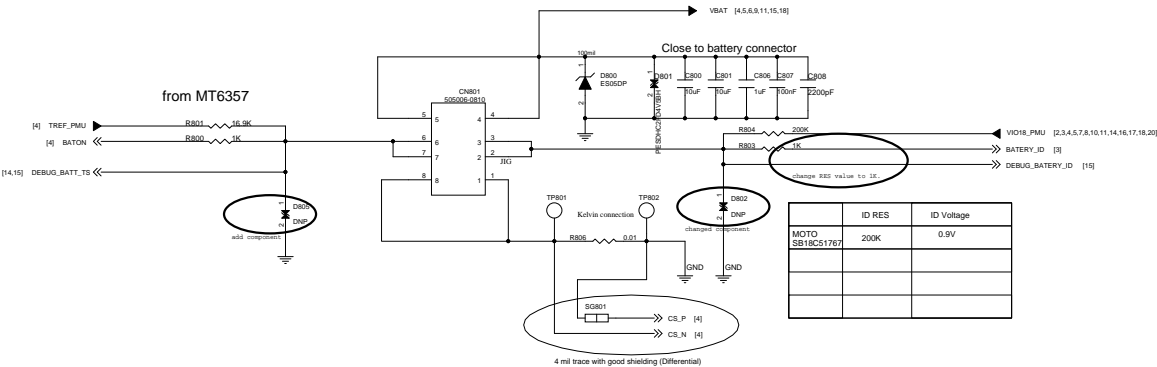


Earphone MICPHONE

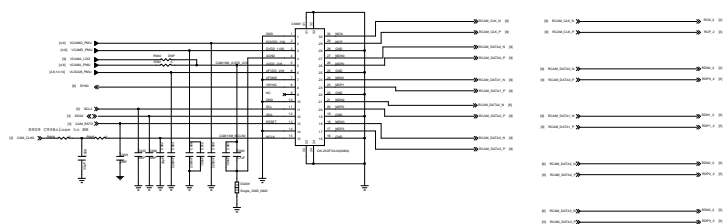




BATTERY CONNECTOR



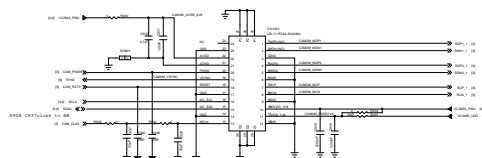
Rear-Main-AF Camera 13M



Rear Camera - 13M

Rear camera SHINTECH(S5K3L6XX03-FGXG) I2C address: (Write:0x20, Read:0x21)
AF driver xxxx I2C address (Write:xxxx, Read:xxxx)

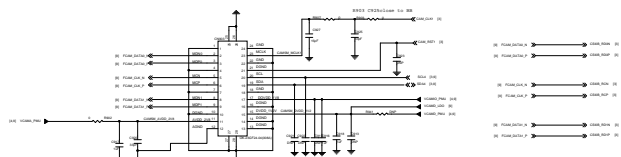
Rear-Slave-FF Camera 2M



Rear Slave Camera - 2M

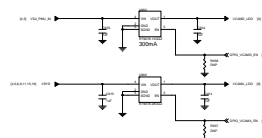
Rear camera SHINETECH(GC2375-C23Y0) I2C address: (Write:0x2E, Read:0x2F)

Front-FF-5M Camera Connector

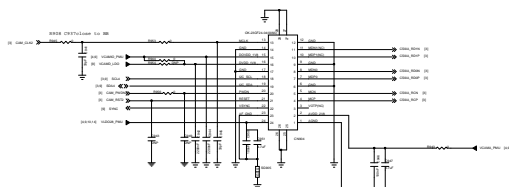


Front Camera - 5M

```
first camera xxxx(xxxx) I2C address: (Write-0x50 Read-0x51)
```



Rear-Slave-AF Camera 2M

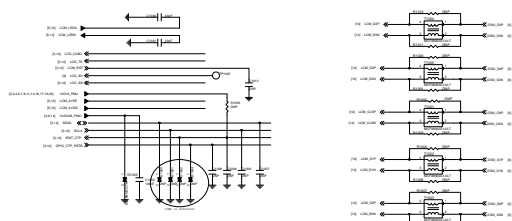


Rear Slave AF Camera - 2ME

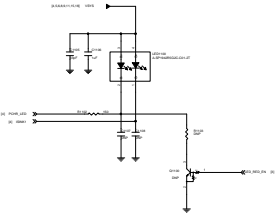
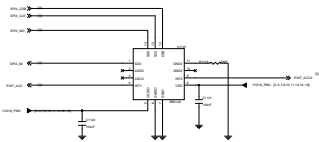
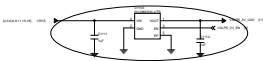
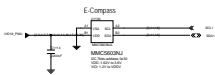
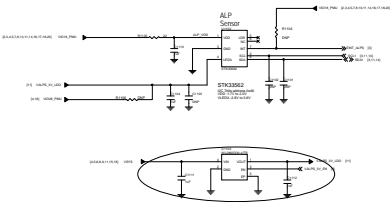
Rear camera SHINETECH/GC2375-C23Y0) IC address: (Write 0x2E, Read 0x2F)

ONTIM Technologies Ltd.	
Tax: 44008	
Particulars: 88-04584	

LCM Connector

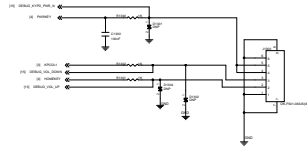


GT1151 I2C address: 0x5D (Write:0xBA, Read:0xBB)
or 0x14 (Write:0x28, Read:0x29)



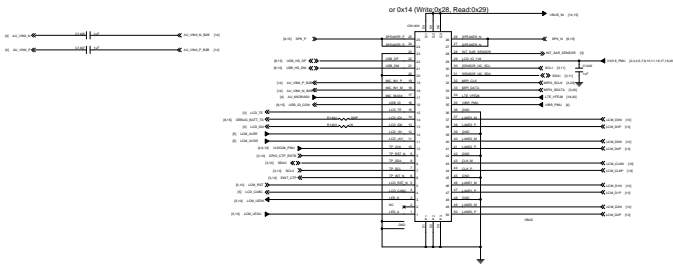
KEY base M3901-P 0626

Volume Up : HOME Key / OND
Volume Down : KP/COL F1/END
DO NOT put pull-up resistor on PWRKEY

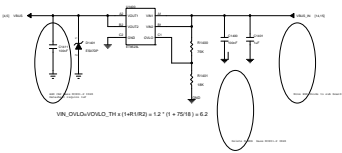


base M3901-P 0626

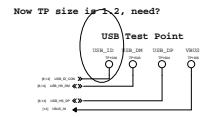
LCM Connector



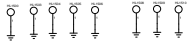
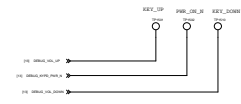
OVP



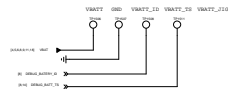
USB Test point



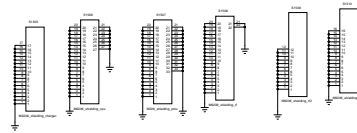
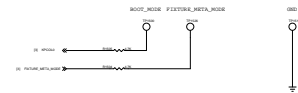
KEY Test point



BATTERY Test point

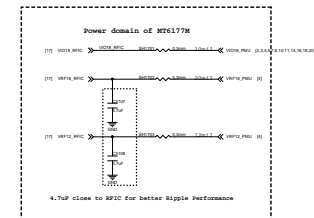


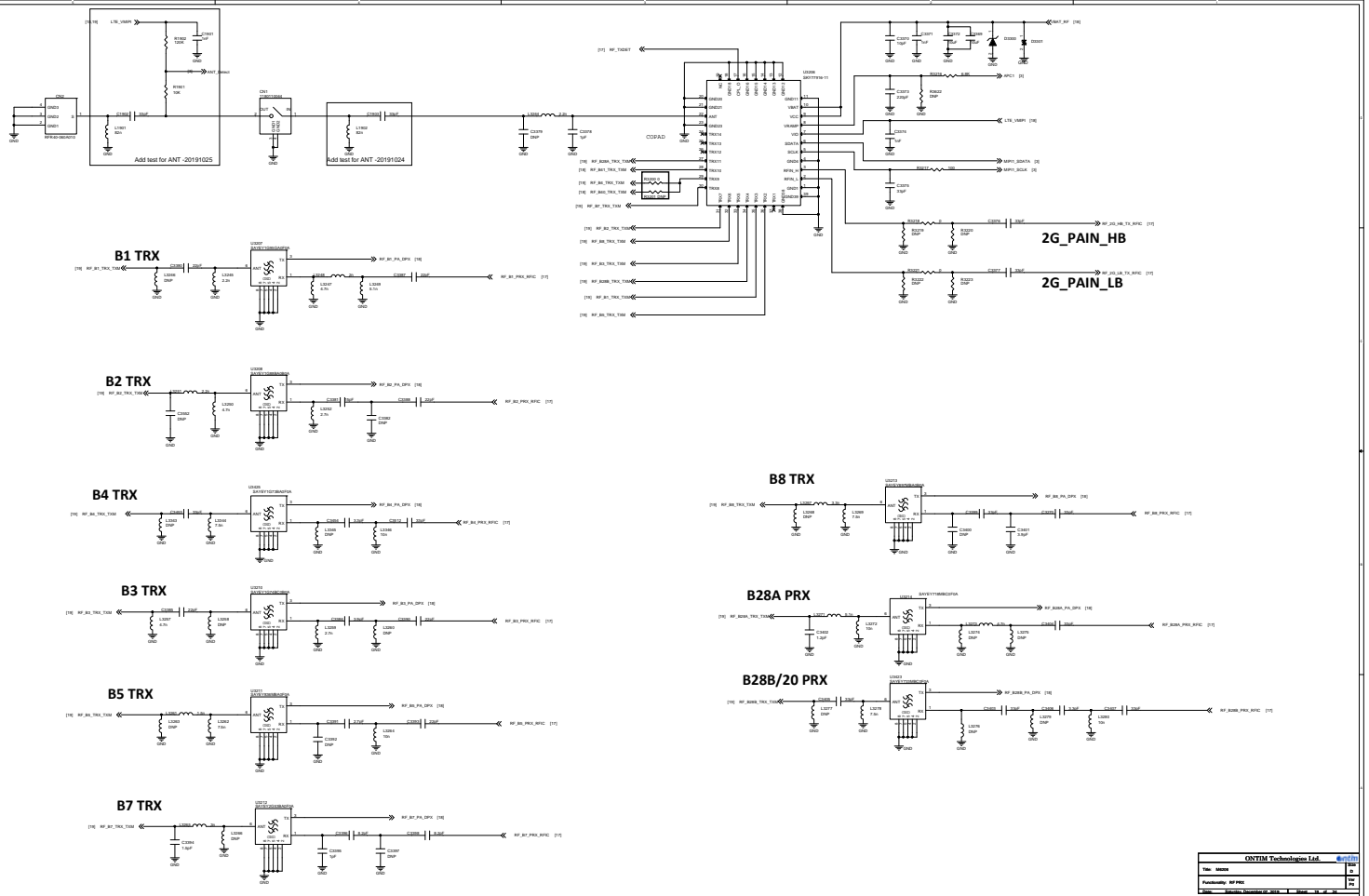
Test point

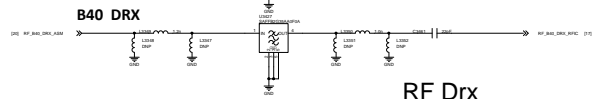
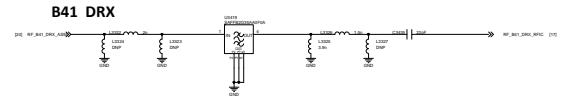
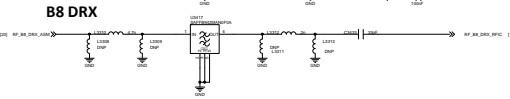
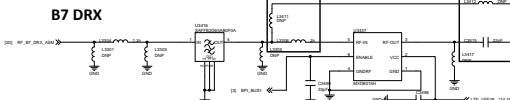
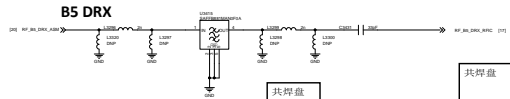
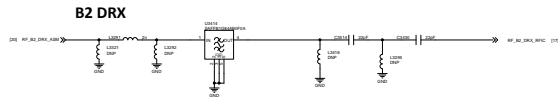
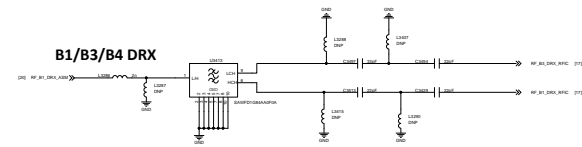
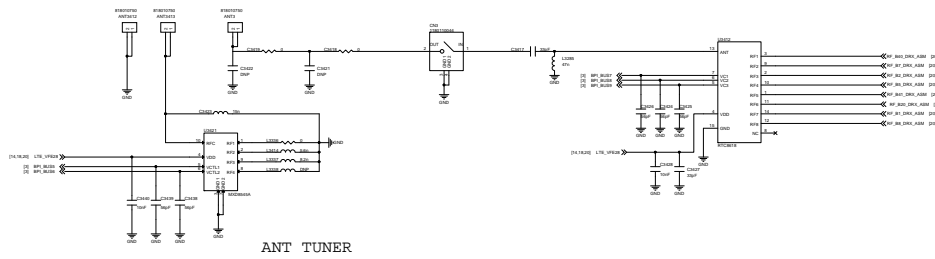


SPK Test point









RF Drx

