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# M32 5G M326B REV03A

SM-M326B  
Common

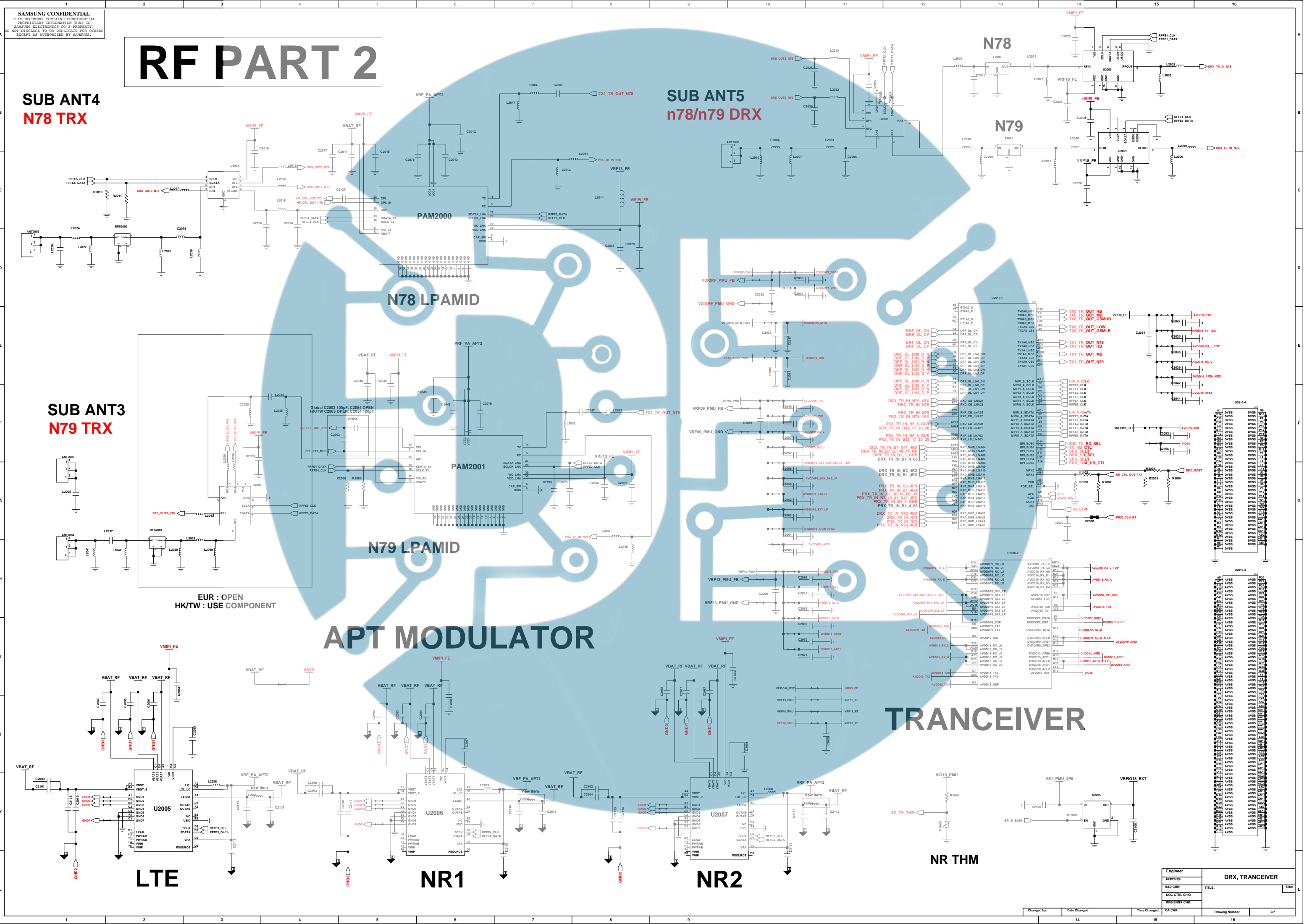
2020.12.07(SMD)

- sheet01 : GSM / WCDMA / LTE / 5G sub6 RF
- sheet02 : TRANCEIVER, DRX
- sheet03 : SENSORS,CONNECTIVITY
- sheet04 : AP(MT6853)
- sheet05 : PMIC(Main,IF), KEY, MOTOR, UFS
- sheet06 : AUDIO, SD/SIM, SHIELD CAN, IF CON
- sheet07 : CAMERA, DISPLAY

	A32_5G_RF_feature			
NR	n1/n3/n5/n7/n8/n20/n28 n38/n40/n41/n78/n79			
FDD	1/2/3/4/5/7/8/12/17/20/26/28/66			
TDD	38/ 40/ 41	4X4 MIMO	1/3/7/41/n78/n79	
3G	1/2/4/5/8	SRS	n41/n78/n79	
2G	850/ 900/ 1800/ 1900	Mid-Mid ENDC	n1/n3	

Engineer:			
Drawn By:			
R&D CHK:	TITLE: SM-A515FM		Size:
DOC CTRL CHK:			
MFG ENGR CHK:			
QA CHK:	REV:	Drawing Number:	Page: 6





1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
A															
B															
C															
D															
E															
F															
G															
H															
I															
J															
K															
L															



**BT/WIFI  
GPS**

# NFC

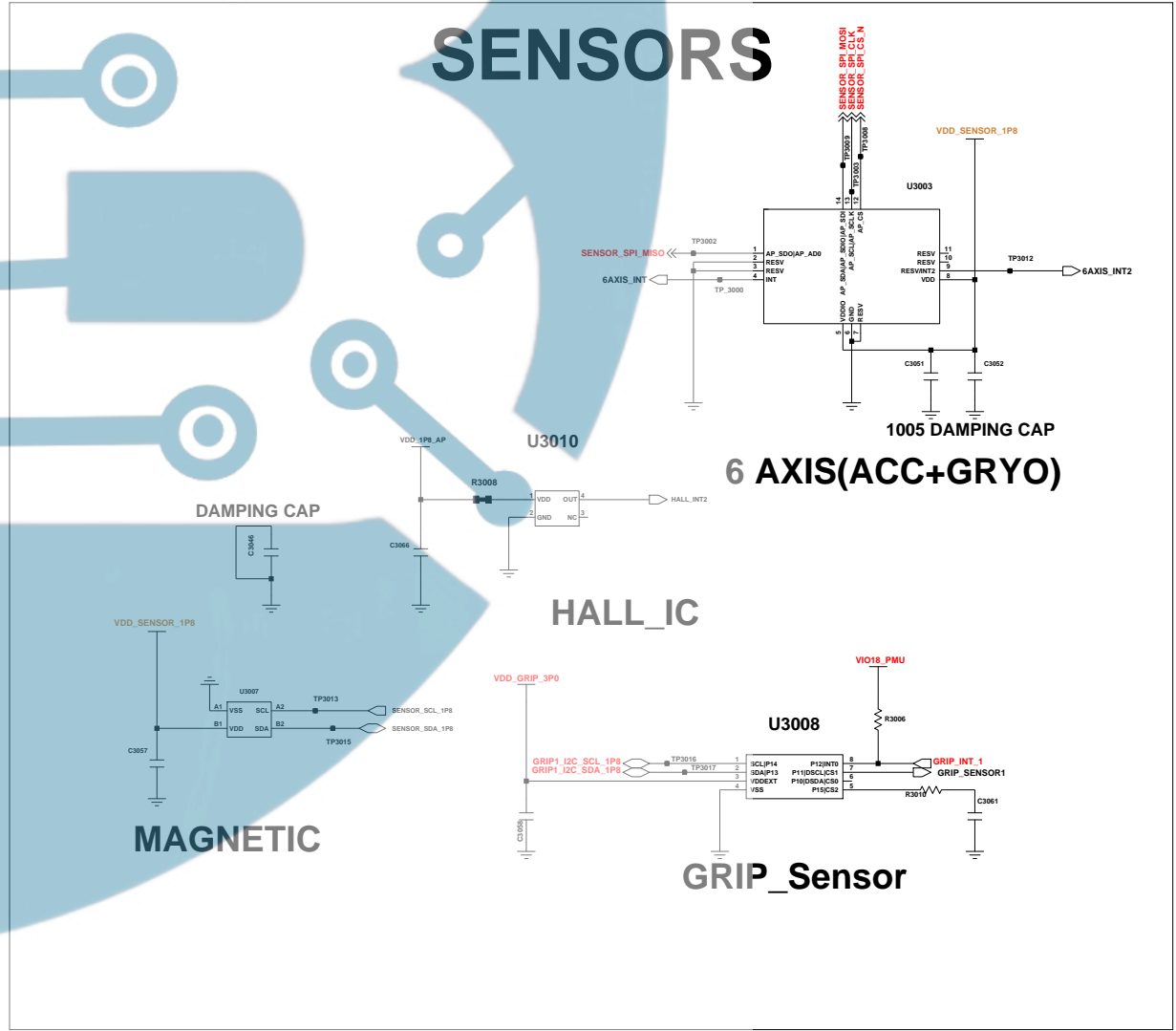
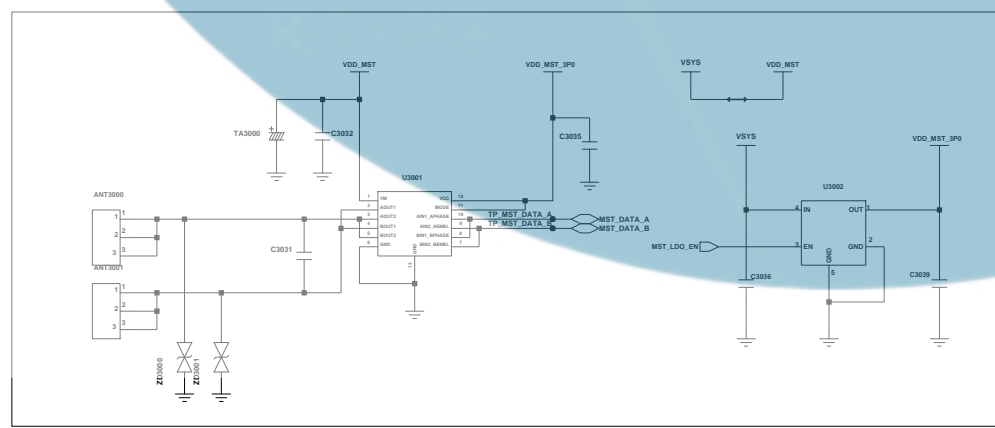
# SENSORS

# MST - KOR only

## MAGNETIC

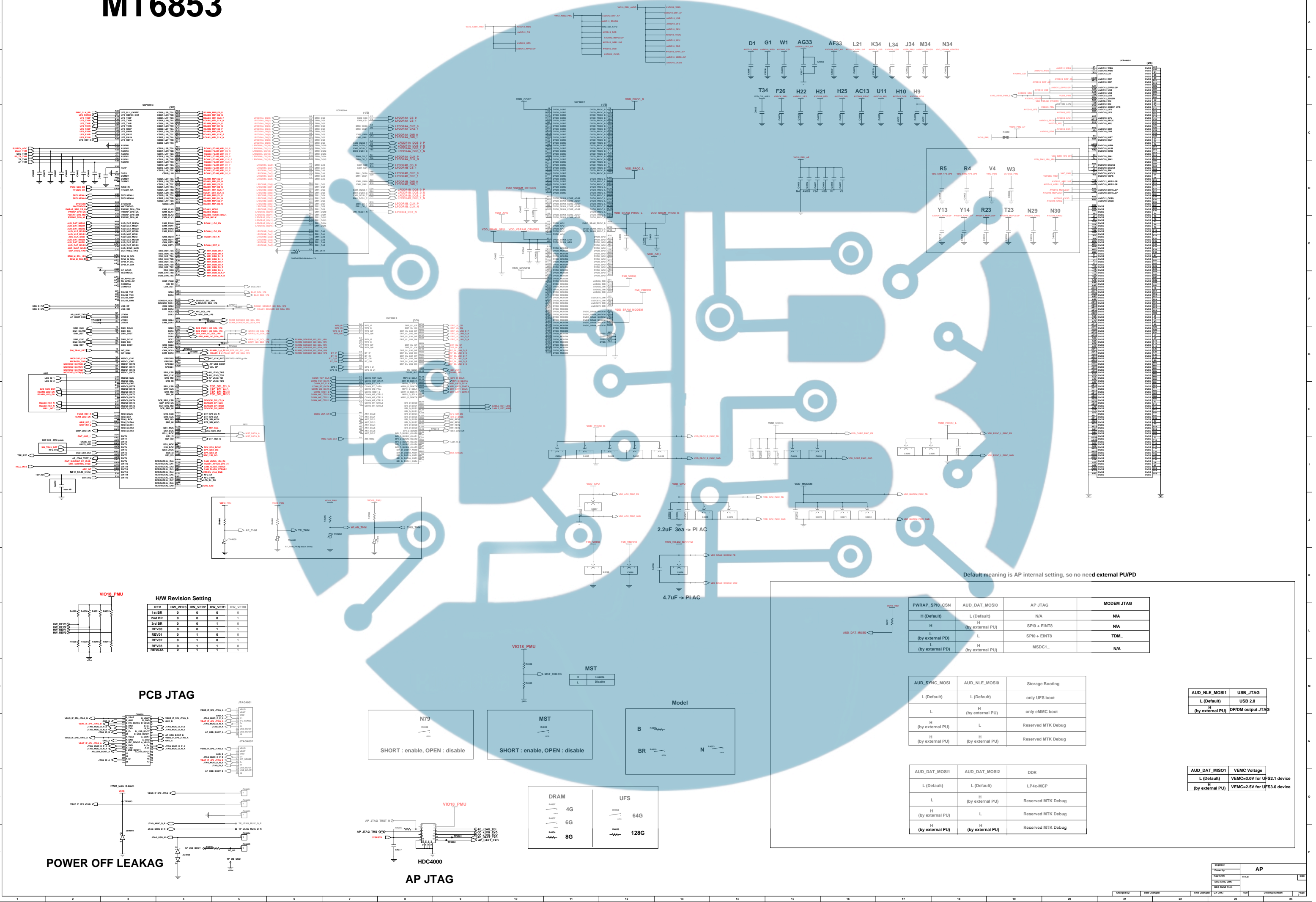
## GRIP\_Sensor

**BTP**



Engineer:	SENSOR, WIFI/BT/GPS/NFC				
Drawn by:					
R&D CHK:				TITLE:	Size:
DOC CTRL CHK:					
MFG ENGR CHK:					
QA CHK:	REV:	Drawing Number:	Page: 37		

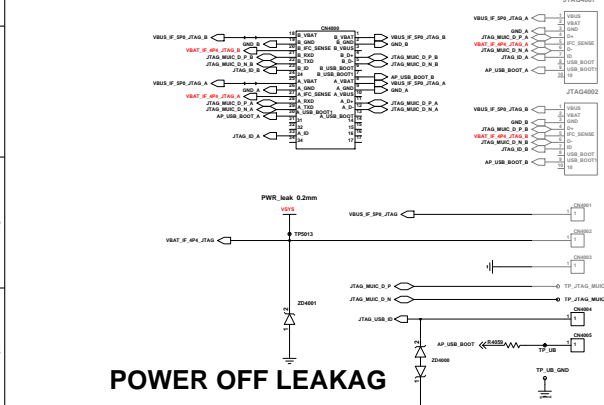
# MT6853



**H/W Revision Setting**

REV	HW_VER1	HW_VER2	HW_VER3	HW_VER0
1st BR	0	0	0	0
2nd BR	0	0	0	1
3rd BR	0	0	0	1
REV00	0	0	1	1
REV01	0	1	0	0
REV02	0	1	0	1
REV03	0	1	1	0
REV04	0	1	1	1

## PCB JTAG



## POWER OFF LEAKAG

## AP JTAG

**Default meaning is AP internal setting, so no need external PU/PD**

PWRAP_SPI0_CSN	AUD_DAT_MOSI0	AP JTAG	MODEM JTAG
H (Default)	L (Default)	N/A	N/A
H	H (by external PU)	SPI0 + EINT8	N/A
L (by external PD)	L	SPI0 + EINT8	TDM
L (by external PD)	H (by external PU)	MSDC1	N/A

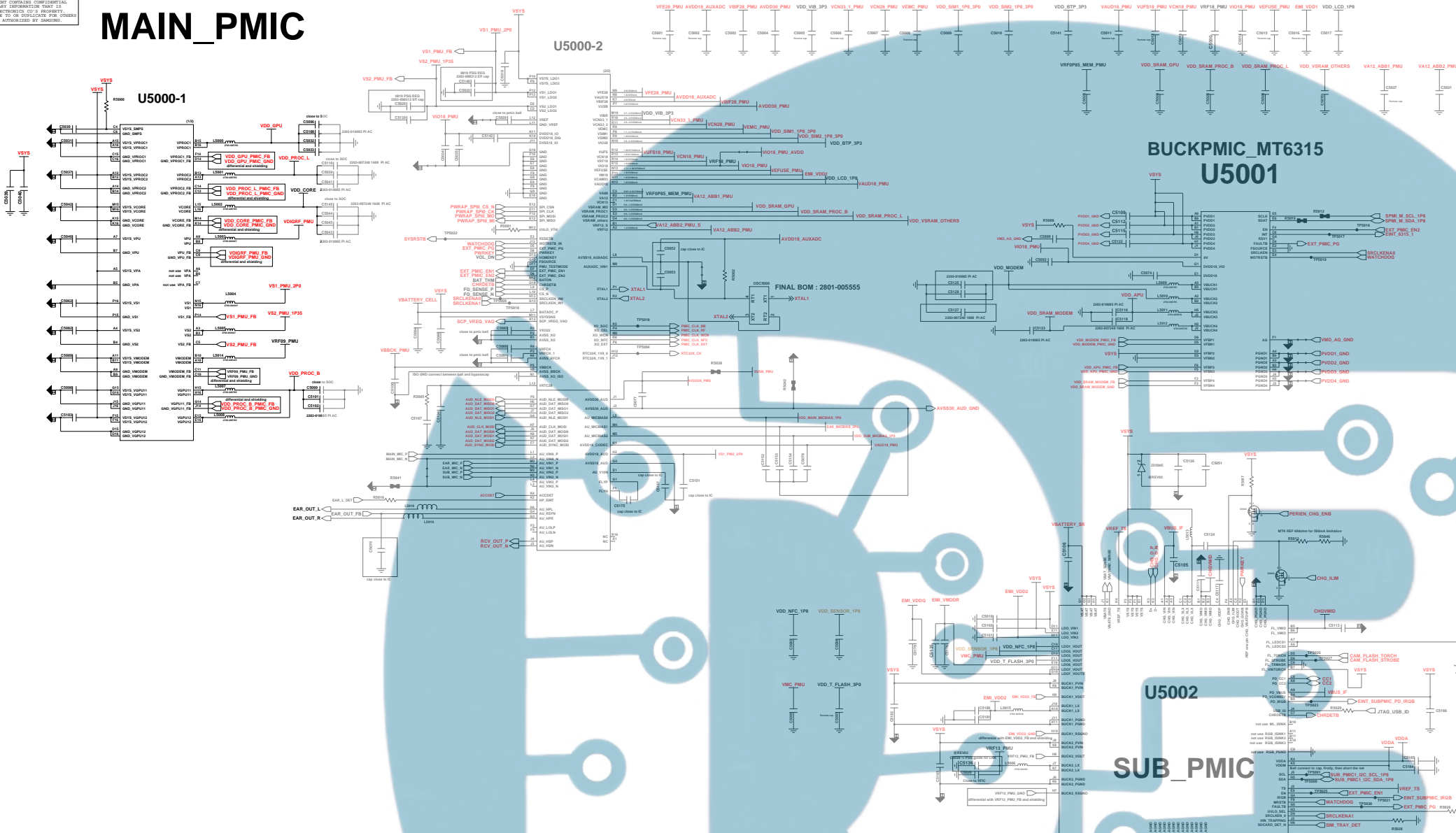
AUD_SYNC_MOSI	AUD_NLE_MOSI0	Storage Booting
L (Default)	L (Default)	only UFS boot
L	H (by external PU)	only eMMC boot
H (by external PU)	L	Reserved MTK Debug
H (by external PU)	H (by external PU)	Reserved MTK Debug

AUD_DAT_MISO1	AUD_DAT_MOSI2	DDR
L (Default)	L (Default)	LP4x-MCP
L	H (by external PU)	Reserved MTK Debug
H (by external PU)	L	Reserved MTK Debug
H (by external PU)	H (by external PU)	Reserved MTK Debug

AUD_NLE_MOSI1	USB_JTAG
L (Default)	USB 2.0
H (by external PU)	DP/DM output JTAG

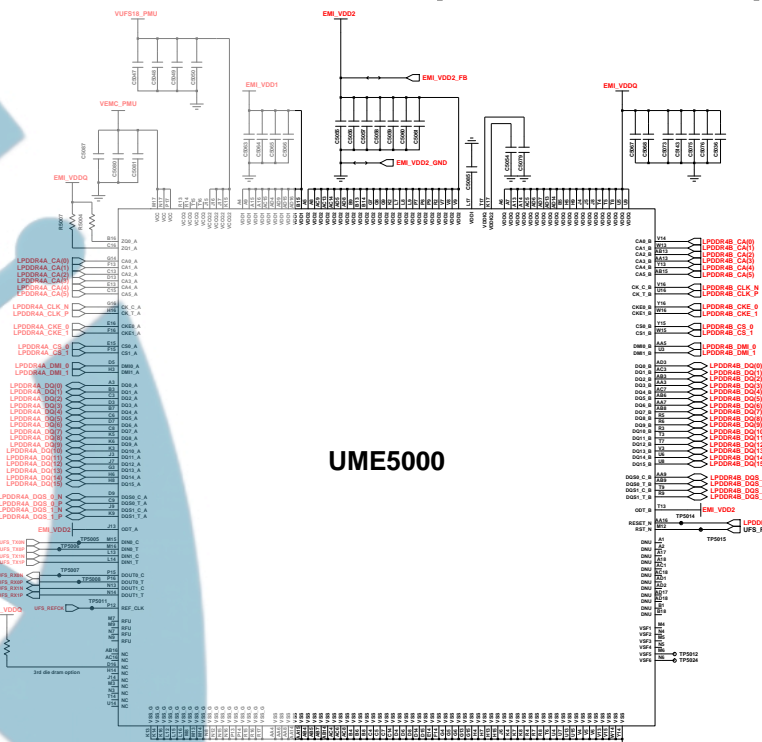
AUD_DAT_MISO1	VEMC Voltage
L (Default)	VEMC=3.0V for UFS2.1 device
H (by external PU)	VEMC=2.5V for UFS3.0 device

# MAIN\_PMIC



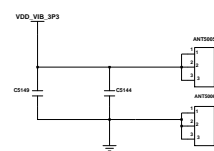
# UFS MEMORY

DV : 4 + 64 SEC V4  
PV : 4 + 64 SEC V6  
PR : 8 + 128 SEC(1108-000675)

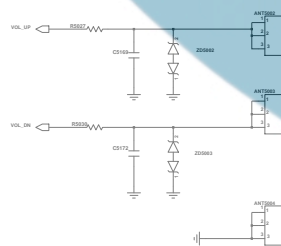


## UME5000

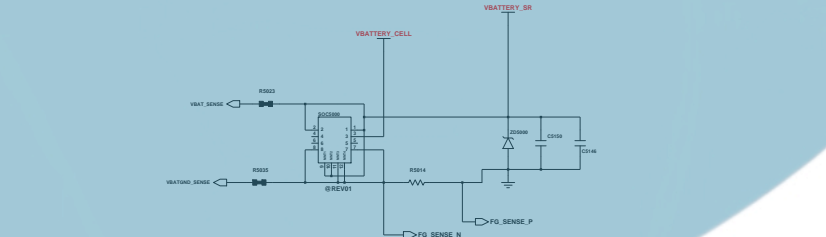
## MOTOR



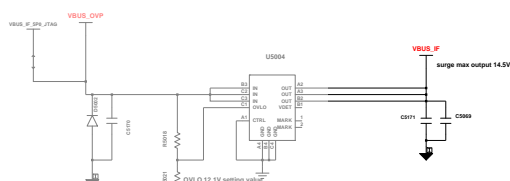
## VOLUME KEY



# BATTERY CON

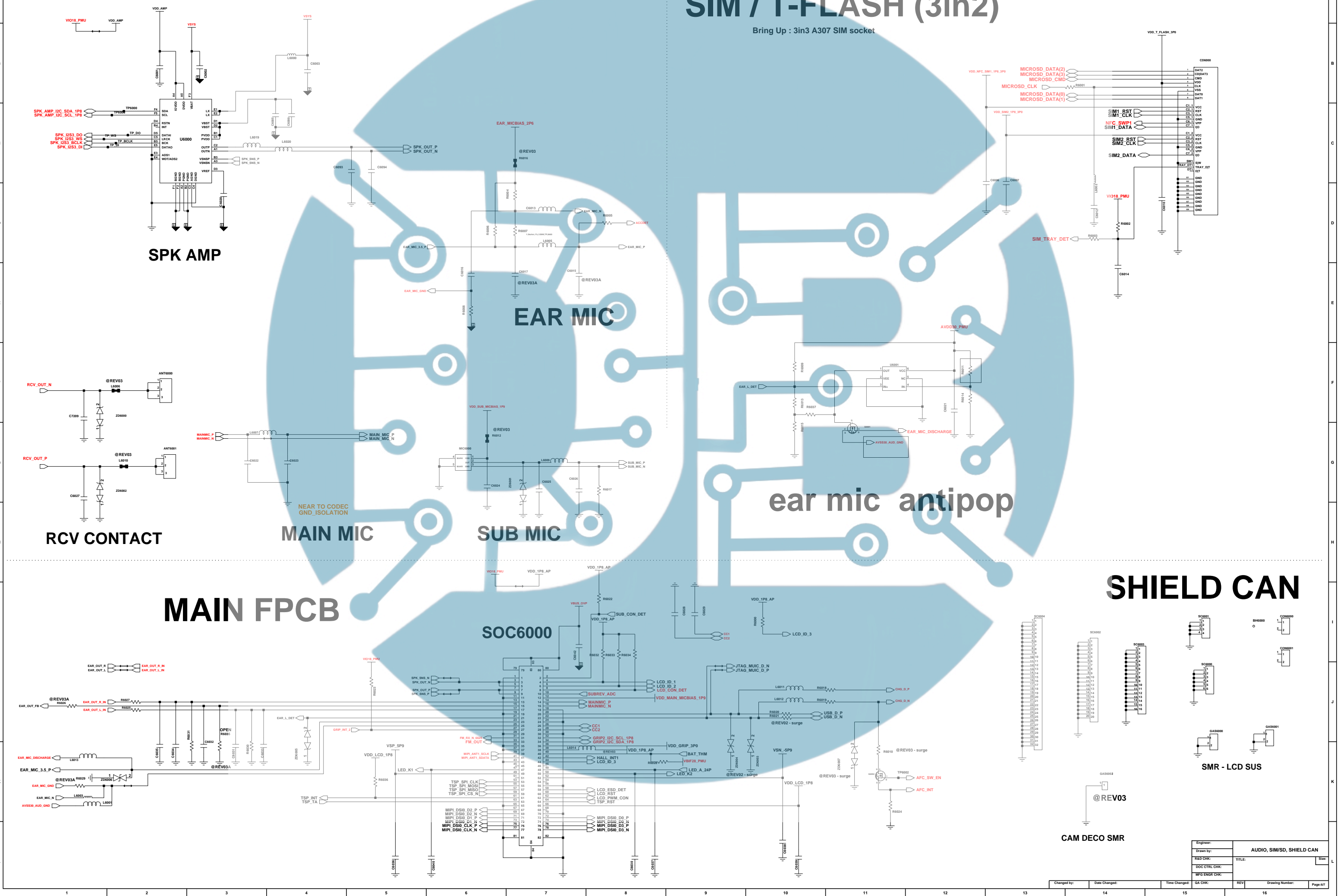


## OVP CIRCUIT (FOR FAST CHARGING)

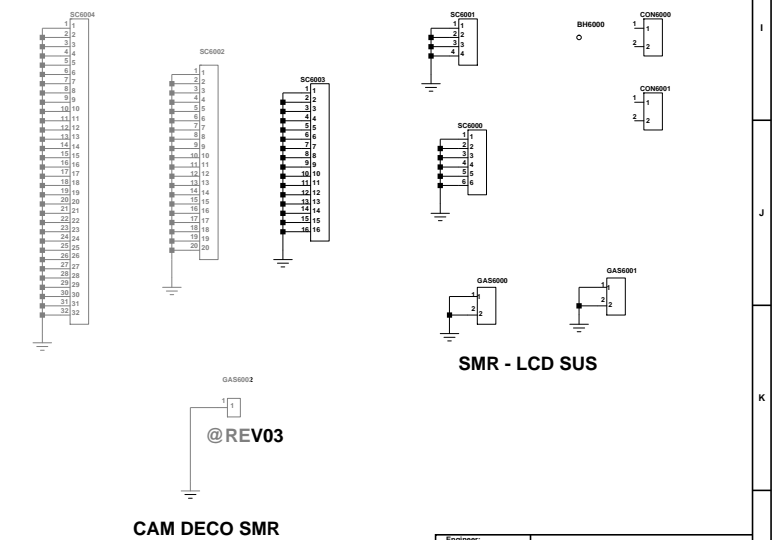




Bring Up : 3in3 A307 SIM socket



# SHIELD CAN



SMR - LCD SUS

CAM DECO SMR

Engineer:		AUDIO, SIM/SD, SHIELD CAN	
Drawn by:			
R&D CHK:		TITLE:	Size:
DOC CTRL CHK:			
MFG ENGR CHK:			
ged:	QA CHK:	REV:	Drawing Number:
			Page: 6/7

