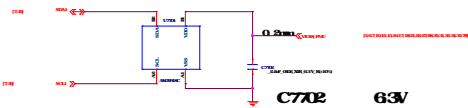
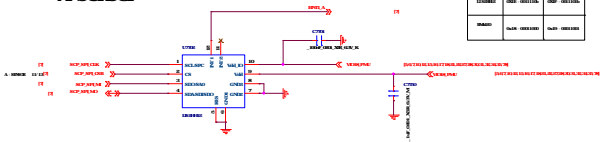


MSensor

	12C ADDRESS
ADDRESS	0ch
OPTION IN	2ch



A sensor

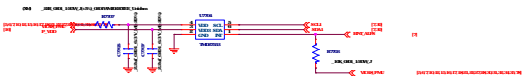


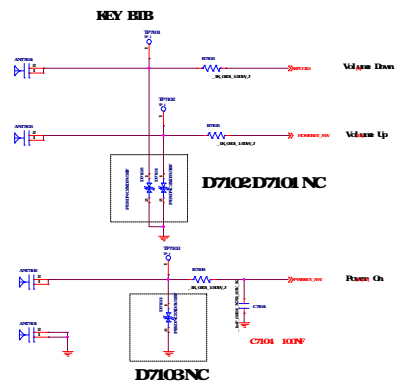
	12C ADDRESS
ADDRESS	0ch
OPTION IN	2ch

PSensor

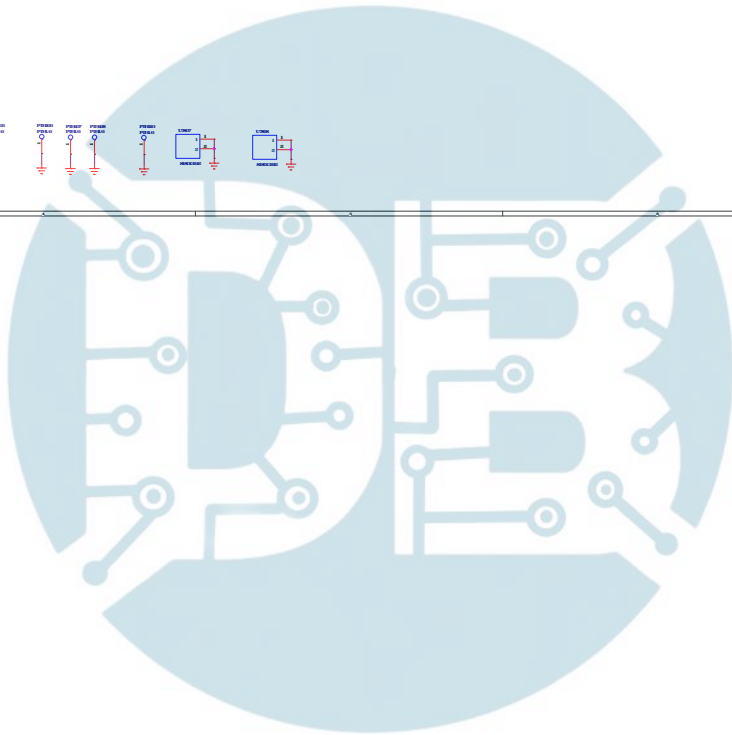
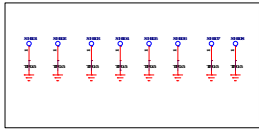
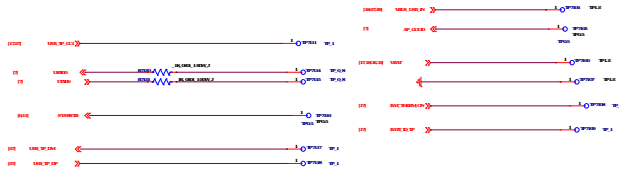
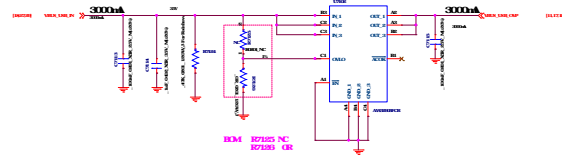
	12C ADDRESS
ADDRESS	0ch
OPTION IN	2ch

ALS&PS



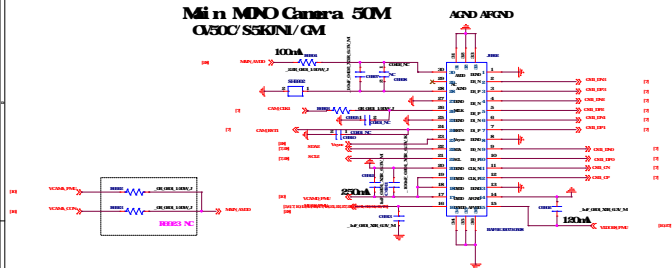


OMP

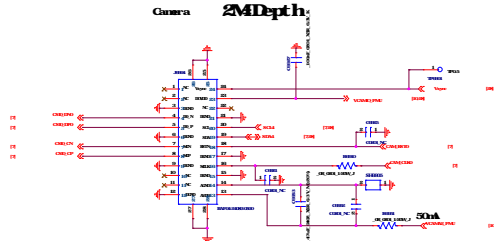


Min MDO Camera 50M O50C/S50N/GM

AGND/AFND

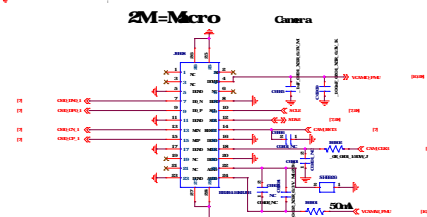


Camera 2MDepth



2M=Micro

Camera

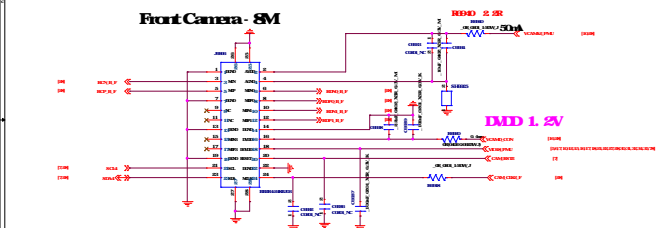


Front Camera - 8M

R800 2.2R

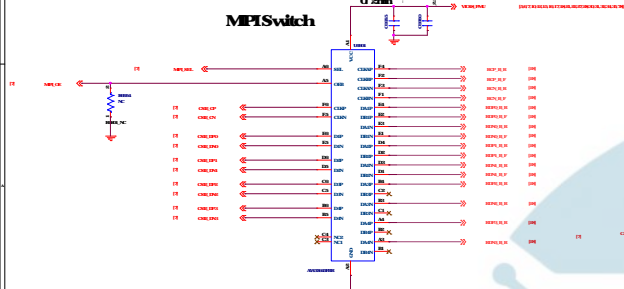
50k

DMD 1.2V



MPISwitch

0.2mm



CLK Front 8M

R8007 R8008 closed

R8003 NC

R8004 NC

R8005 NC

R8006 NC

R8007 NC

R8008 NC

R8009 NC

R8010 NC

R8011 NC

R8012 NC

R8013 NC

R8014 NC

R8015 NC

R8016 NC

R8017 NC

R8018 NC

R8019 NC

R8020 NC

R8021 NC

R8022 NC

R8023 NC

R8024 NC

R8025 NC

R8026 NC

R8027 NC

R8028 NC

R8029 NC

R8030 NC

R8031 NC

R8032 NC

R8033 NC

R8034 NC

R8035 NC

R8036 NC

R8037 NC

R8038 NC

R8039 NC

R8040 NC

R8041 NC

R8042 NC

R8043 NC

R8044 NC

R8045 NC

R8046 NC

R8047 NC

R8048 NC

R8049 NC

R8050 NC

R8051 NC

R8052 NC

R8053 NC

R8054 NC

R8055 NC

R8056 NC

R8057 NC

R8058 NC

R8059 NC

R8060 NC

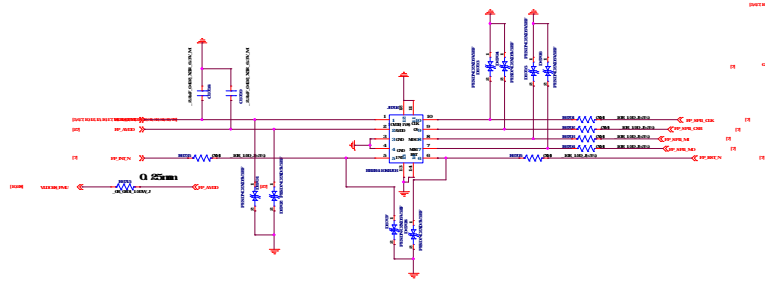
R8061 NC

R8062 NC

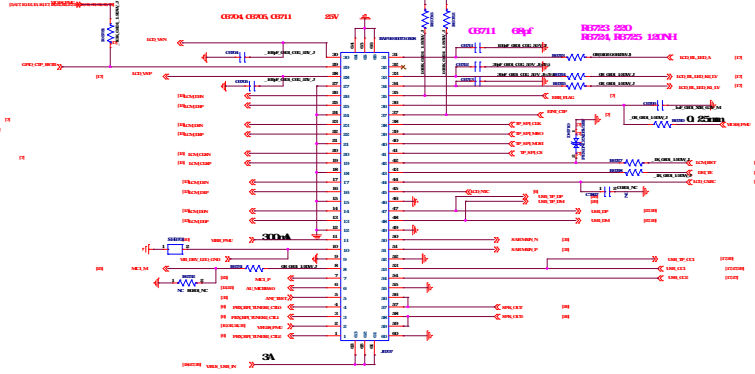
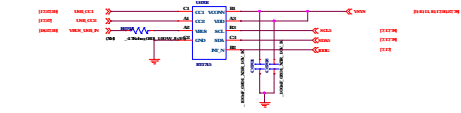
R8063 NC

R8064 NC

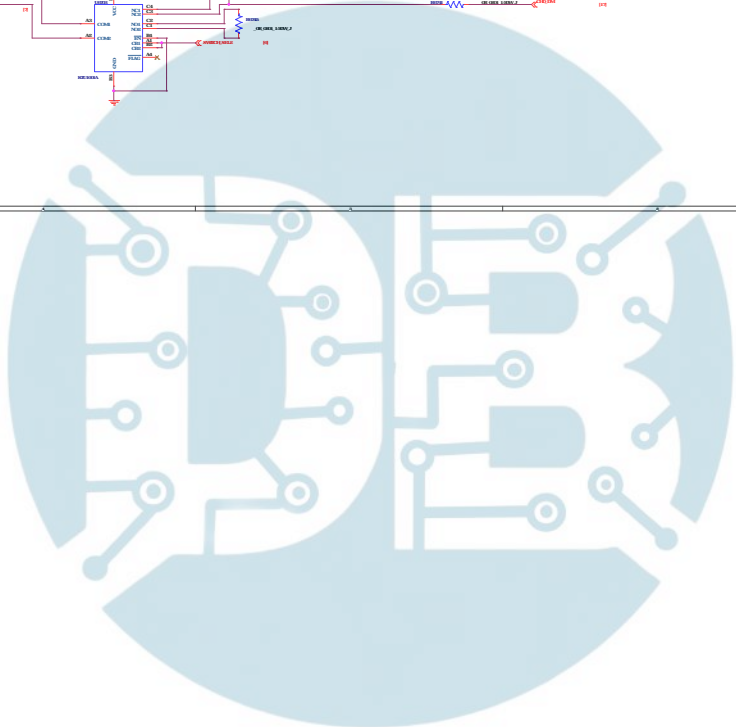
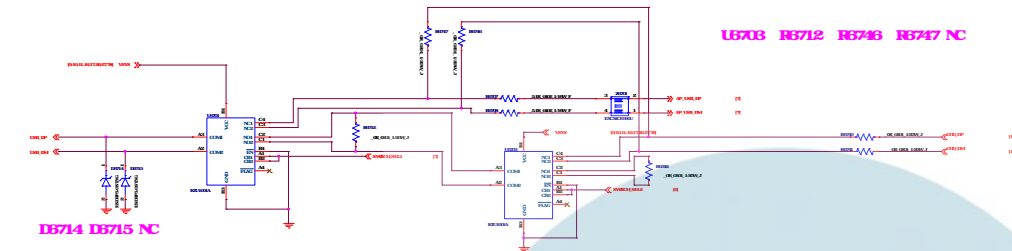
Right Pin



ED

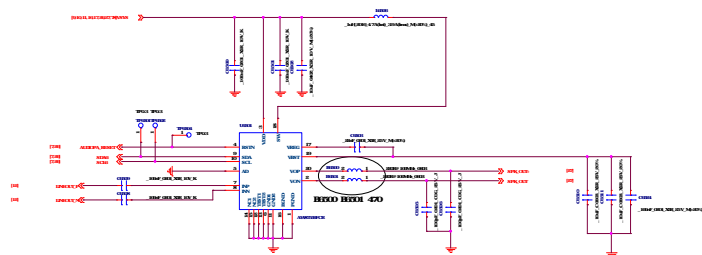


U6703 R6712 R6746 R6747 NC

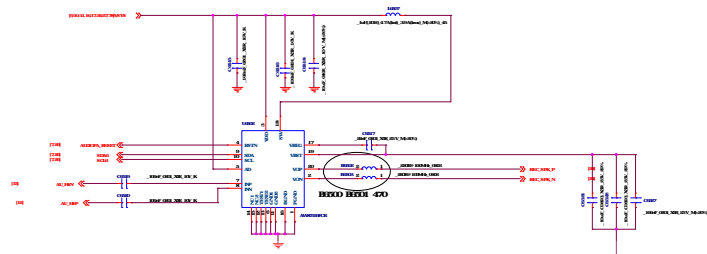


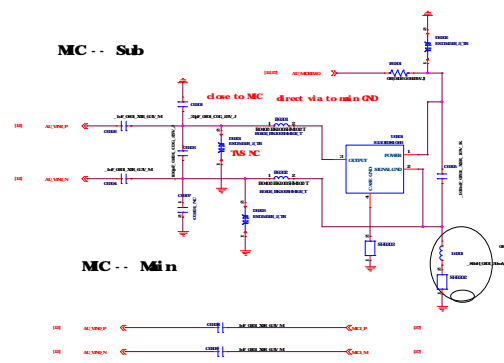
SPEAKER

	DATE RECEIVED
ADDRESS	CITY (7 DIGIT)



REC&SPEAKER

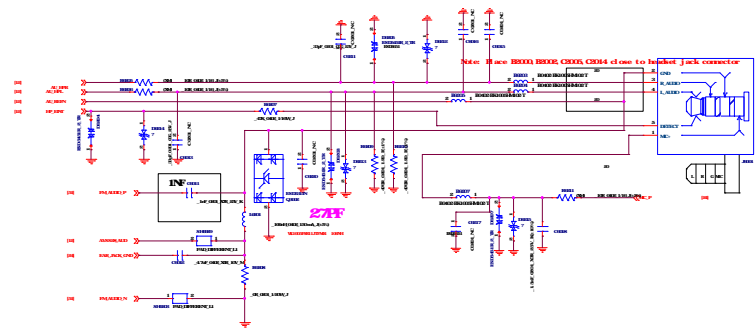
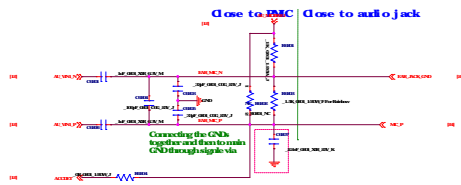




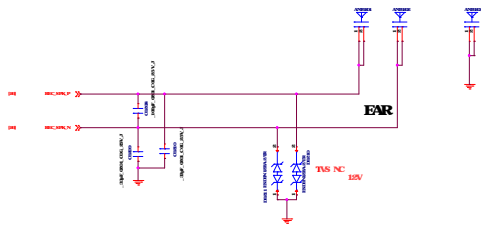
Schematic design notice of "03_PRR_AUDIO_IO" page
 Note 03 1: 1 uF for ACC mode

0301VS D8212 D8214 D8213 D8215
0402VS D8205 D8204 D8202 D8207

Earphone Microphone



Receiver



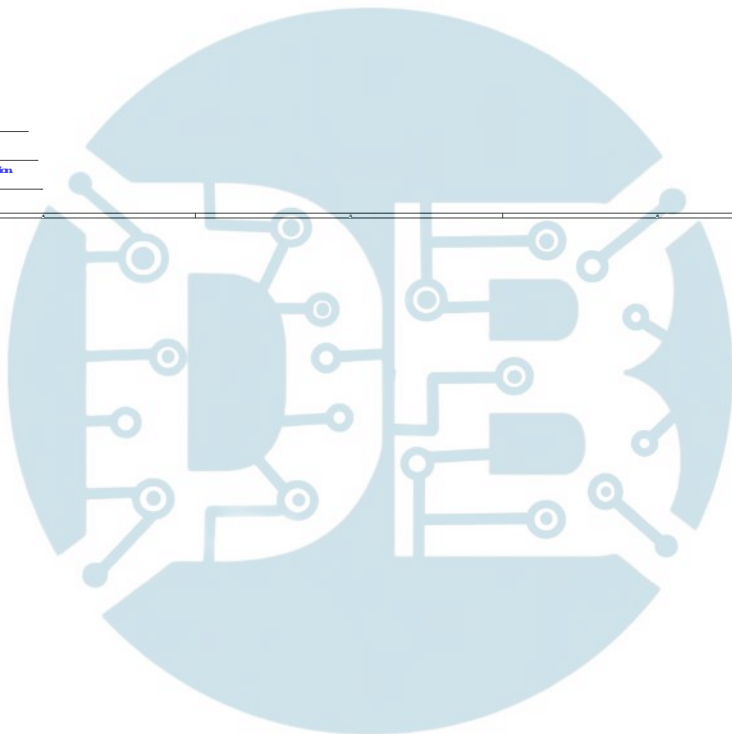
Schematic design notice of "02_PCB_AUDIO" page.

Note 02 1: Part # of BEAD805, BEAD803, BEAD804 and BEAD805 needs changed to "BEM8080SN" for high THD performance (-50dB) but this BOM change will result in FM RSSI 10dB degraded.

Note 02 2: Reserved Gap for CS/IS test, please double check ml1-key function when used

Note 02 3:

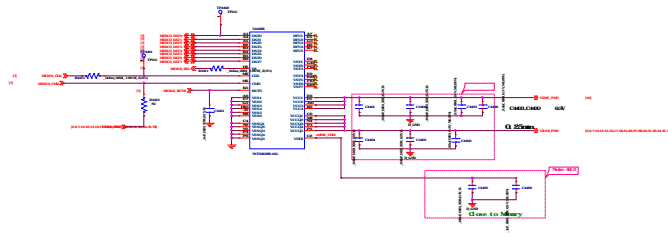
Reference mark	Reference mark
0301VS	0402VS



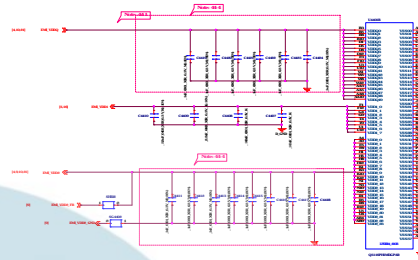
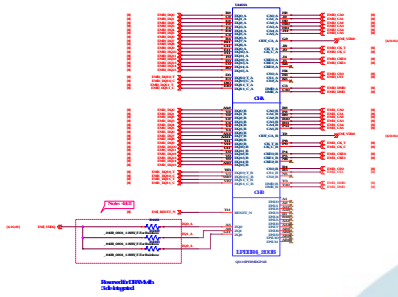
[Schematic design notice of "4G_MEMORY_SD Card" page](#)

For better ESD performance, please select suitable components for system protection

DISCRETE EMC HQ1121708000



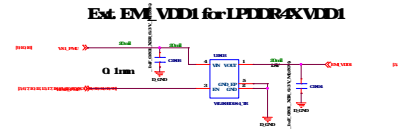
discrete IPX 160 IPX1X



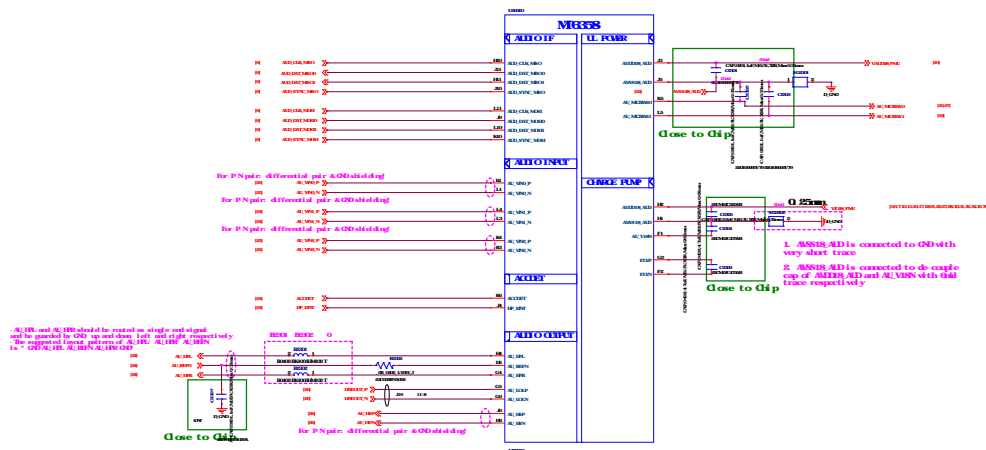
- Schematic design number: 01, Mission: EMC, IPX1X1X
- Note-01-1: Please refer to power supply section for correct VCC/VDD/VDDQ output voltage polarity for IPX1X1X
- Note-01-2: I/O VDDQ pin(s) must be connected to VDDQ
- Note-01-3: Please refer to VCC/VDDQ section for correct VCC/VDDQ/VDDQ output voltage polarity for IPX1X1X
- Note-01-4: VDD/VDDQ pin(s) must be connected to VDD/VDDQ
- Please refer to IPX1X1X pin(s) for correct VCC/VDDQ/VDDQ output voltage polarity for IPX1X1X

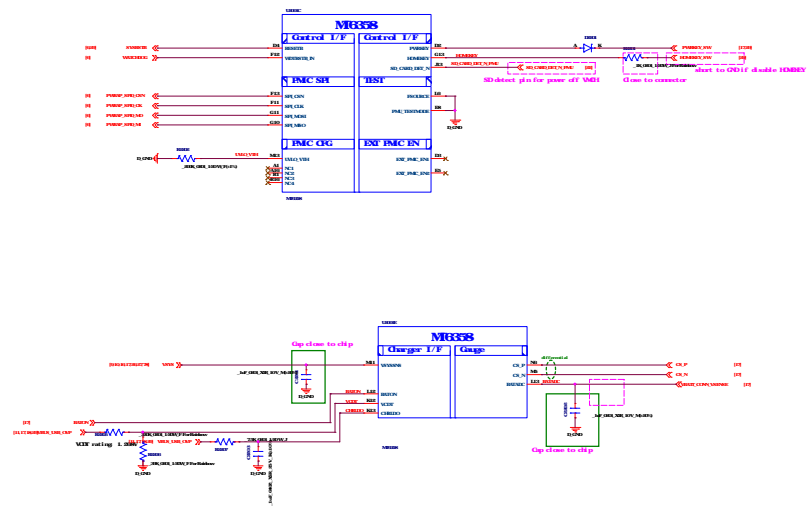
[illegible]

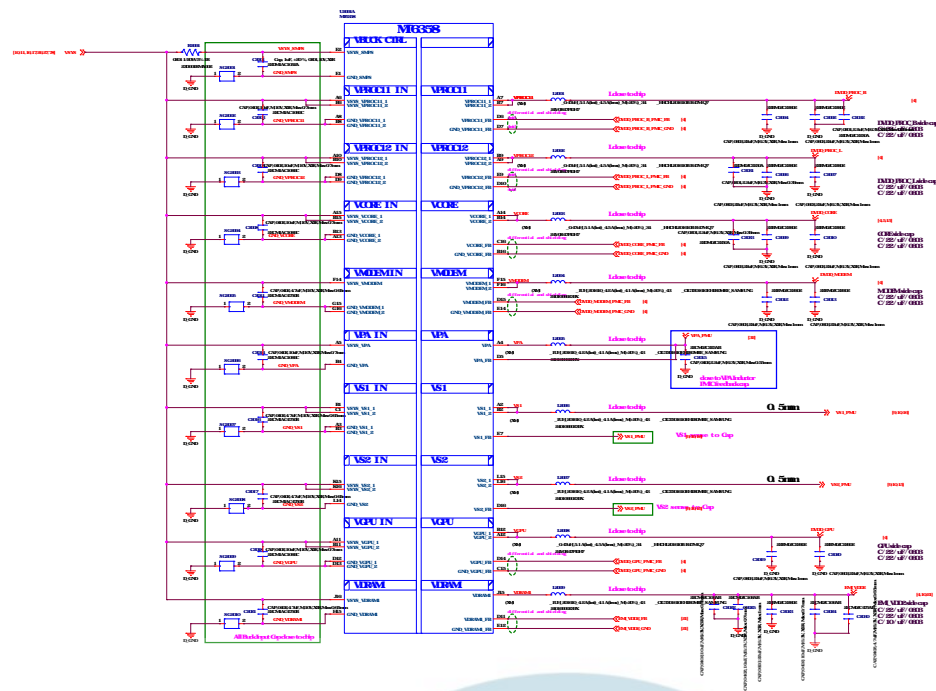
Ext. EM₁² VDD1 for LPDDR4X VDD1

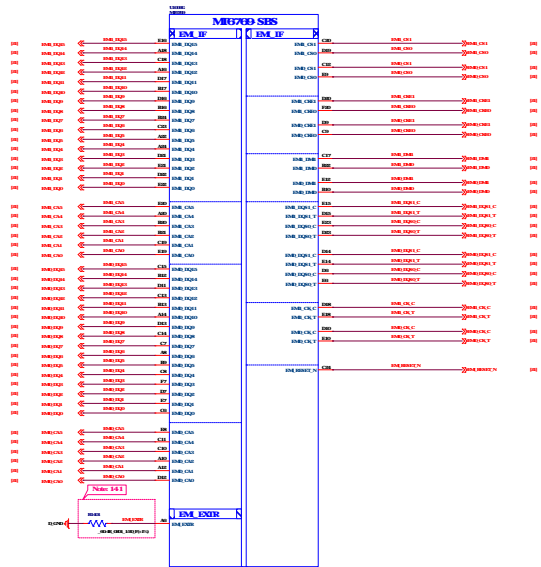
[illegible]

VDD8 3 3V/ 300nA

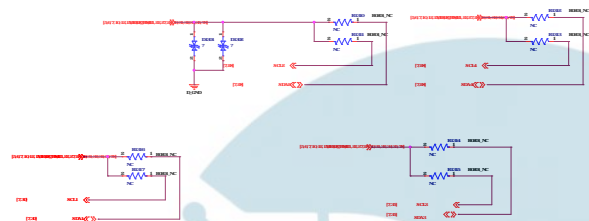
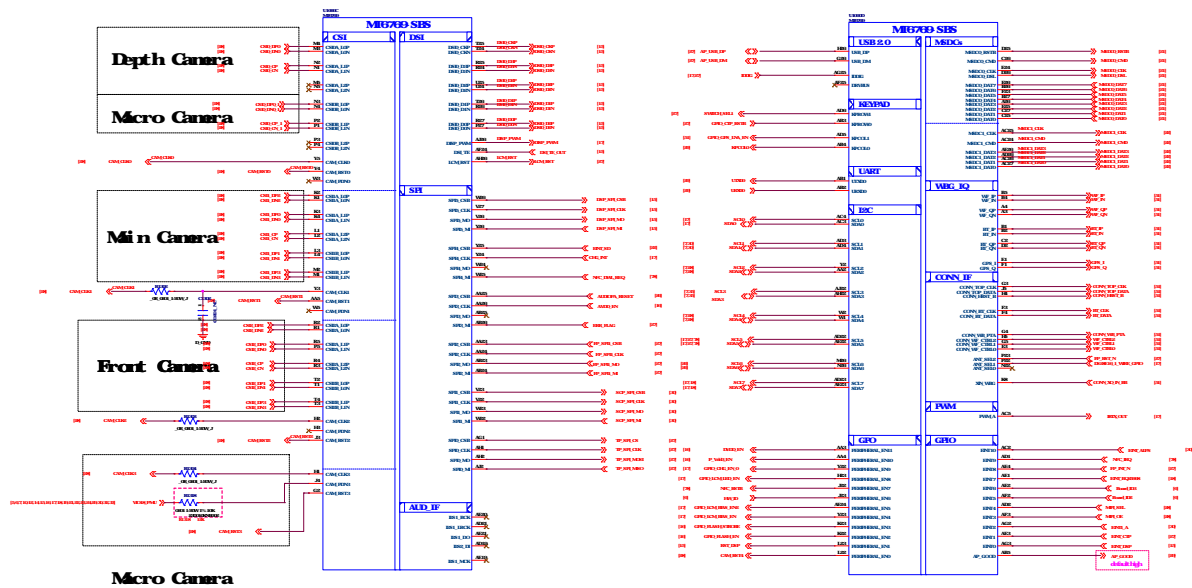


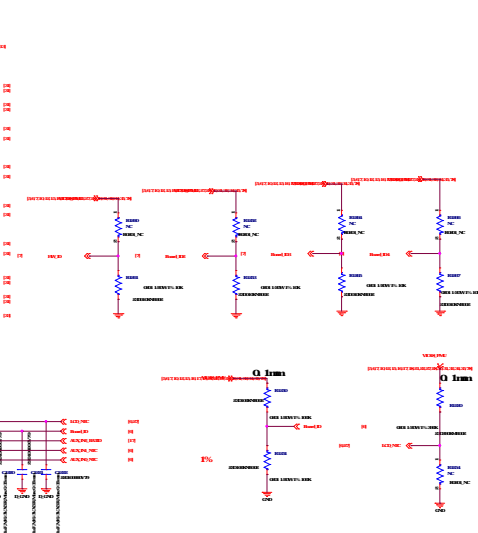
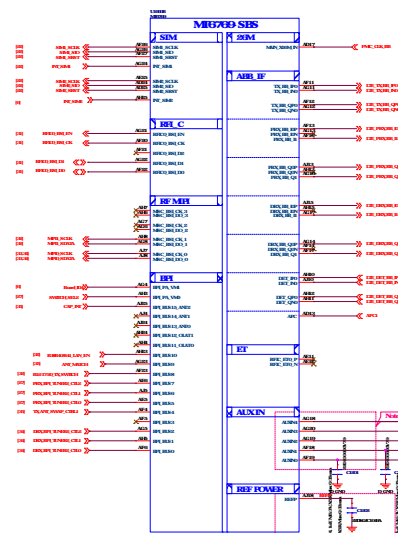
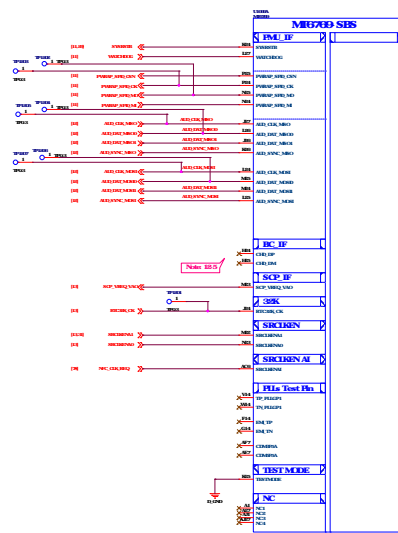






Schematic design of 14-BB-3F page
 Note 14-E: R400 phase select 604 chm(P) resistor





Schematic design notice of "12 BB_1" pages

Note 12-1: PWRP, SPQ, CSN and PWD, DVC, MSEP are located pin to select which interface will be the JTAG pinout.

PWRP, SPQ, CSN	AP, JTAG	JTAG Function
IO	IO	AP, JTAG
IO	IO	SPQ, DVC
IO	IO	SPQ, DVC
IO	IO	SPQ, DVC

Note 12-2: To test a half capacitor in the ALIN/AIC input to prevent noise coupling. It should be placed as close to BB as possible. Connect the unused ALIN/AIC input to GND.

Note 12-3: The decoupling cap for BBP (AT814) has to be placed as close to BB as possible.

Note 12-4: 10V pin for THER type feature is located (refer to BB PM design Note 4)

ALD, SYNC, MBO	ALD, CLK, MBO	CLK, PDS	PMU, THER, MBO	THER Type
IO	IO	IO	IO	THER
IO	IO	IO	IO	THER
IO	IO	IO	IO	THER
IO	IO	IO	IO	THER

Note 12-5: Change to have D-D pin for change type USB detection. Change to have at least 10kΩ USB current for all change type.

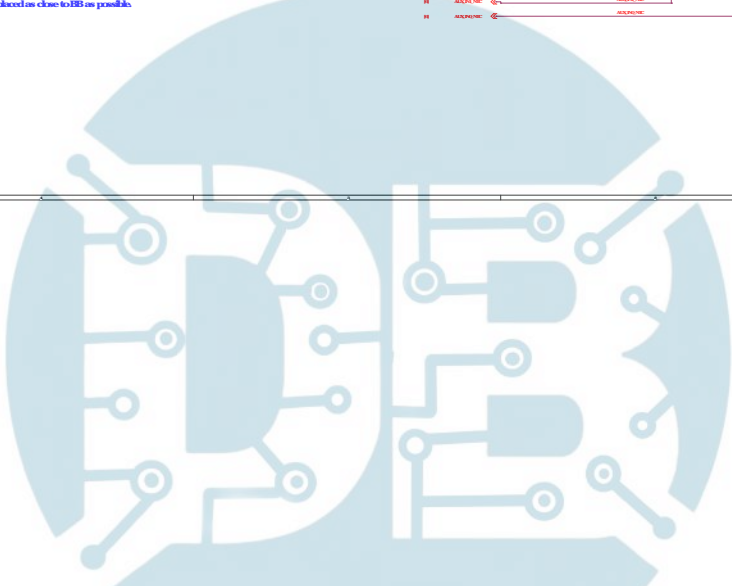
Note 12-6: 10V pin for THER type feature is located (refer to BB PM design Note 4)

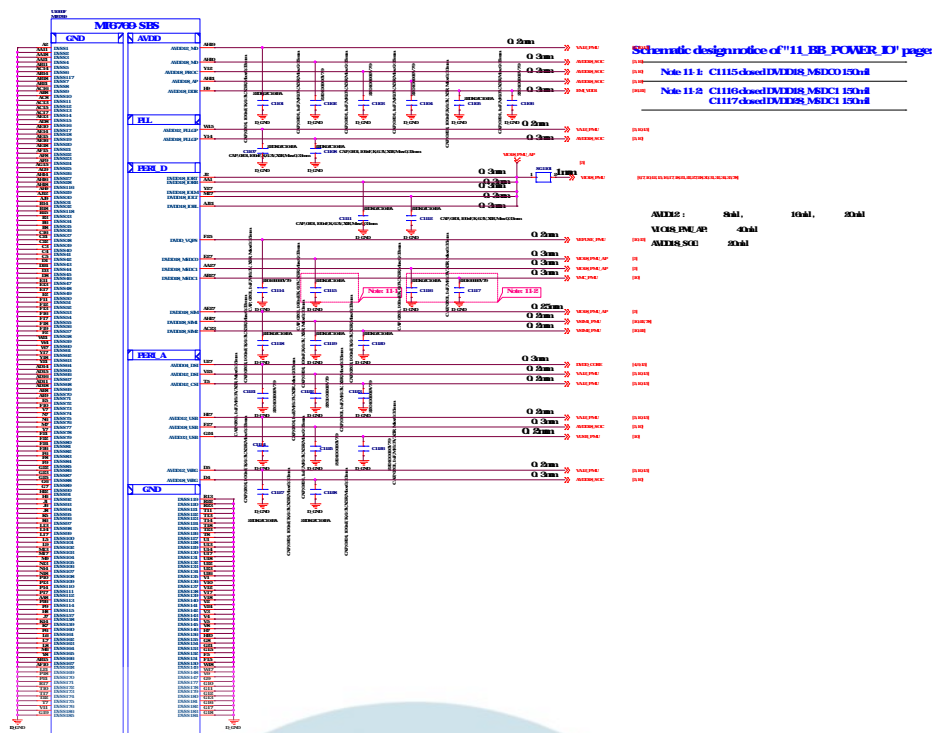
Thermistor to sense RF PA temperature

Thermistor to sense AP temperature

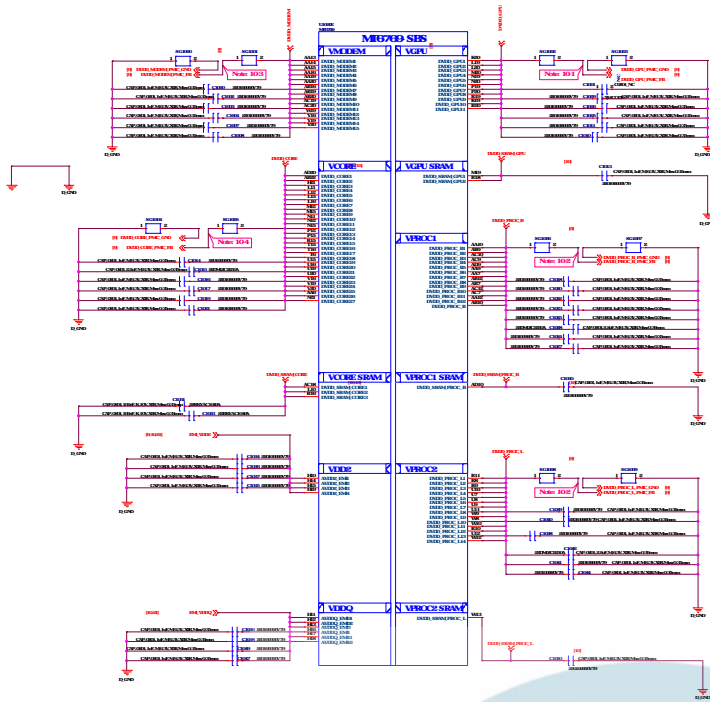
Thermistor to sense AP temperature

Thermistor to sense AP temperature

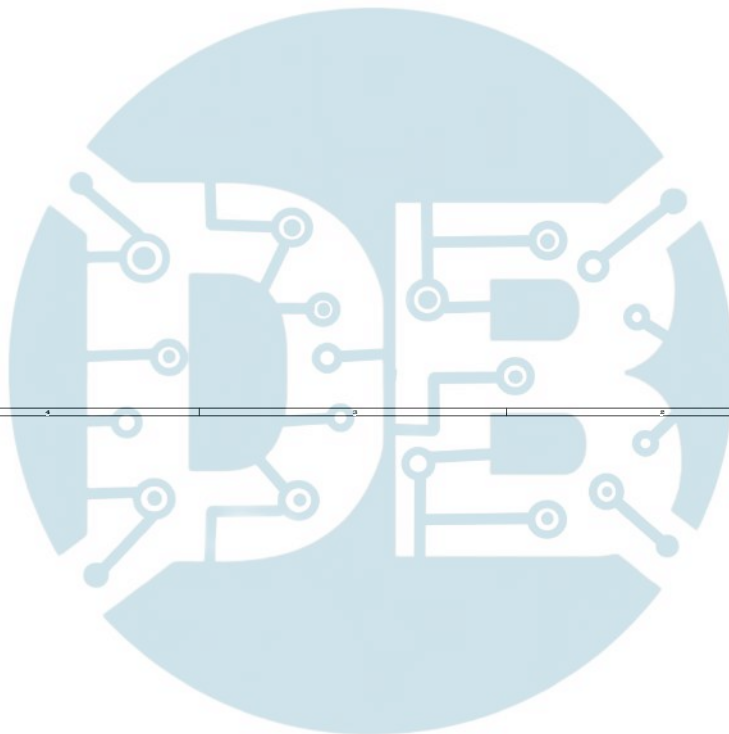


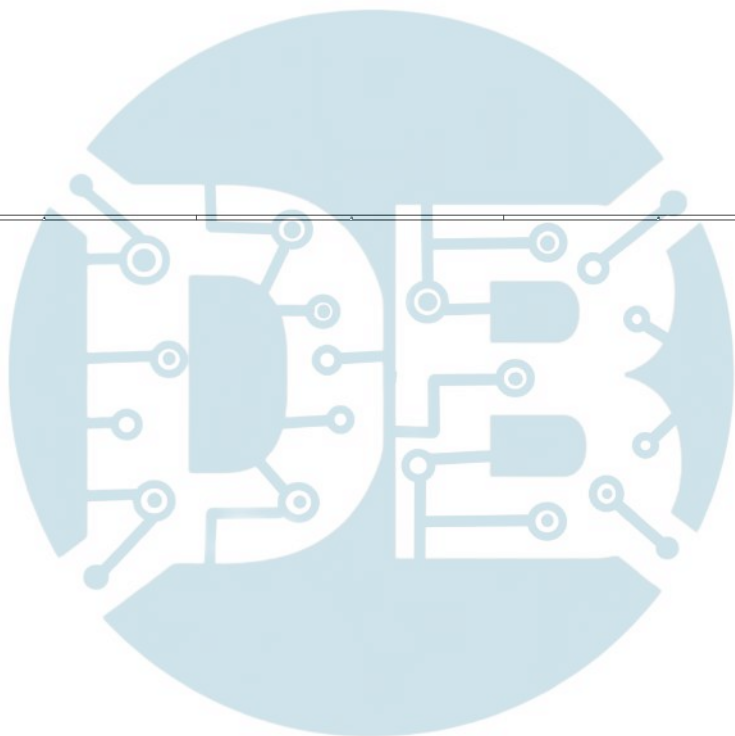
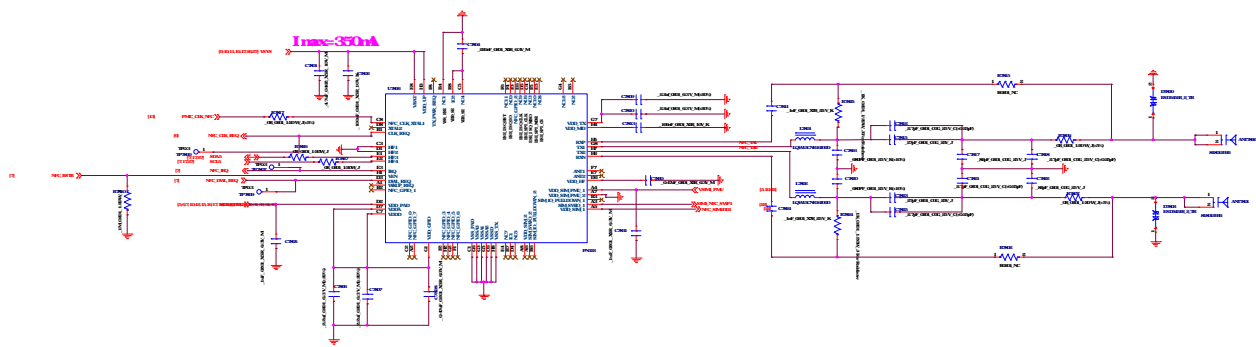


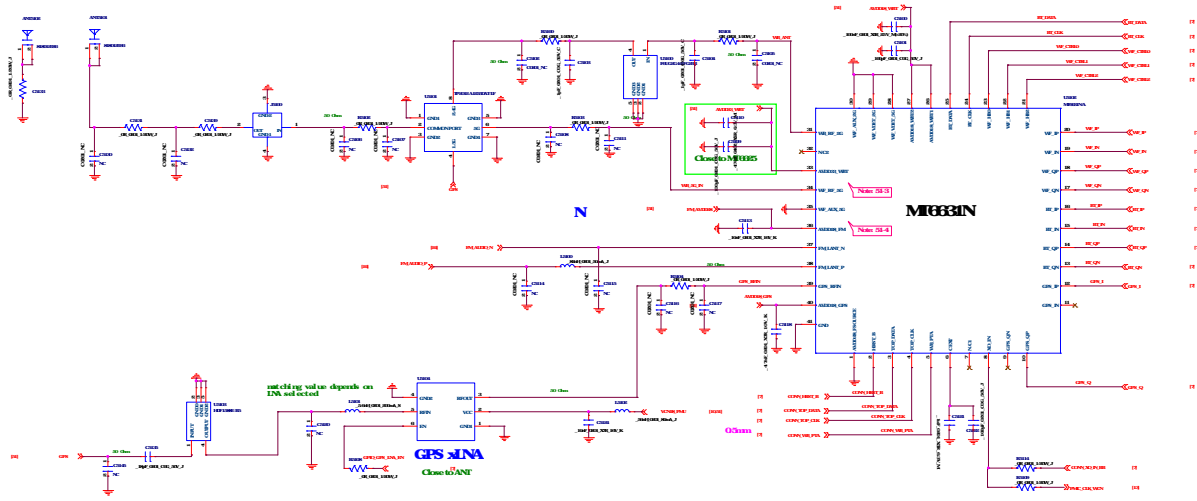
Node ID: 1	Diffusional pair (CATED, GPU) remote sense must be close to 100% Remote sense time with GND adding to PWR: (Diffusional)
Node ID: 2	Diffusional pair (CATED, PRCO) remote sense must be close to 100% Remote sense time with GND adding to PWR: (Diffusional)
Node ID: 3	Diffusional pair (CATED, MDR) remote sense must be close to 100% Remote sense time with GND adding to PWR: (Diffusional)
Node ID: 4	Diffusional pair (CATED, CORE) remote sense must be close to 100% Remote sense time with GND adding to PWR: (Diffusional)



Date	Category	Item
2018.12.14 (V0.1)	Page 04	V0.1 Release
2019.01.11 (V0.2)	Page 12	Add CAMPIB3(GPIO10) HWpin for Nite 12.4 HWpin for IDR type feature in bootstrap
	Page 13	Delete RI007 and RI012 ext. pull for LP4X eMP
	Page 69	







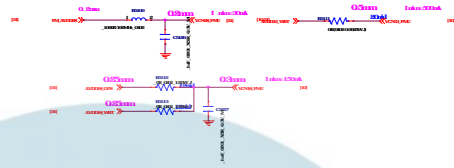
Schematic design notes of "SI_CONNECTIVITY_CONSYS_MI683"

Note SI-1 For RCU5000, please select 00E00000 or 00E00001

Note SI-2 Please refer to MI683 Pin-based design guide for VCN03 LED selection guide

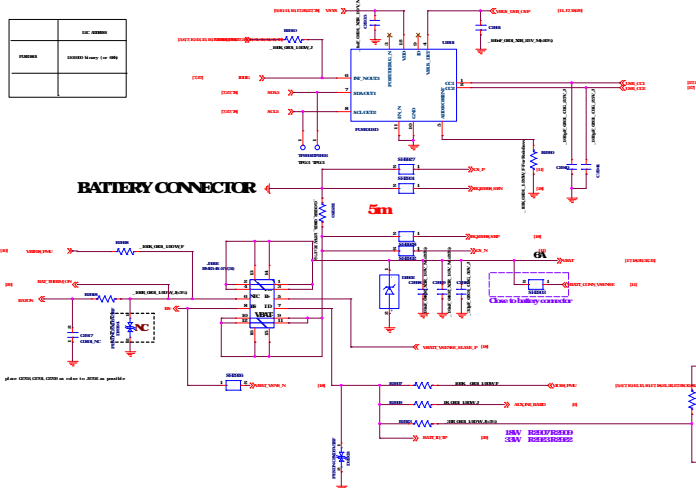
Note SI-3 If VCN03 not support, connect pin3(WF_RP_5C) to GND

Note SI-4 Pin3(MI683_F3) must be connected to VCN03 sensor FM input support



CC logic

CC	CC
CC	CC

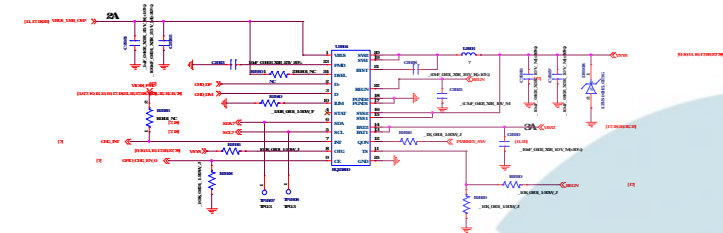


CC	CC
CC	CC

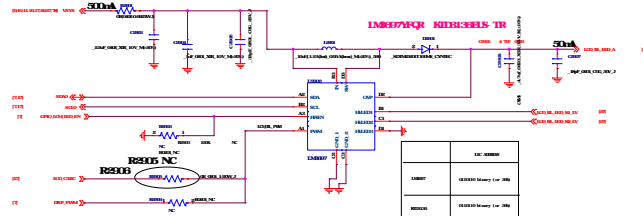
Schematic design of "S1 EX1 WLED DB-FL-CH-PO" page

Note: S1: For better ESD damage performance we need choose suitable device for system protection. Please refer to RoHS table selection guide V2.0 provided by SMC.

SWCharger

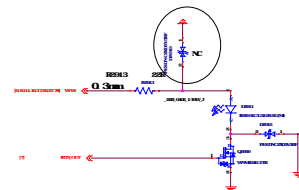
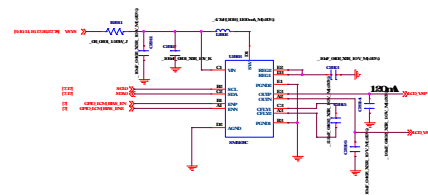


LCMBacklight LEDDriver



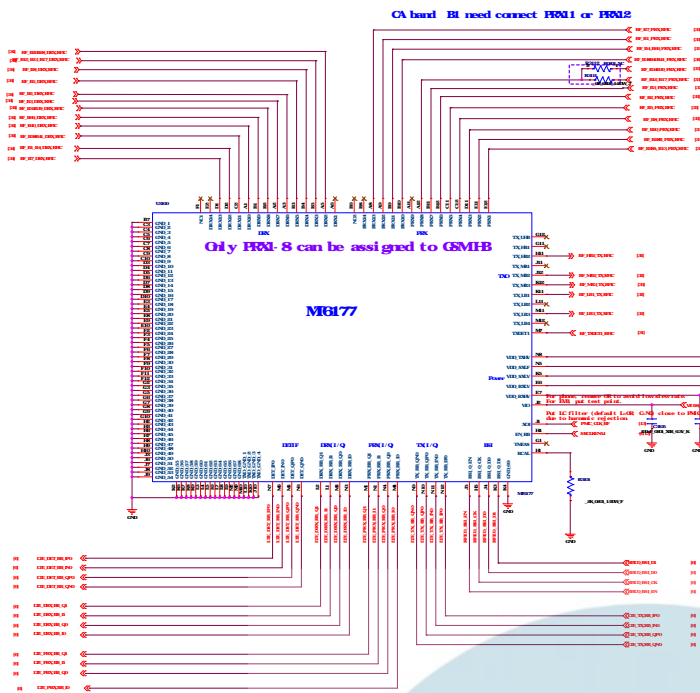
LCM as

SM400C



Attenuator for Detector

Transceiver

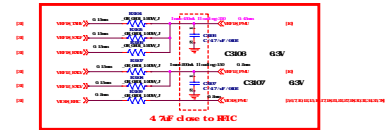


The linear detection range is 0.00001 coupling factor is 10-20dB

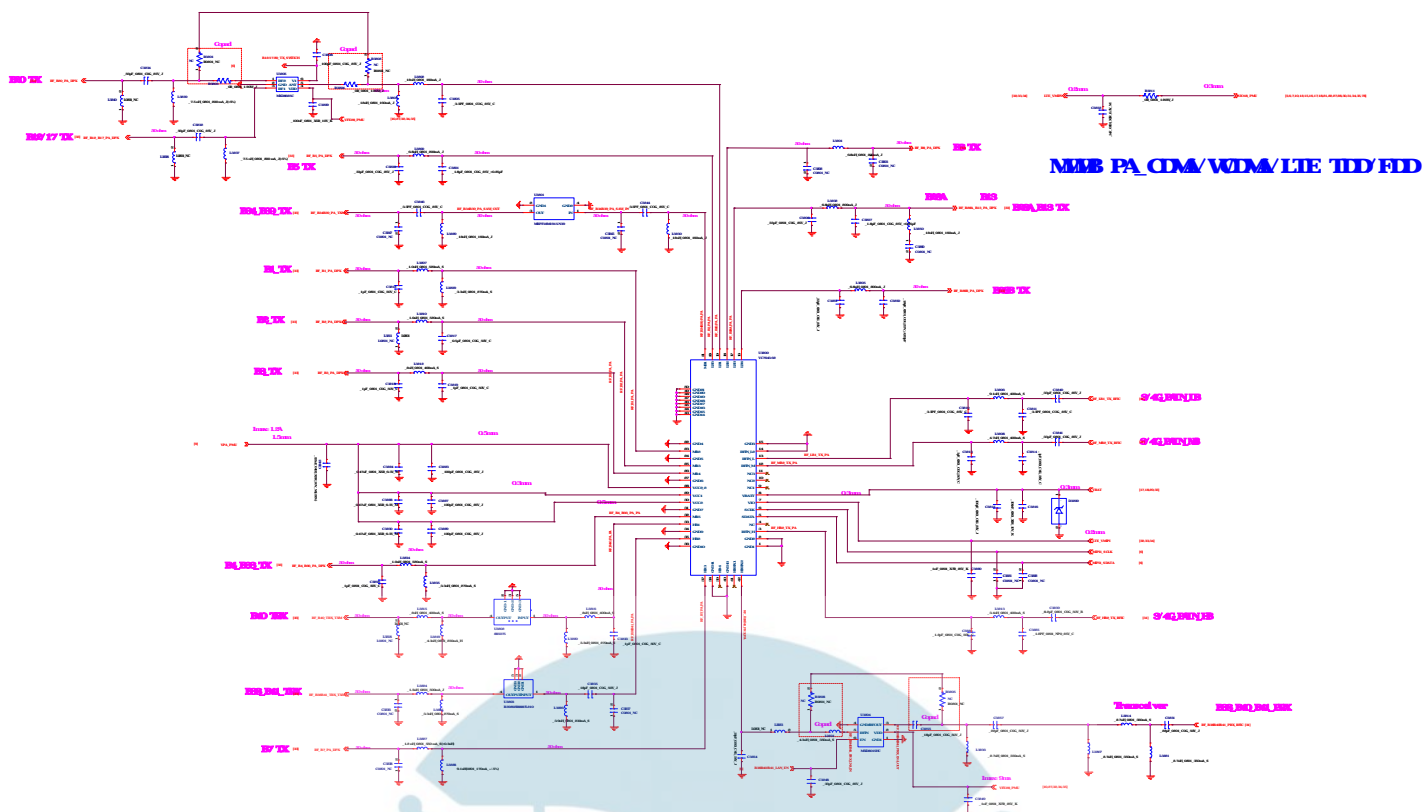
FR01-8 connector



Power For M6177

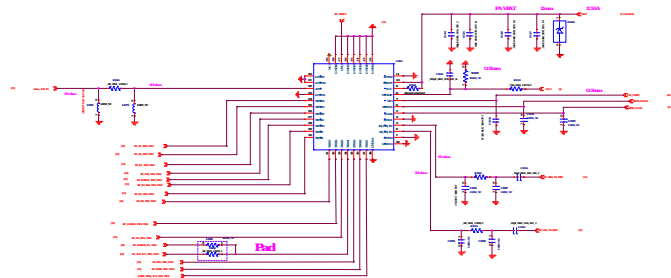


GND



MIN_ANT

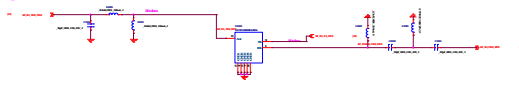
TXM_GSM



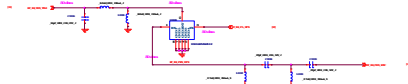
TRX_MB

TRX_LB

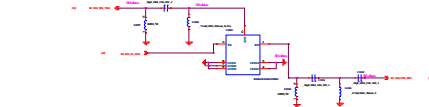
B5TRX



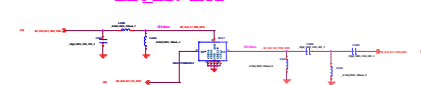
B8TRX



B30TRX



B12_B17TRX



CO PAD LPF

BCM

B13/B28A LPF

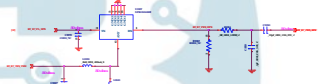
B28A_B13TRX

B28BTRX

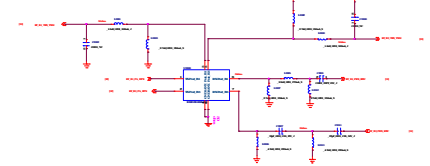


TRX_LB

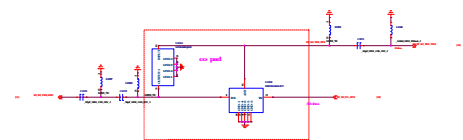
B7TRX



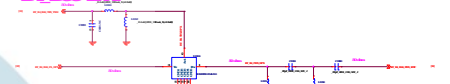
B1_B3TRX



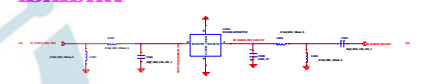
B2TRX



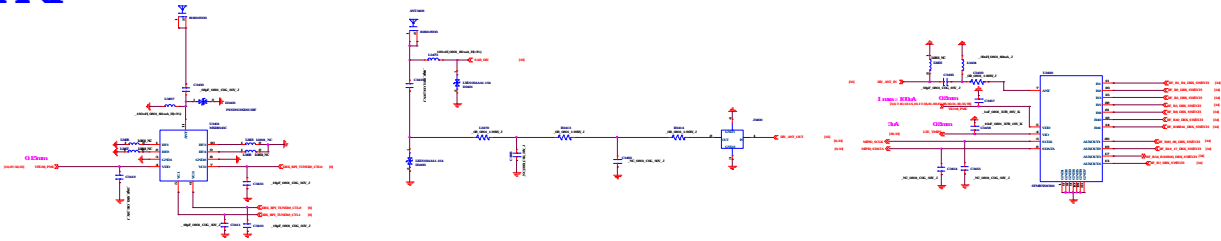
B4_B6BTRX



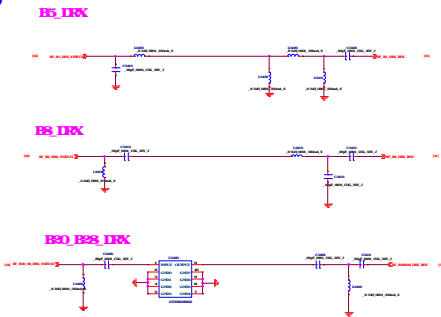
B34B30HTRX



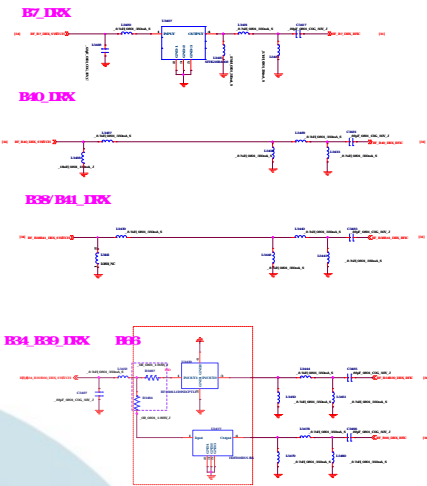
DIV_ANT



DIV_LB



DIV_HB



DIV_MB



5

4

3

2

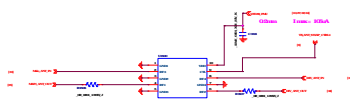
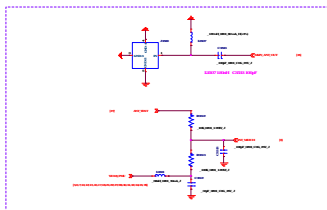
1

D

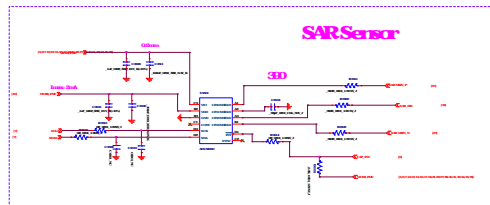
D

DDT

TAS MINIDRUMSWICH



SAR Sensor



C

C

B

B

A

A

5

4

3

2

1

