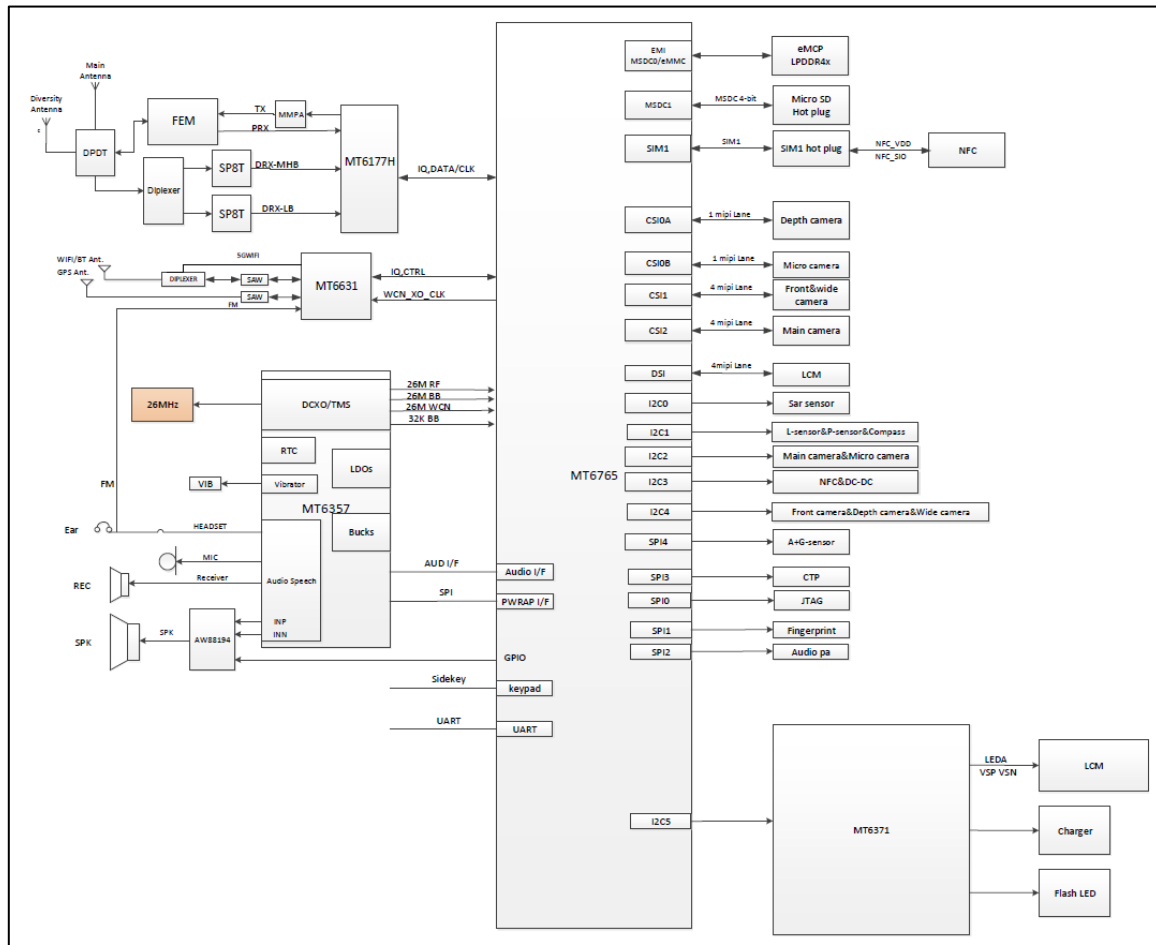
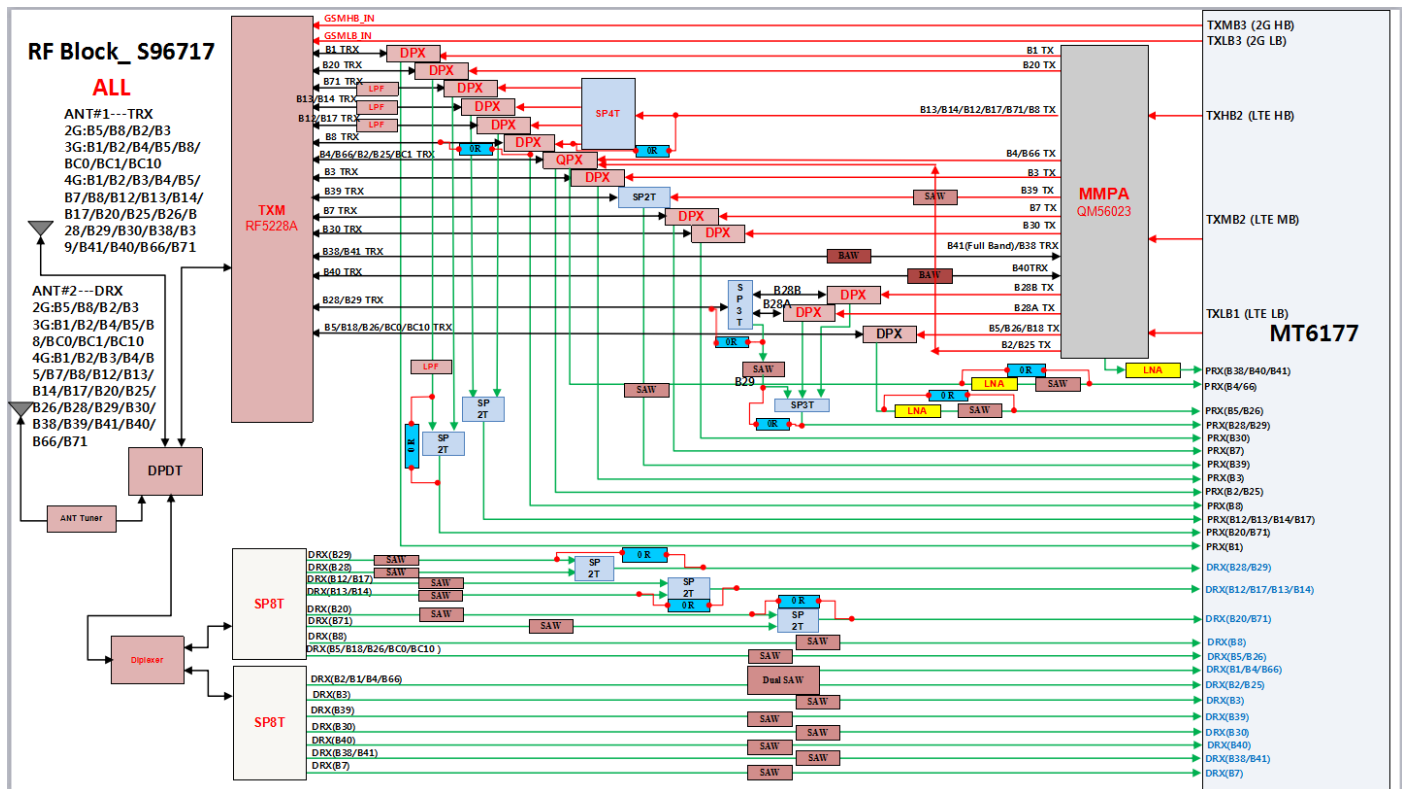


## 8. Level 3 Repair

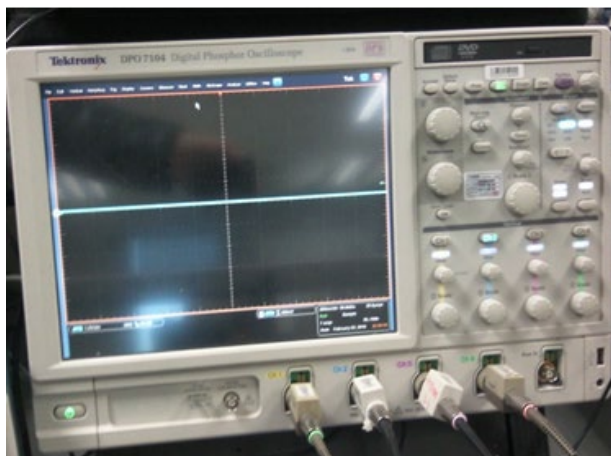


## 8. Level 3 Repair



## 8. Level 3 Repair

### Fluxograma de solução de problemas.



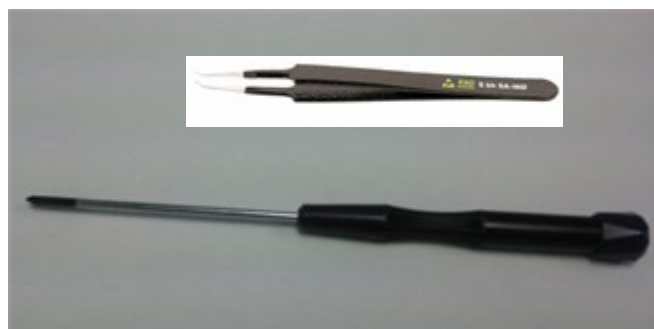
**Osciloscópio**



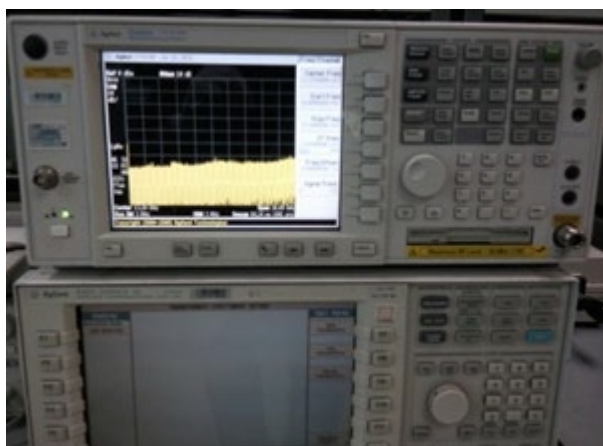
**Multímetro digital**



**Fonte de energia**



**+ driver, pinça segura ESD**



**8960 & Analisador de Espectro**

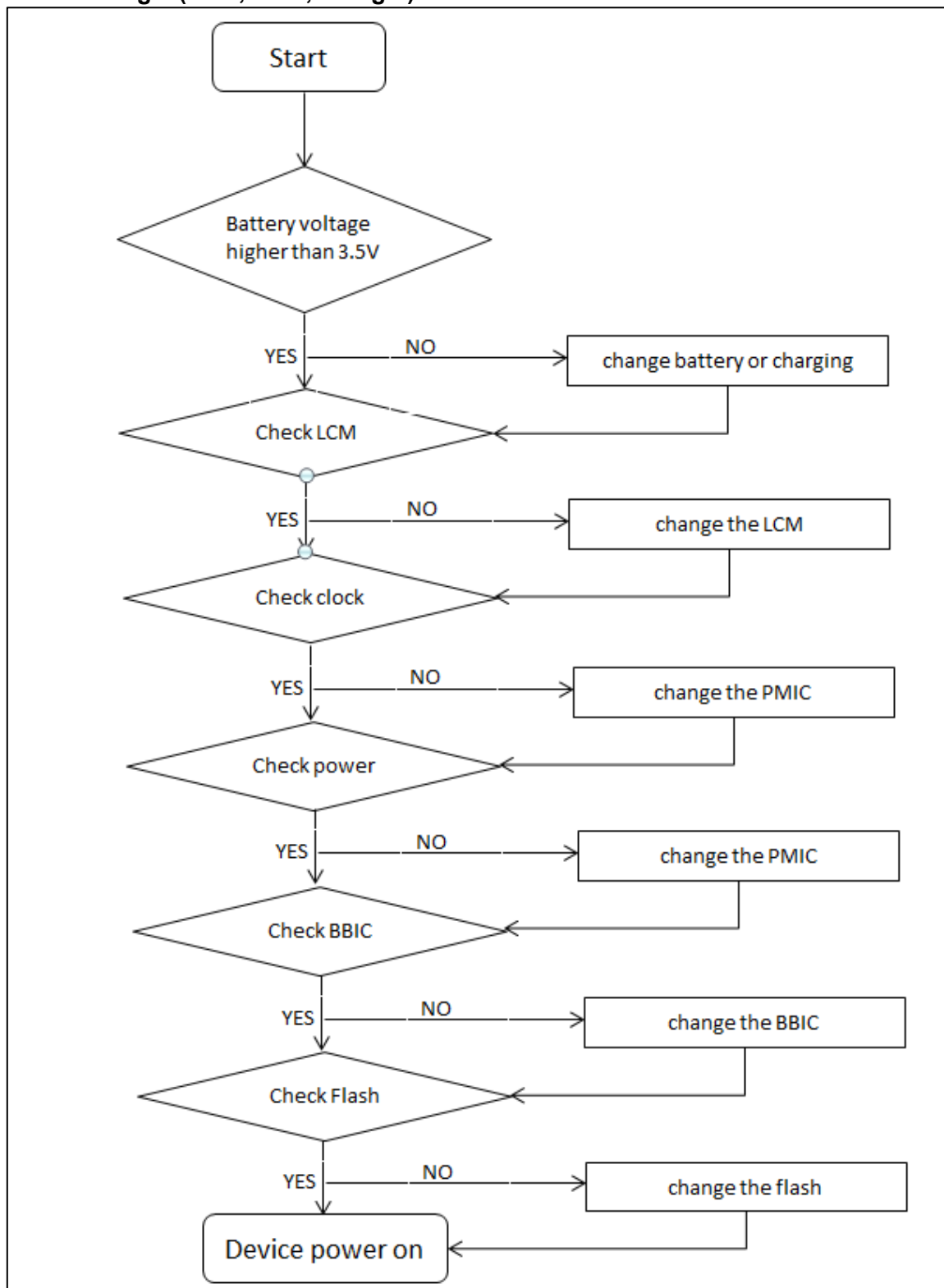


**Ferro de solda**

## 8. Level 3 Repair

### 8-4-1. Power On

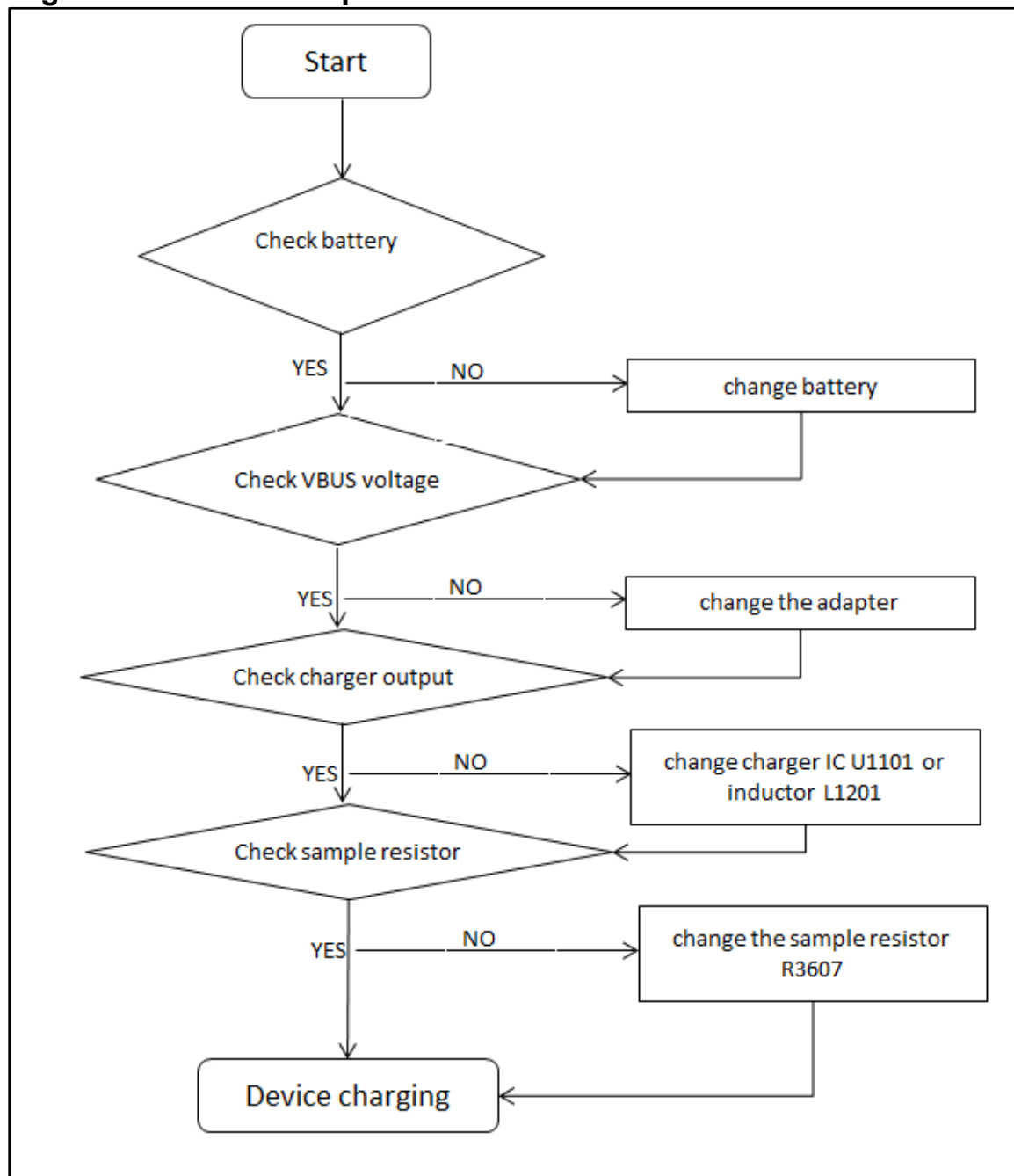
Verificando o sinal de energia (LCM, PMU, Relógio)



## 8. Level 3 Repair

### 8-4-2. Charging

#### O carregamento controlado pelo MT6371



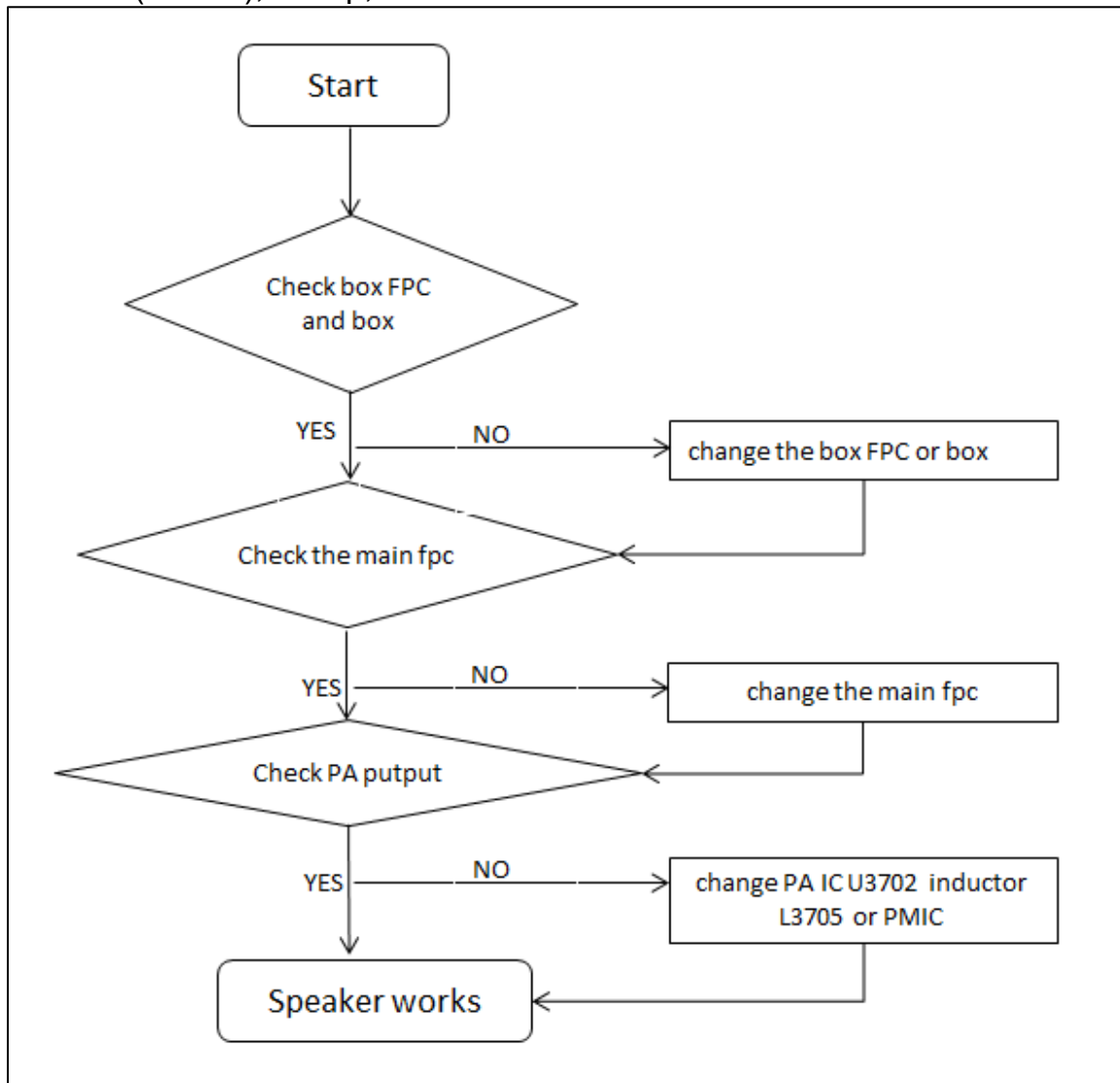
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## 8. Level 3 Repair

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### 8-4-3. Audio speaker

Os sinais de controle do alto-falante são gerados por MT6357 (U701) e PA AW88194 (U3702), o chip, o PA e o alto-falante devem ser verificados.



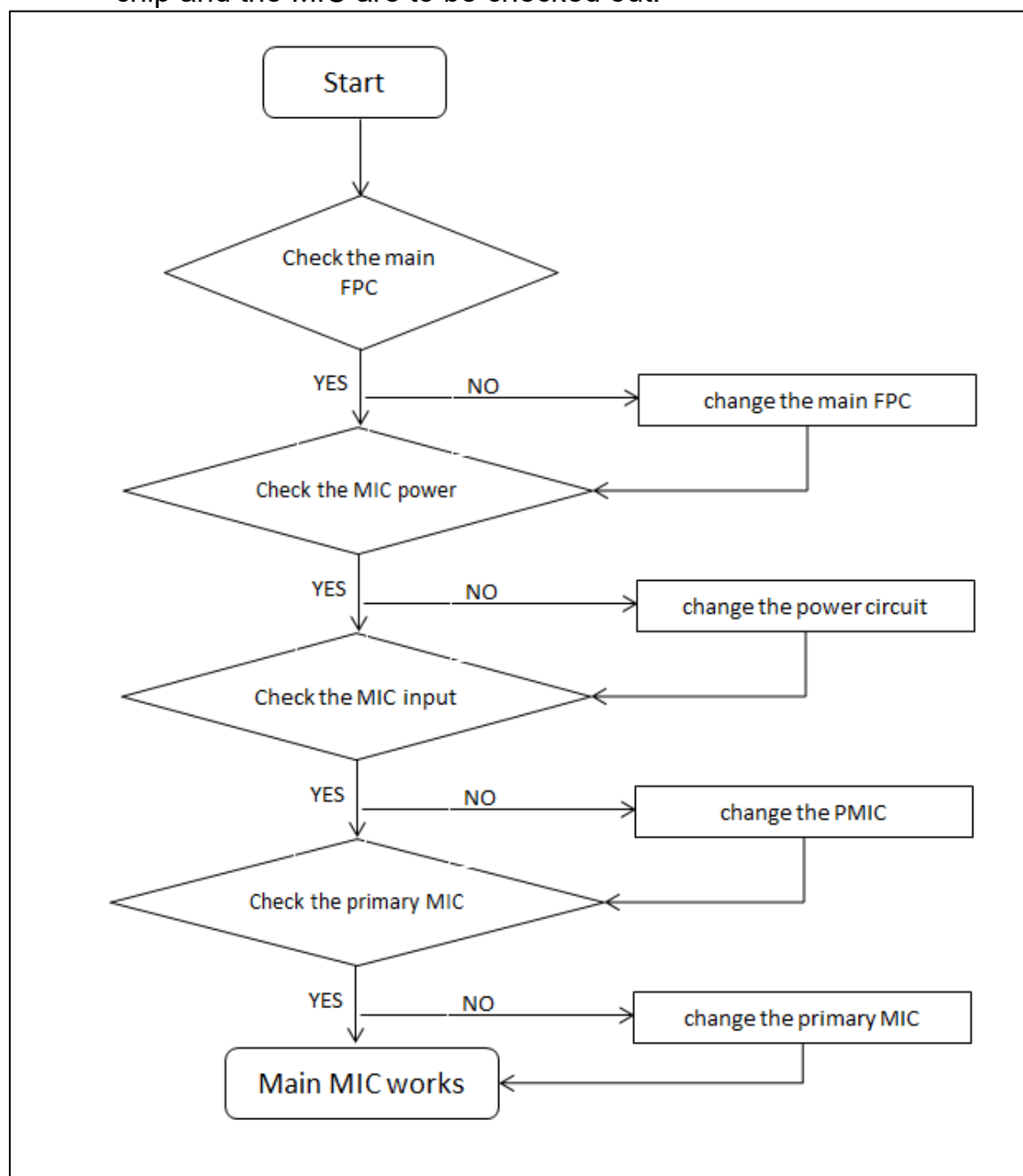
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## 8. Level 3 Repair

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### 8-4-4. Audio Main MIC

Os sinais de controle MIC são gerados pelo chip PMU MT6357 (U701), o PMU chip and the MIC are to be checked out.



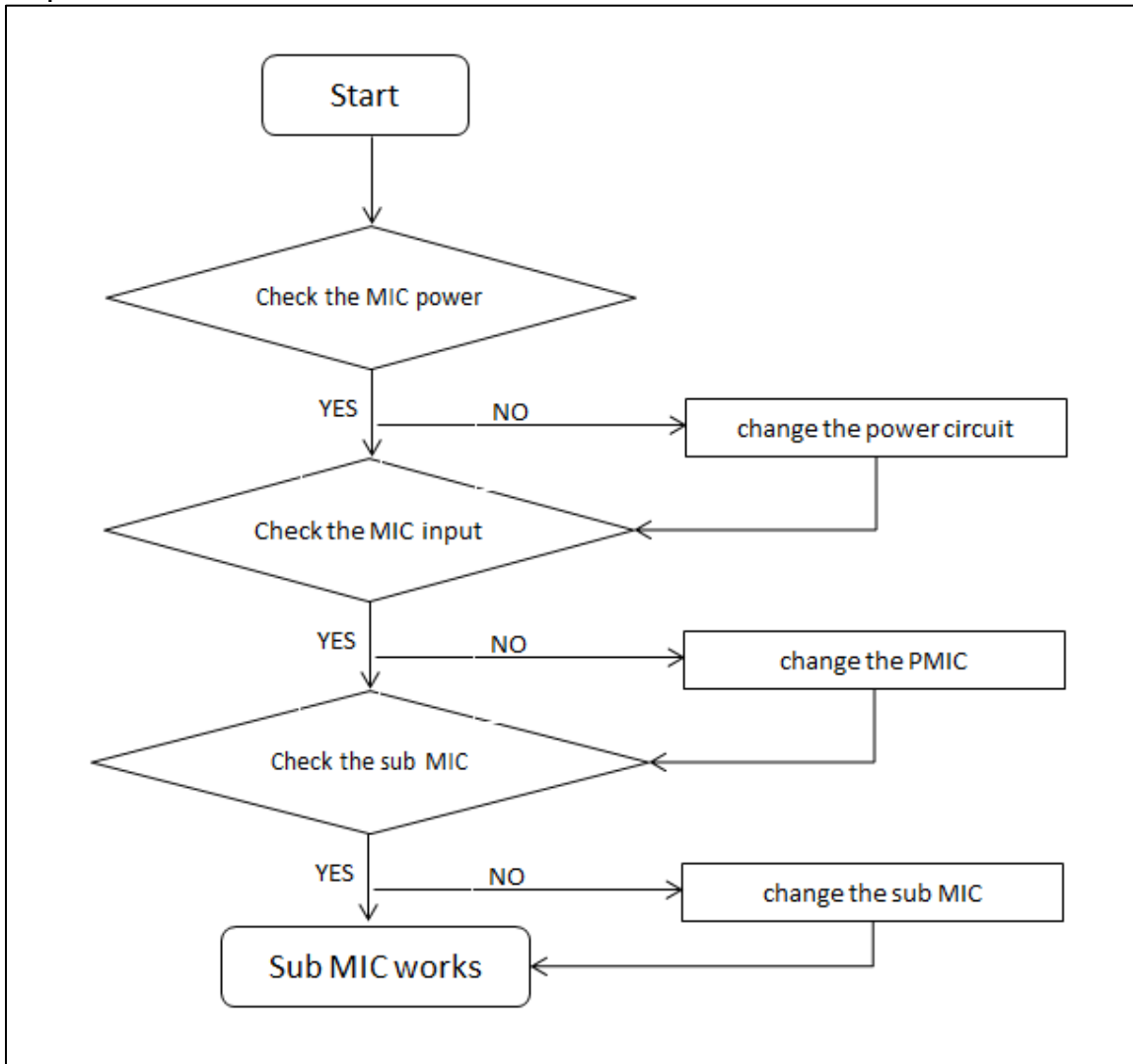
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## 8. Level 3 Repair

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### Sub MIC

: Os sinais de controle do MIC são gerados pelo chip PMU MT6357 (U701), o chip PMU e o MIC devem ser verificados.





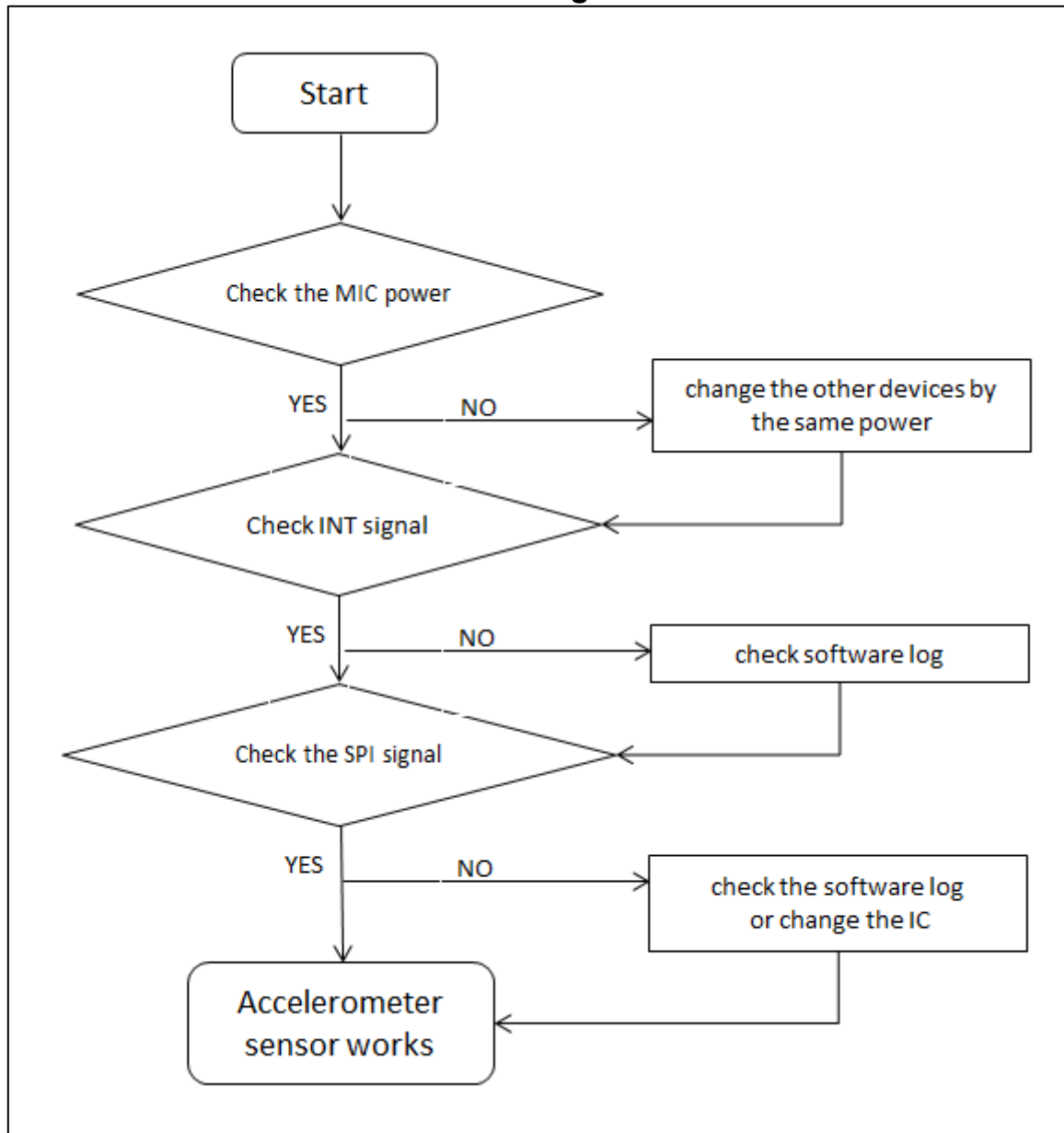
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## 8. Level 3 Repair

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### 8-4-6. Accelerometer sensor

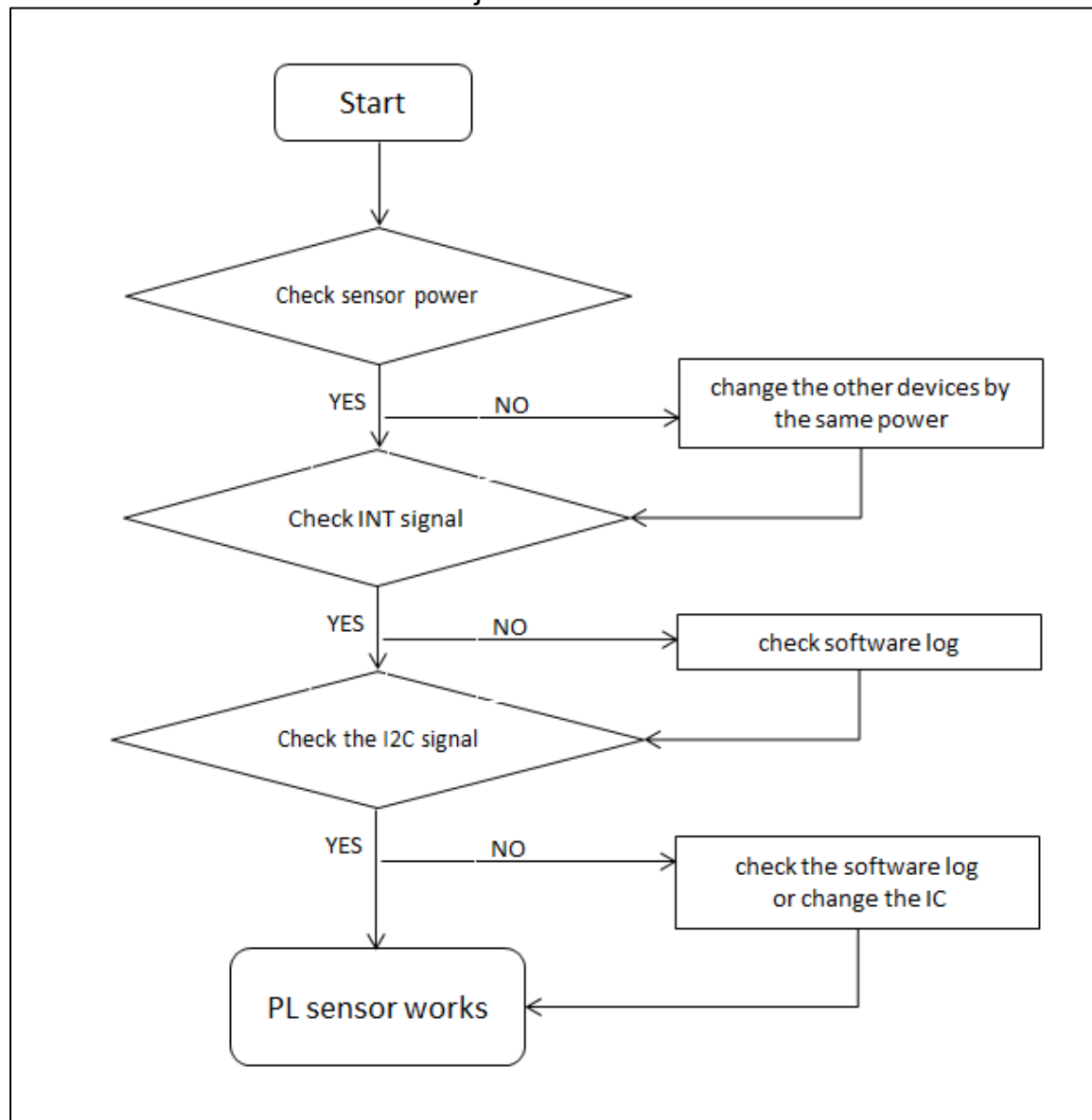
O sensor do acelerômetro é calibrado usando o algoritmo SW.



## 8. Level 3 Repair

### 8-4-7. Proximity and light sensor

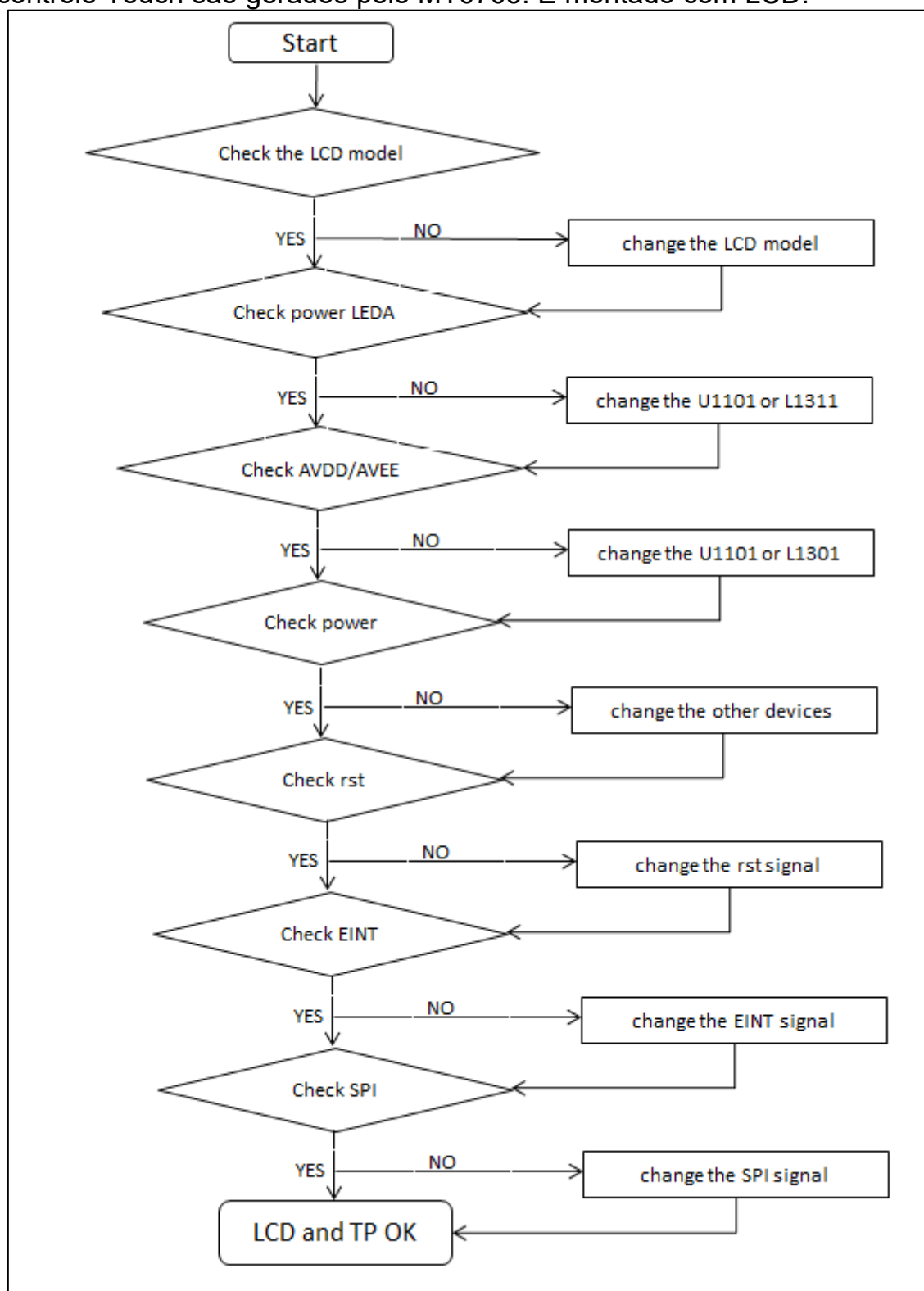
: Proximidade (U4417) e sensor de luz (U4416) funcionam como abaixo:  
Controle a operação de ligar/desligar da tela automaticamente ao fazer chamadas telefônicas e ajuste o brilho da tela de acordo com a luz ambiente.



## 8. Level 3 Repair

### 8-4-8. TOUCH SCREEN AND DISPLAY

Os sinais de controle Touch são gerados pelo MT6765. É montado com LCD.



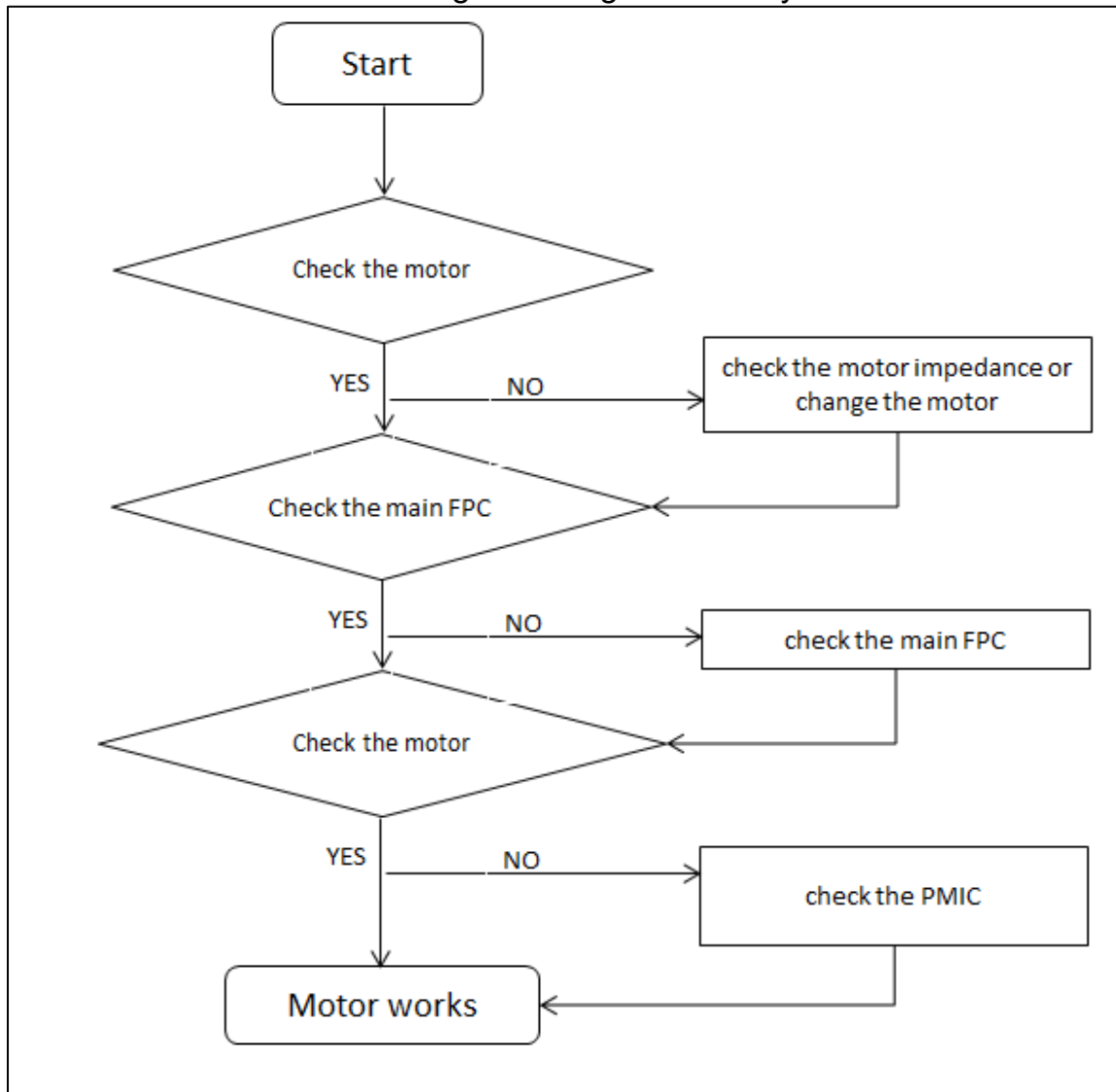
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## 8. Level 3 Repair

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### 8-4-9. Vibrator

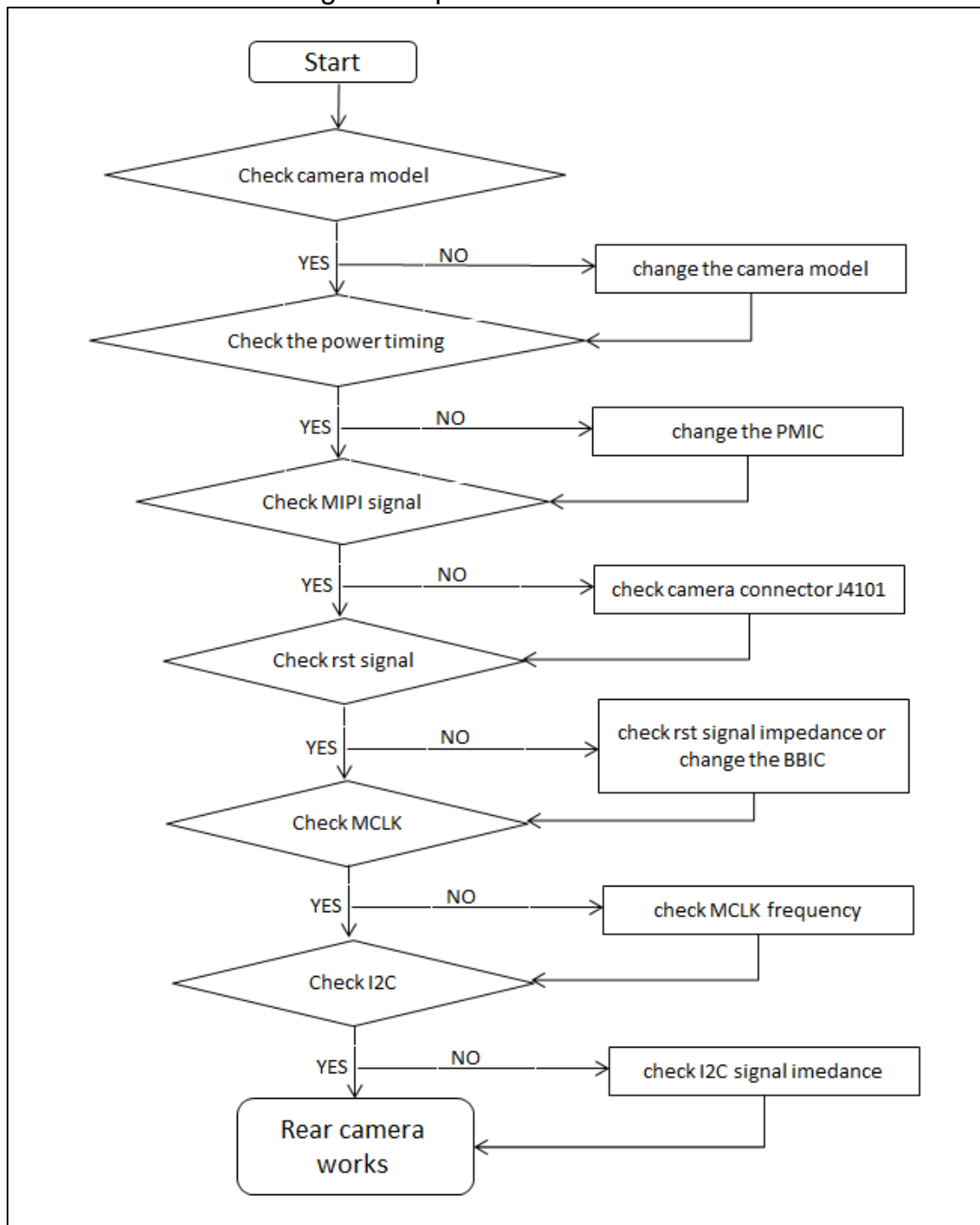
: The Vibrator control signals are generated by MT6357.



## 8. Level 3 Repair

### 8-4-10. Rear Camera

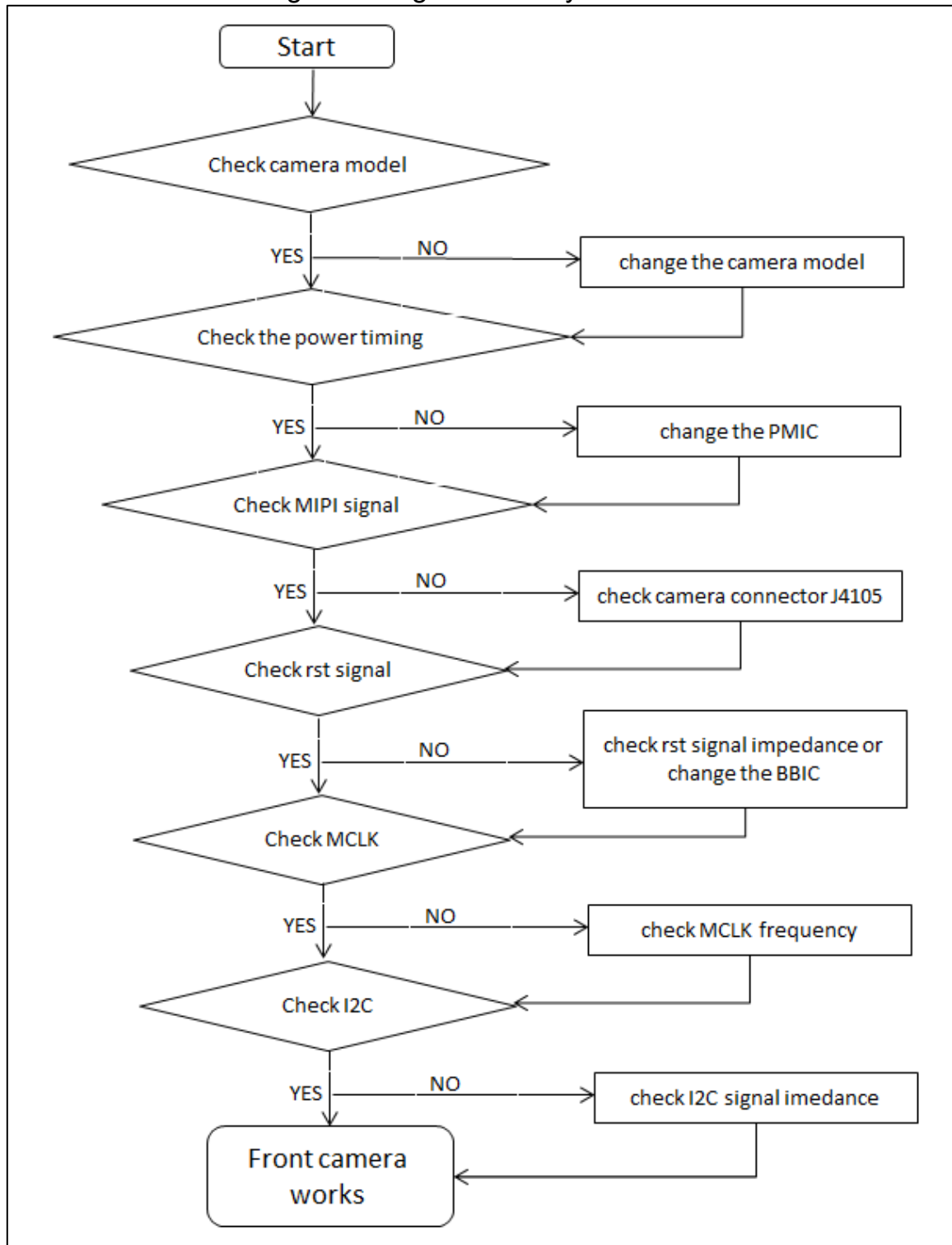
Os sinais de controle da câmera são gerados pelo MT6762.



## 8. Level 3 Repair

### 8-4-11. Front Camera

The Camera control signals are generated by MT6765.



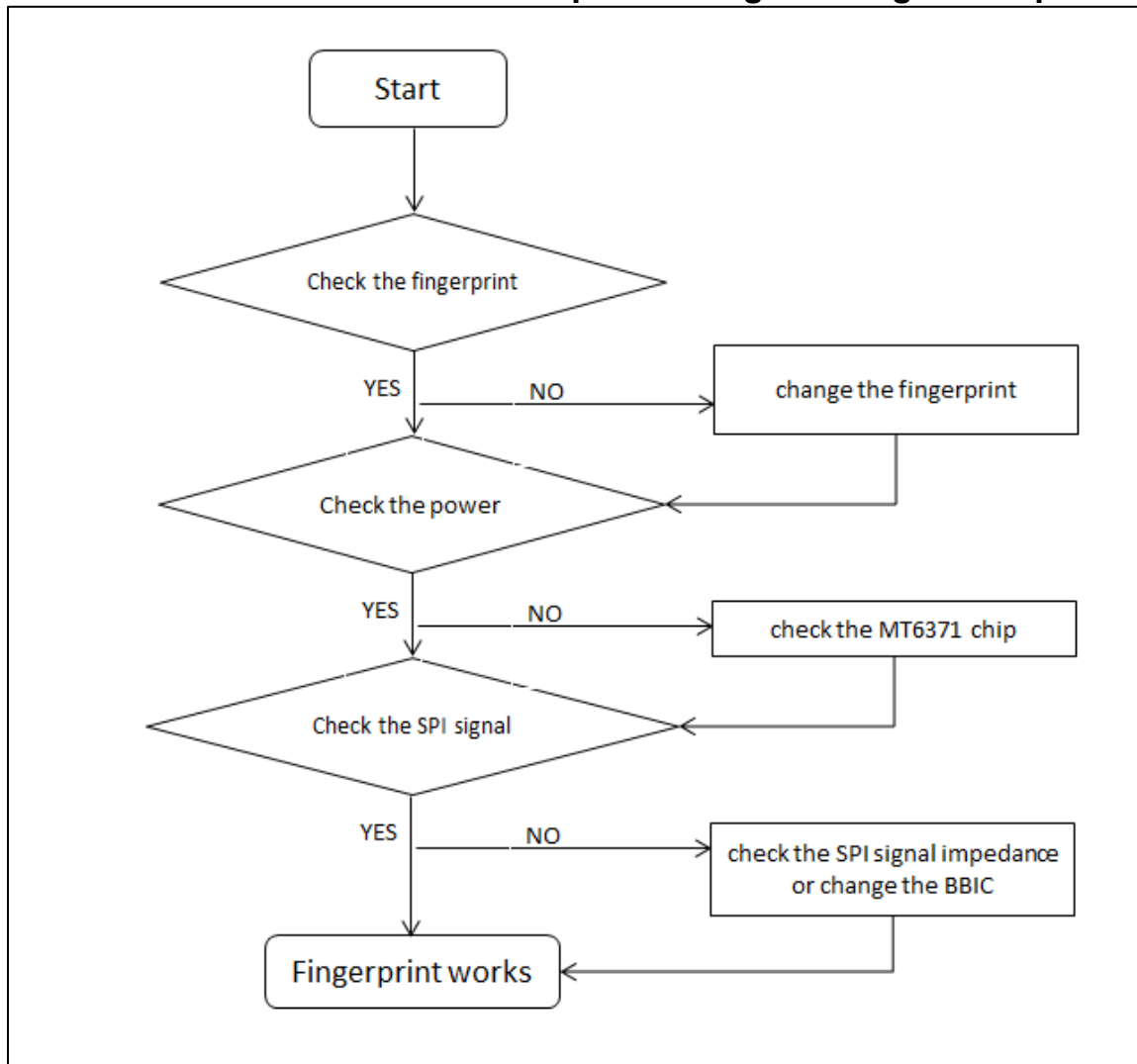
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## 8. Level 3 Repair

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### 8-4-12. Fingerprint

Os sinais de controle de impressão digital são gerados pelo MT6765.



## 8. Level 3 Repair

### 8-5. Service Schematics

#### - U101\_MT6765\_BB chip IC , Digital Baseband Processor(Top)

558	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28
A	NC	WF_IN	WF_IP	EMIO_CAB		NC		EMIO_CAB			EMIO_DQ00		EMIO_DQ01		EMIO_DQ02		EMIO_DQ03		EMIO_DQ04		EMIO_DQ05		MSDC0_DAT4	MSDC0_DAT2	MSDC0_DAT7	DVDD01_R_MSD	NC	A
B	WF_QP	WF_QN	EMIO_CAB	EMIO_CAB		EMIO_CAB	EMIO_CAB	NC		EMIO_DQ01	EMIO_DQ02	EMIO_DQ03		EMIO_DQ04	EMIO_DQ05	EMIO_DQ06	EMIO_DQ07	EMIO_DQ08	EMIO_DQ09	EMIO_DQ10	EMIO_DQ11	MSDC0_DAT5	MSDC0_DAT6	MSDC0_DAT8	MSDC0_DAT9	USB_D_M	AVDD01_2_USB	B
C		DVSS	AVDD01_2_VDD0	NC	EMIO_CAB	DVSS	EMIO_CAB		NC	EMIO_DQ01	EMIO_DQ02	EMIO_DQ03	EMIO_DQ04	EMIO_DQ05	EMIO_DQ06	EMIO_DQ07	EMIO_DQ08	EMIO_DQ09	EMIO_DQ10	EMIO_DQ11	EMIO_DQ12	DVSS	MSDC0_DAT3	MSDC0_RSTB	MSDC0_DAT0	USB_D_P		C
D	BT_IN	BT_IP	DVSS	NC	EMIO_CAB	EMIO_CAB	NC	EMIO_CAB	DVSS	EMIO_DQ01	EMIO_DQ02	EMIO_DQ03	EMIO_DQ04	EMIO_DQ05	EMIO_DQ06	EMIO_DQ07	EMIO_DQ08	EMIO_DQ09	EMIO_DQ10	EMIO_DQ11	EMIO_DQ12	DVSS	MSDC0_DAT3	MSDC0_RSTB	CHD0_P	DVSS	AVDD01_2_USB	D
E		BT_QP	DVSS	EMIO_CAB	EMIO_CAB	EMIO_CAB	EMIO_CAB	EMIO_CAB	EMIO_CAB	EMIO_DQ01	EMIO_DQ02	EMIO_DQ03	EMIO_DQ04	EMIO_DQ05	EMIO_DQ06	EMIO_DQ07	EMIO_DQ08	EMIO_DQ09	EMIO_DQ10	EMIO_DQ11	EMIO_DQ12	NC	DVSS	DVSS	CHD0_P	SYSTRST_B	AVDD01_2_USB	E
F	GPS_I	DVSS	BT_QN	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	TESTM_ODE		F
G	GPS_Q	CONN_WB_P7	CONN_HRST	DVSS			XIN_W_BG			DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	NC						DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	G
H		AVDD01_2_WB0	CONN_WB_P7	CONN_WB_P7	CONN_WB_P7	CONN_WB_P7				AVDD01_2_WB0	AVDD01_2_WB0	AVDD01_2_WB0	AVDD01_2_WB0	DVSS	DVSS		AVDD01_2_WB0	AVDD01_2_WB0	AVDD01_2_WB0	AVDD01_2_WB0	AVDD01_2_WB0	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS		H
J	DVDD01_2_WB0	CAM_C_LK2	SC16	SDA6	CONN_WB_P7	CONN_WB_P7																DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	J
K		CAM_P_DN2	CAM_R_ST2	ANT_S_E12	ANT_S_E10	ANT_S_E11	CONN_TOP_D_ATA	CONN_TOP_D_ATA	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	K
L	CS1A_L1P	CS1A_L1N	CS1A_L0P	CS1A_L0N	CS1B_L0P	CS1B_L0N				DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	L
M	CS1A_L2P	CS1A_L2N	CS1B_L1P	CS1B_L1N	CS1B_L0P	CS1B_L0N				DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	M
N	CS1A_L1P	CS1A_L1N	CS1A_L0P	CS1A_L0N	CS1B_L2P	CS1B_L2N				DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	N
P	CS1A_L2P	CS1A_L2N	CS1B_L1P	CS1B_L1N	CS1B_L0P	CS1B_L0N				DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	P
R	CS2A_L1P	CS2A_L1N	CS2A_L0P	CS2A_L0N	CS2B_L1P	CS2B_L1N				DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	R
T	CS2A_L1P	CS2A_L1N	CS2A_L0P	CS2A_L0N	CS2B_L1P	CS2B_L1N				DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	T
U	DVDD01_2_WB0	AVDD01_2_WB0	CS2A_L1P	CS2A_L1N	CS2A_L0P	CS2A_L0N	SC14			DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	U
V	CAM_R_ST1	CAM_R_ST0	SDA2	SC12	SDA4					DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	V
W		CAM_C_LK1	ENT10	ENT9	CAM_C_LK0	CAM_P_DN0	ENT11			DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	W
Y	SRCLK_NAI	CAM_P_DN1	ENT7	ENT6	ENT4	ENT5				DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	Y
AA	KPRO_W0	UTXD0	URXD0	ENT3	ENT2	ENT1				DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	AA
AB	KPRO_W1	KPCOL_1	KPCOL_0	ENT0	SC10	CDMSH_5A				DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	AB
AC	SPI0_M_0	SPI0_M_1	PWM0	SDA0	CDMSH_5A	SPI0_M_0				DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	AC
AD	SPI0_M_1	SPI0_M_0	SPI0_M_1	SPI0_M_0	SPI0_M_1	SPI0_M_0				DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	AD
AE	SC11	SPI0_M_1	SPI0_M_0	SPI0_M_1	SPI0_M_0	SPI0_M_1				DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	AE
AF	SDA1	SPI0_M_1	SPI0_M_0	SPI0_M_1	SPI0_M_0	SPI0_M_1				DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	AF
AG	NC	SPI0_M_1	SPI0_M_0	SPI0_M_1	SPI0_M_0	SPI0_M_1				DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	DVSS	ADD_C_LK_MIS	AVDD01_2_USB	AG
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28

Figure 2-1. Ball map view