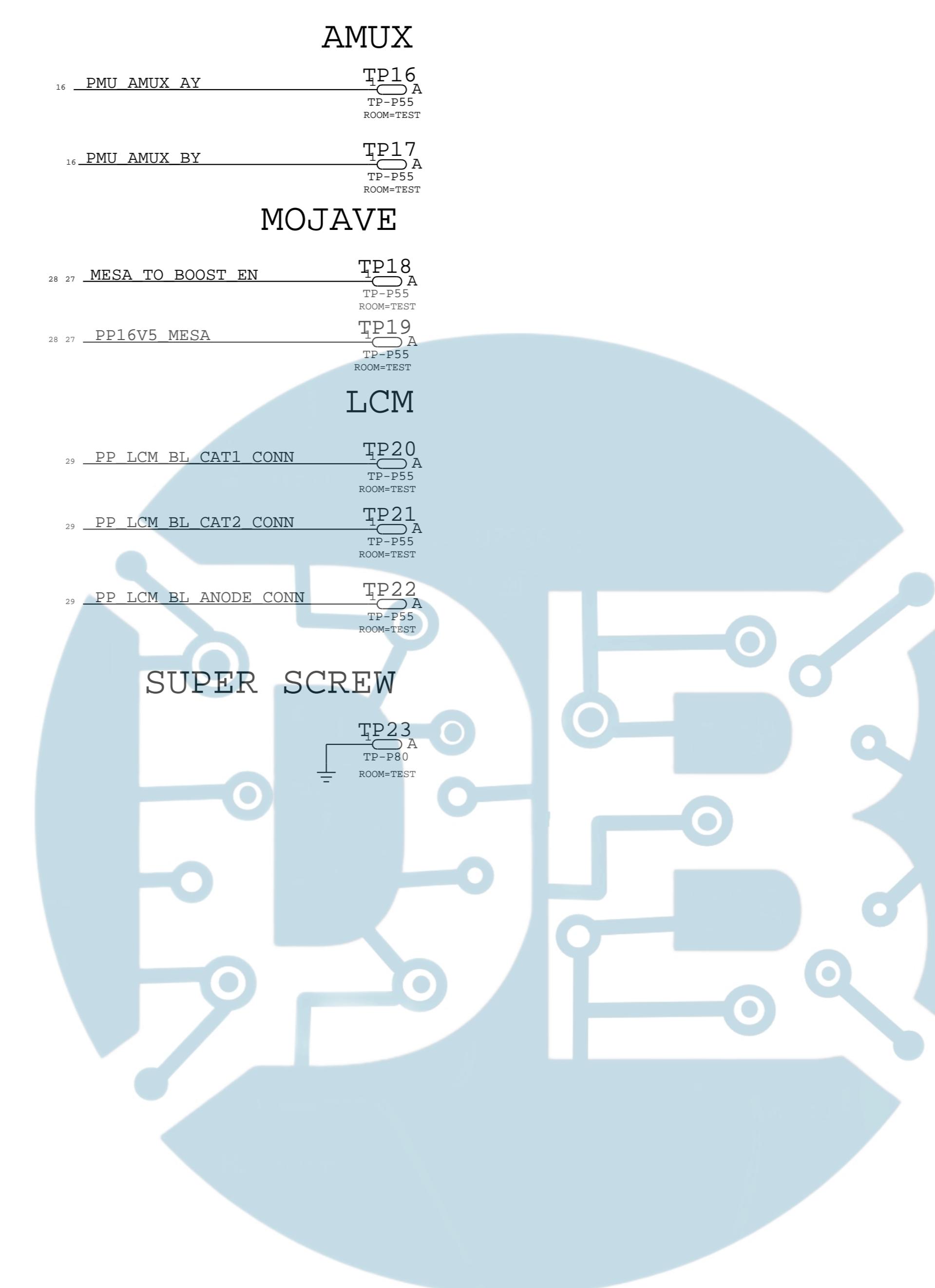
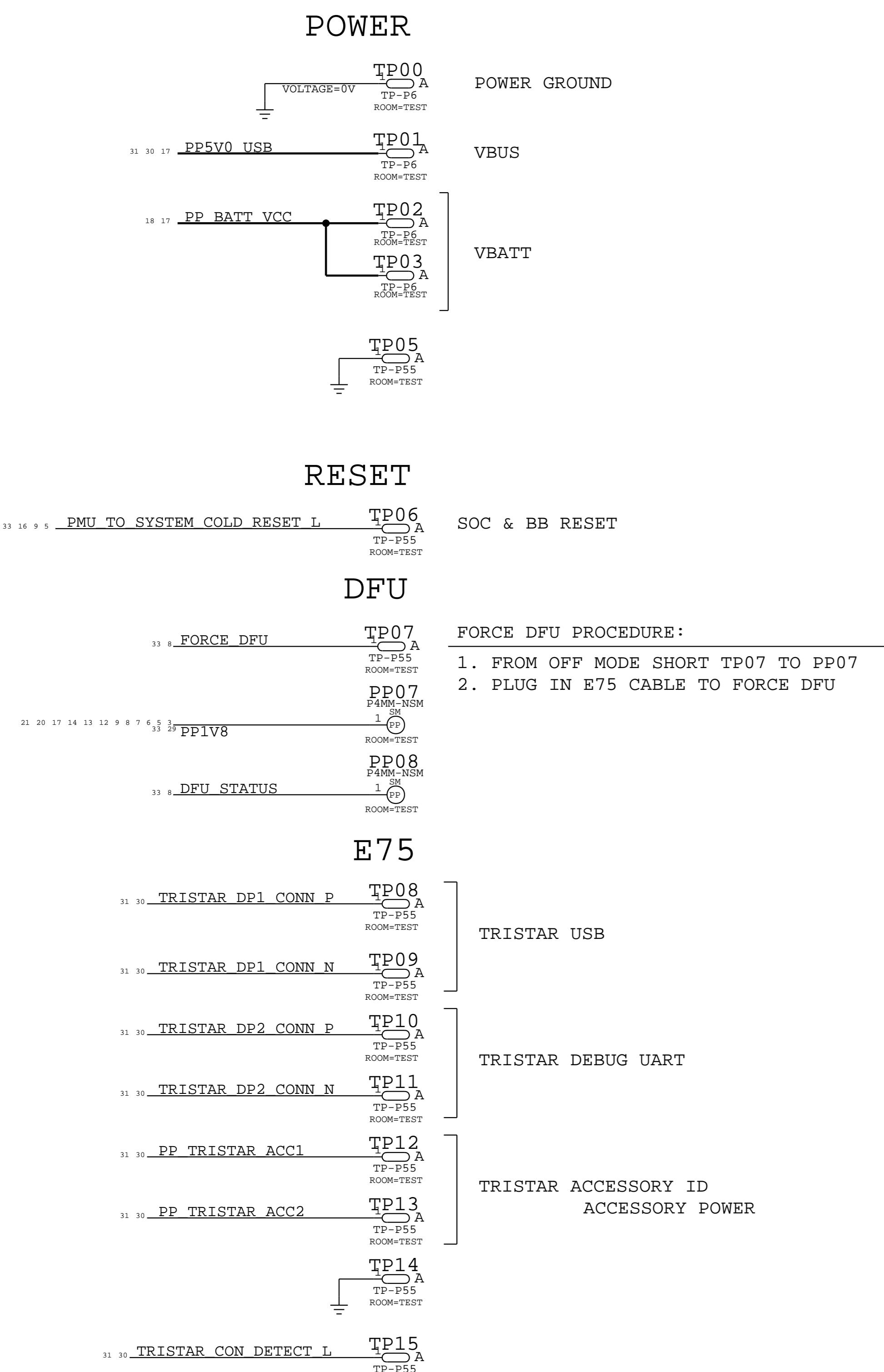


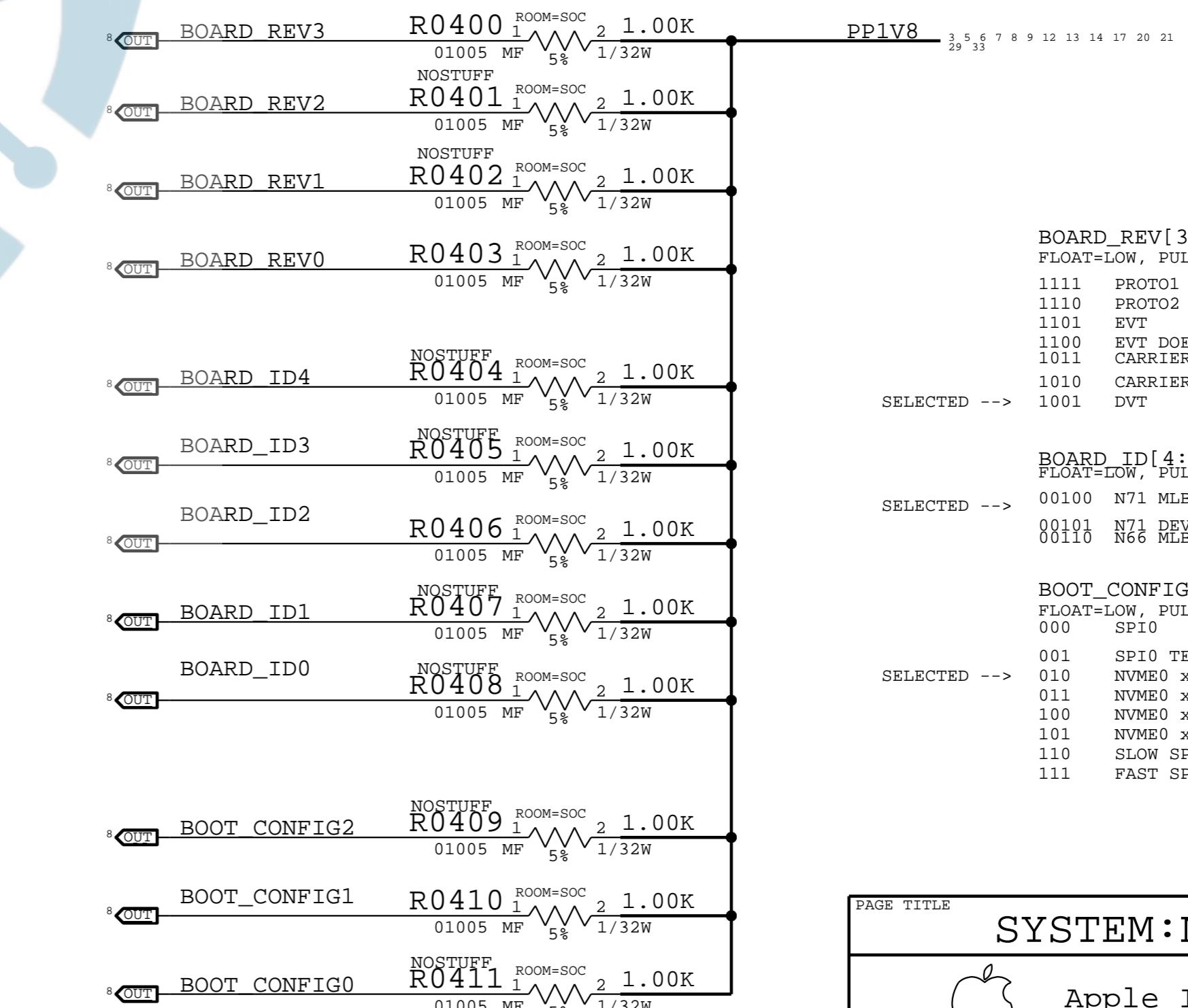
TESTPOINTS



N71 I2C DEVICE MAP

I2C BUS	DEVICE	BINARY	7-BIT HEX	8-BIT HEX
I2C0	ANTIGUA PMU	1110100X	0X74	0XE8
	CHESTNUT	0100111X	0X27	0XA4
	MUON	1100010X	0X62	0XC4
I2C1	TIGRIS	1110101X	0X75	0XE9
	ARC DRIVER	1000001X	0X41	0XB2
	SPEAKER AMP	1000000X	0X40	0XB0
	TRISTAR	0011010X	0XA	0X34
I2C2	ALS	0101001X	0X29	0XA2
	DISP EEPROM	1010001X	0X51	0XA2
OWL	UNUSED	N/A	N/A	N/A
ISP I2C0	REAR CAM	TBD	TBD	TBD
	LED DRIVER	1100011X	0X63	0XC6
ISP I2C1	FRONT CAM	0010000X	0XA0	0XB0
	MESON	1000000X	0XA0	0XB0
TOUCH I2C	MAMBA	1100000X	0XA0	0XB0
	DOPPLER	1011000X	0XA8	0XB0
SEP I2C	SEP EEPROM	1010001X	0X51	0XA2

BOOTSTRAPPING: BOARD REV BOARD ID BOOT CONFIG



PAGE TITLE	SYSTEM:N71 SPECIFIC		
Apple Inc.	DRAWING NUMBER	051-1902	SIZE
REVISION	A.0.0	D	
BRANCH			
PAGE	4 OF 49		
SHEET	3 OF 59		

RESISTOR STUFF = HIGH '1'
RESISTOR NOSTUFF = LOW '0'

PENINSULA STANDOFFS

BS0506
STDOFF-2.20D0.25H-0.50-1.70
ROOM=ASSEMBLY

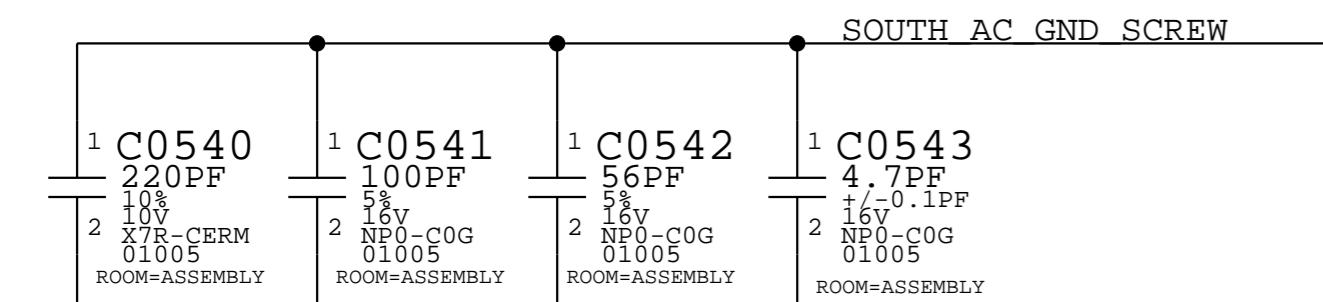
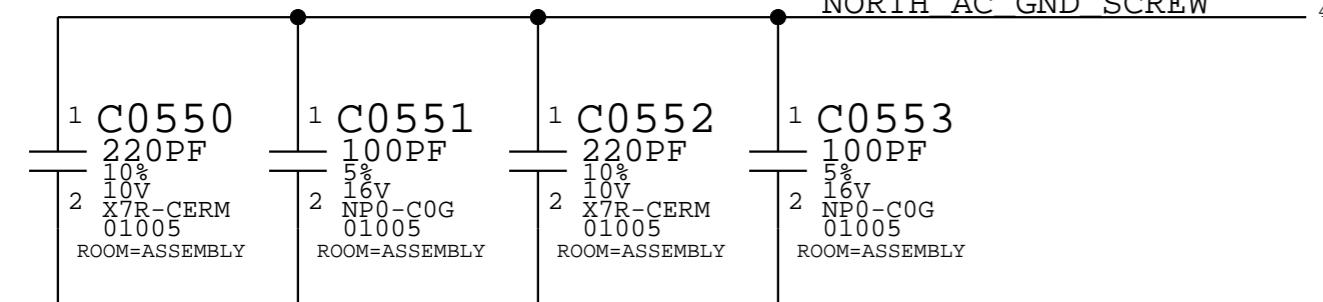
BS0507
STDOFF-2.60D0.5H-0.5-1.7-TH
ROOM=ASSEMBLY

860-8396

33

TOP-SIDE

D
SHIM WASHER
806-02971
BS0508
2.7X1.94X0.25
RING-TH1
ROOM=ASSEMBLY

C
SOUTH DC CURRENT BLOCKING CAPSB
NORTH DC CURRENT BLOCKING CAPSA
SOUTH TUBE STANOFF

BS0500
STDOFF-2.70OD1.84ID-0.88H-TH
860-7862

TODO: UPDATE REF DES

STOCKHOLM FEED

BS0505
STDOFF-2.56OD1.4ID.99H-SM
ROOM=ASSEMBLY

860-00109

33 AP TO STOCKHOLM ANT

860-00096

BS0503
STDOFF-2.60D0.81H-TH
ROOM=ASSEMBLY

860-00111

BS0502
STDOFF-2.85OD1.4ID-0.84H
ROOM=ASSEMBLY

PLATED SLOTTED THRU-HOLE

CL0502
TH-NSP

SL-1.20X0.40-1.50X0.70-NSP

860-00111

860-00096

C
UPPER SHIELD

OMIT_TABLE

SH0503
SM

SHLD-EMI-UPPER-BACK-N61

C
UPPER SHIELD

OMIT_TABLE

SH0500
SM

SHLD-EMI-UPPER-FRONT-N61

C
LOWER SHIELD

OMIT_TABLE

SH0501
SM

SHLD-EMI-LOWER-FRONT-N61

C
SA SHIELD

OMIT_TABLE

SH0502
SM

SHLD-EMI-SA-N71

ROOM=ASSEMBLY

860-01802

CLIP-RETENTION-COAX-DOUBLE

CL0501
SM

ROOM=ASSEMBLY

806-01802

32 4 SOUTH AC GND SCREW

OP5SQ-SMP3SQ-NSP

1 ROOM=ASSEMBLY

FD0513
FID

OP5SQ-SMP3SQ-NSP

1 ROOM=ASSEMBLY

BOTTOM-SIDE

D
FIDUCIALSFD0501
FID

OP5SM1POSQ-NSP

1 ROOM=ASSEMBLY

FD0502
FID

OP5SM1POSQ-NSP

1 ROOM=ASSEMBLY

FD0503
FID

OP5SM1POSQ-NSP

1 ROOM=ASSEMBLY

FD0505
FID

OP5SM1POSQ-NSP

1 ROOM=ASSEMBLY

FD0510
FID

OP5SQ-SMP3SQ-NSP

1 ROOM=ASSEMBLY

FD0511
FID

OP5SQ-SMP3SQ-NSP

1 ROOM=ASSEMBLY

FD0512
FID

OP5SQ-SMP3SQ-NSP

1 ROOM=ASSEMBLY

FD0514
FID

OP5SQ-SMP3SQ-NSP

1 ROOM=ASSEMBLY

FD0515
FID

OP5SQ-SMP3SQ-NSP

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FD0504
FID

OP5SM1POSQ-NSP

1 ROOM=ASSEMBLY

FD0513
FID

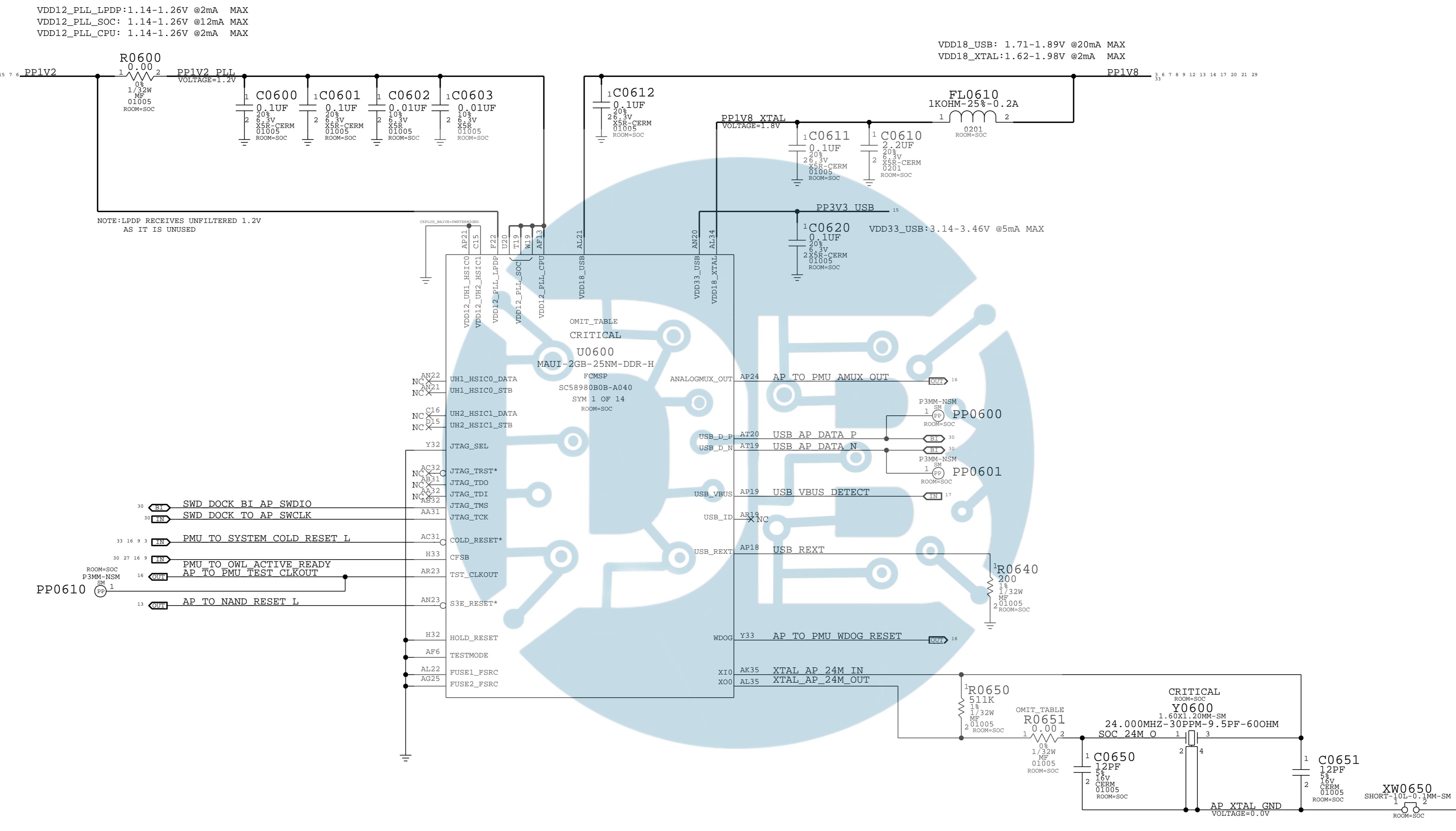
OP5SQ-SMP3SQ-NSP

1 ROOM=ASSEMBLY

PAGE TITLE	SYSTEM: MECHANICAL	
Apple Inc.	DRAWING NUMBER	SIZE
	051-1902	D
REVISION	A.0.0	
BRANCH		
PAGE	5 OF 49	
SHEET	4 OF 59	

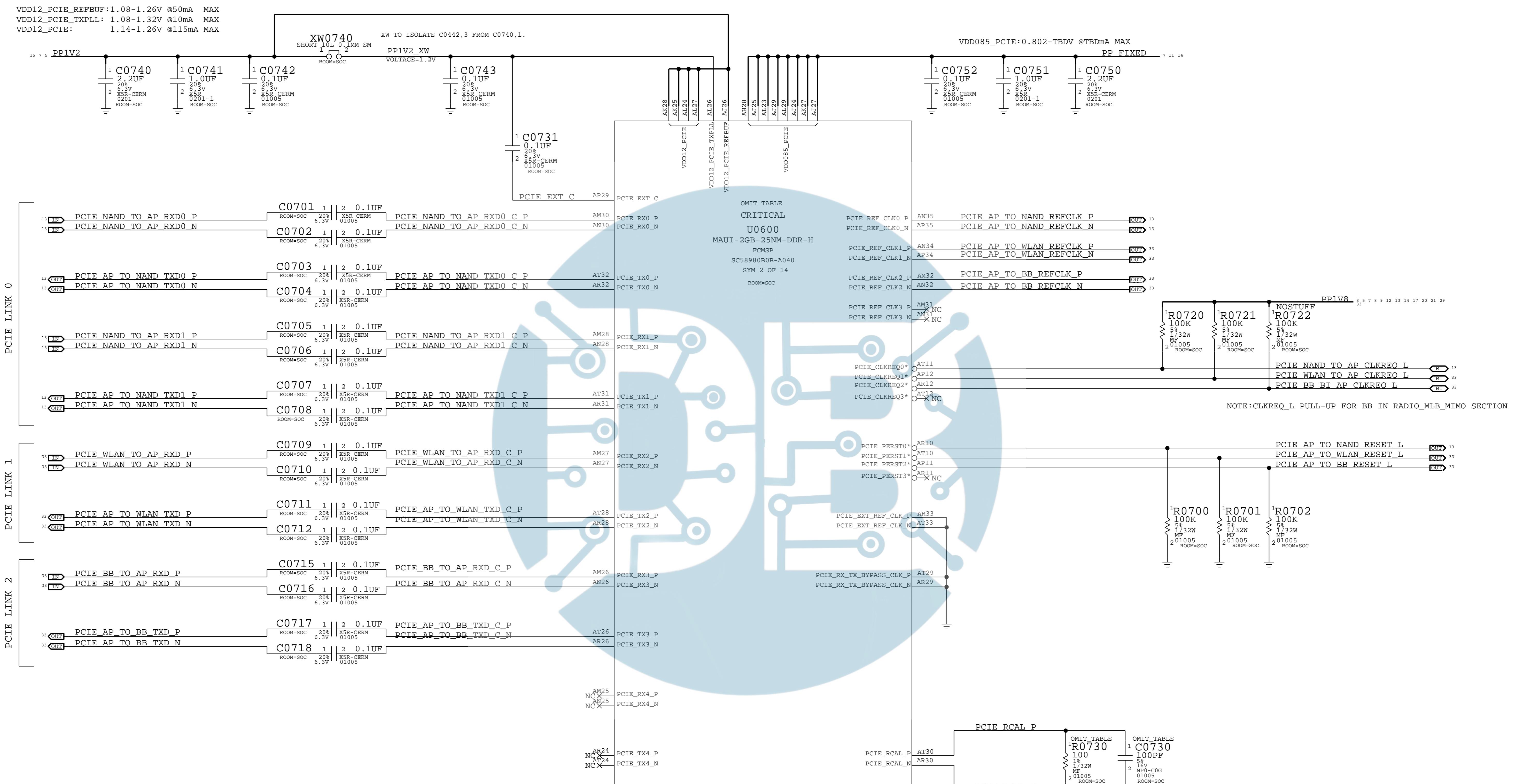
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MAUI - USB, JTAG, XTAL



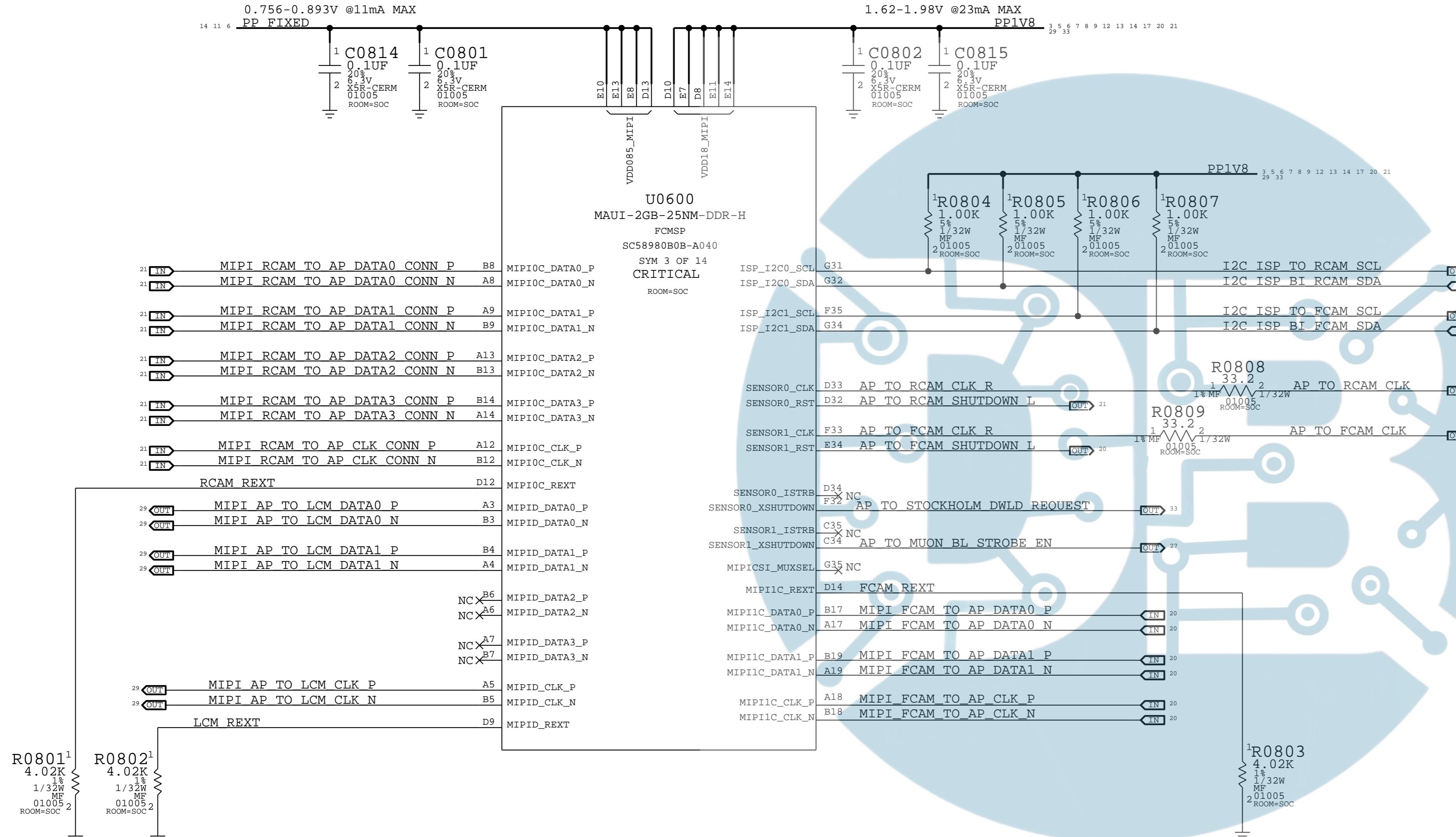
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Apple Inc.	DRAWING NUMBER 051-1902 SIZE D
REVISION A.0.0	BRANCH
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	SHEET 5 OF 59

MAUI - PCIE INTERFACES



SYNC_MASTER=N/A	SYNC_DATE=N/A
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SOC:PCIE	Apple Inc.
DRAWING NUMBER	051-1902
SIZE	D
REVISION	A.0.0
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BRANCH	
PAGE	7 OF 49
SHEET	6 OF 59

MAUI - CAMERA & DISPLAY INTERFACES



SYNC_MASTER=N/A	SYNC_DATE=N/A
PAGE TITLE	
Apple Inc.	DRAWING NUMBER 051-1902 SIZE D
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	SHEET 7 OF 59

MAUI - GPIO & SERIAL INTERFACES

D

D

C

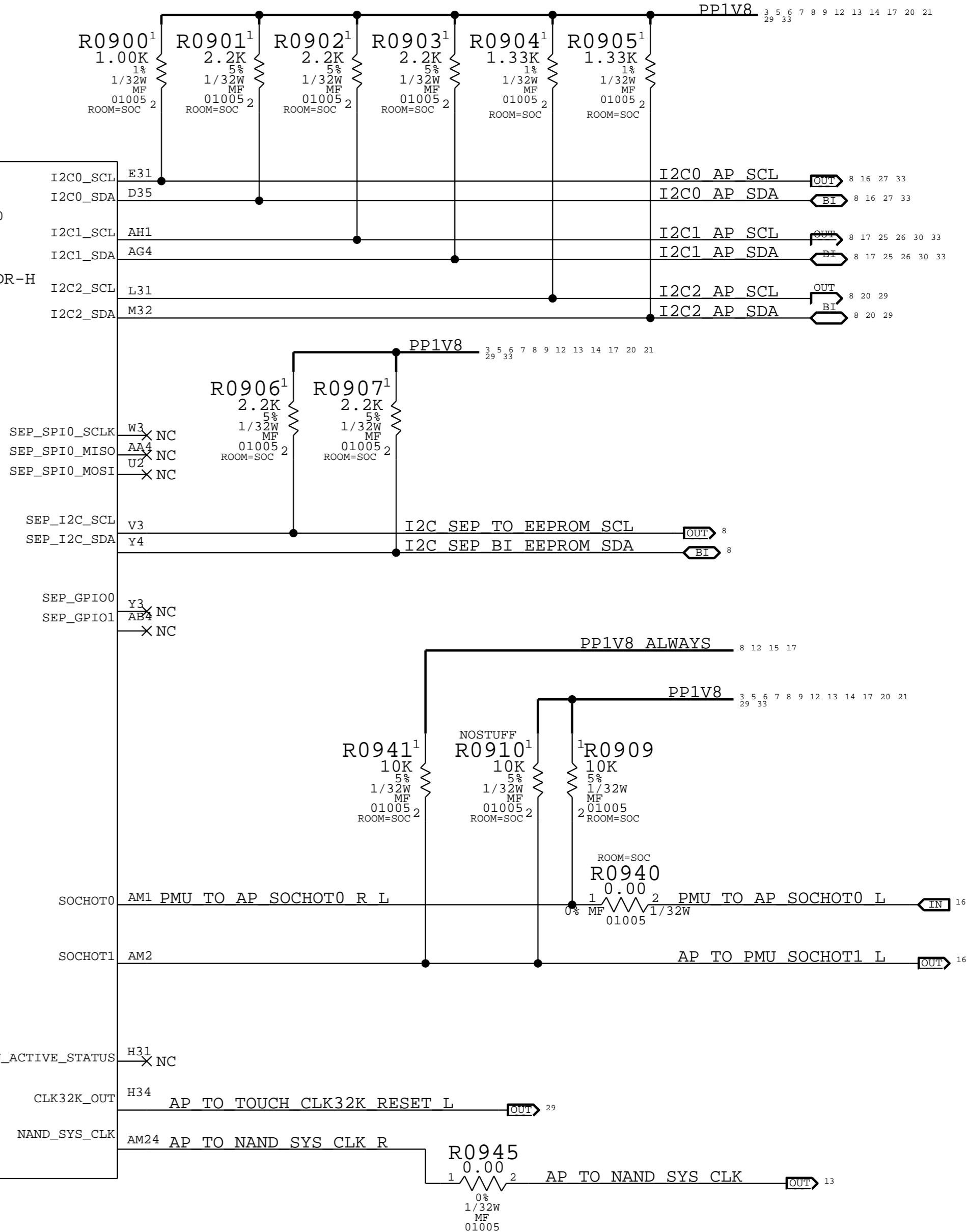
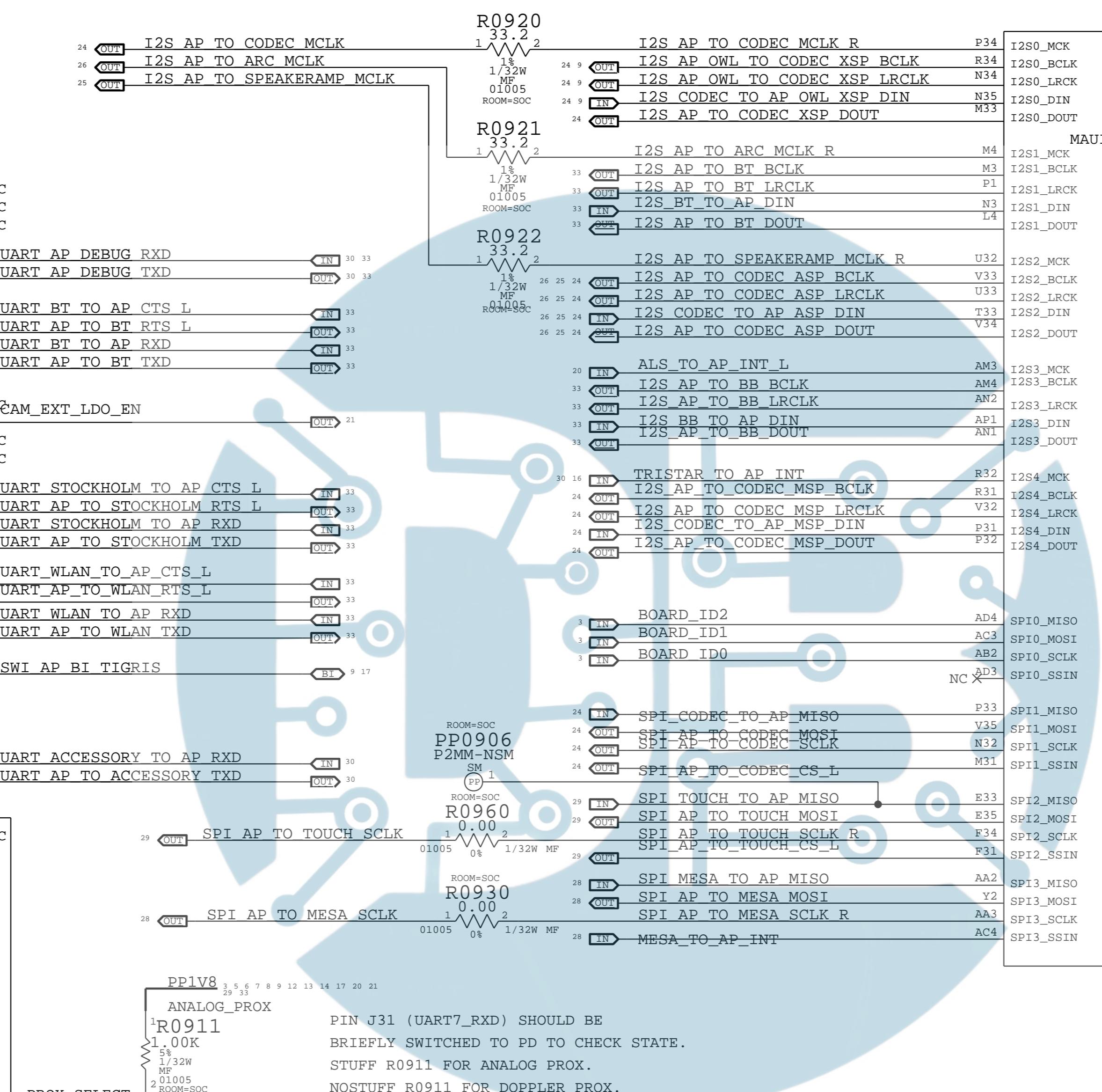
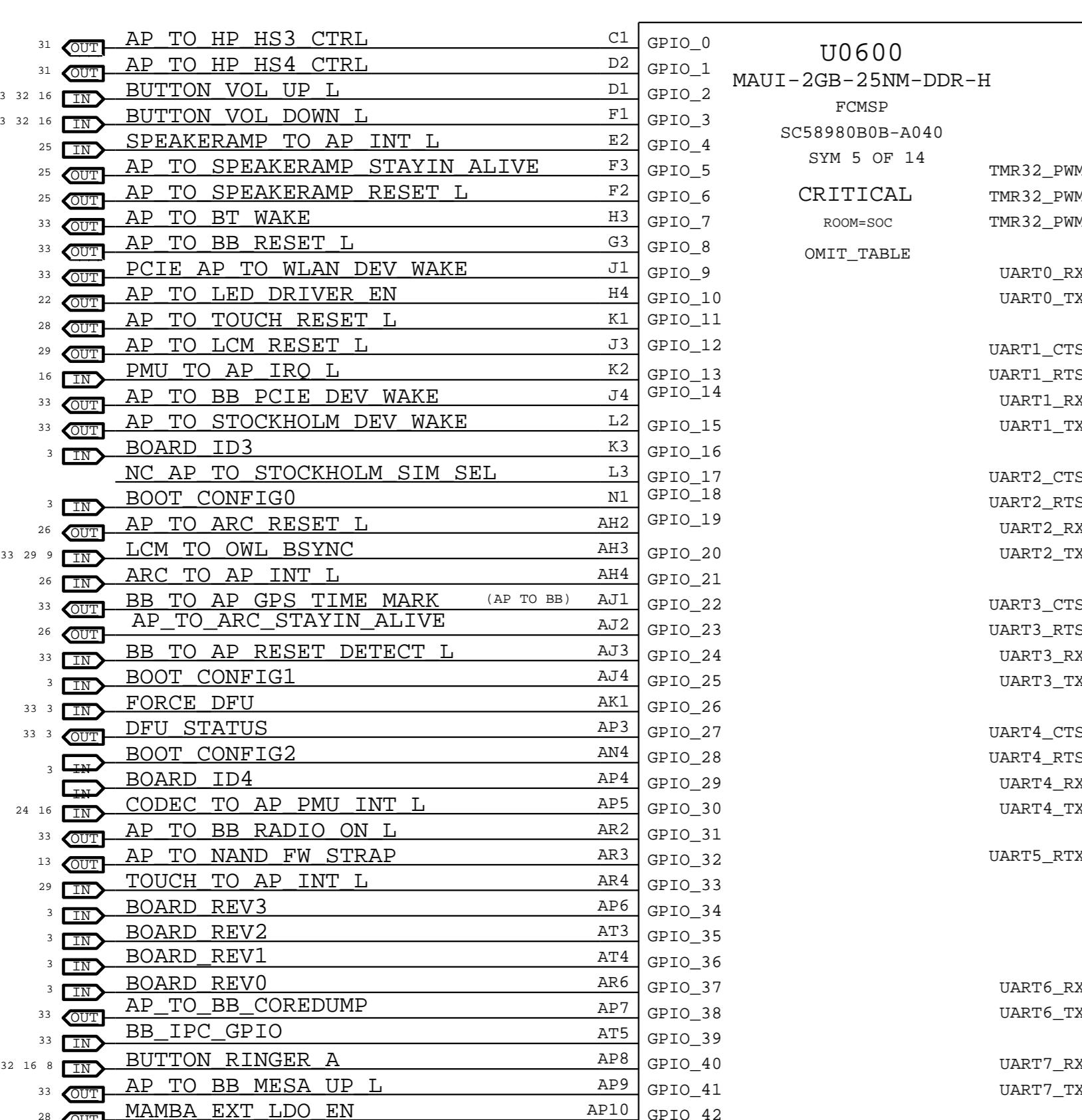
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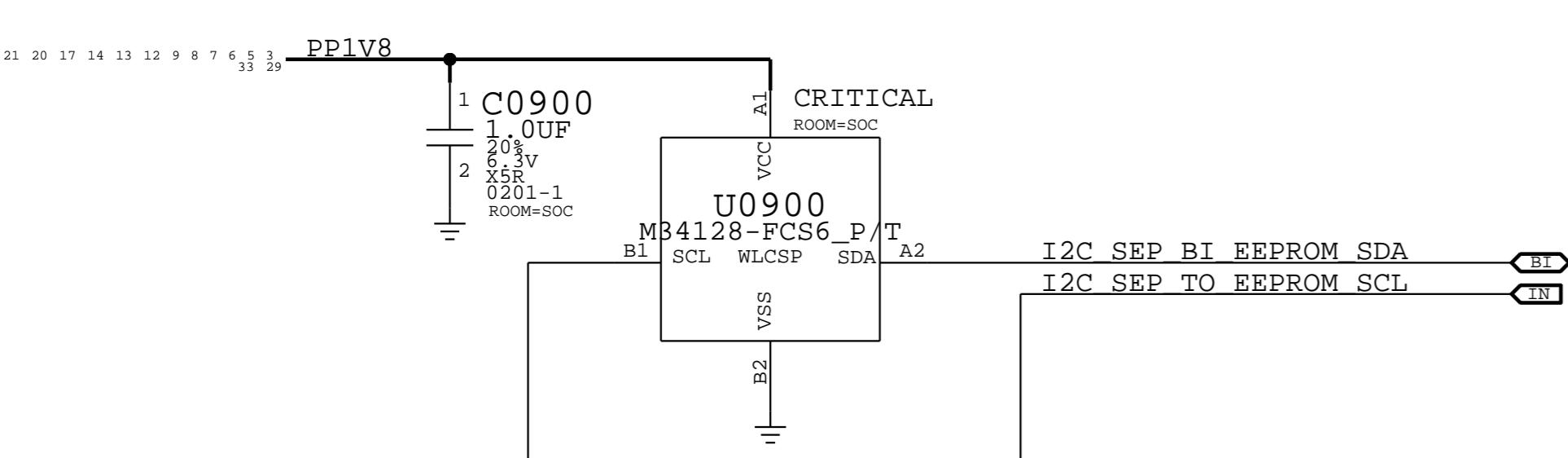
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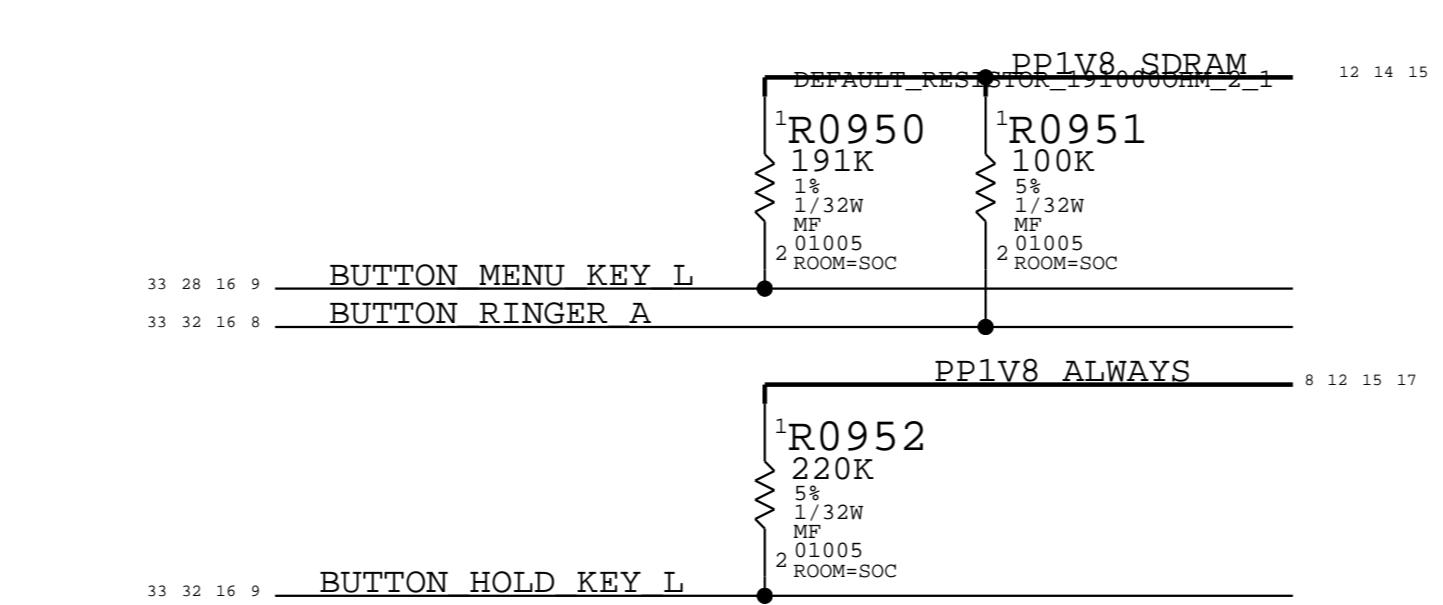
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ANTI-ROLLBACK EEPROM
128kbit
APN: 335S0946



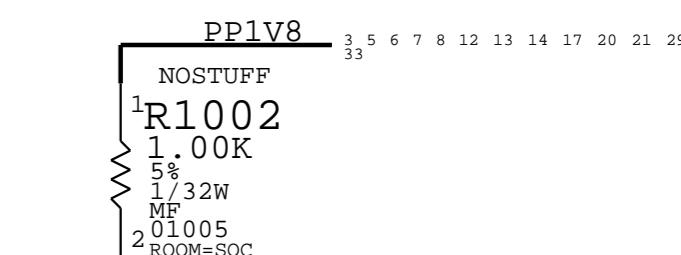
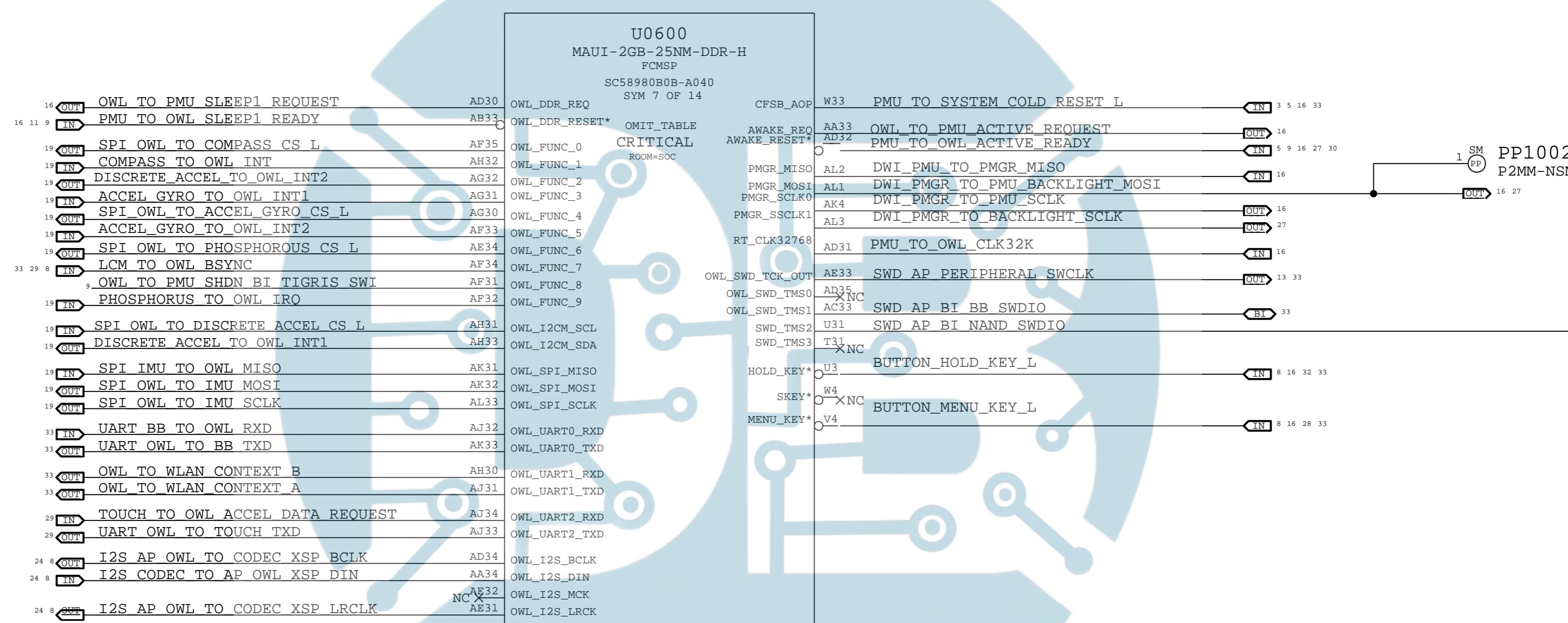
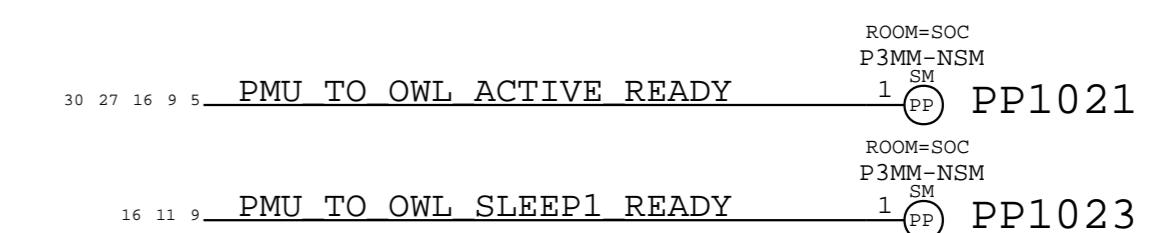
BUTTON PULL-UP RESISTORS



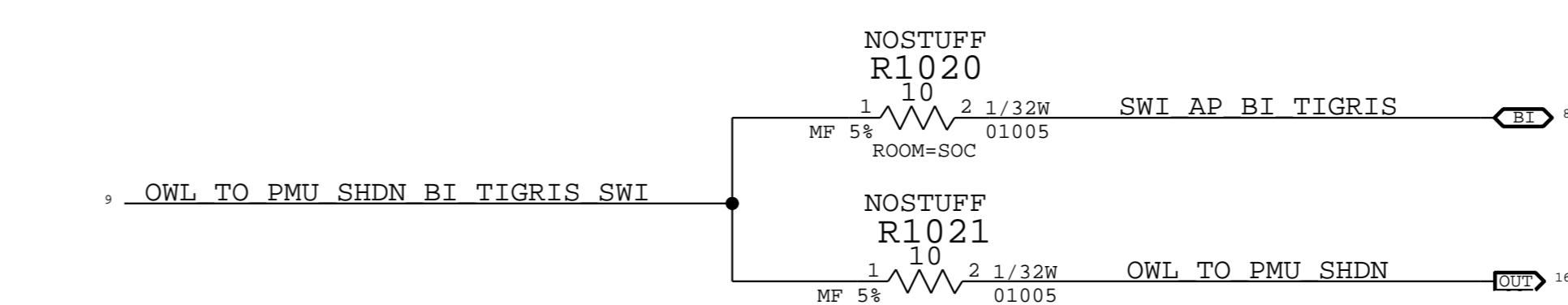
MAUI - OWL



POWER STATE CONTROL PROBE POINTS

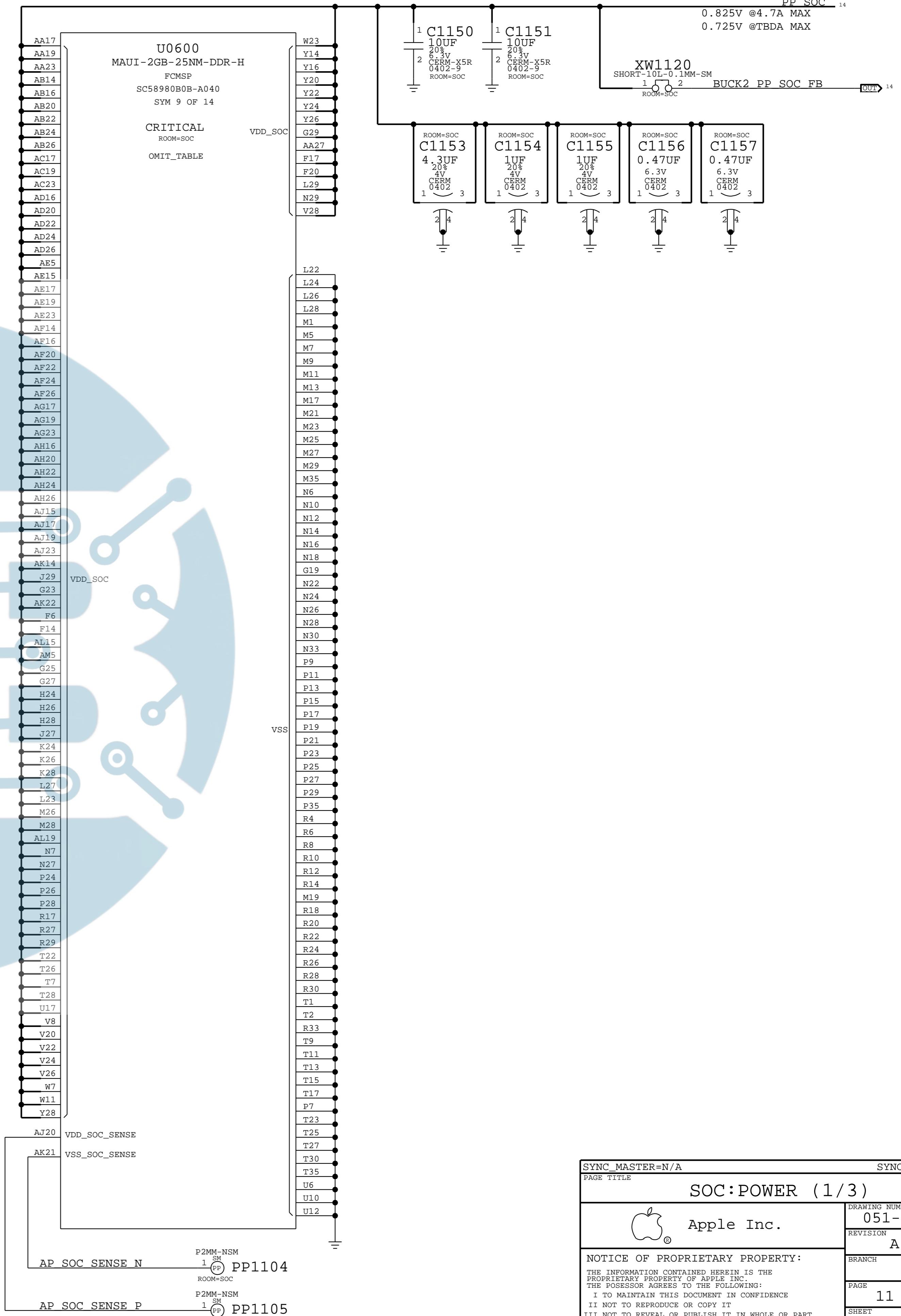
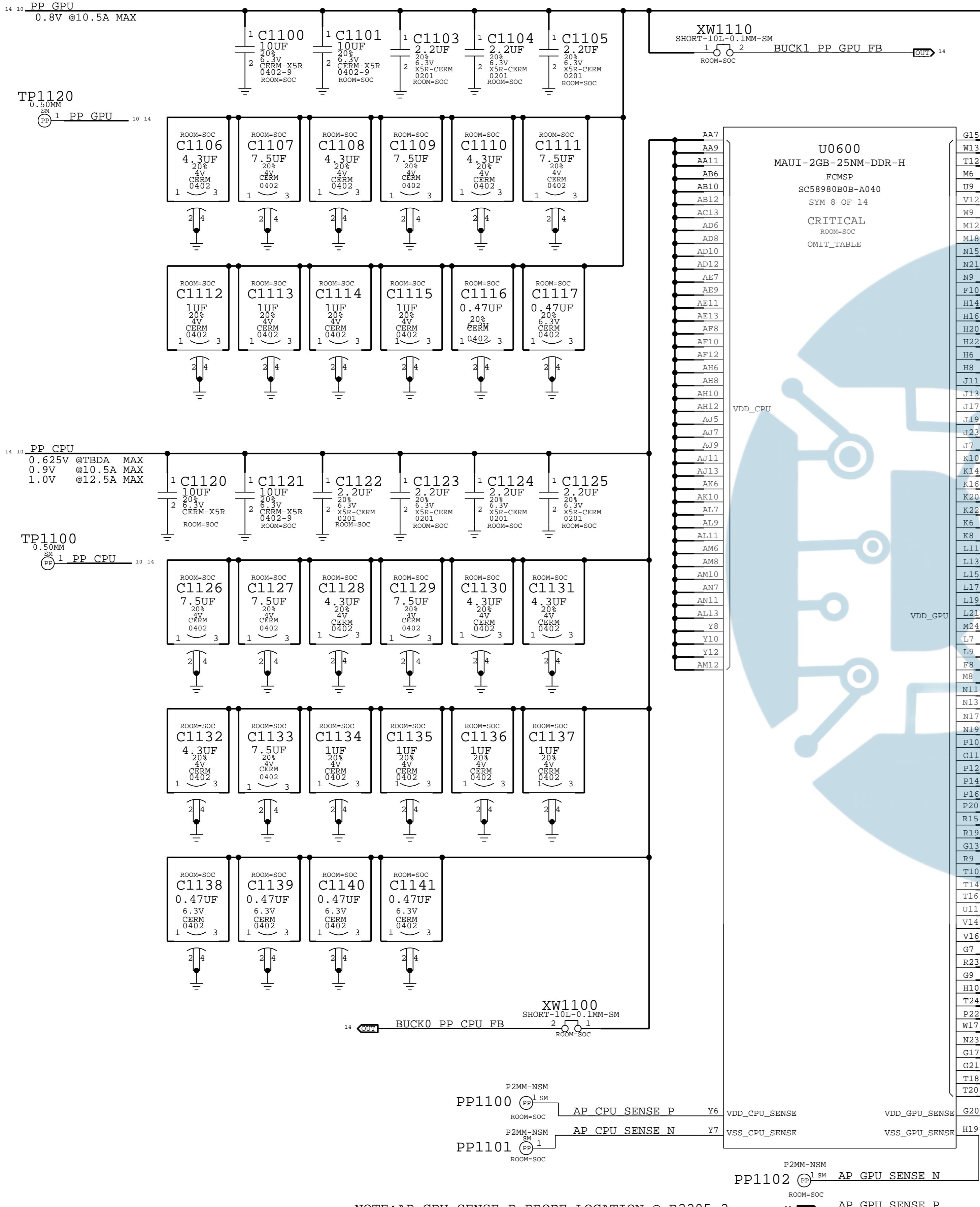


OWL SYSTEM SHUTDOWN OPTION



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	SHEET 9 OF 59

MAUI - CPU, GPU & SOC RAILS



MAUI - POWER SUPPLIES

D

D

C

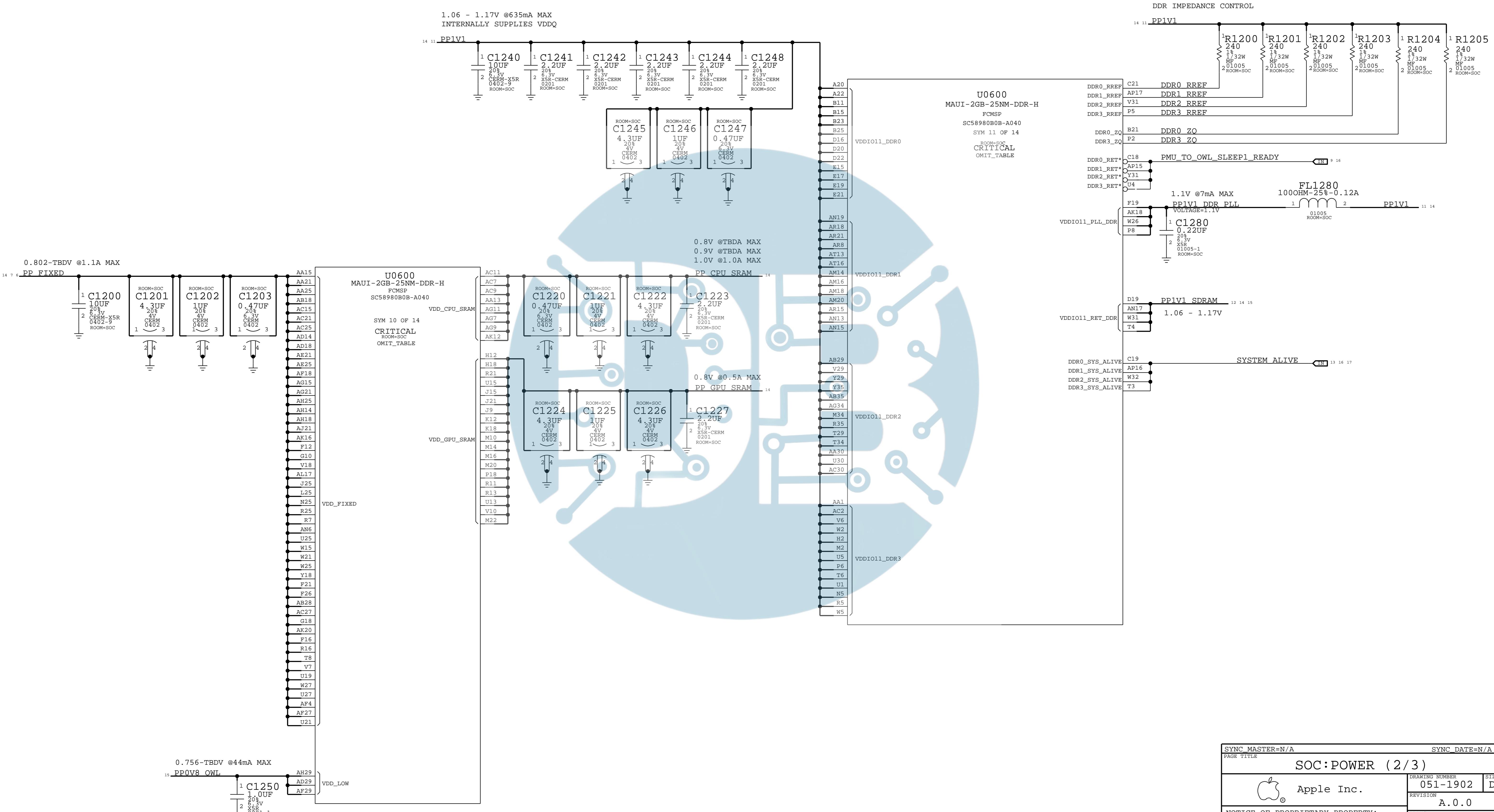
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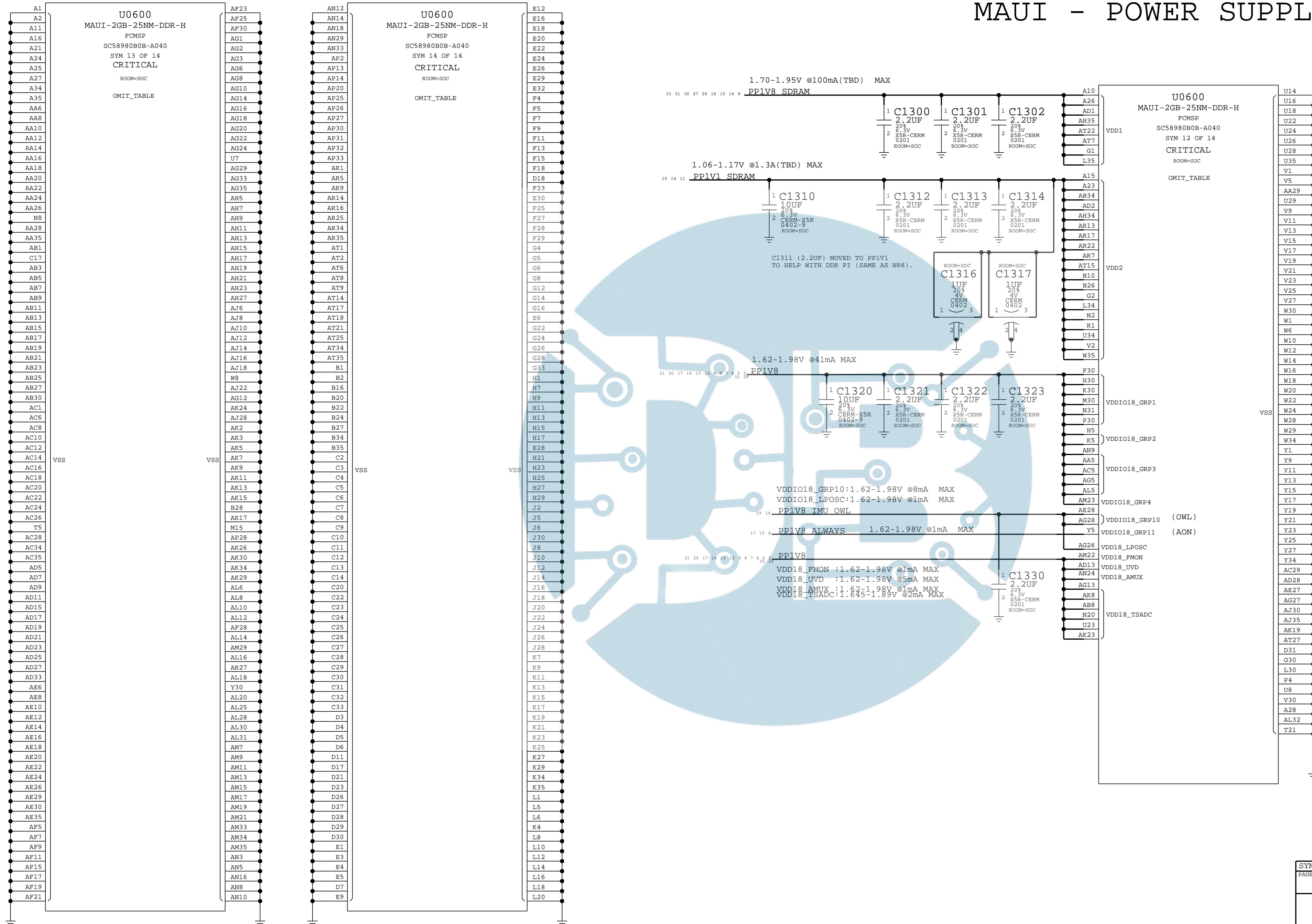
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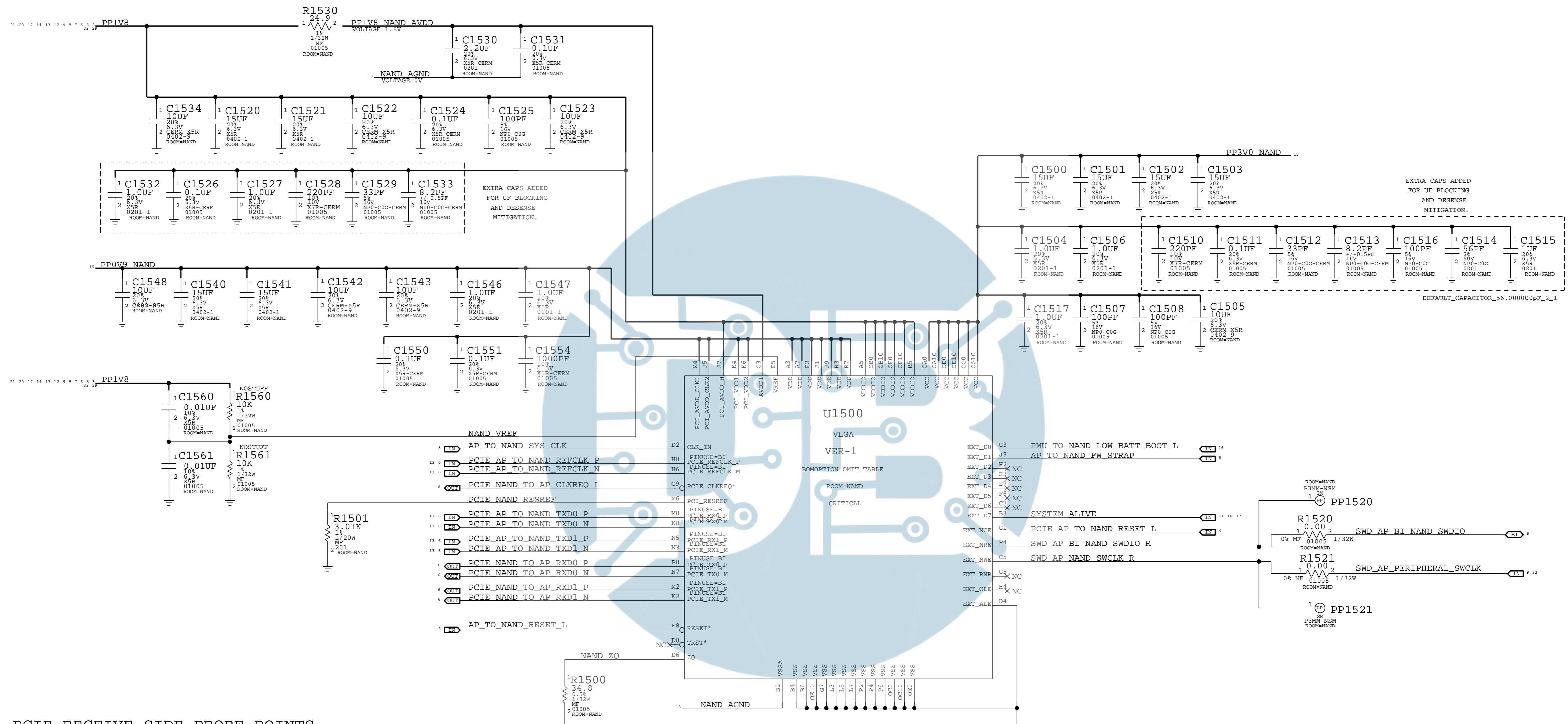


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MAUI - POWER SUPPLIES



S3E NAND



PCIE RECEIVE-SIDE PROBE POINTS

13 6 PCIE AP TO NAND REFCLK P 1 PP1500
13 6 PCIE AP TO NAND REFCLK N 1 PP1501

PCIE_AP_TO_NAND_TXD0_P

PCIE_AP_TO_NAND_TXD0_N

ROOM=NAND
P3MM-NSM
SM

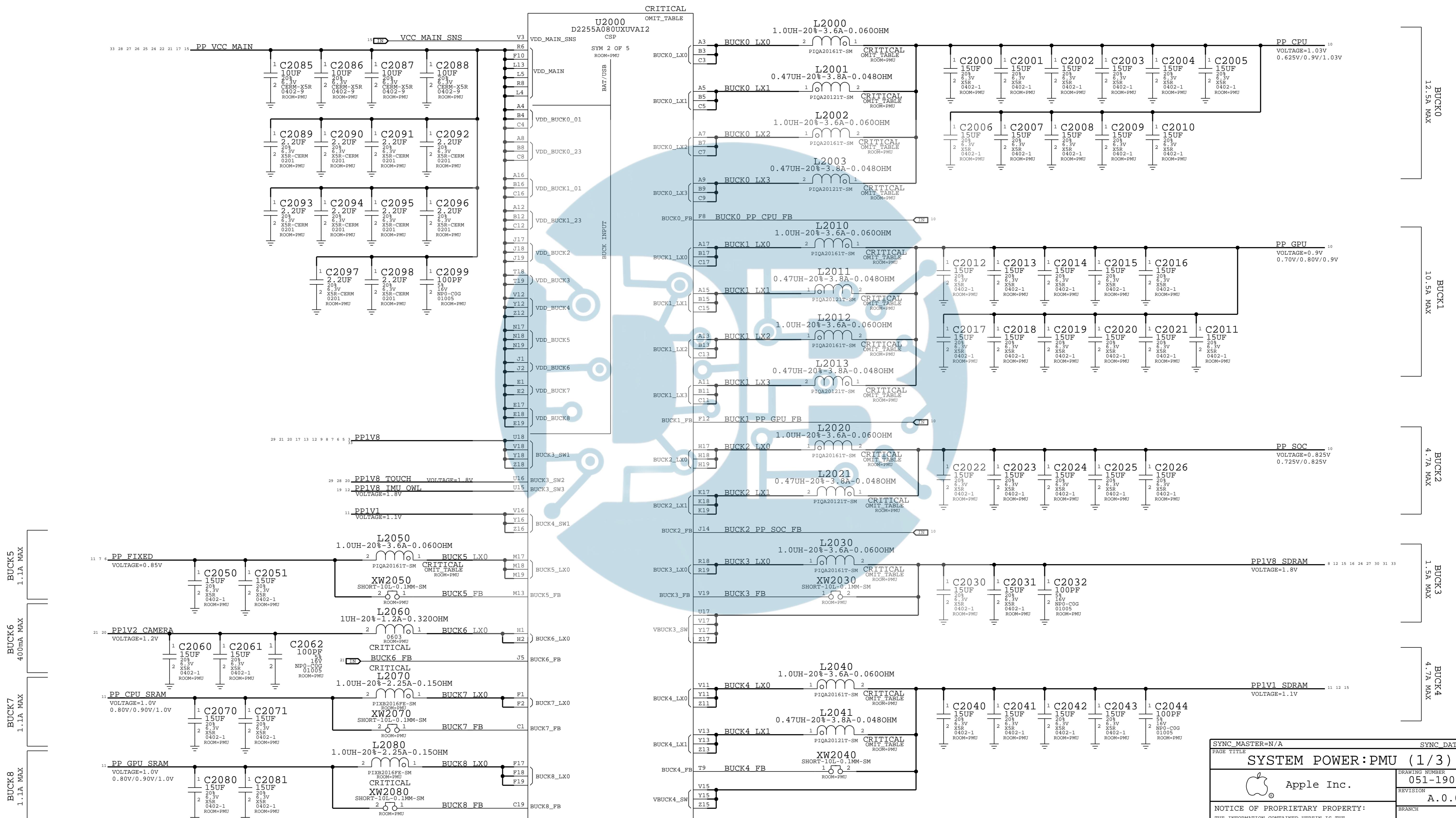
PP1502

PP1503

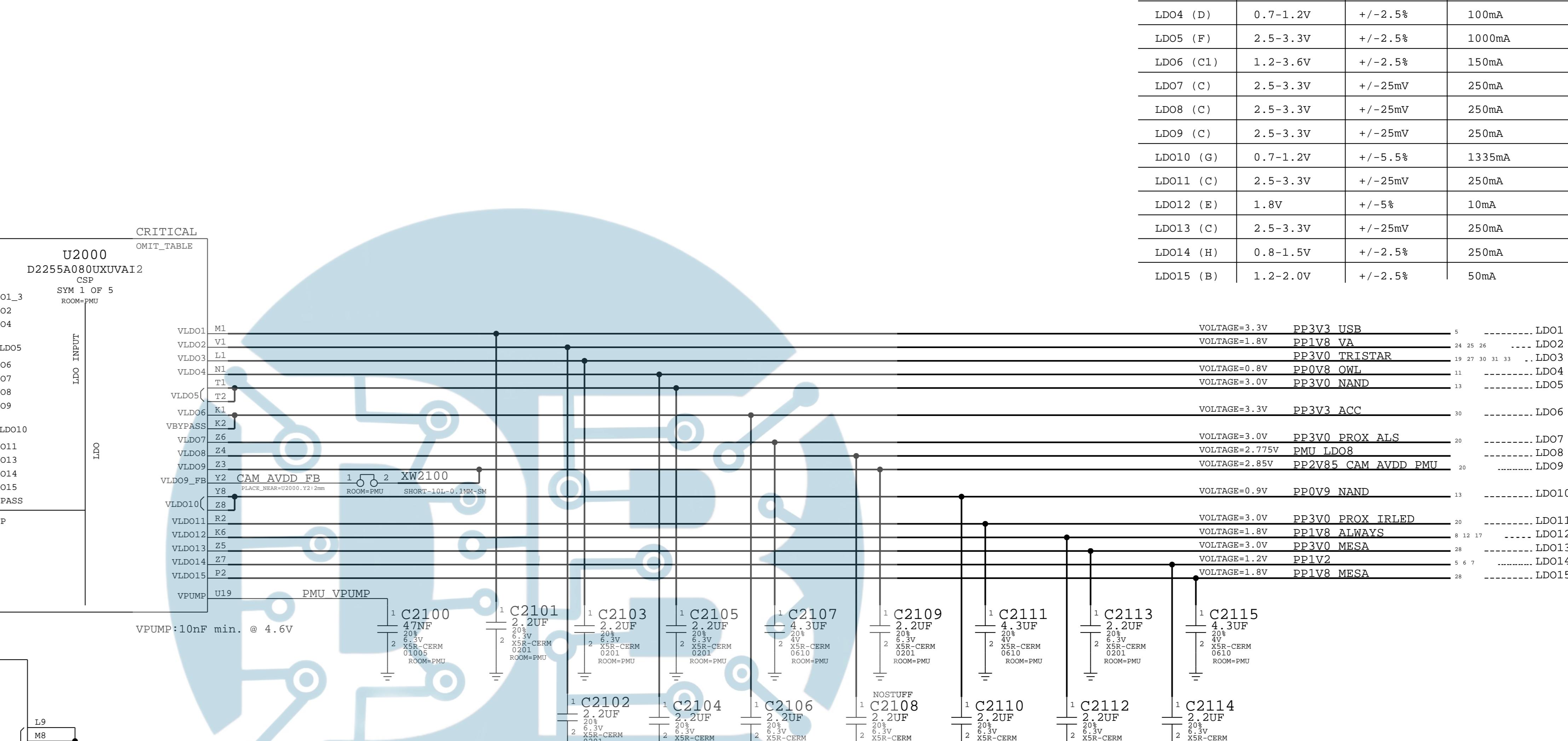
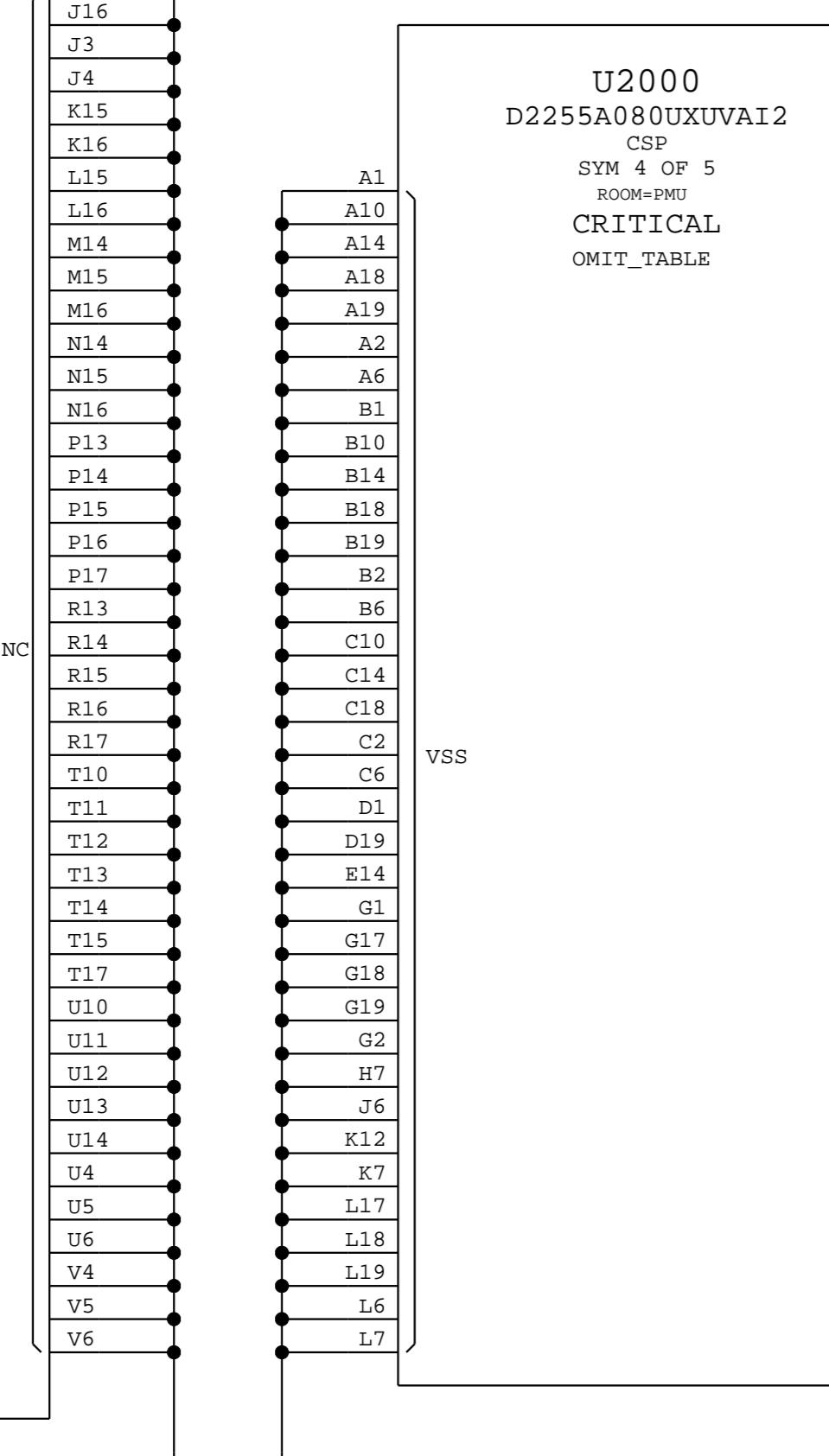
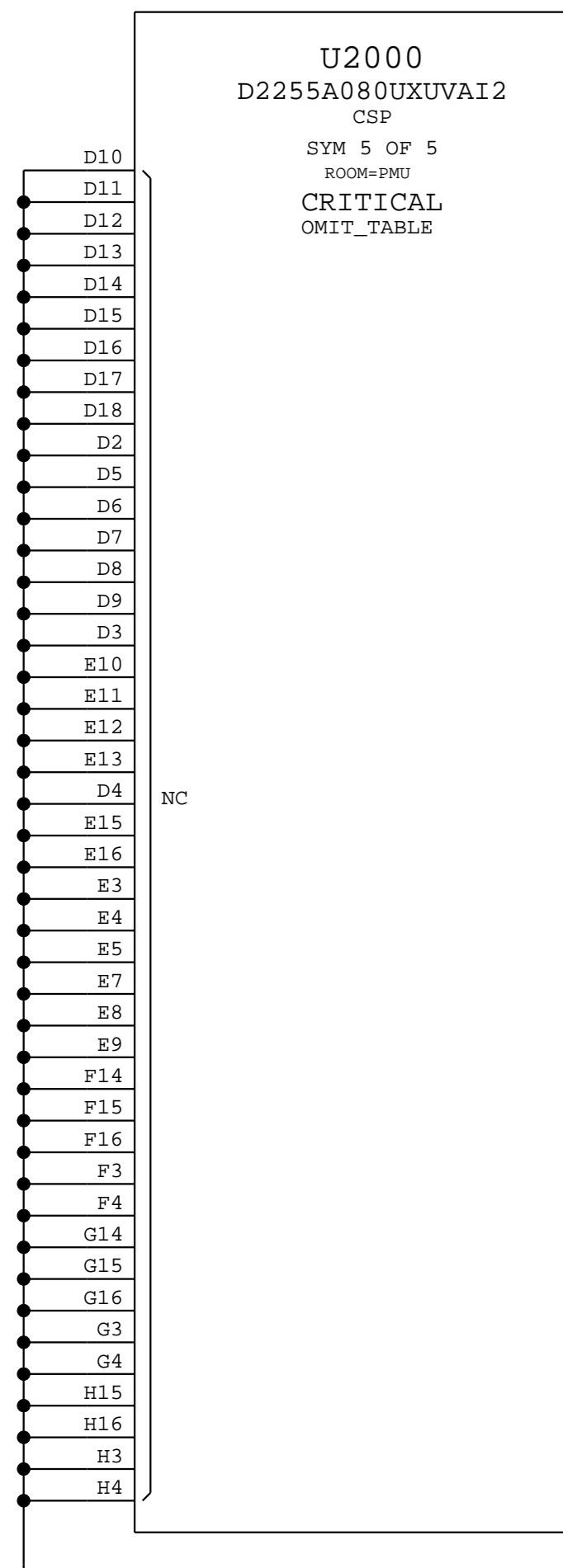
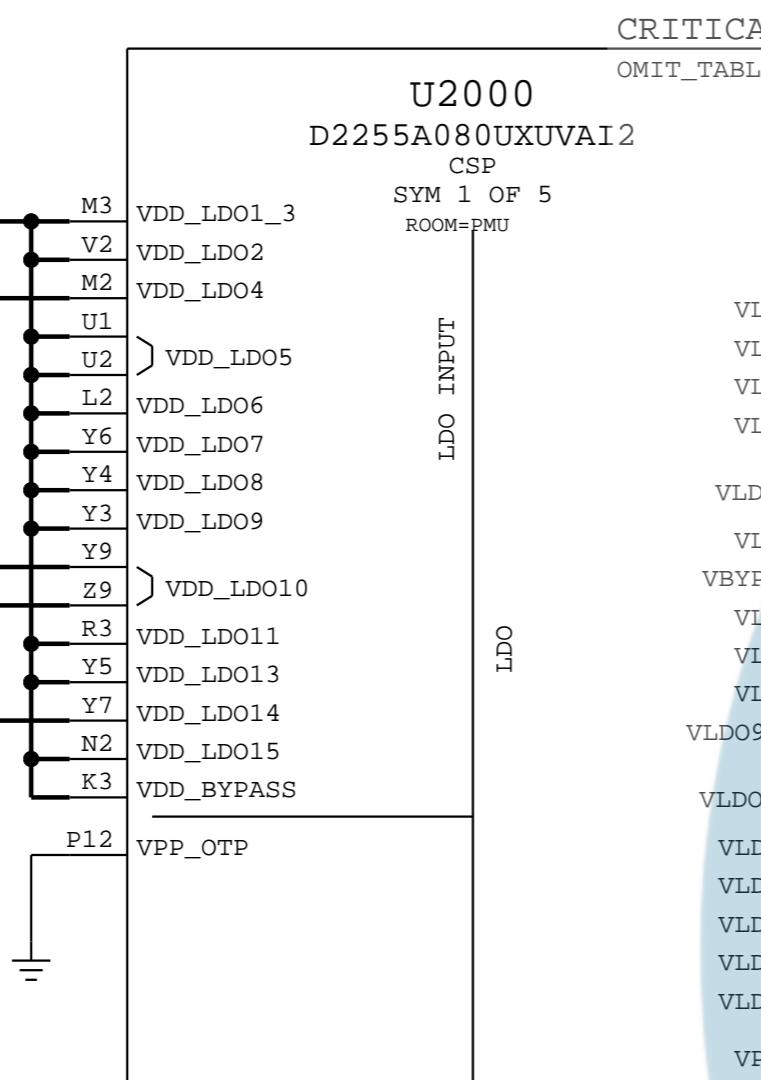
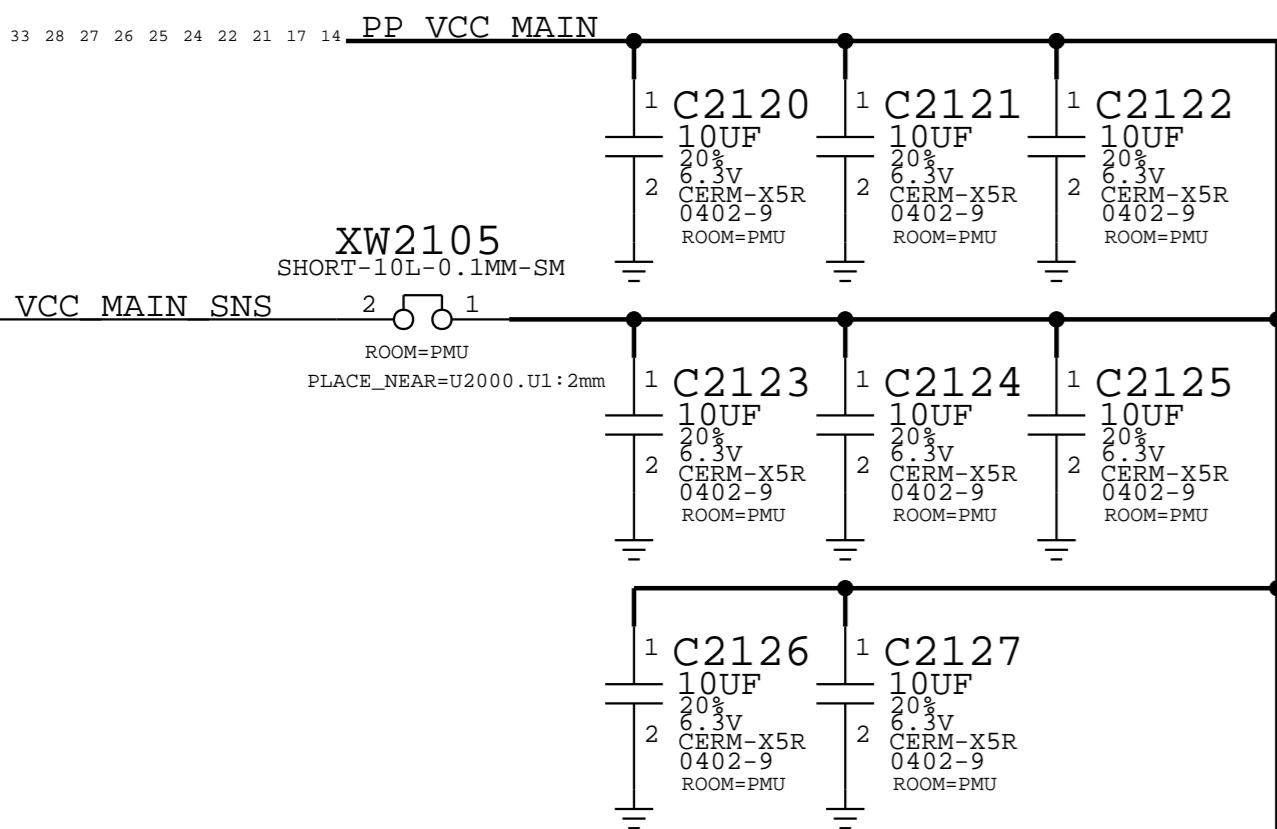
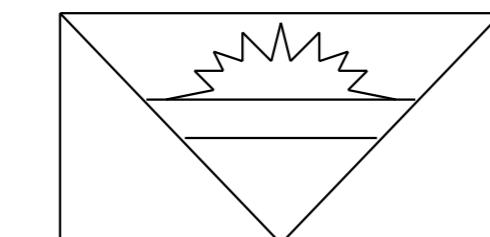
13 6 PCIE_AP_TO_NAND_TXD1_P 1 SM
13 6 PCIE_AP_TO_NAND_TXD1_N 1 PP1504
PP1505

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 Apple Inc.	DRAWING NUMBER 051-1902 SIZE D
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	PAGE 15 OF 49
	SHEET 13 OF 59

ANTIGUA PMU - Buck Supplies

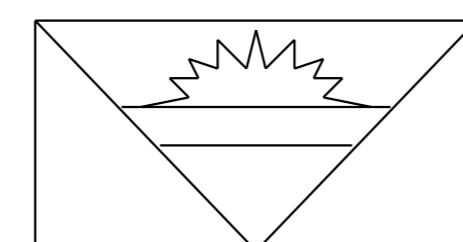


ANTIGUA PMU - LDOs



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Apple Inc.	DRAWING NUMBER 051-1902 SIZE D
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ANTIGUA PMU - GPIOs, NTCs



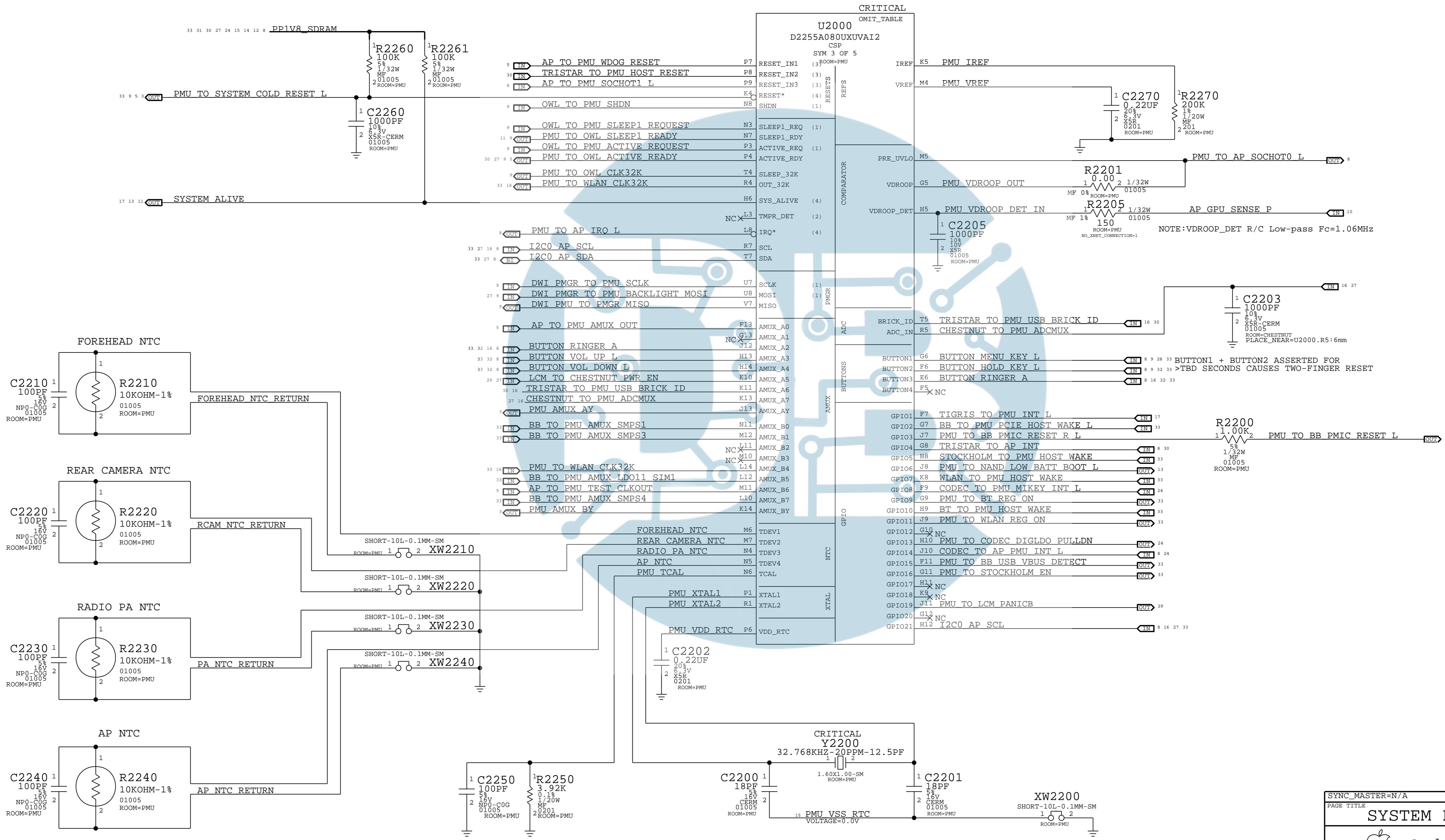
CONTROL PIN NOTES:

NOTE (1): INPUT PULL-DOWN 100-300k

NOTE (2): INPUT PULL-DOWN 1M

NOTE (3): INPUT PULL-UP OR DOWN 100k-300k

NOTE (4): OUTPUT OPEN-DRAIN, REQUIRES PULL-UP



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PAGE TITLE	
SYSTEM POWER: PMU (3 / 3)	
Apple Inc.	DRAWING NUMBER 051-1902 SIZE D
REVISION A.0.0	BRANCH
NOTICE OF PROPRIETARY PROPERTY:	PAGE 22 OF 49
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D

D

C

C

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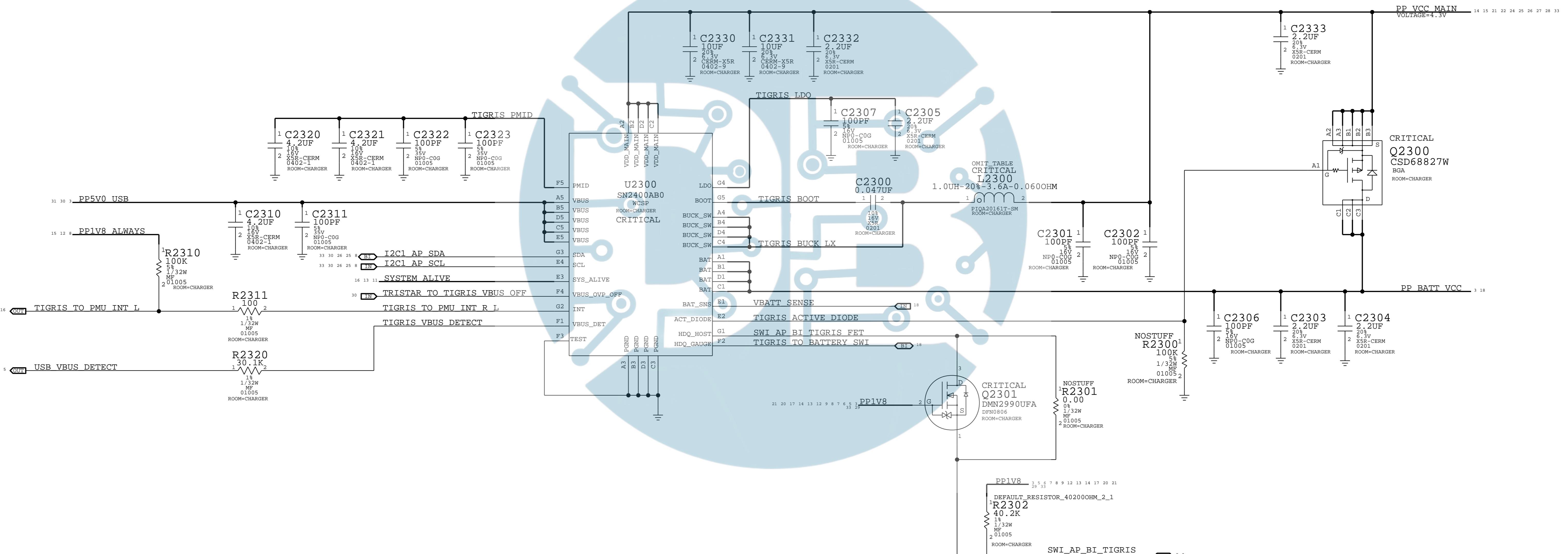
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A

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TIGRIS CHARGER

APN: 343S00033



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PAGE TITLE	SYSTEM POWER:CHARGER
Apple Inc.	
DRAWING NUMBER	051-1902
SIZE	D
REVISION	A.0.0
BRANCH	
PAGE	23 OF 49
SHEET	17 OF 59

D

D

C

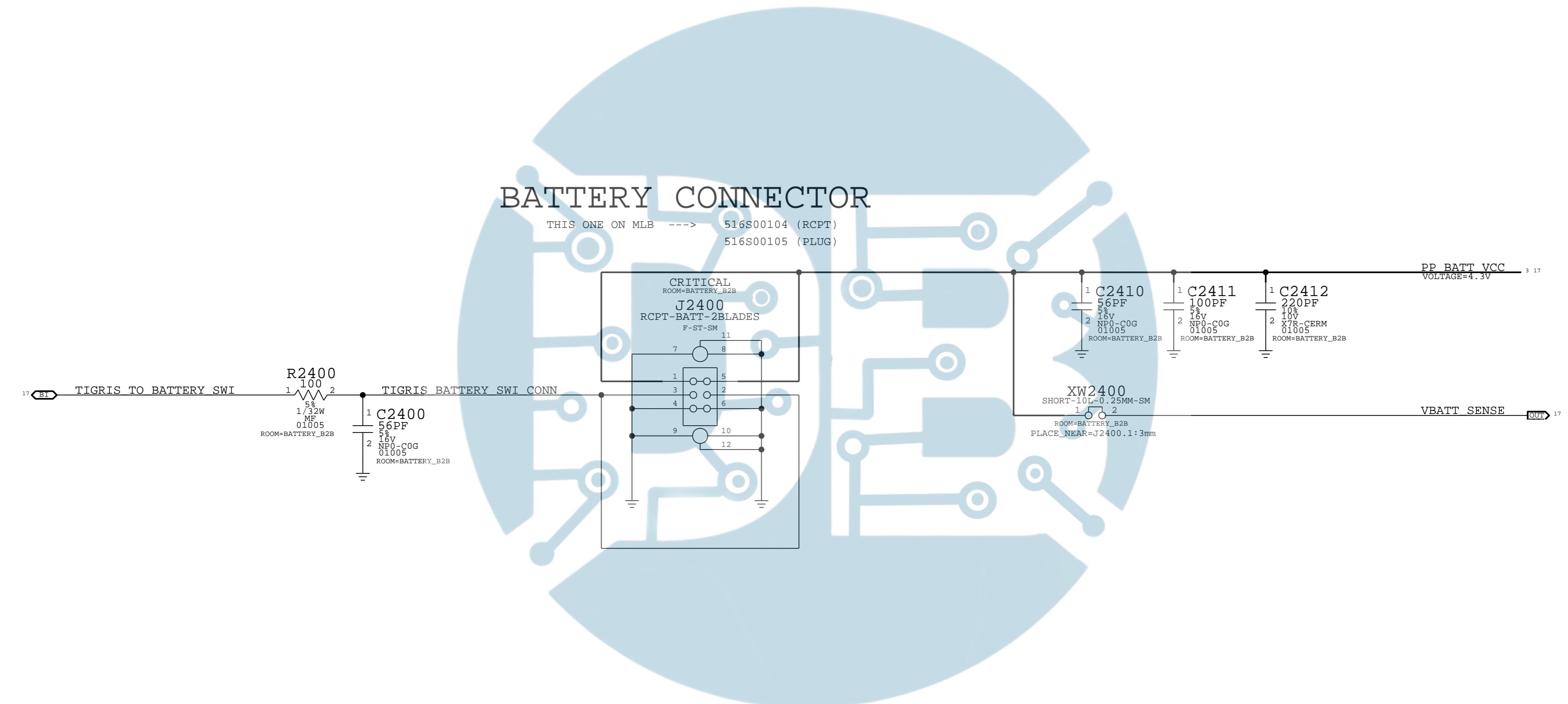
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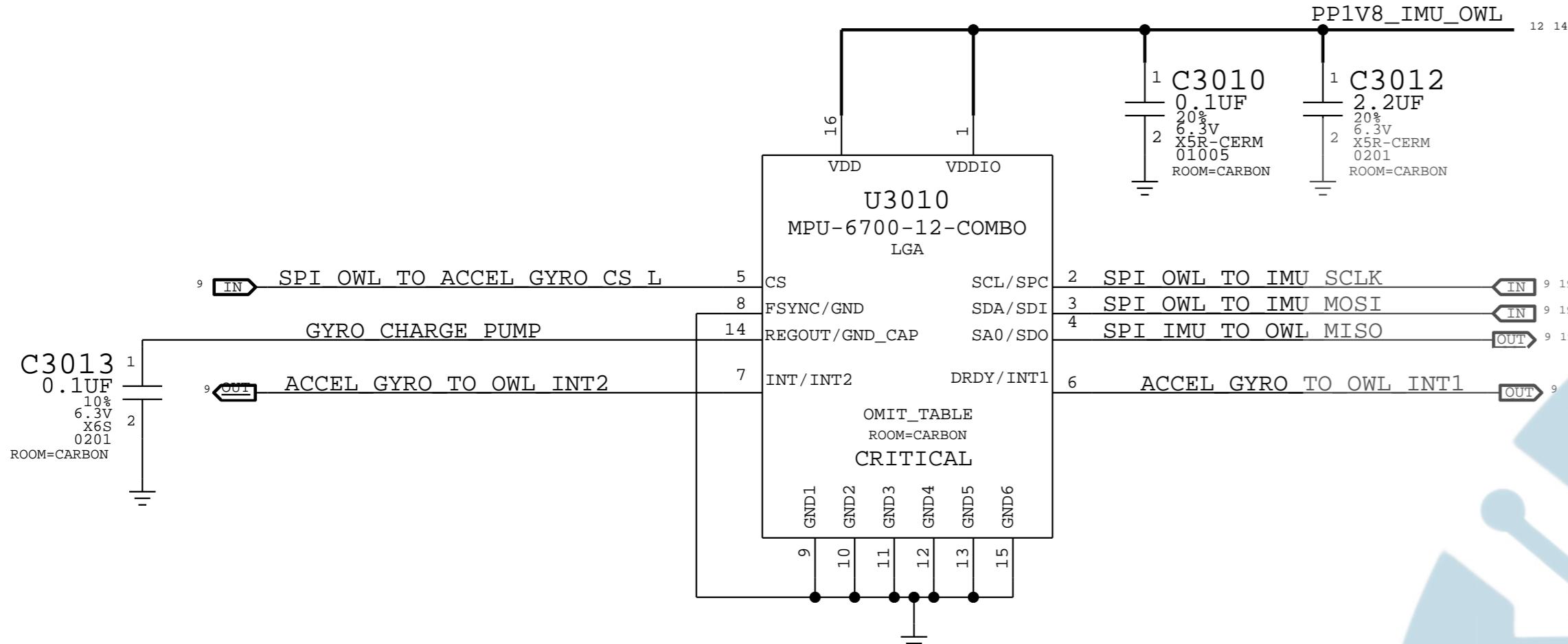
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DRAWING NUMBER 051-1902	SIZE D
REVISION A.0.0	
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PAGE 24 OF 49	
SHEET 18 OF 59	

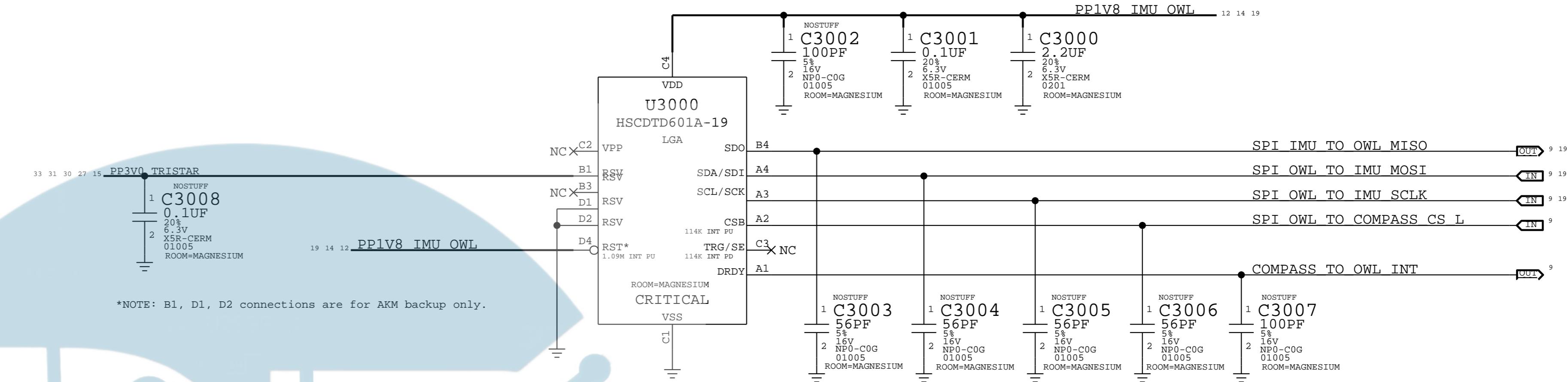
CARBON – ACCEL & GYRO

INVENSENSE (APN: 338S00017, 338S00087)



MAGNESIUM - COMPASS

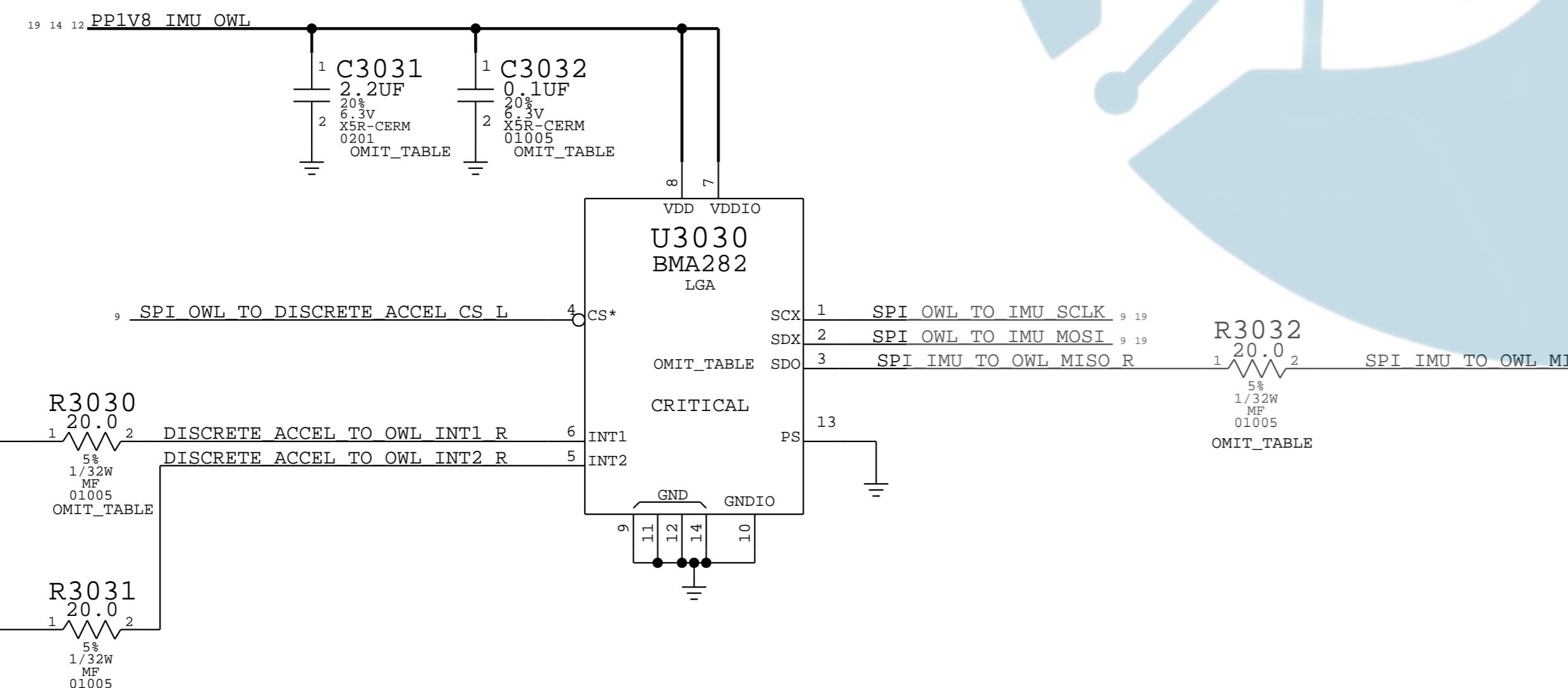
ALPS (APN: 338S00084)



*NOTE: B1, D1, D2 connections are for AKM backup or

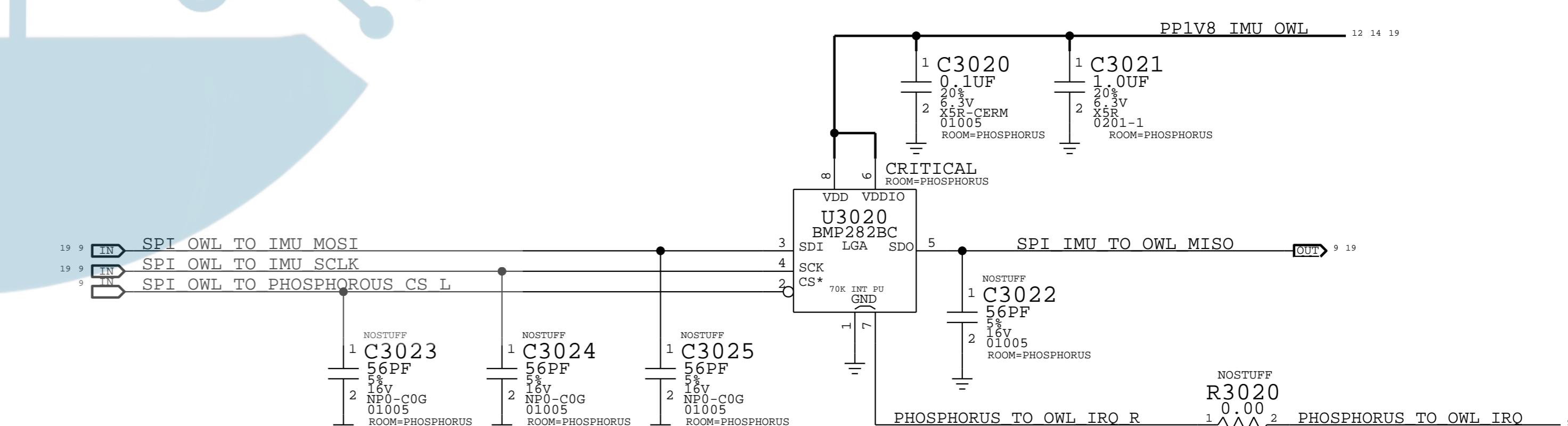
DISCRETE ACCEL

BOSCH (APN: 338S1163)



PHOSPHOROUS

BOSCH (APN: 338S00044)

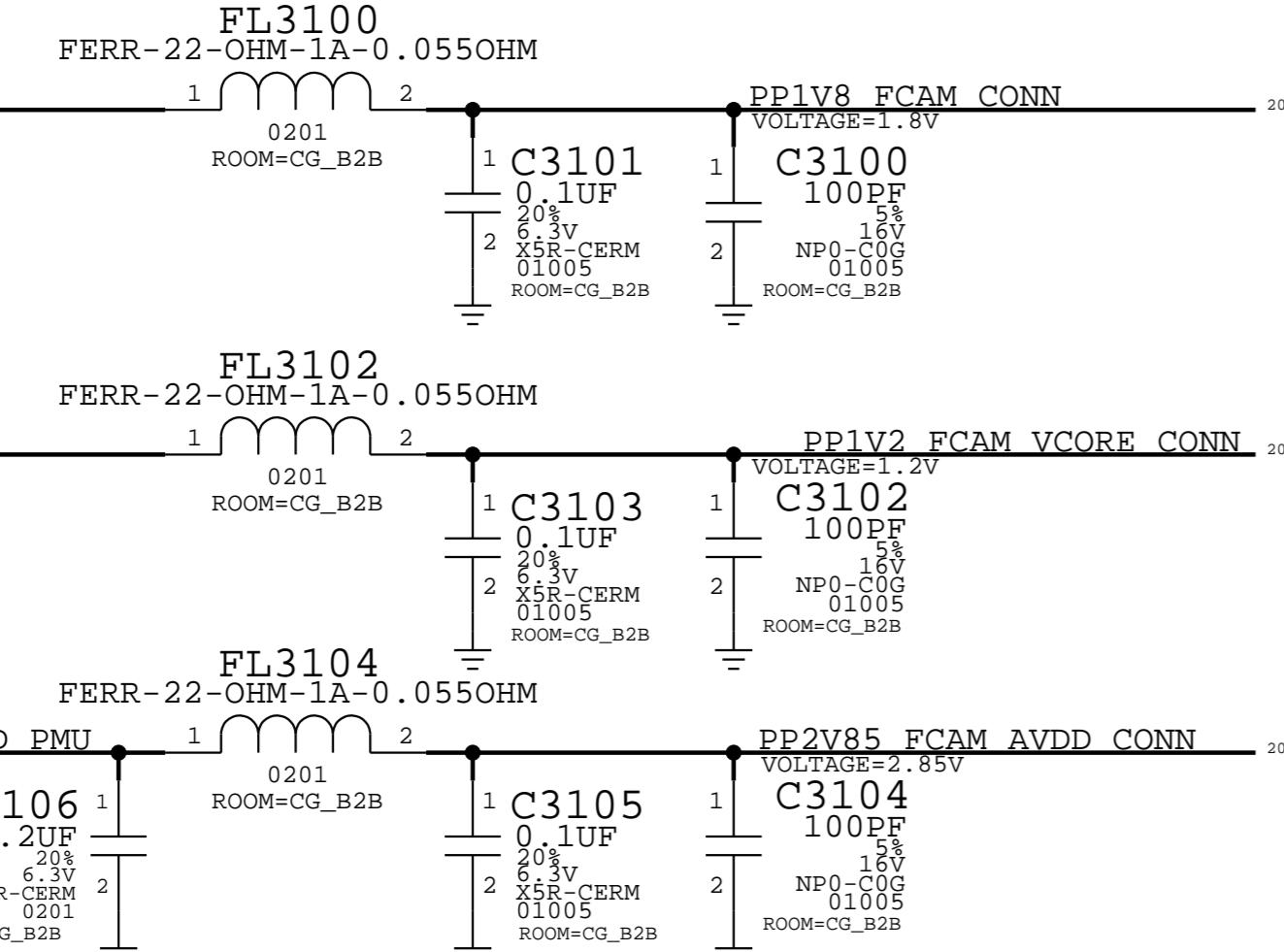


R3020 SHOULD BE STUFFED FOR ST PHOSPHORUS ONLY.
FOR BOSCH PHOSPHORUS, PINS 1 AND 7 ARE SHORTED INTERNALLY,
SO NO NEED FOR 0 OHM TO GROUND OPTION ON PIN 7.

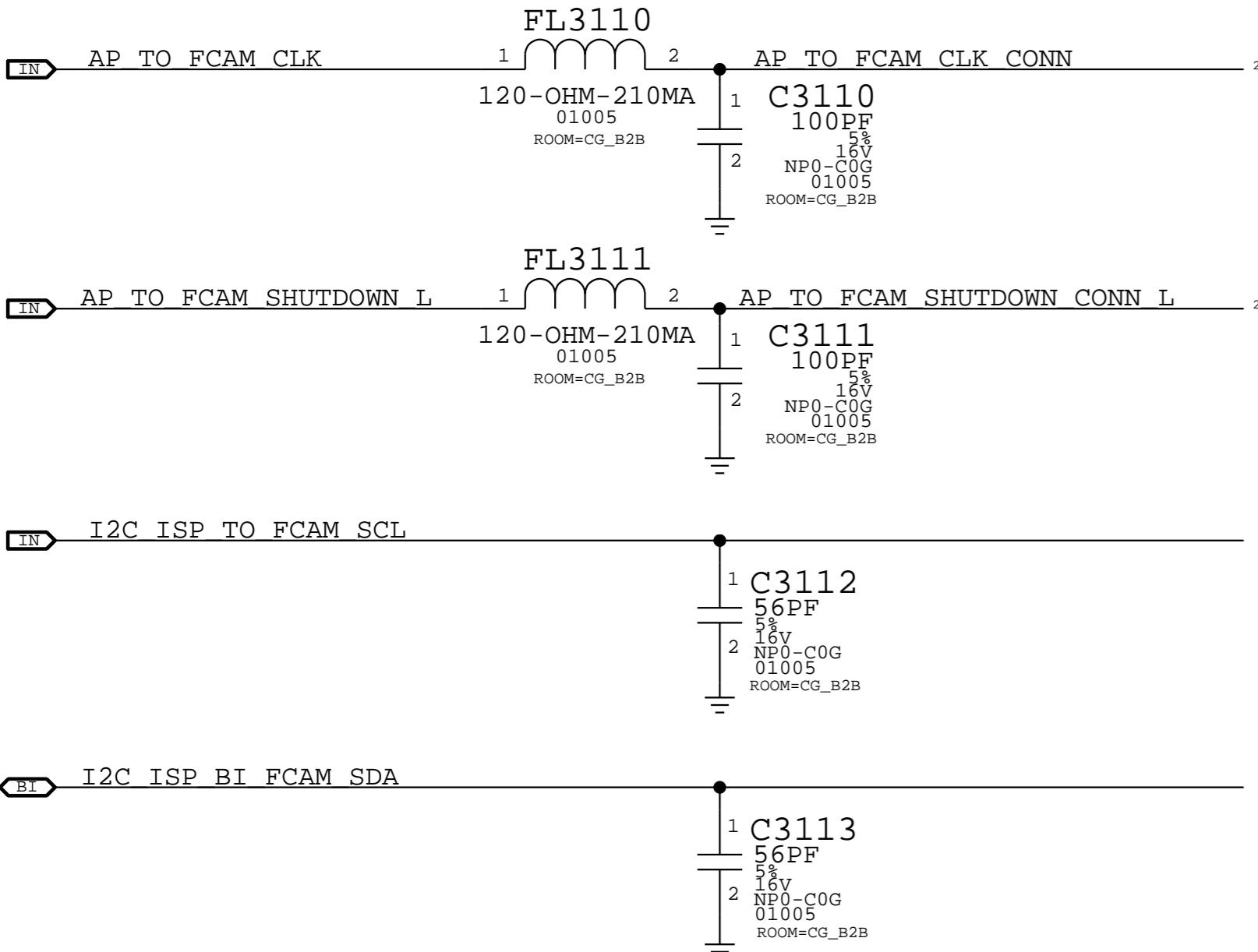
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FOREHEAD FLEX (FCAM)

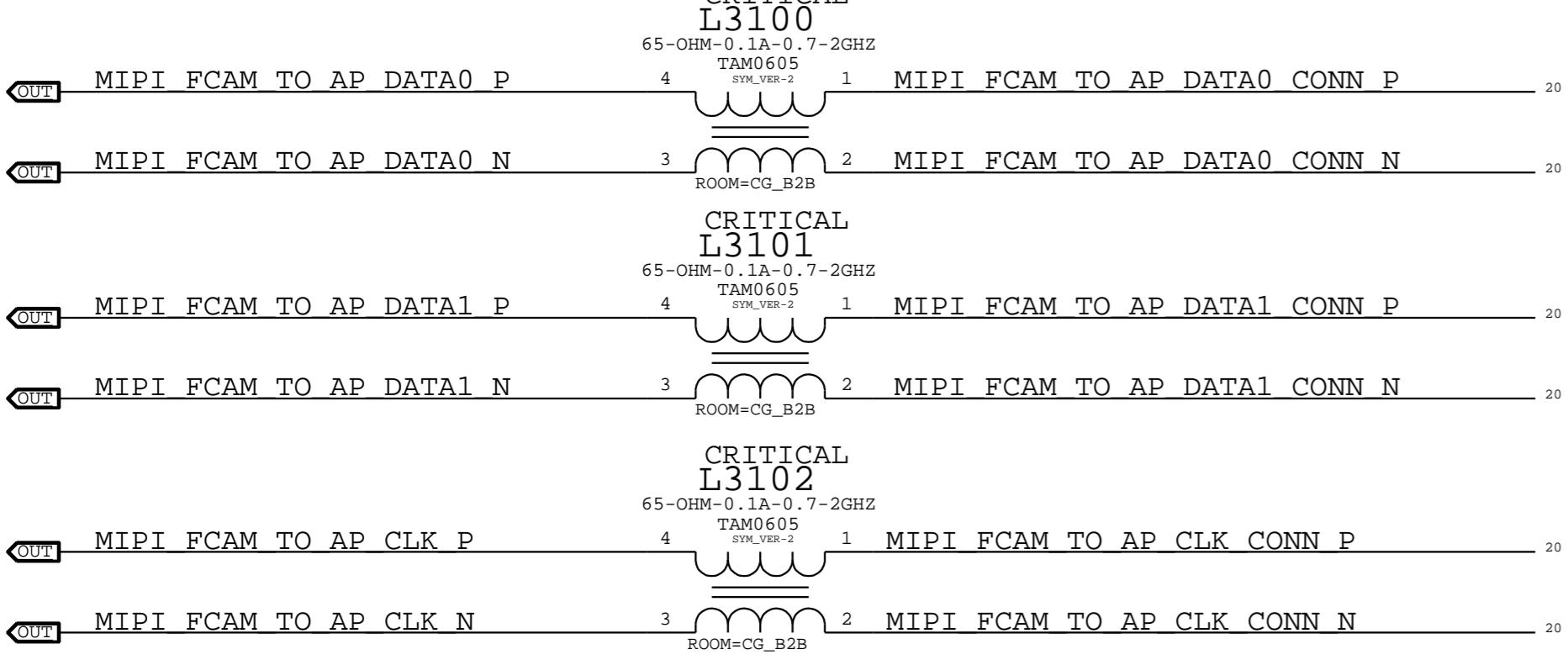
CAMERA POWER



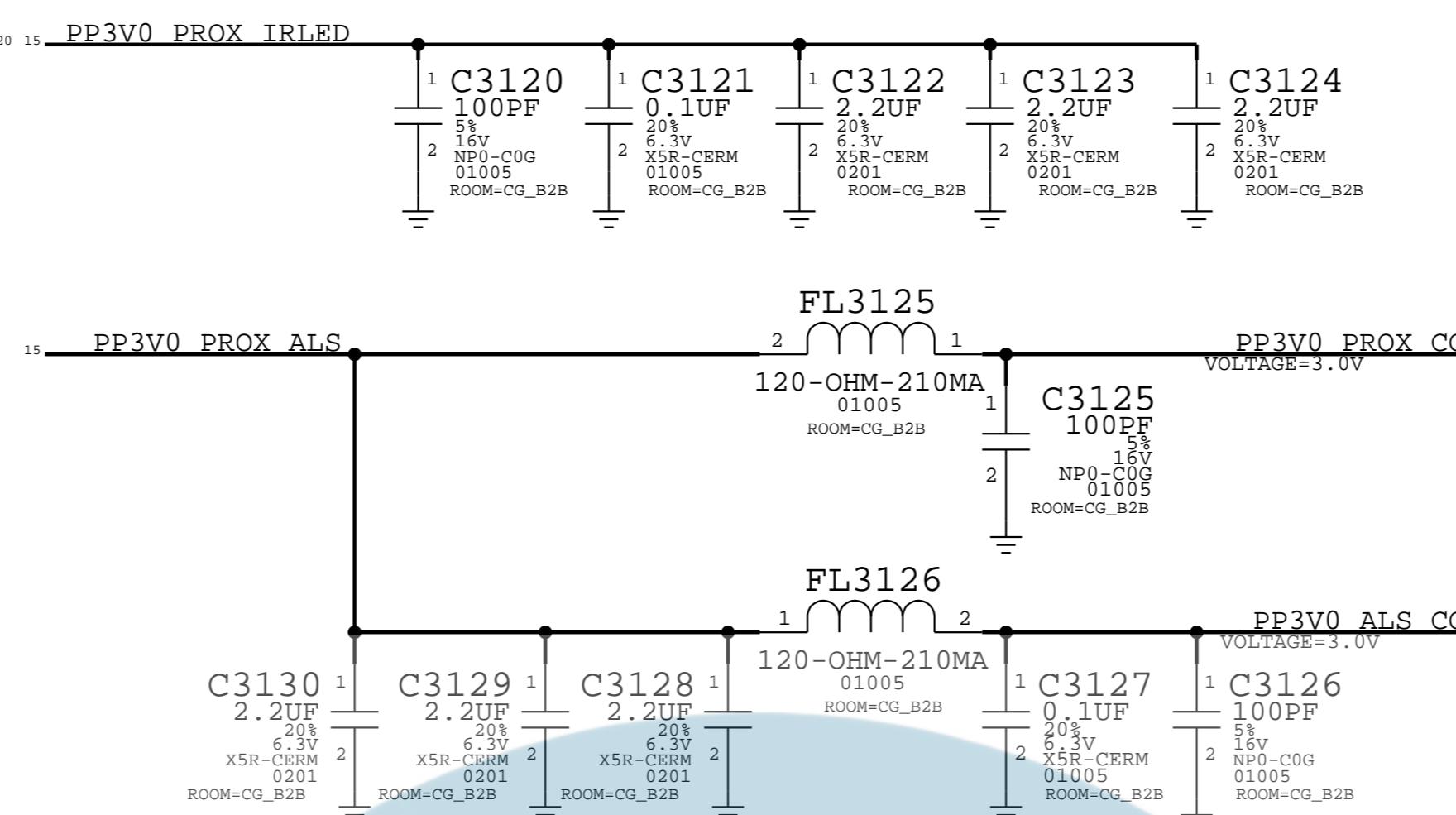
CAMERA I/O



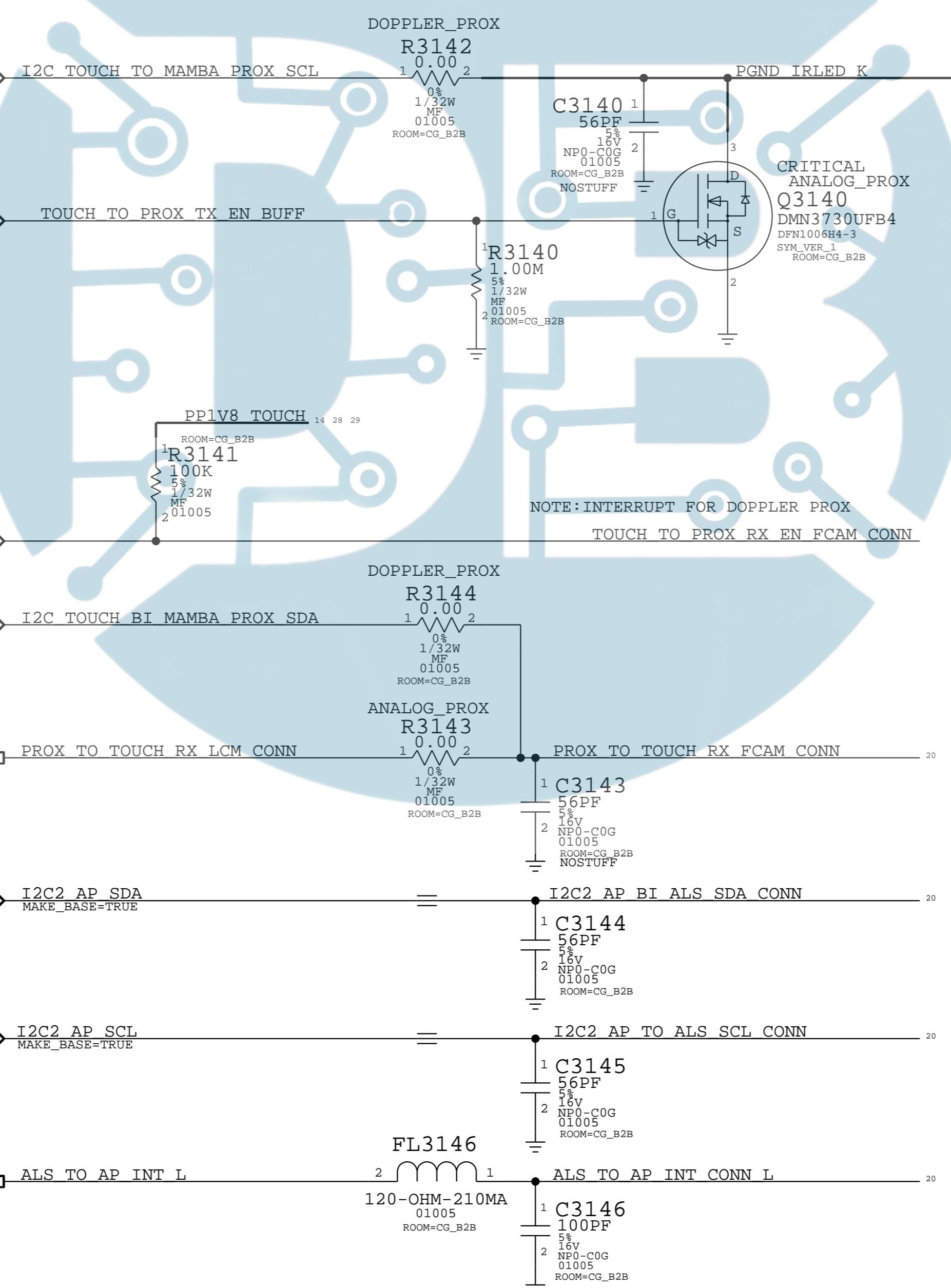
CAMERA MIPI



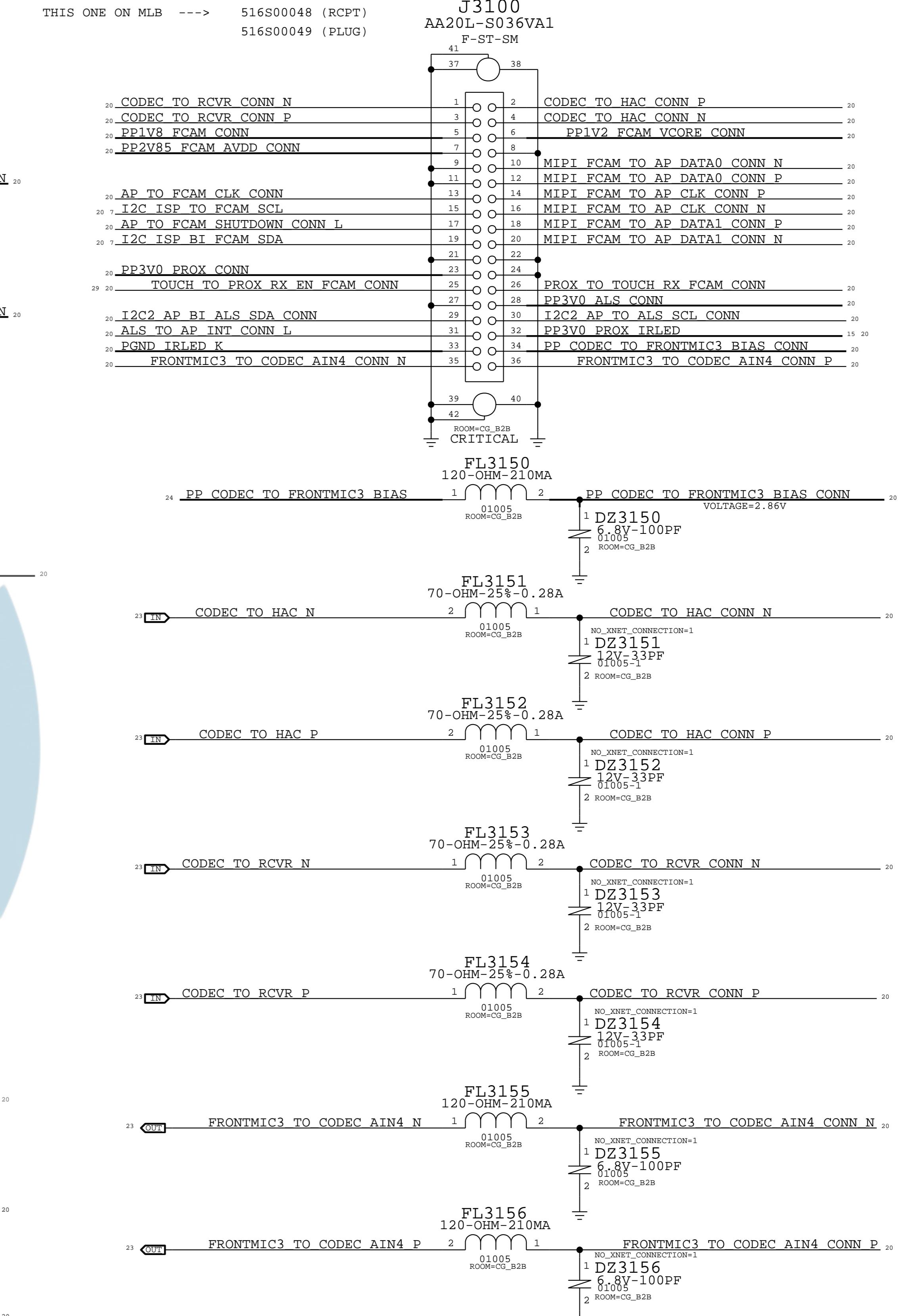
PROX & ALS POWER



PROX & ALS INTERFACE



FOREHEAD CONNECTOR



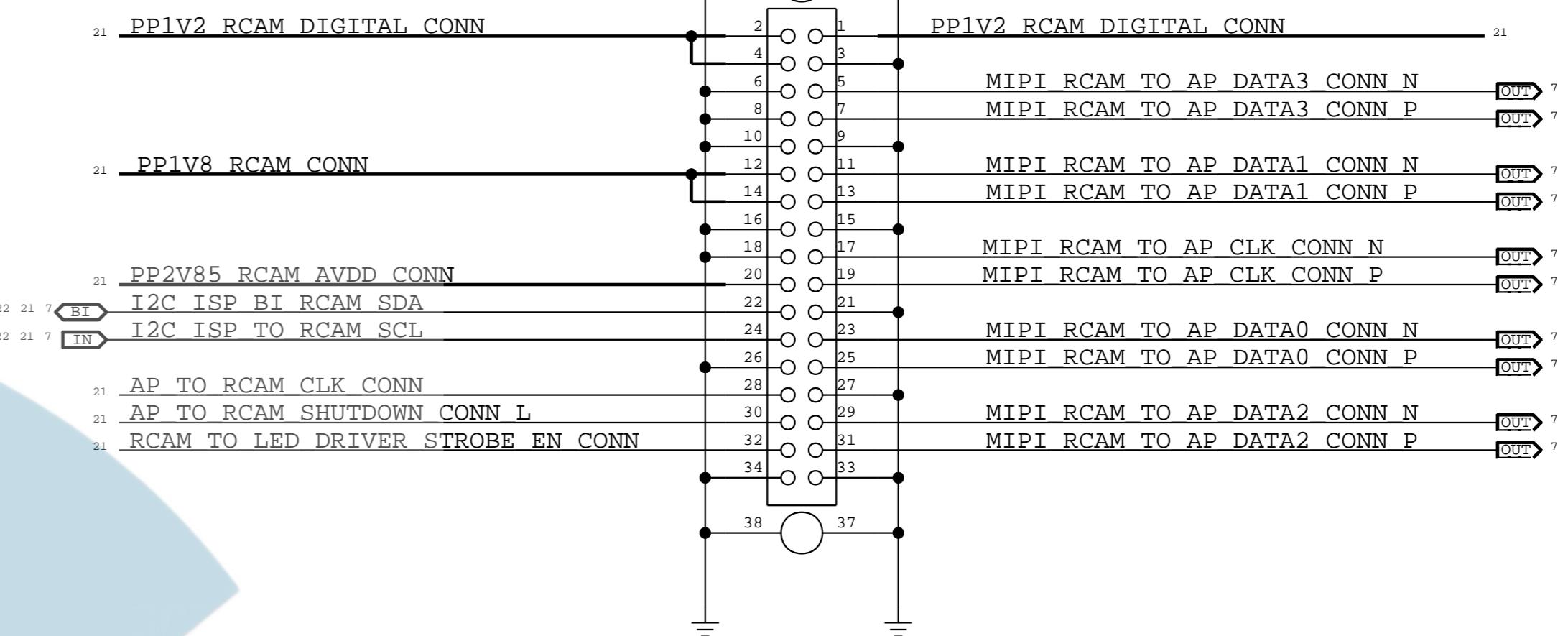
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Revision	A.0.0
Branch	
Page	31 OF 49
Sheet	20 OF 59

REAR CAMERA FLEX

RCAM CONNECTOR

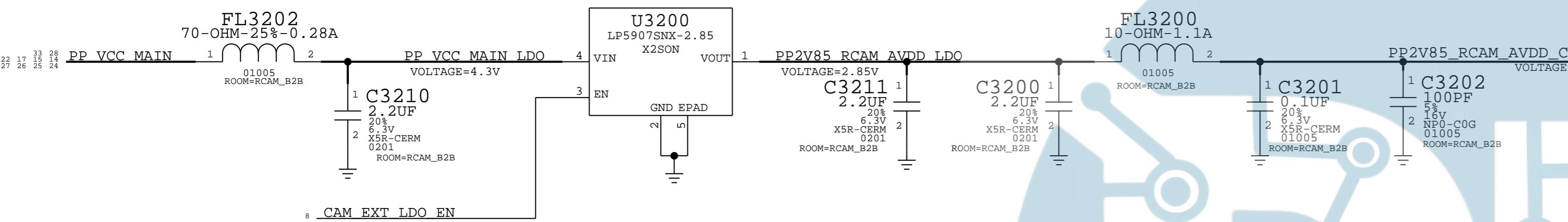
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516S00042 (PLUG)

CRITICAL
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F-ST-SM

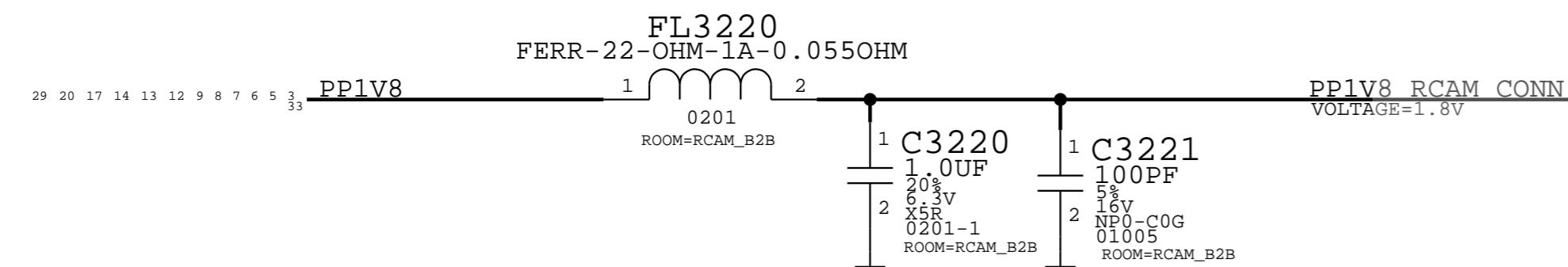
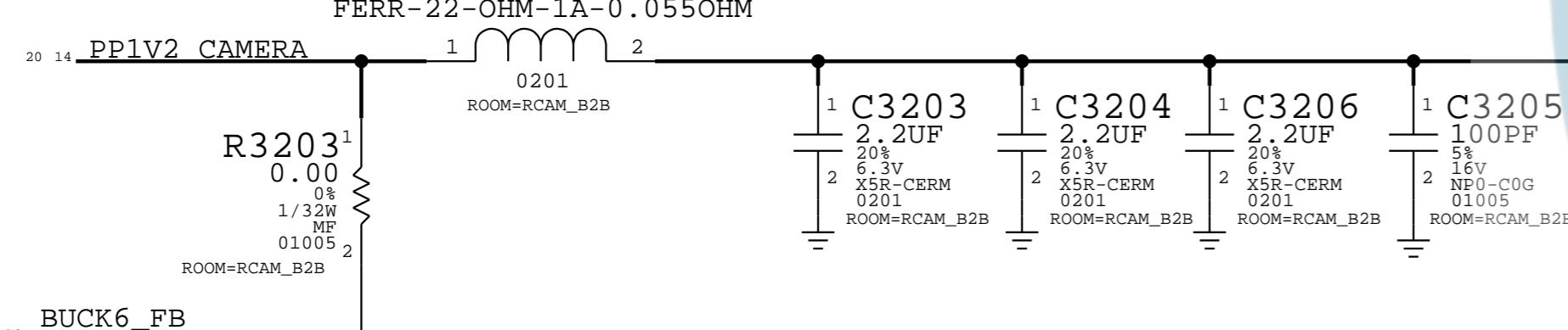


CAMERA POWER

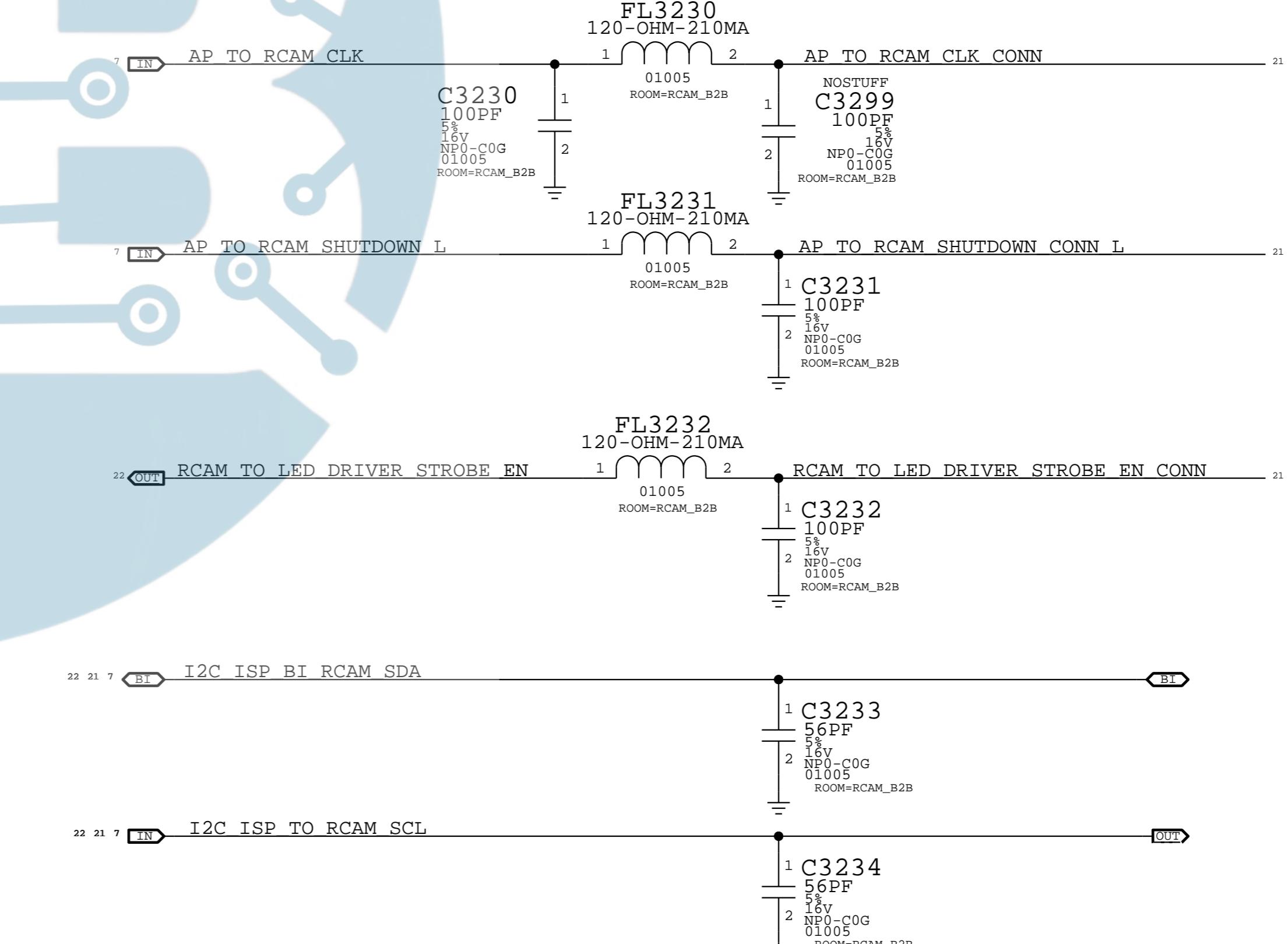
NOTE: OUTPUT IMPEDANCE MUST BE >0.01-OHM
IN ORDER TO MEET CAP ESR REQUIREMENT PER LDO SPEC.



8 CAM EXT LDO EN



DIGITAL I/O



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Apple Inc.	DRAWING NUMBER 051-1902 SIZE D
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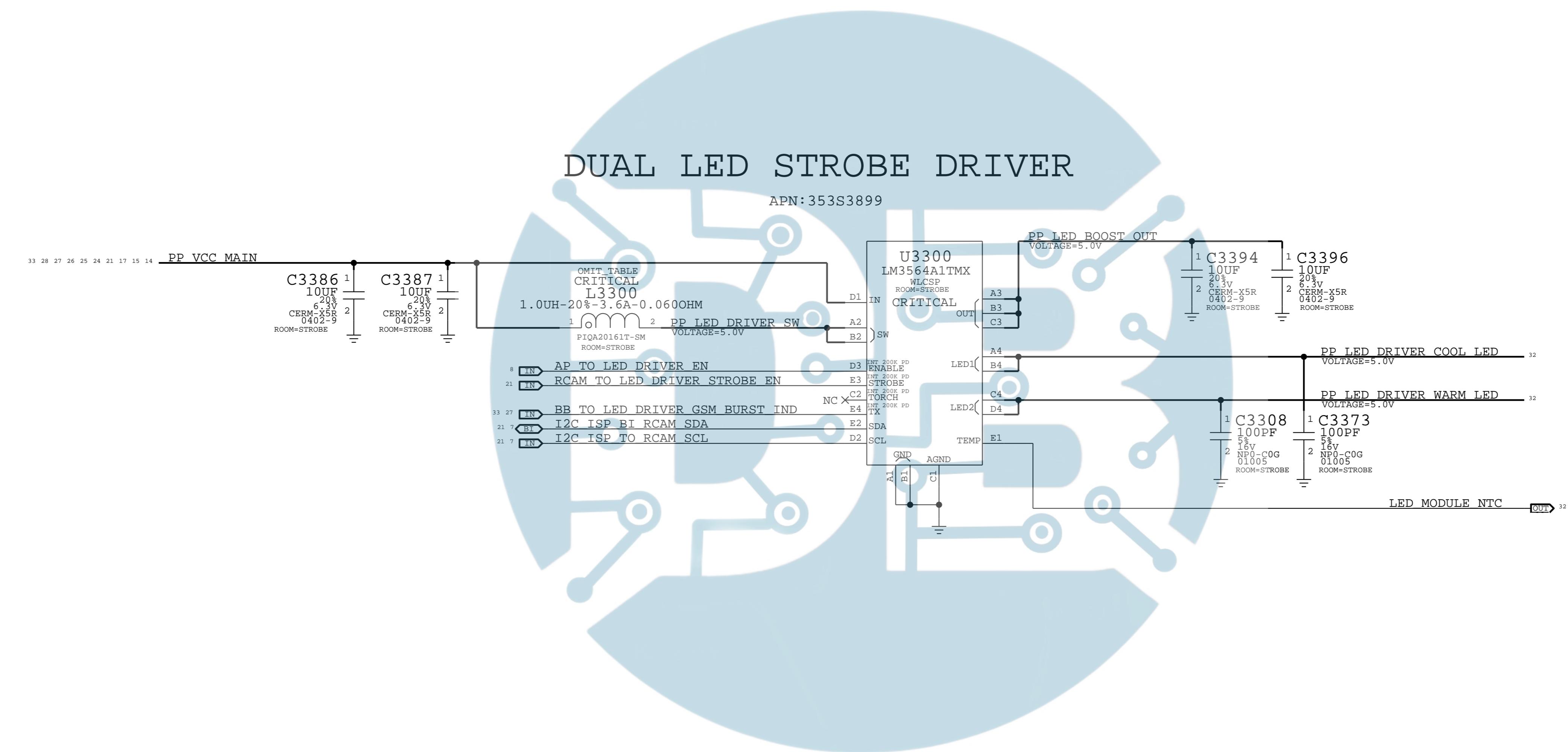
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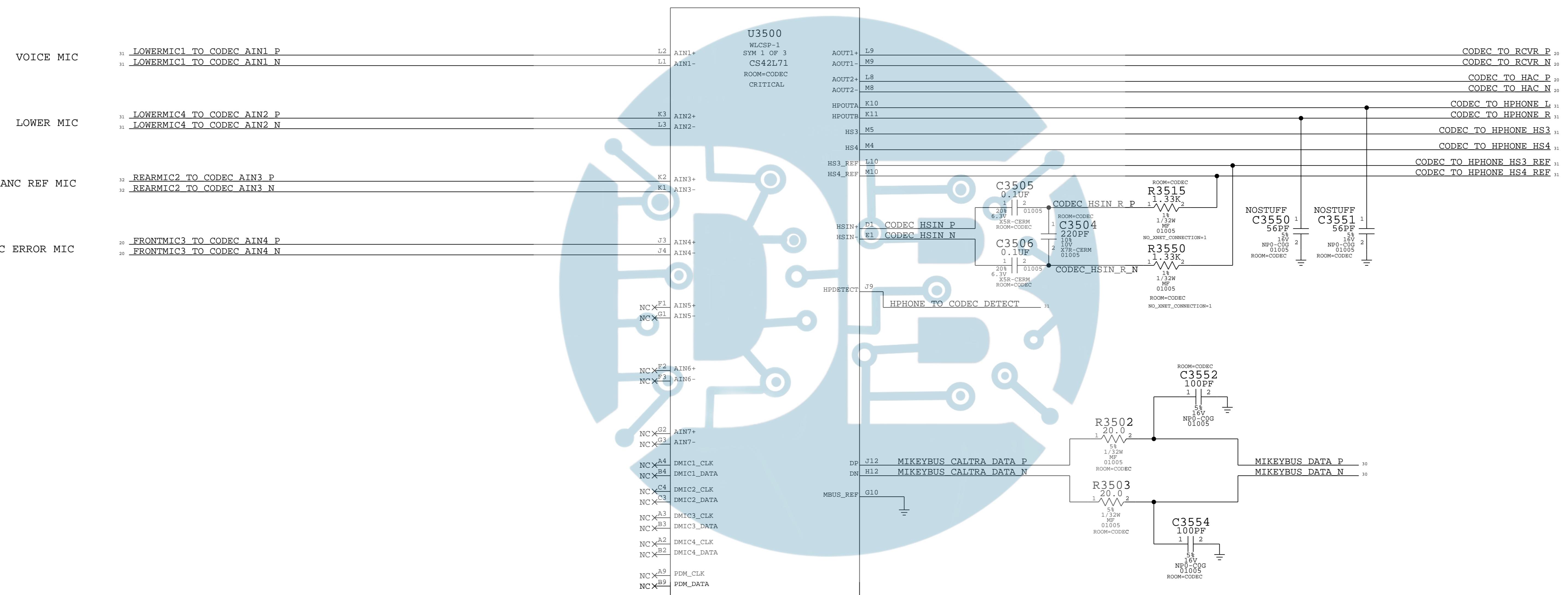
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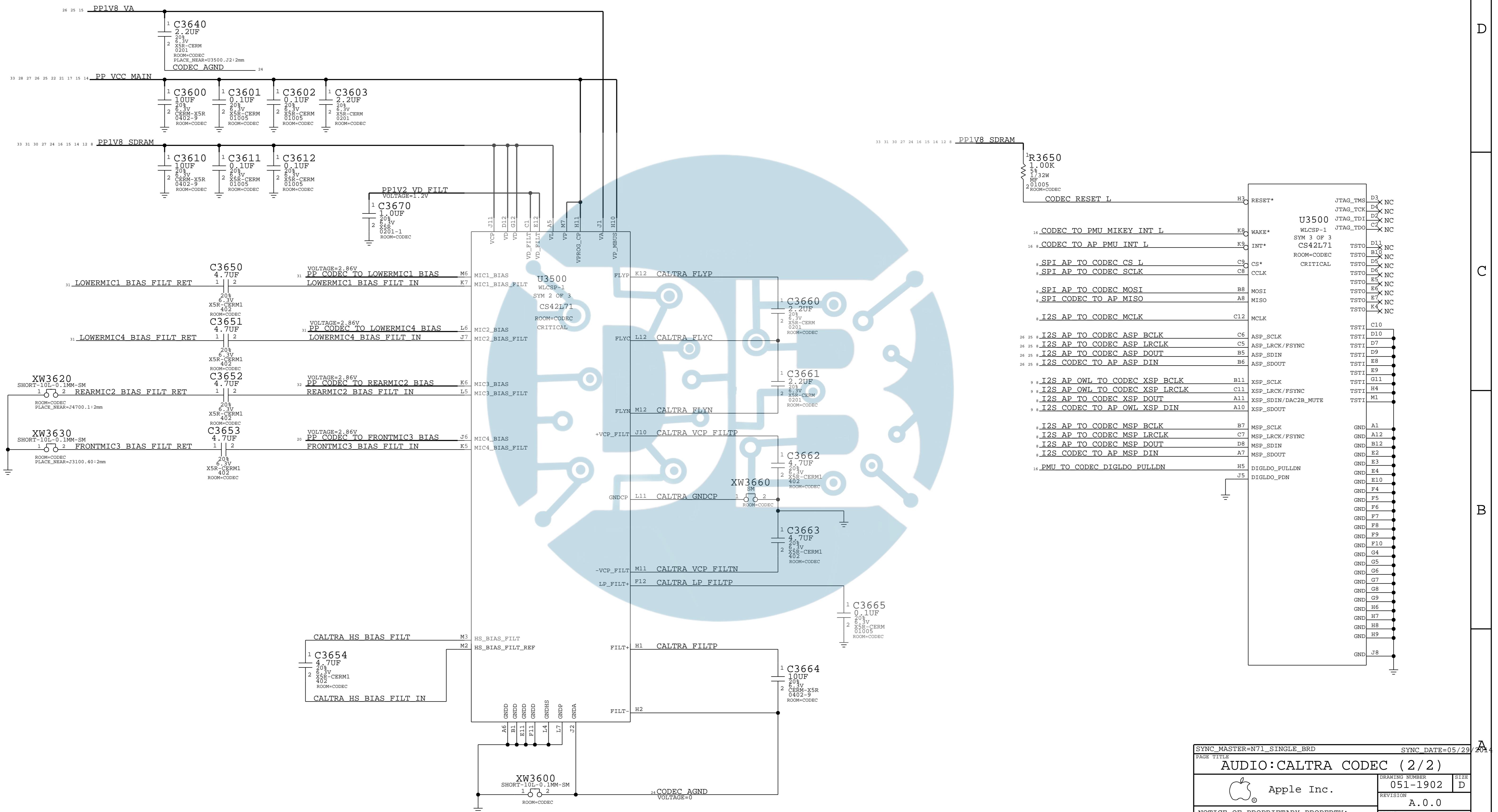
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PAGE 33 OF 49	
SHEET 22 OF 59	

CALTRA AUDIO CODEC (ANALOG INPUTS & OUTPUTS)



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DRAWING NUMBER	051-1902
SIZE	D
REVISION	A.0.0
BRANCH	
PAGE	35 OF 49
SHEET	23 OF 59

CALTRA AUDIO CODEC (POWER & I/O)



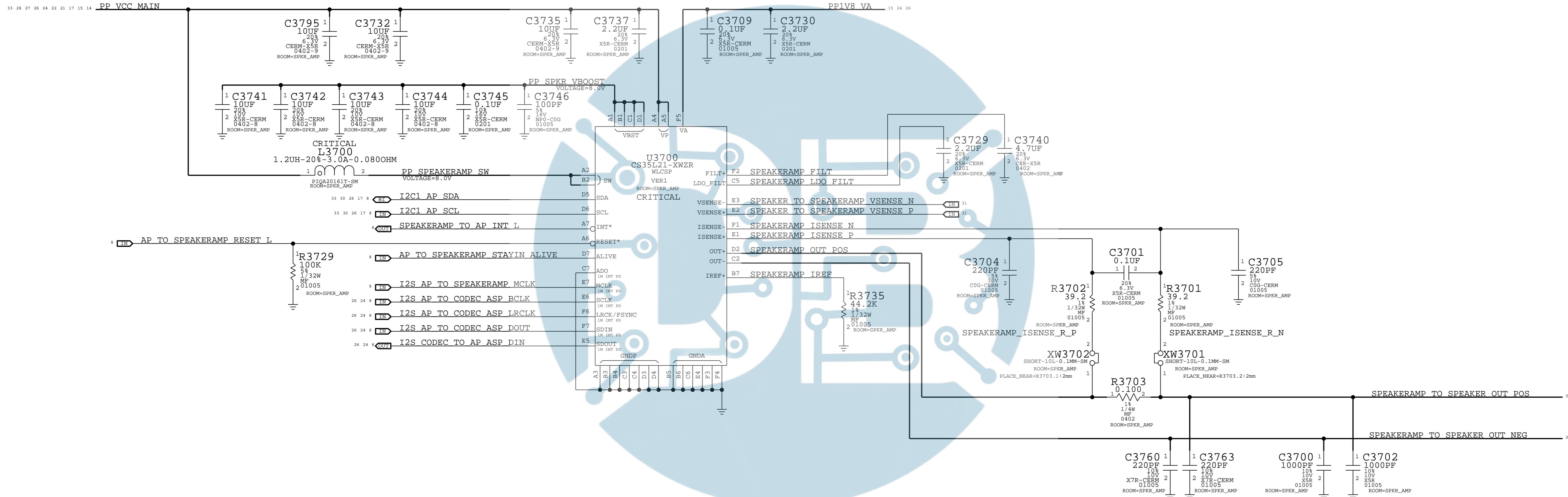
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BRANCH		
PAGE		
36 OF 49		
SHEET		
24 OF 59		

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D

SPEAKER AMPLIFIER

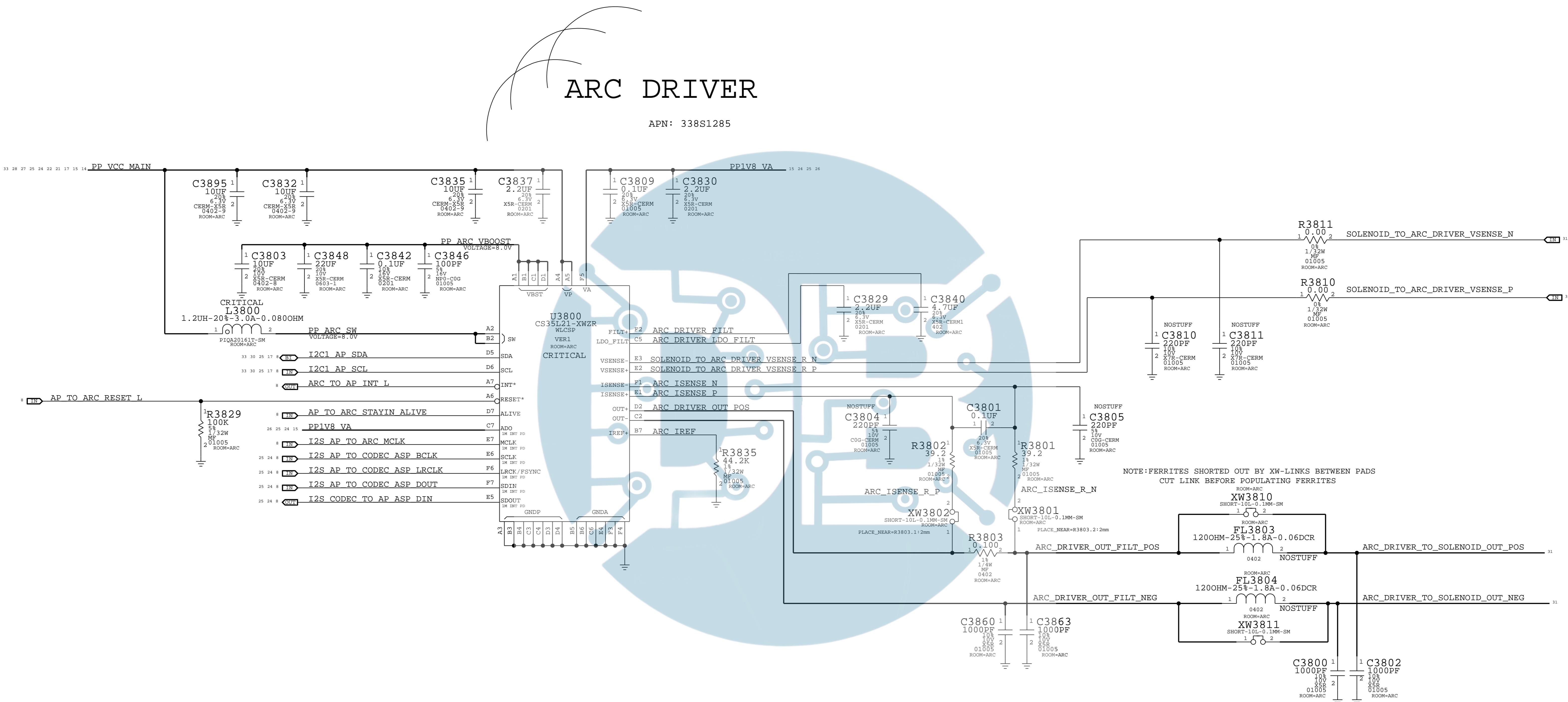
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BRANCH	
PAGE 37 OF 49	
SHEET 25 OF 59	

ARC DRIVER

APN: 338S128

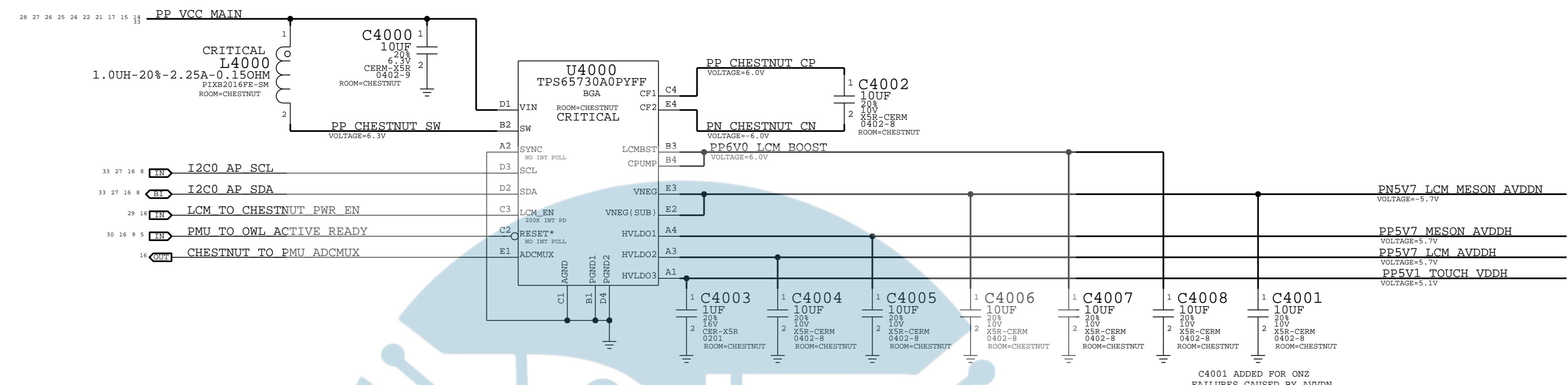


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	PAGE 38 OF 49	
	SHEET 26 OF 59	

DISPLAY & TOUCH - POWER SUPPLIES

CHESTNUT DISPLAY PMU

APN: 338S1172

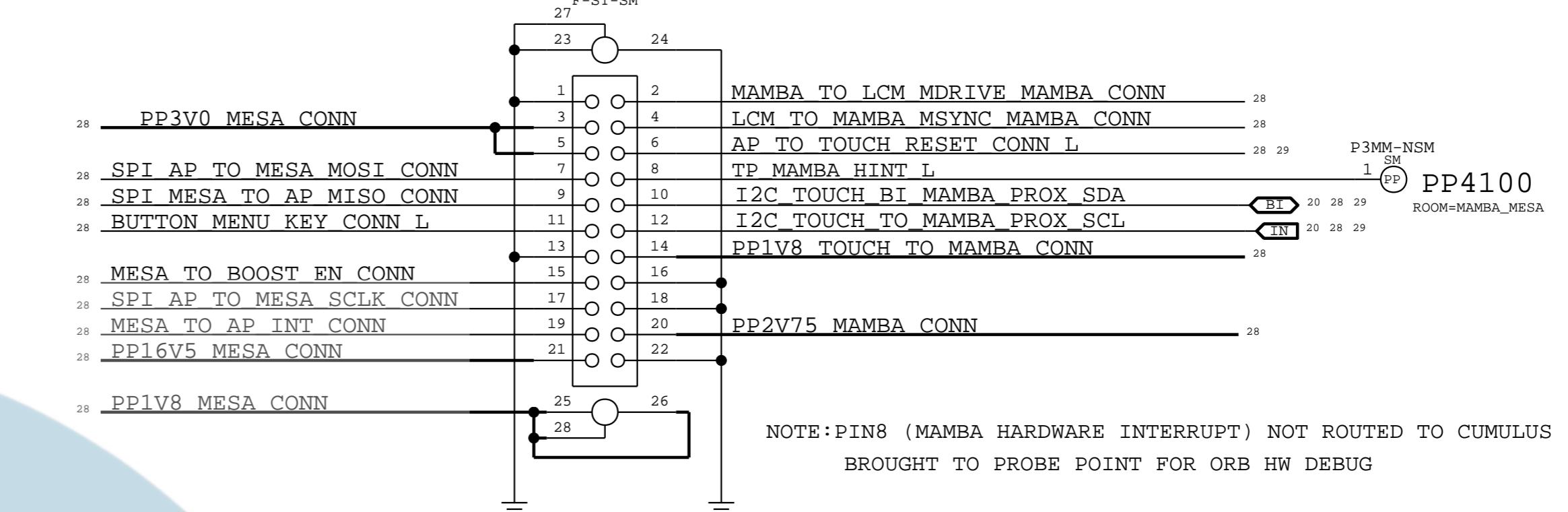


MAMBA & MESA (M&M) FLEX

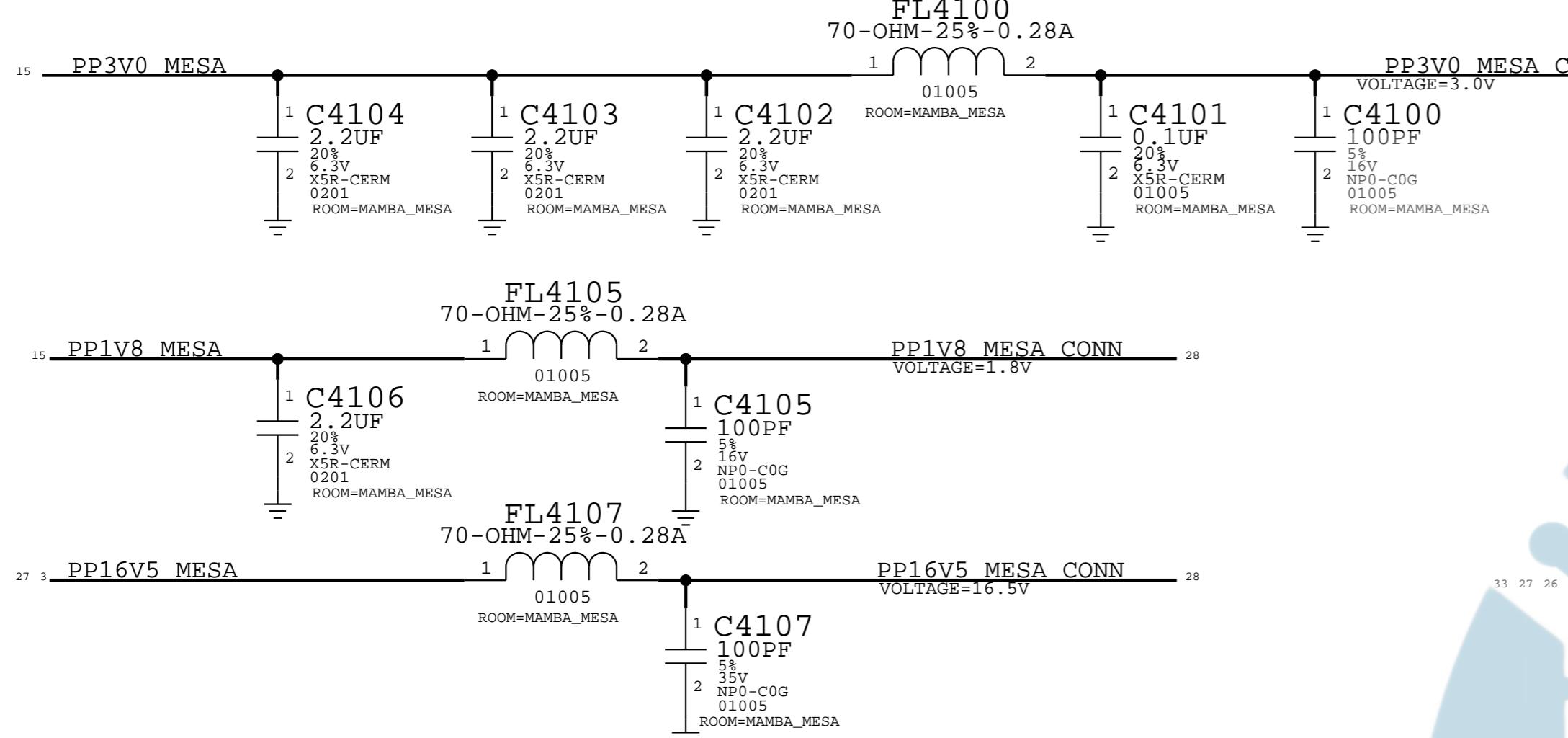
MAMBA & MESA CONNECTOR

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516S00057 (PLUG)

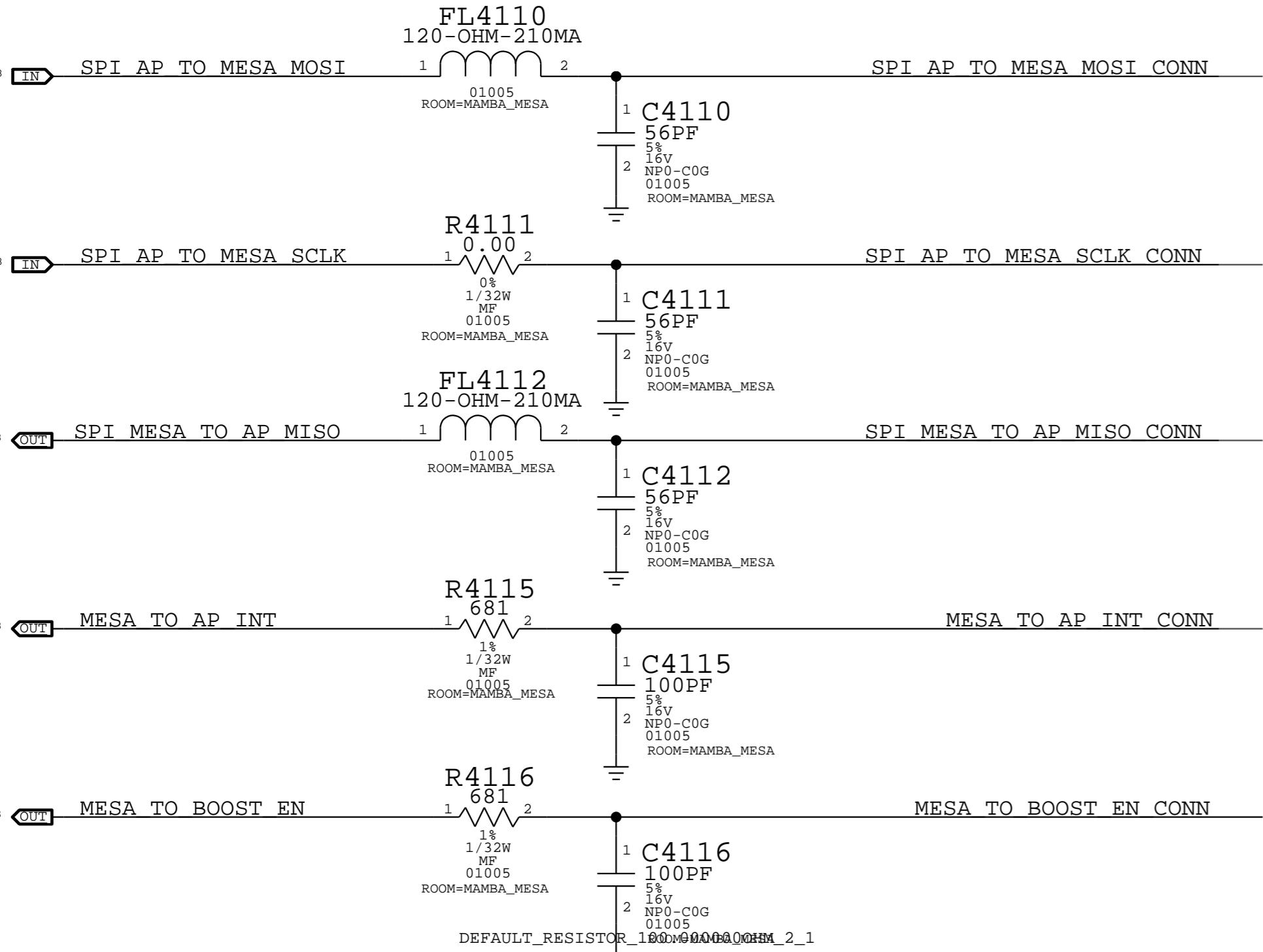
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F-ST-SM



MESA POWER

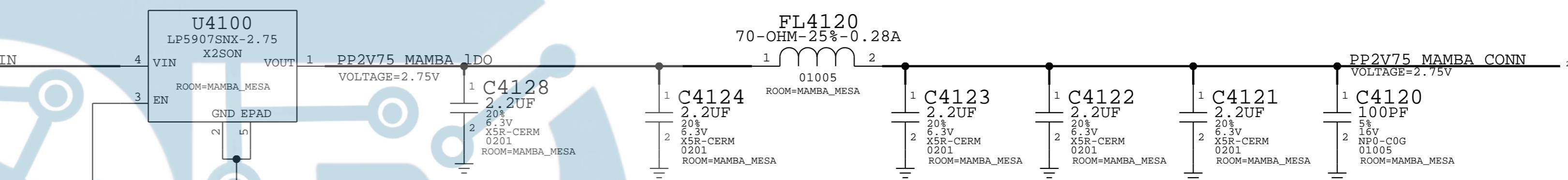


MESA DIGITAL I/O

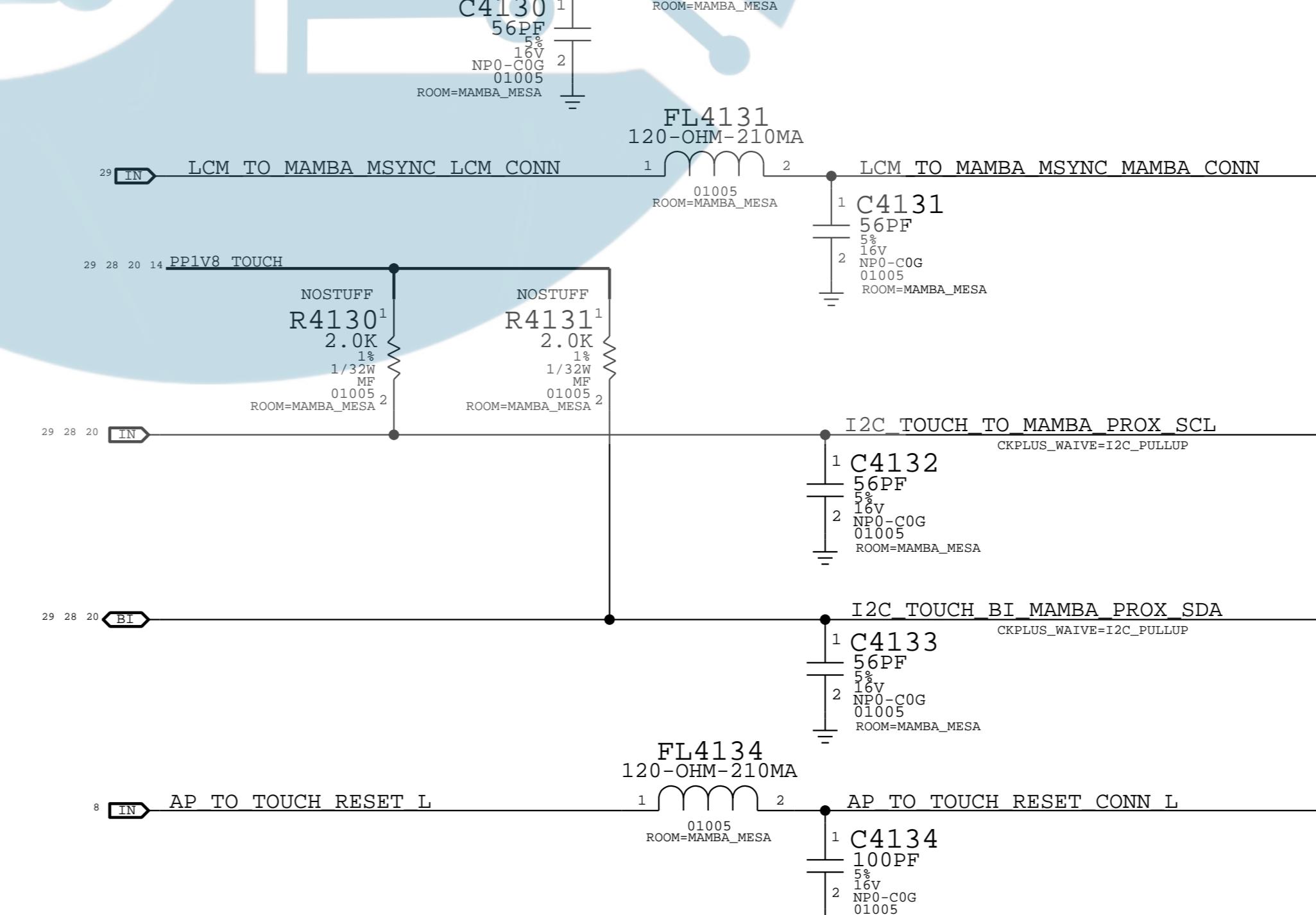


MAMBA POWER

NOTE: OUTPUT IMPEDANCE MUST BE > 0.01-OHM
IN ORDER TO MEET CAP ESR REQUIREMENT PER LDO SPEC.



MAMBA DIGITAL I/O



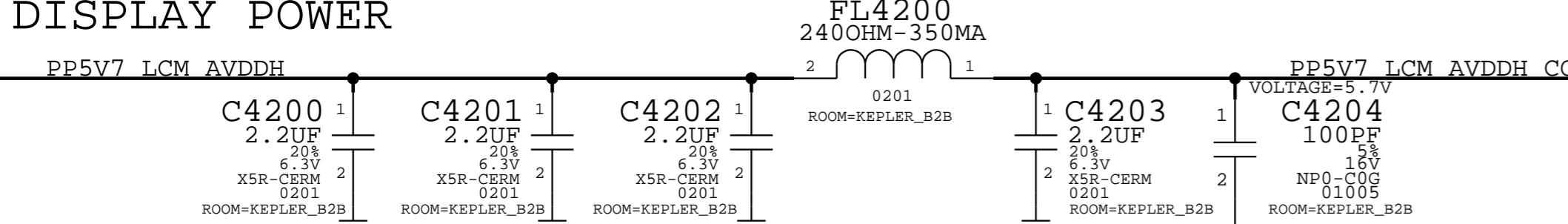
NOTE: TOUCH I2C PULL-UPS TO PP1V8_TOUCH INSIDE KEPLER
ADDING R4130, R4131 AS OPTION FOR TWEAKING VALUES.

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PAGE TITLE	TOUCH:ORB & MESA B2B
DRAWING NUMBER	051-1902
SIZE	D
REVISION	A.0.0
BRANCH	
PAGE	41 OF 49
SHEET	28 OF 59

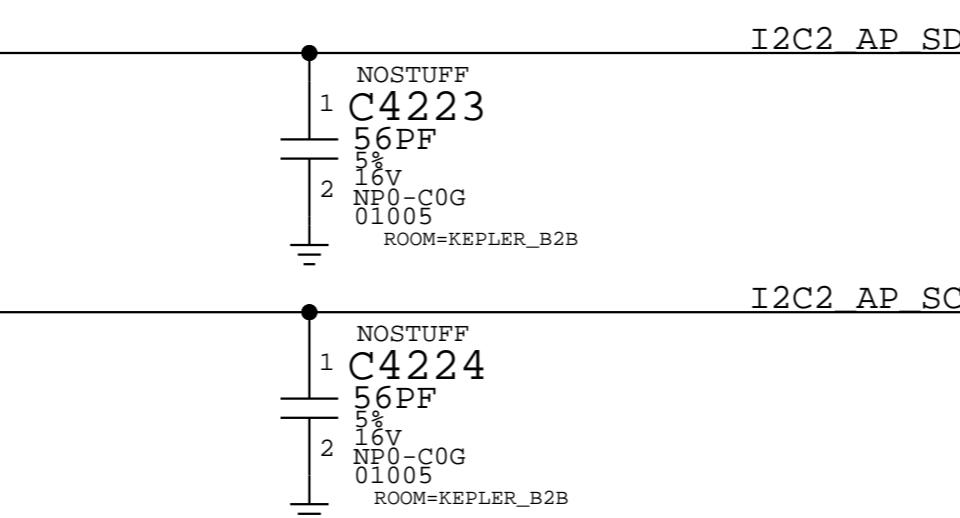
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DISPLAY & TOUCH FLEX

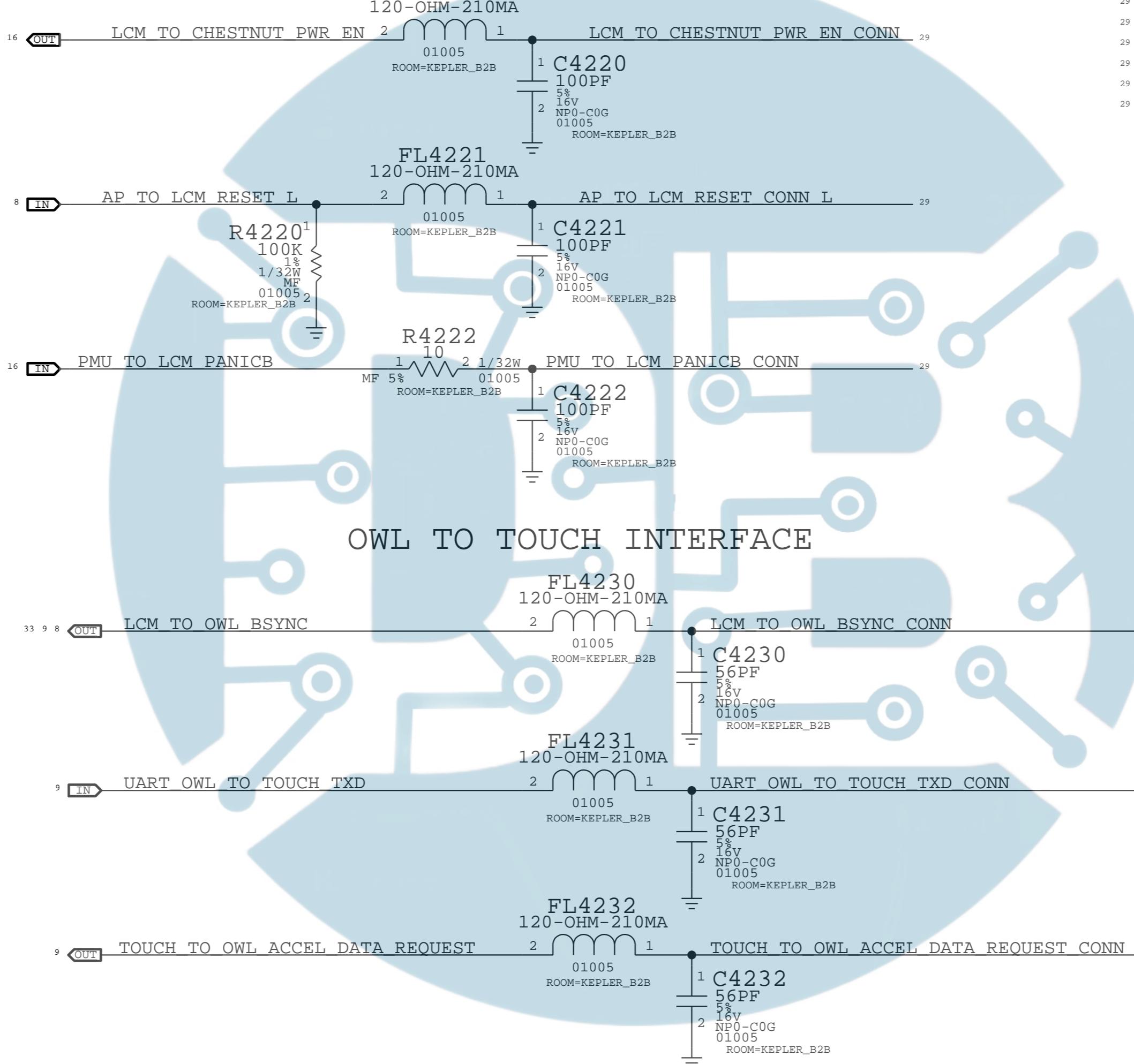
DISPLAY POWER



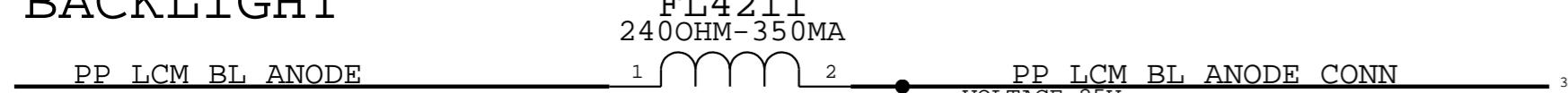
DISPLAY EEPROM I2C



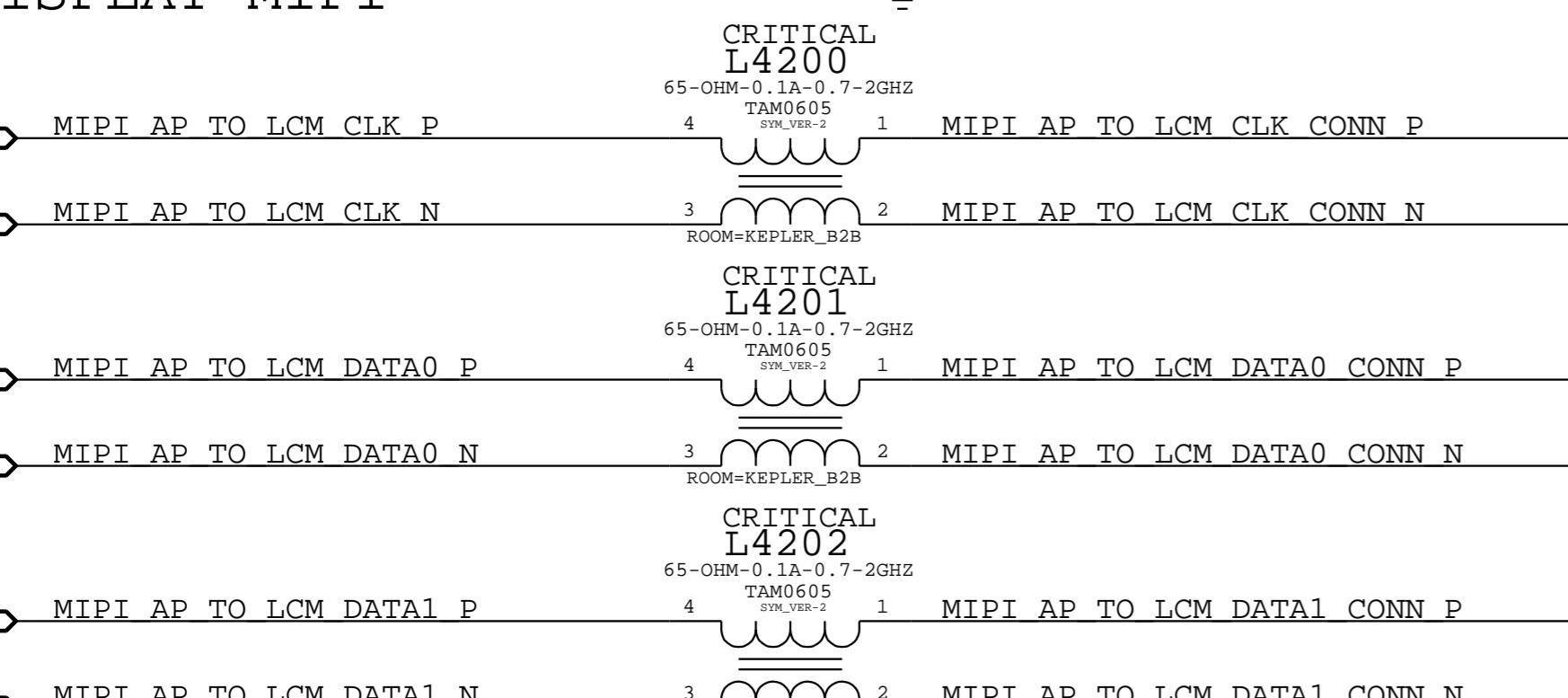
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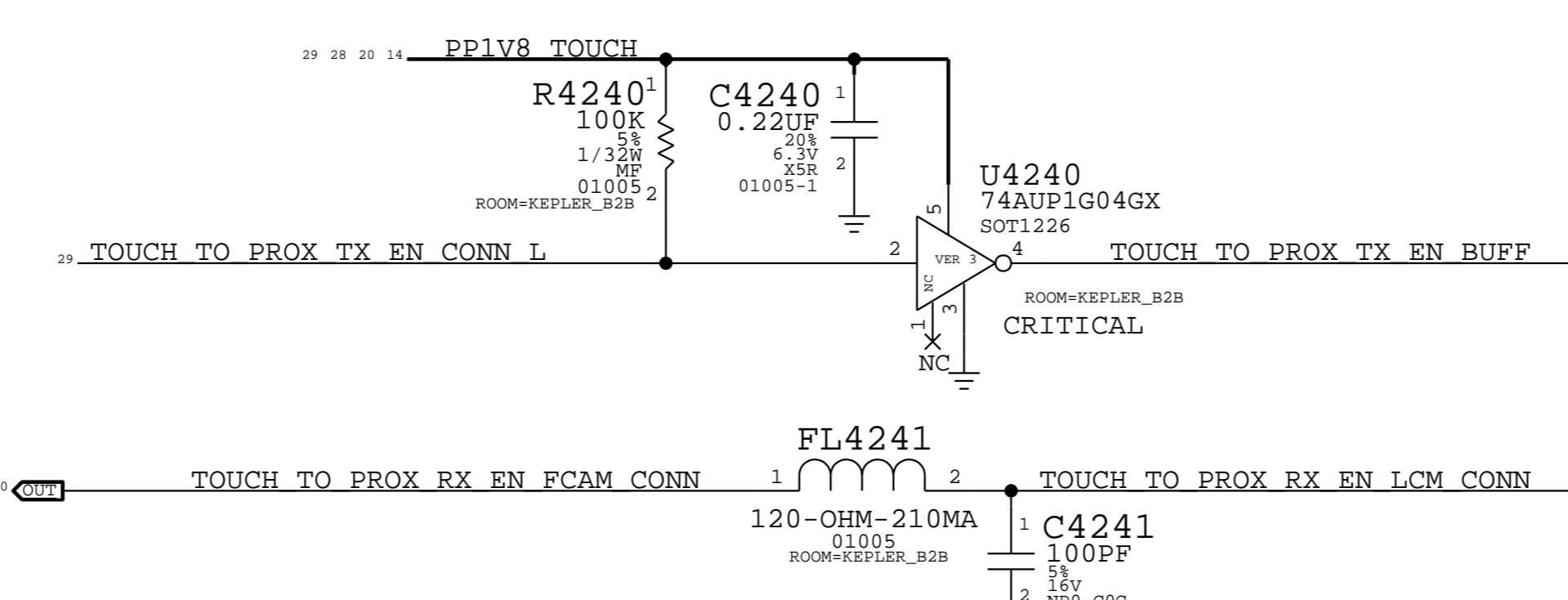
BACKLIGHT



DISPLAY MIPI

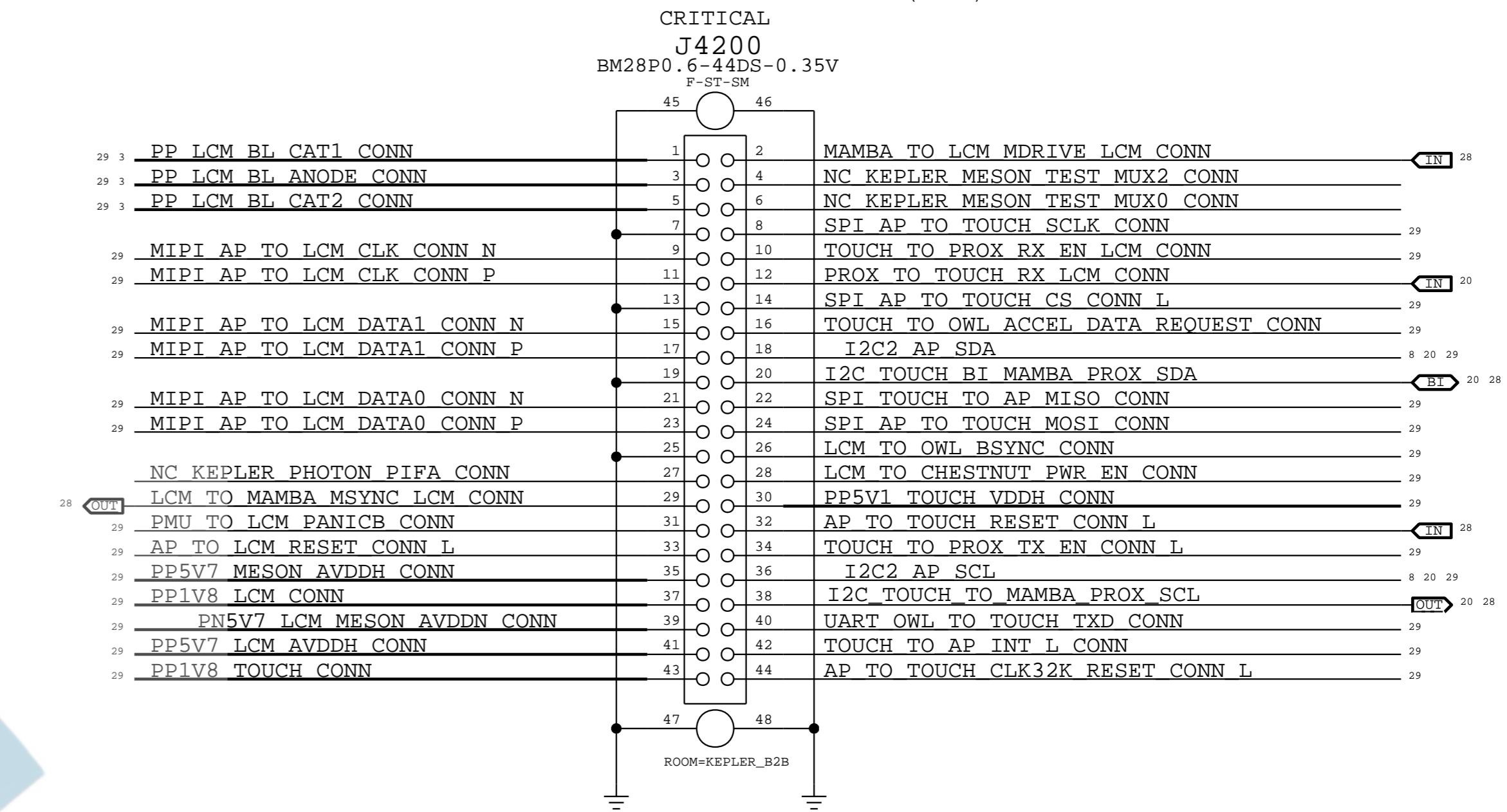


PROX TO TOUCH INTERFACE

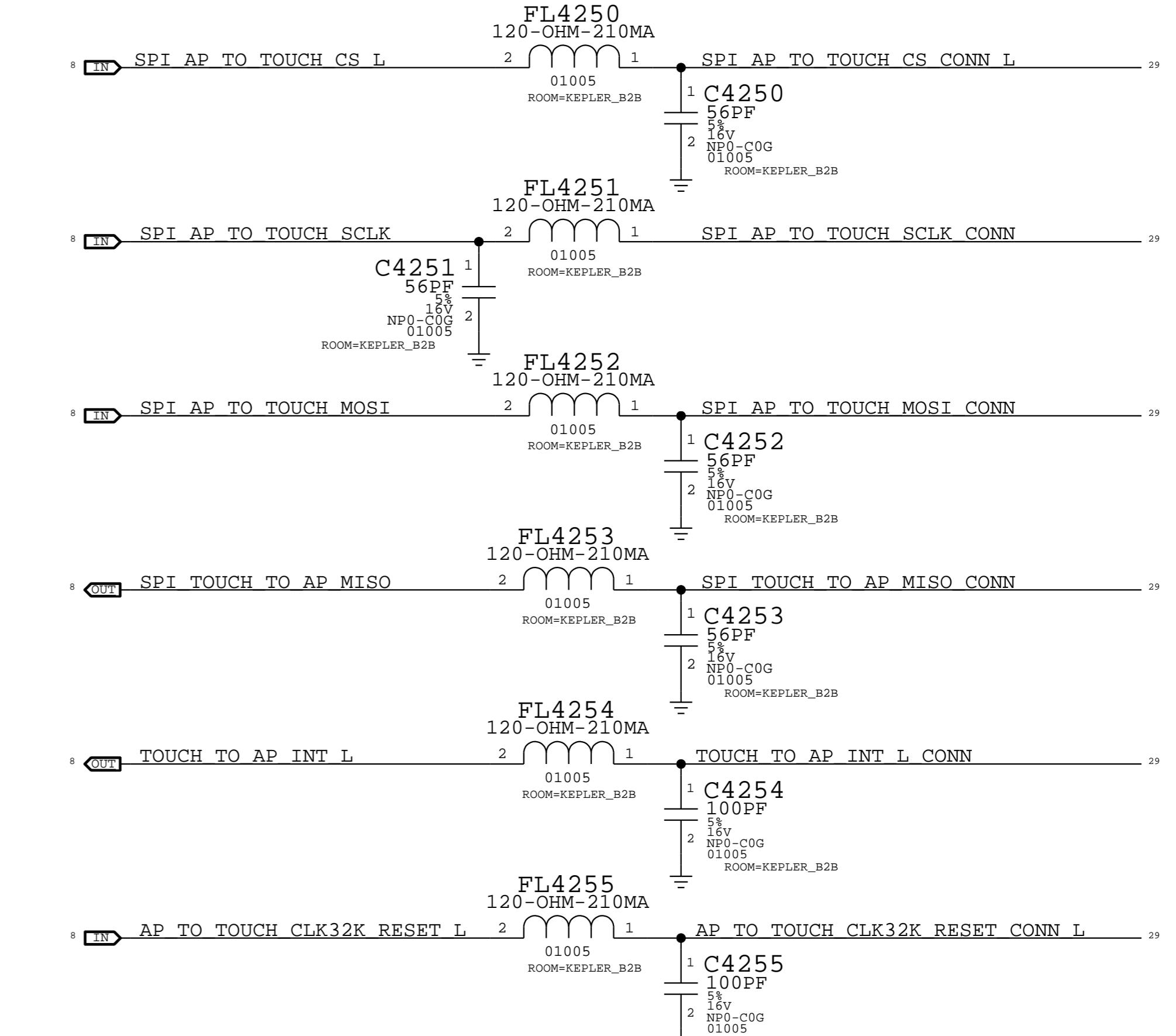


DISPLAY CONNECTOR

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516S00037 (PLUG)



AP TO TOUCH INTERFACE



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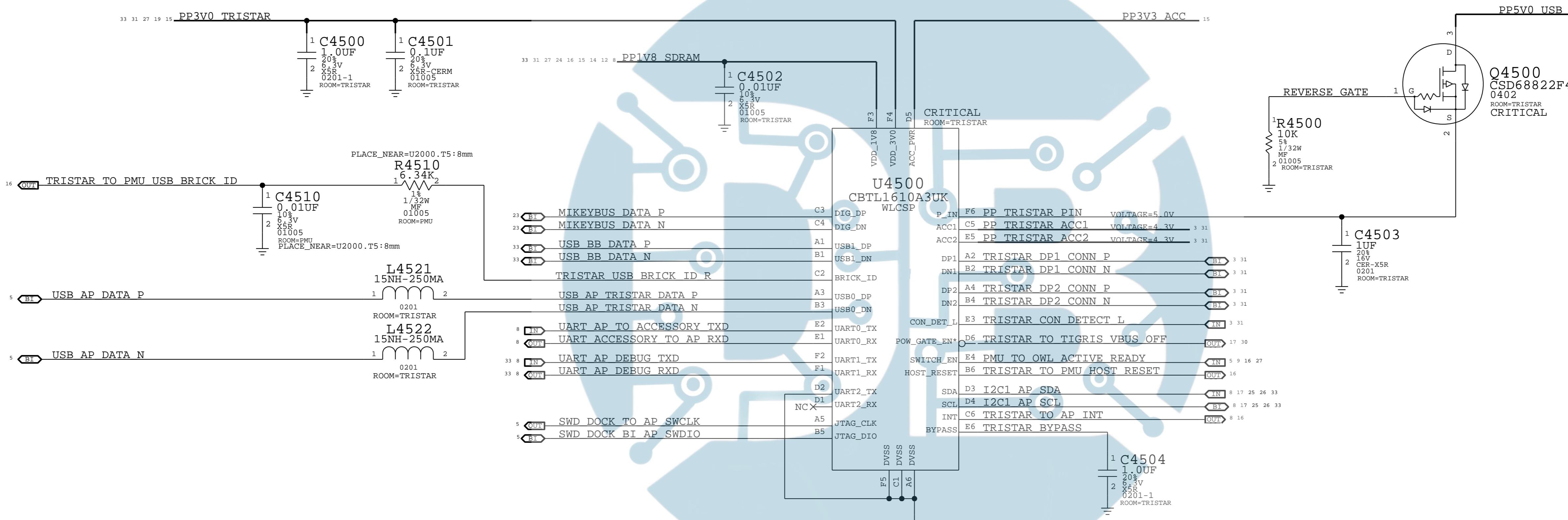
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TRISTAR 2 (A3)

APN: 343S0695

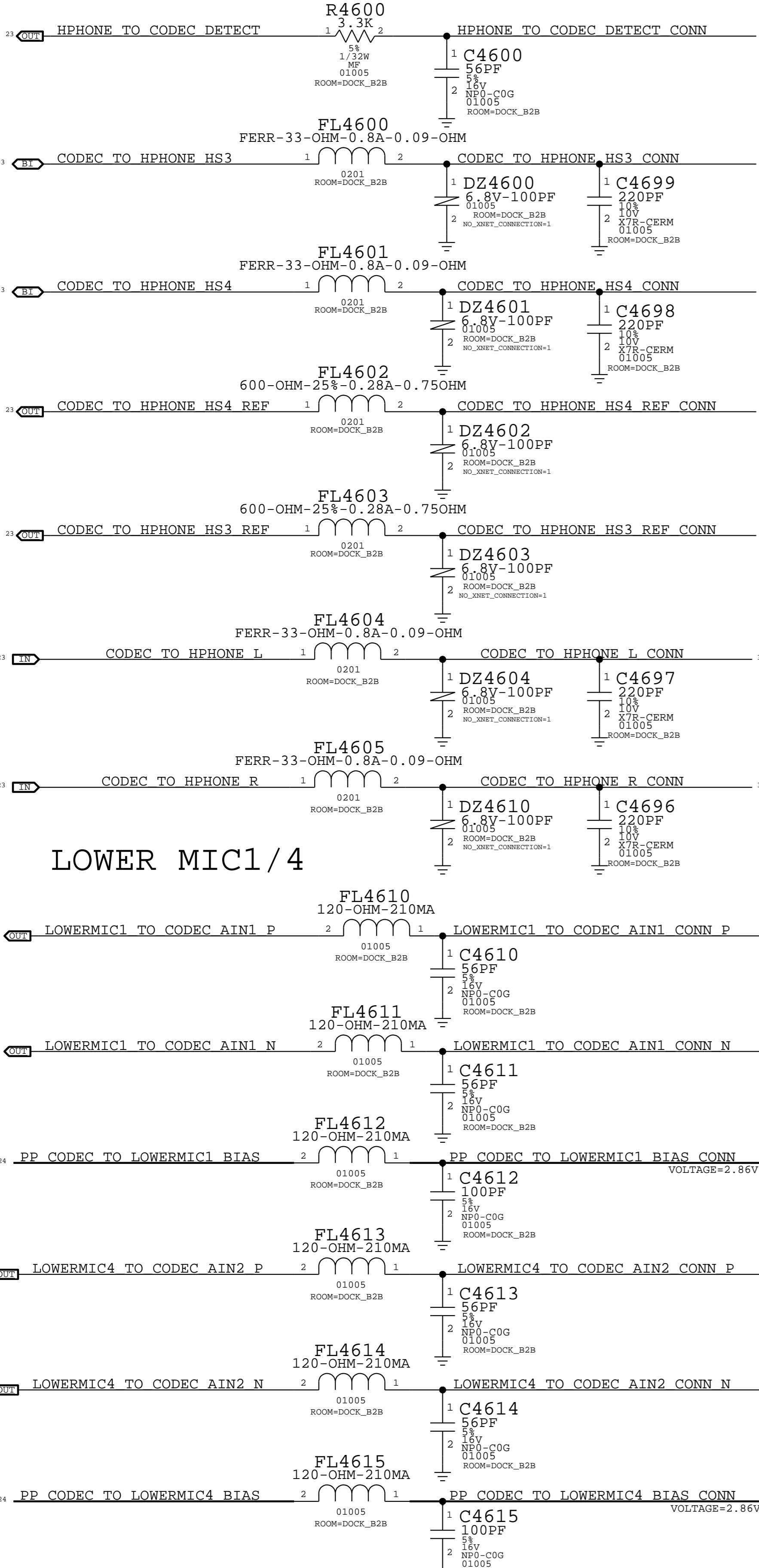


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1 PP4500

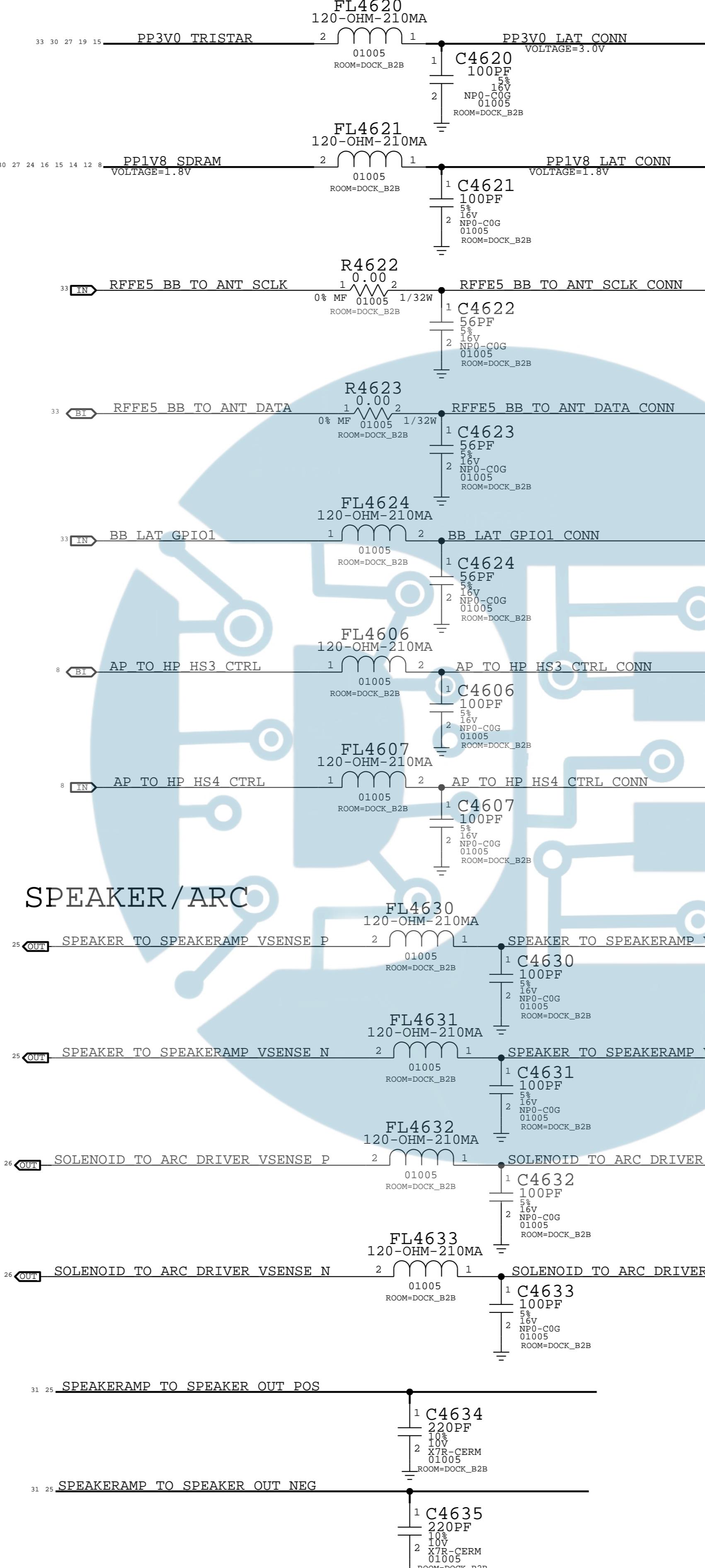
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I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE	PAGE 45 OF 49
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DOCK FLEX CONNECTOR

AUDIO JACK

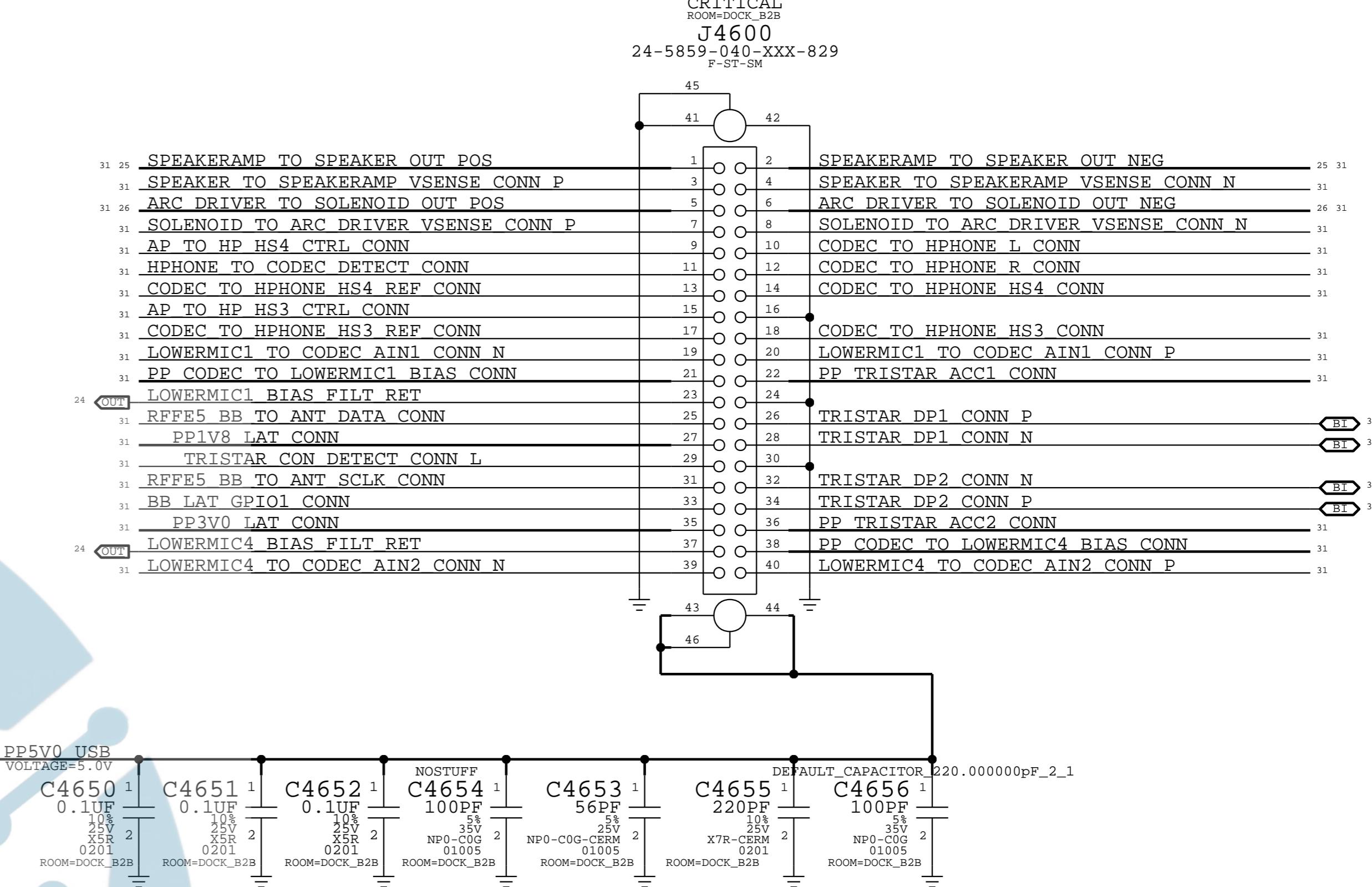


ANTENNA

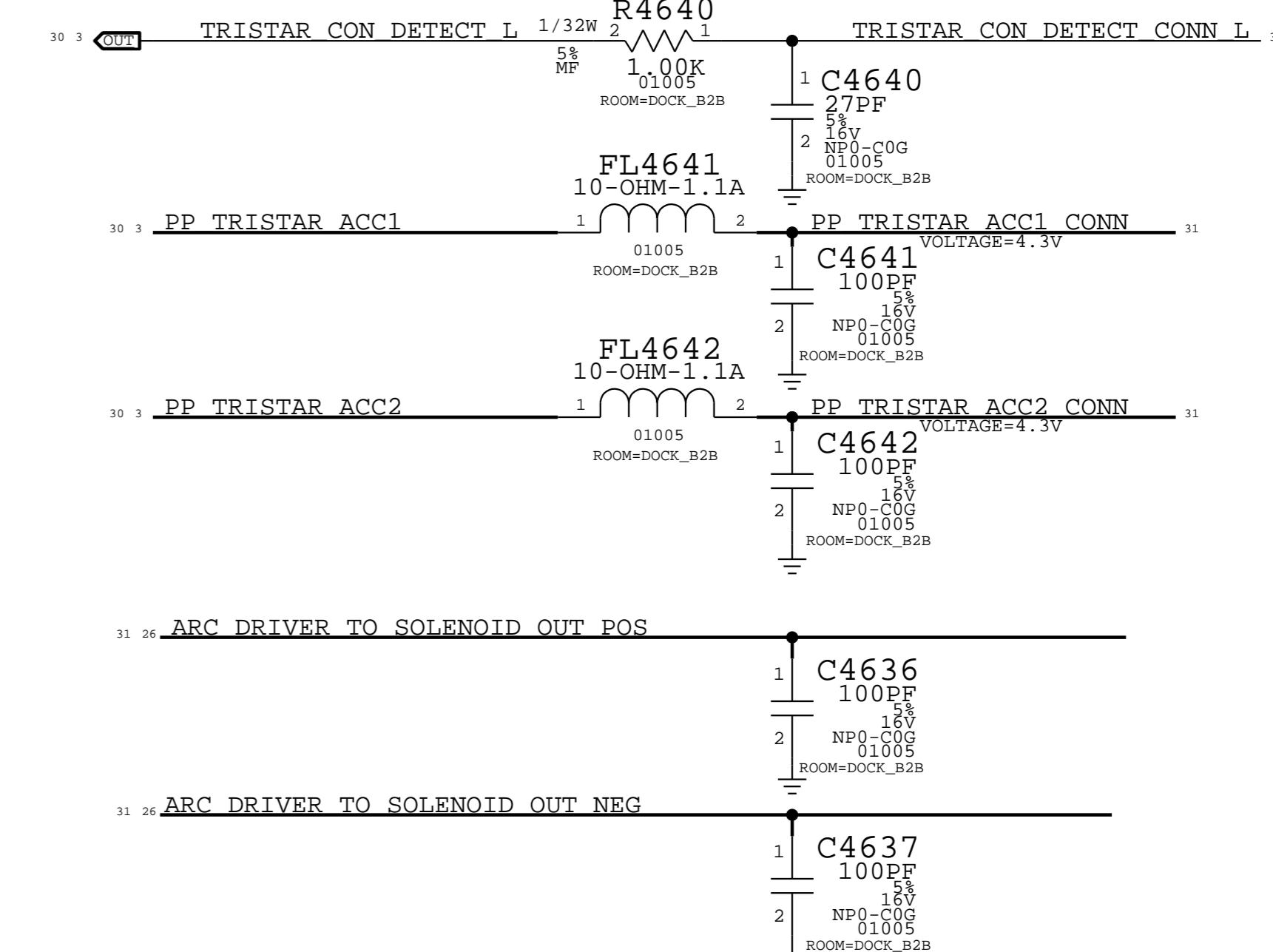


DOCK FLEX CONNECTOR

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516S00034 (PLUG)



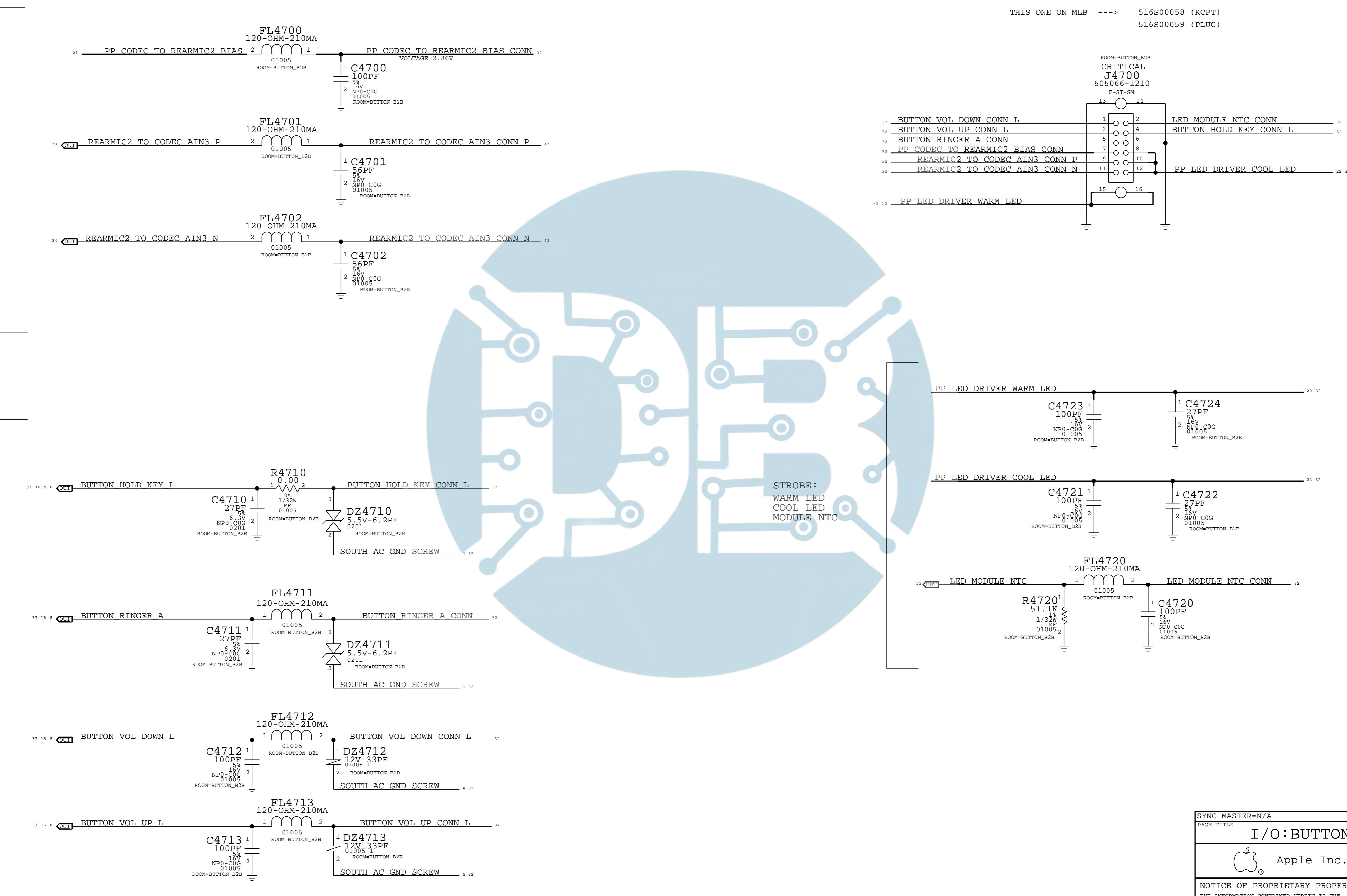
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PAGE TITLE	I/O:DOCK FLEX B2B
 Apple Inc.	DRAWING NUMBER 051-1902
	SIZE D
	REVISION A.0.0
	BRANCH
	PAGE 46 OF 49
	SHEET 31 OF 50
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BUTTON FLEX

BUTTON FLEX CONNECTOR

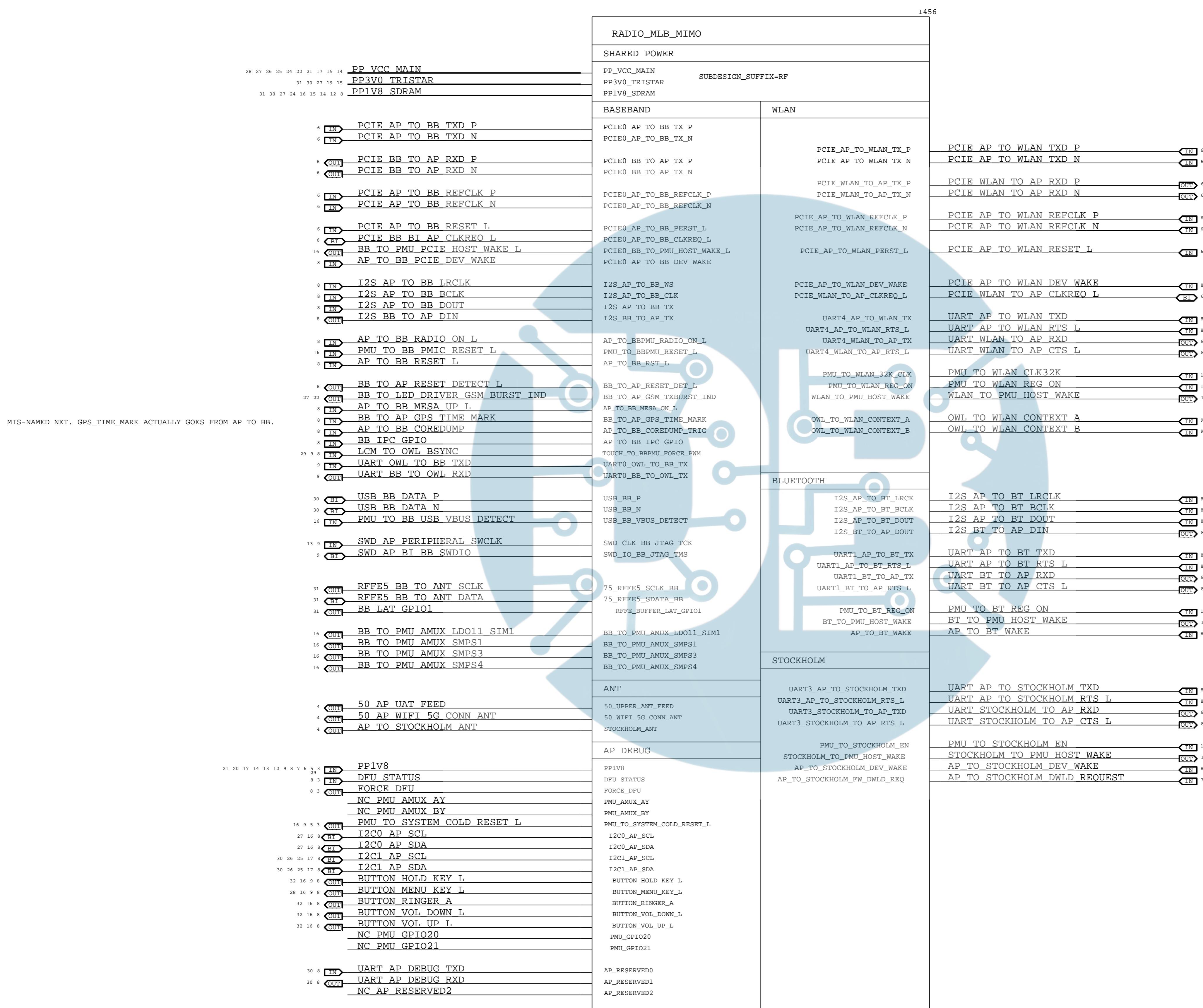


BUTTONS :

HOLD
RINGER
VOL UP/DOWN

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BASEBAND, WLAN, BT & STOCKHOLM



1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ECN	DESCRIPTION OF REVISION	CK APPD DATE
A	0004536627	PRODUCTION RELEASED	2015-07-21

N71 RADIO_MLB_MIMO - PVT

JULY 07, 2015

PDF PAGE	CSC PAGE	CONTENTS
2	2	ELNA & UAT ANT FEED
3	3	FE: ANT CONNECTORS AND UAT TUNER
4	30	DEBUG CONN & TEST POINTS
5	31	CELLULAR BASEBAND: POWER1
6	32	CELLULAR BASEBAND: POWER2
7	33	CELLULAR BASEBAND: CONTROL AND INTERFACES
8	34	CELLULAR BASEBAND: GPIOS
9	35	CELLULAR PMU: CONTROL AND CLOCKS
10	36	CELLULAR PMU: SWITCHERS AND LDOS
11	37	CELLULAR PMU: ET MODULATOR
12	38	CELLULAR TRANSCEIVER: POWER
13	39	CELLULAR TRANSCEIVER: PRX PORTS
14	40	CELLULAR TRANSCEIVER: DRX/GPS PORTS
15	41	CELLULAR TRANSCEIVER: TX PORTS
16	42	CELLULAR FRONT END: LB PAD
17	43	CELLULAR FRONT END: MB PAD
18	44	CELLULAR FRONT END: HB PAD
19	45	CELLULAR FRONT END: 2G PA
20	46	CELLULAR FRONT END: LB ASM
21	47	CELLULAR FRONT END: MB-HB ASM
22	48	CELLULAR FRONT END: DIVERSITY
23	49	SIM
24	50	WIFI/BT: WIFI/BT MODULE
25	51	STOCKHOLM

LAT DIPLEXER1

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	ROW
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	RF2
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	RFC
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	DARWIN

HB PAD

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
353S00376	1	IC,PWR AMP,HB_PAD,TQS	UHBPA_RF	ROW
353S4494	1	IC,PWR AMP,HB_PAD,AVAGO	UHBPA_RF	RF2
353S00376	1	IC,PWR AMP,HB_PAD,TQS	UHBPA_RF	RFC
353S00478	1	IC,PWR AMP,HB_PAD,PT	UHBPA_RF	DARWIN

19.2MHZ XTAL ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
197S0565	197S0593	ALTERNATE	Y_XO_RF	XTAL, 19.2MHZ
197S0598	197S0593	ALTERNATE	Y_XO_RF	XTAL, 19.2MHZ

VINYL

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
337S00176	1	IC, VINYL	U5101_RF	ROW
337S00176	1	IC, VINYL	U5101_RF	RF2

VINYL RESISTOR

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
117S0161	1	0 OHM, RESISTOR	R3402_RF	RFC
117S0161	1	0 OHM, RESISTOR	R3402_RF	DARWIN

HW_REV1_ID RESISTOR

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
118S0646	1	51.1 KOHM, RESISTOR	R3503_RF	DARWIN

SIM ESD DIODE ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
377S00042	377S0163	ALTERNATE	VR301_RF	ON SEMI ESD DIODE

ROW/RF2 HB PAD MATCHING BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
152S1907	1	3.3NH, INDUCTOR	L4105_RF	ROW
152S1990	1	3.0NH, INDUCTOR	L4105_RF	RF2
131S0377	1	1.2PF, CAPACITOR	C4108_RF	RF2
152S2007	1	8.2NH, INDUCTOR	L4401_RF	ROW
131S0426	1	22PF, CAPACITOR	C4405_RF	ROW
152S2001	1	2.4NH, INDUCTOR	C4405_RF	RF2
131S0631	1	0.3PF, CAPACITOR	L4406_RF	RF2
152S2044	1	2.2NH, INDUCTOR	C4406_RF	ROW
152S2021	1	1.5NH, INDUCTOR	C4406_RF	RF2
131S0631	1	0.3PF, CAPACITOR	L4407_RF	ROW
152S2056	1	5.6NH, INDUCTOR	L4403_RF	ROW
131S0429	1	8.2PF, CAPACITOR	C4407_RF	ROW
152S2036	1	2.5NH, INDUCTOR	C4407_RF	RF2
131S0631	1	0.3PF, CAPACITOR	L4408_RF	RF2
152S00143	1	15NH, INDUCTOR	L4404_RF	ROW
131S0823	1	33PF, CAPACITOR	C4408_RF	ROW
152S2051	1	1.3NH, INDUCTOR	C4408_RF	RF2
152S2042	1	1.8NH, INDUCTOR	C4409_RF	RF2
117S0108	1	51 OHM, RESISTOR	L4410_RF	ROW
131S0163	1	0.6PF, CAPACITOR	L4410_RF	RF2
152S00052	1	3.4NH, INDUCTOR	L3910_RF	ROW
152S00026	1	3.5NH, INDUCTOR	L3910_RF	RF2
152S2039	1	3.8NH, INDUCTOR	L3911_RF	ROW
117S0201	1	0 OHM, RESISTOR	L3911_RF	RF2
131S0279	1	1.3PF, CAPACITOR	L3919_RF	ROW
152S2045	1	3.0NH, INDUCTOR	L3919_RF	RF2
152S00052	1	3.4NH, INDUCTOR	L3912_RF	RF2
131S0599	1	1.5PF, CAPACITOR	C3922_RF	RF2
131S0630	1	27PF, CAPACITOR	C3911_RF	RF2

LB PAD

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
353S00461	1	IC,PWR AMP,LB_PAD,SKWS	ULBPA_RF	ROW
353S00461	1	IC,PWR AMP,LB_PAD,SKWS	ULBPA_RF	RF2
353S00461	1	IC,PWR AMP,LB_PAD,SKWS	ULBPA_RF	RFC
353S00541	1	IC,PWR AMP,LB_PAD,PT	ULBPA_RF	DARWIN

MB PAD

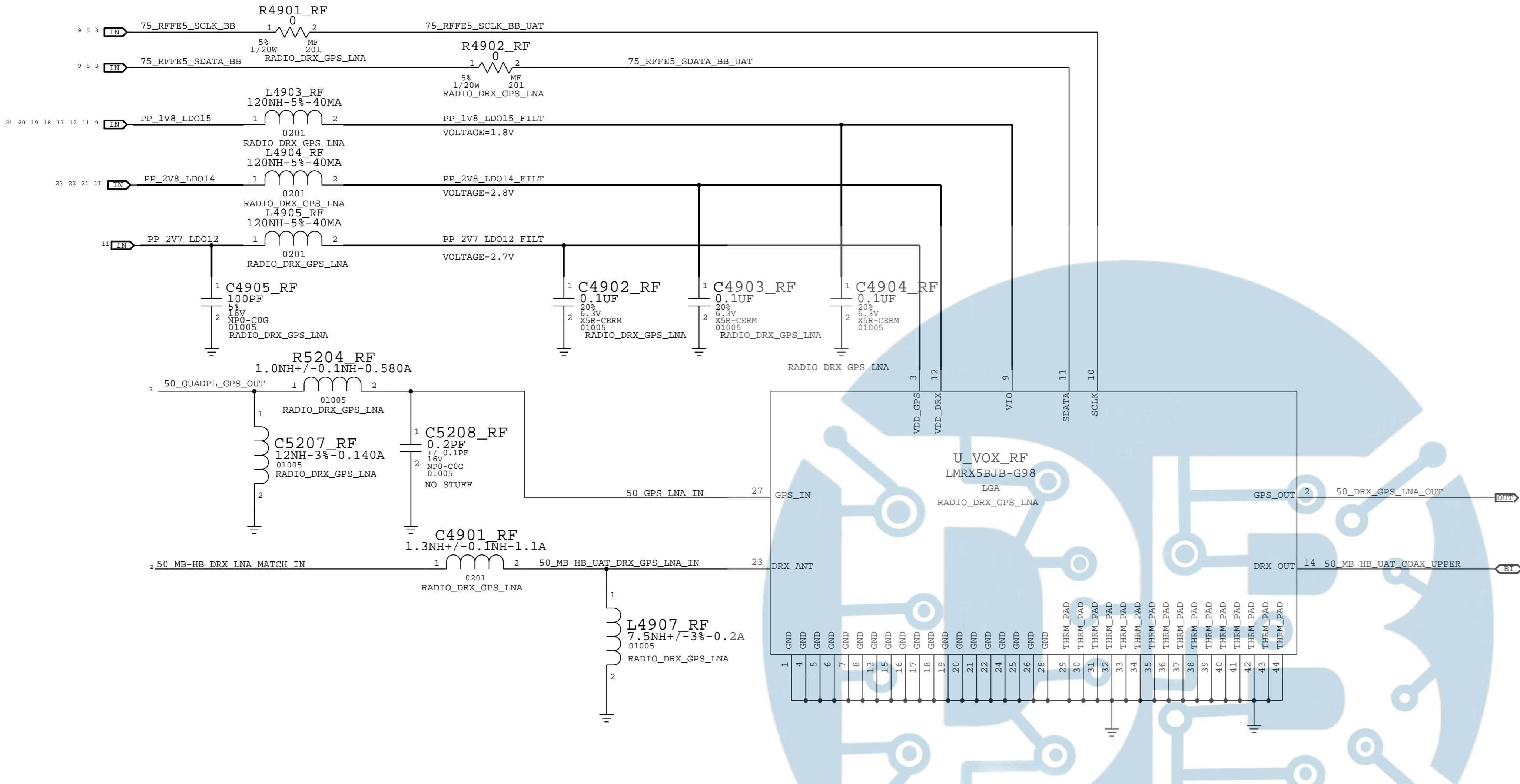
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353S4495	1	IC,PWR AMP,MB_PAD	UMBPA_RF	RF2
353S4495	1	IC,PWR AMP,MB_PAD	UMBPA_RF	RFC
353S00477	1	IC,PWR AMP,MB_PAD, PT	UMBPA_RF	DARWIN

RFC HB PAD MATCHING BOM OPTIONS

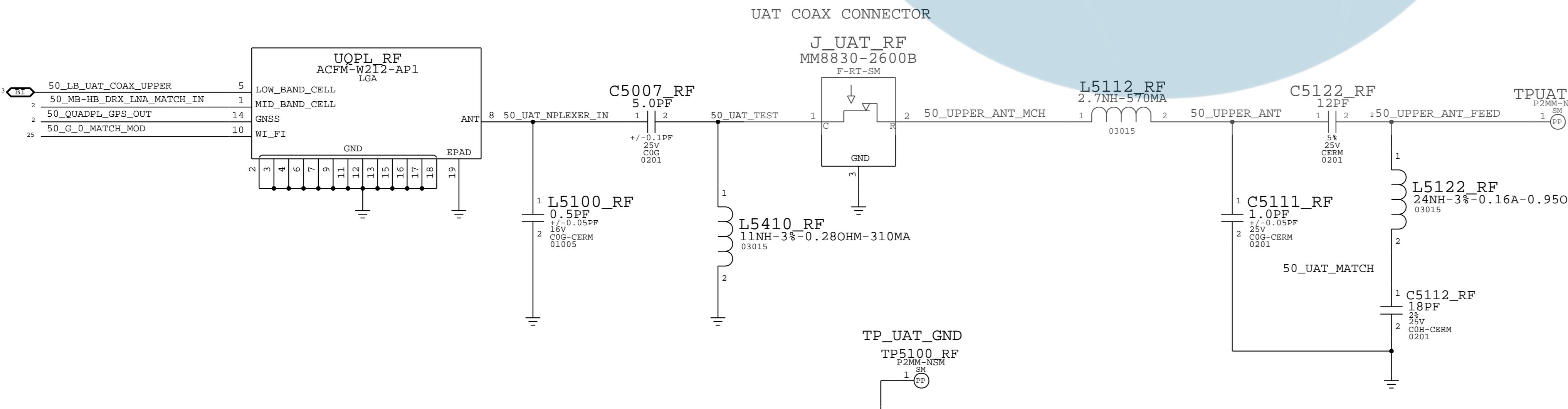
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N71-SPECIFIC RADIO PAGE 2

DIVERSITY LNA

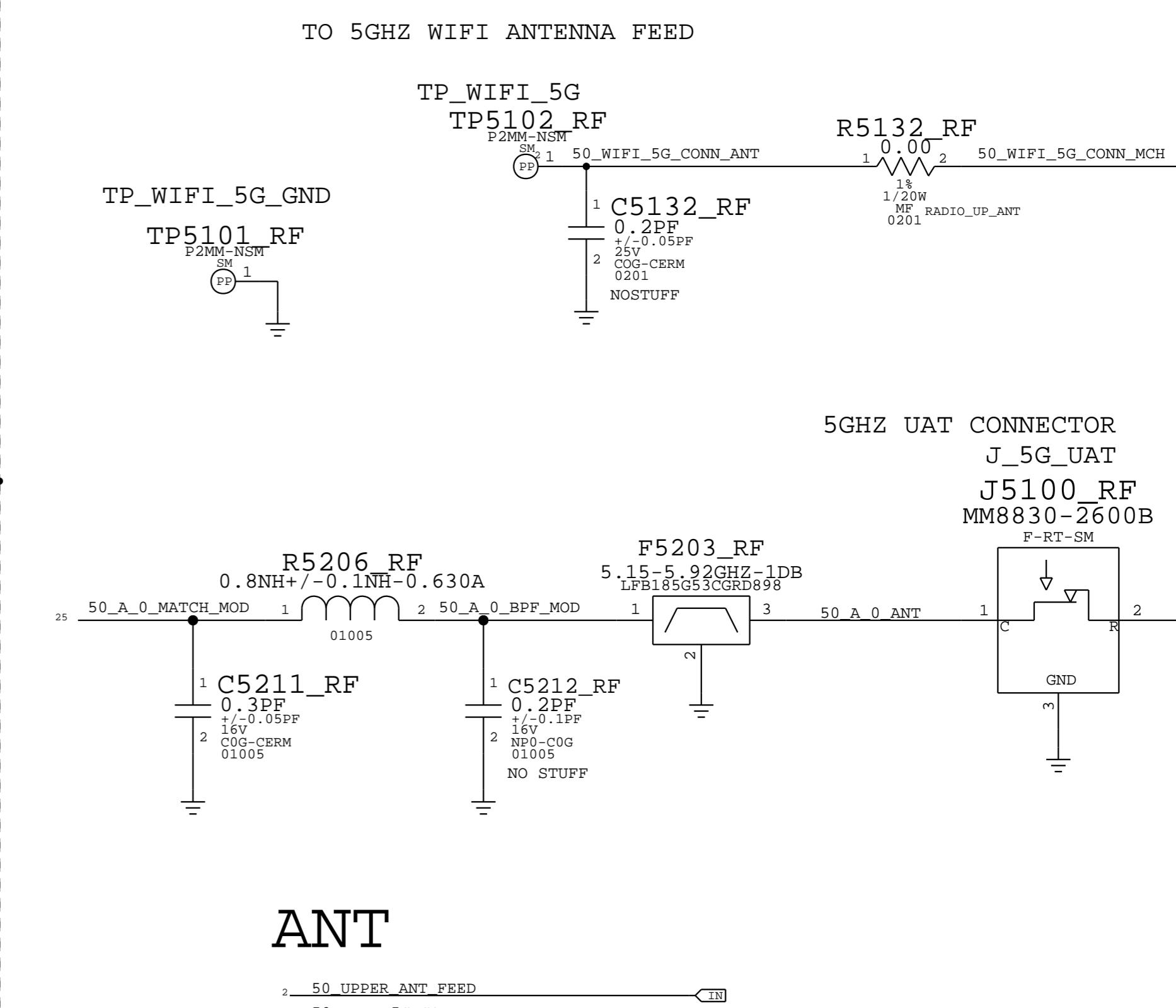


UAT ANT FEED



CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSE ONLY - NOT A CHANGE REQUEST

WIFI ANT FEED



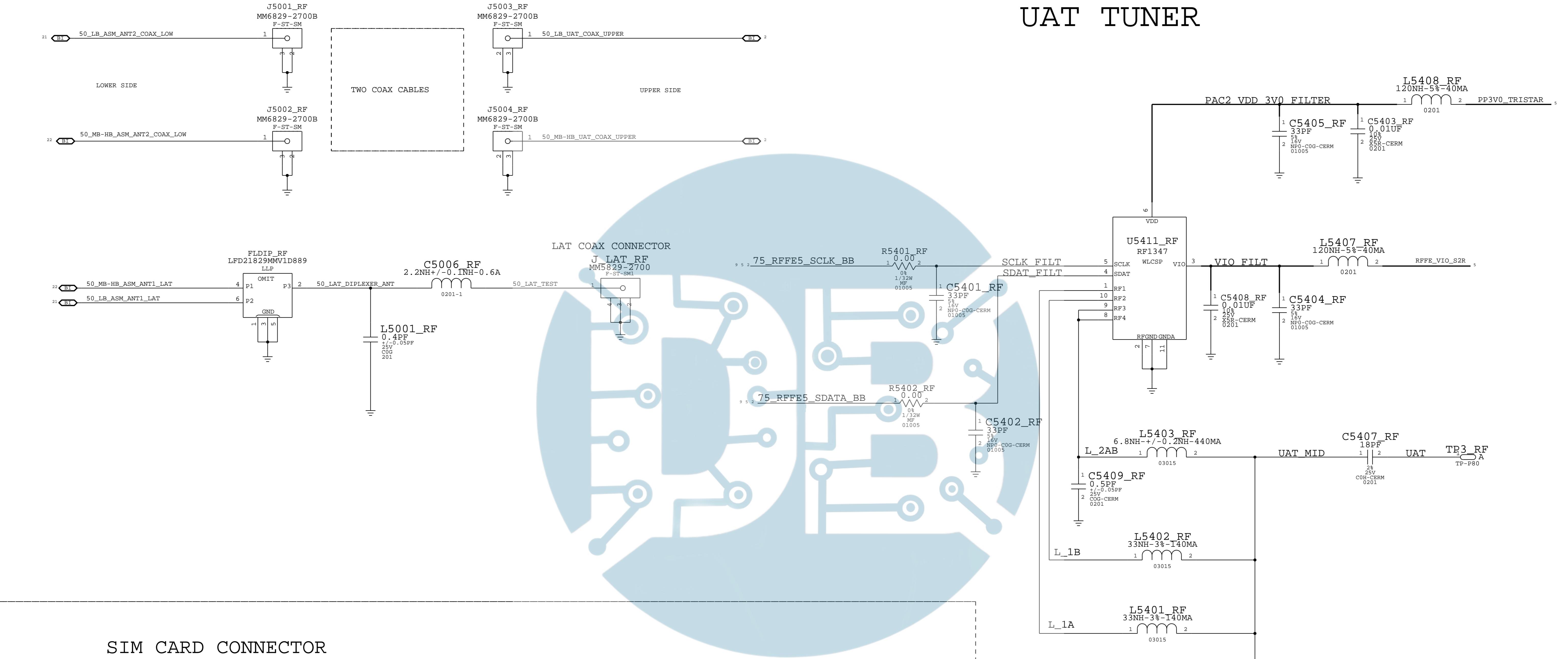
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PAGE TITLE	ELNA & UAT ANT FEED	DRAWING NUMBER	051-1902	SIZE	D
REVISION	A.0.0				
BRANCH					
PAGE	2 OF 51				
HEET	35 OF 59				
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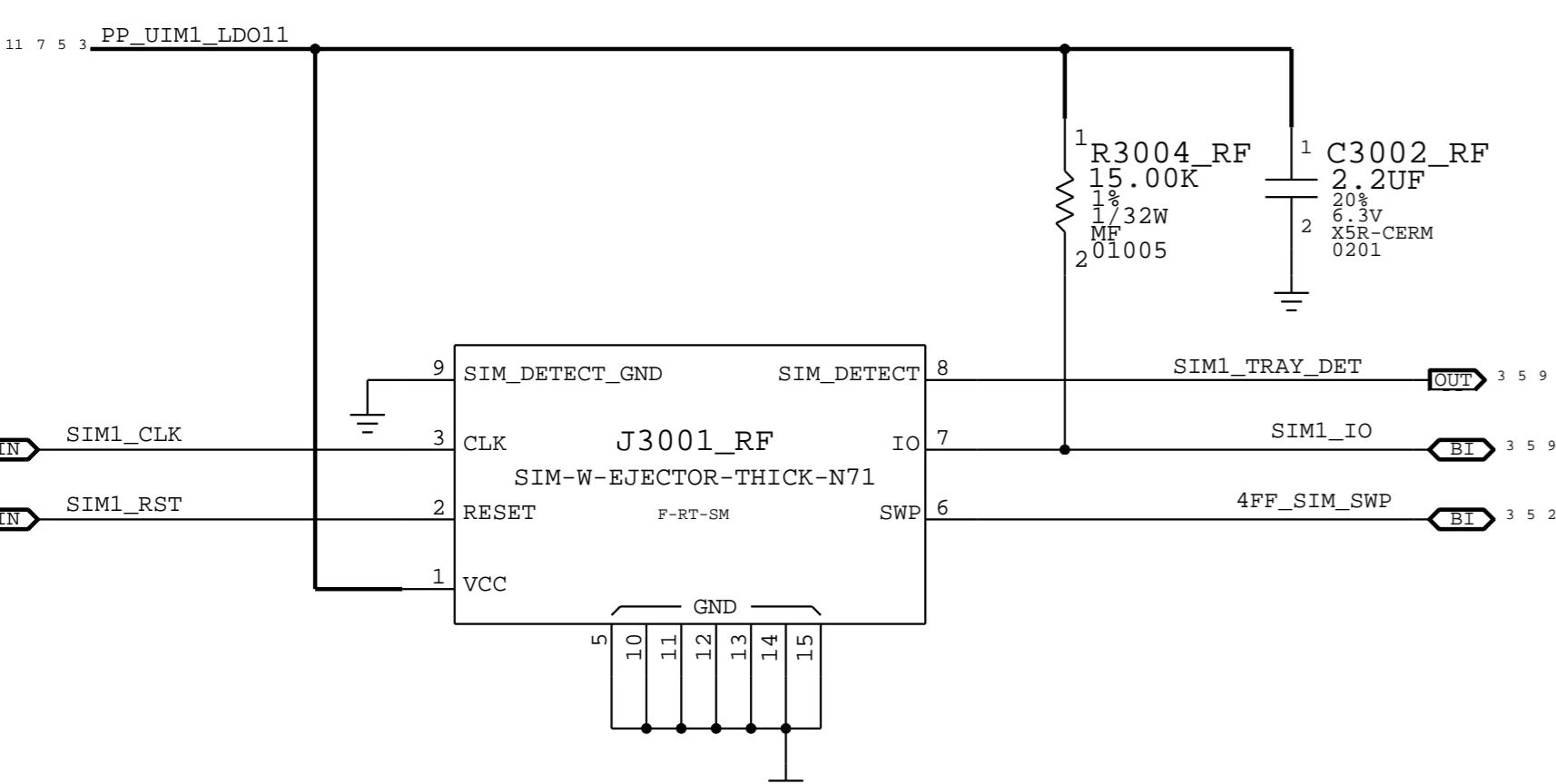
N71-SPECIFIC RADIO PAGE 3

ANTENNA FEEDS AND CONNECTORS

UAT TUNER

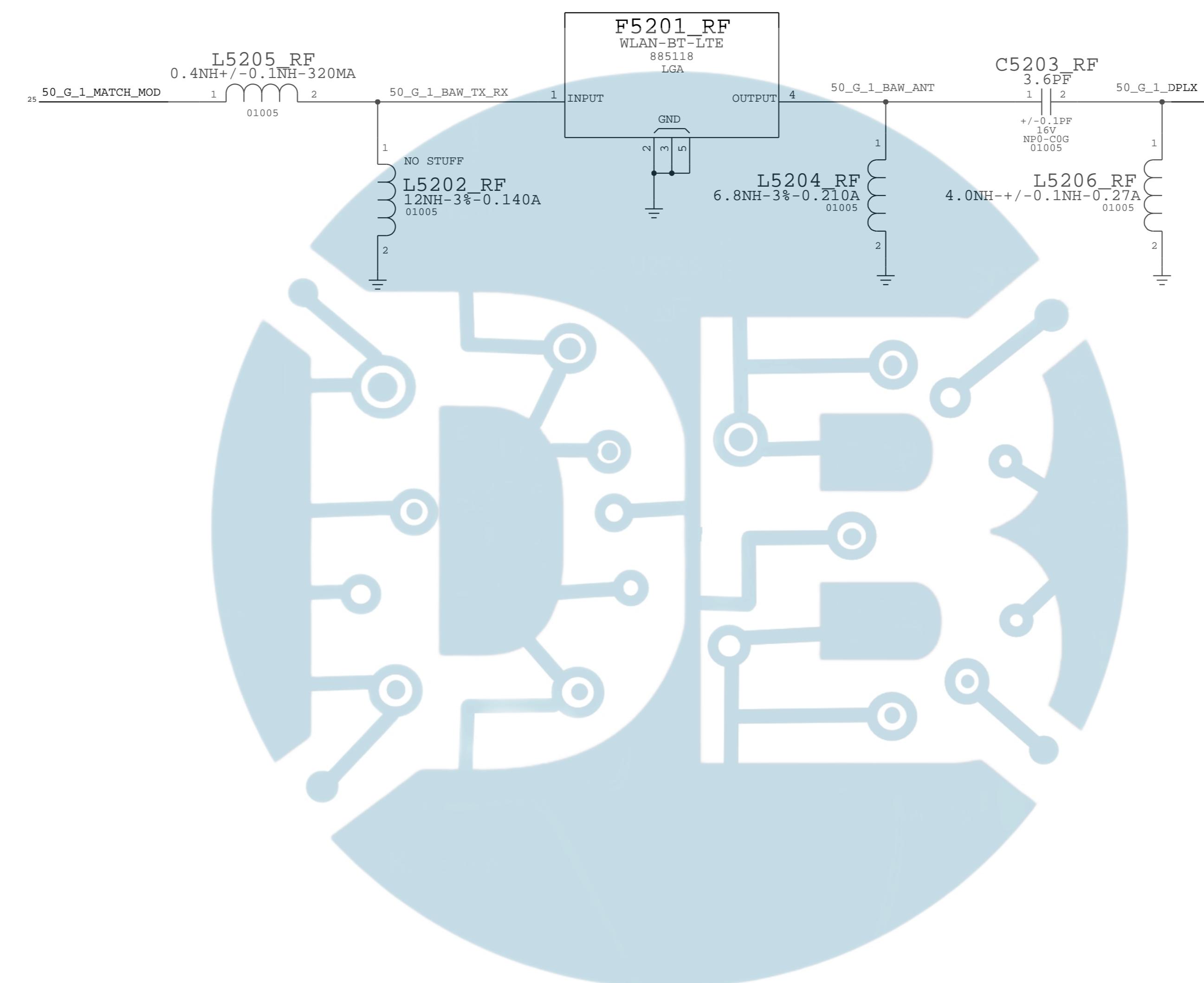


SIM CARD CONNECTOR



N71-SPECIFIC RADIO PAGE 4

WLAN LAT 2.4GHZ BAW BPF

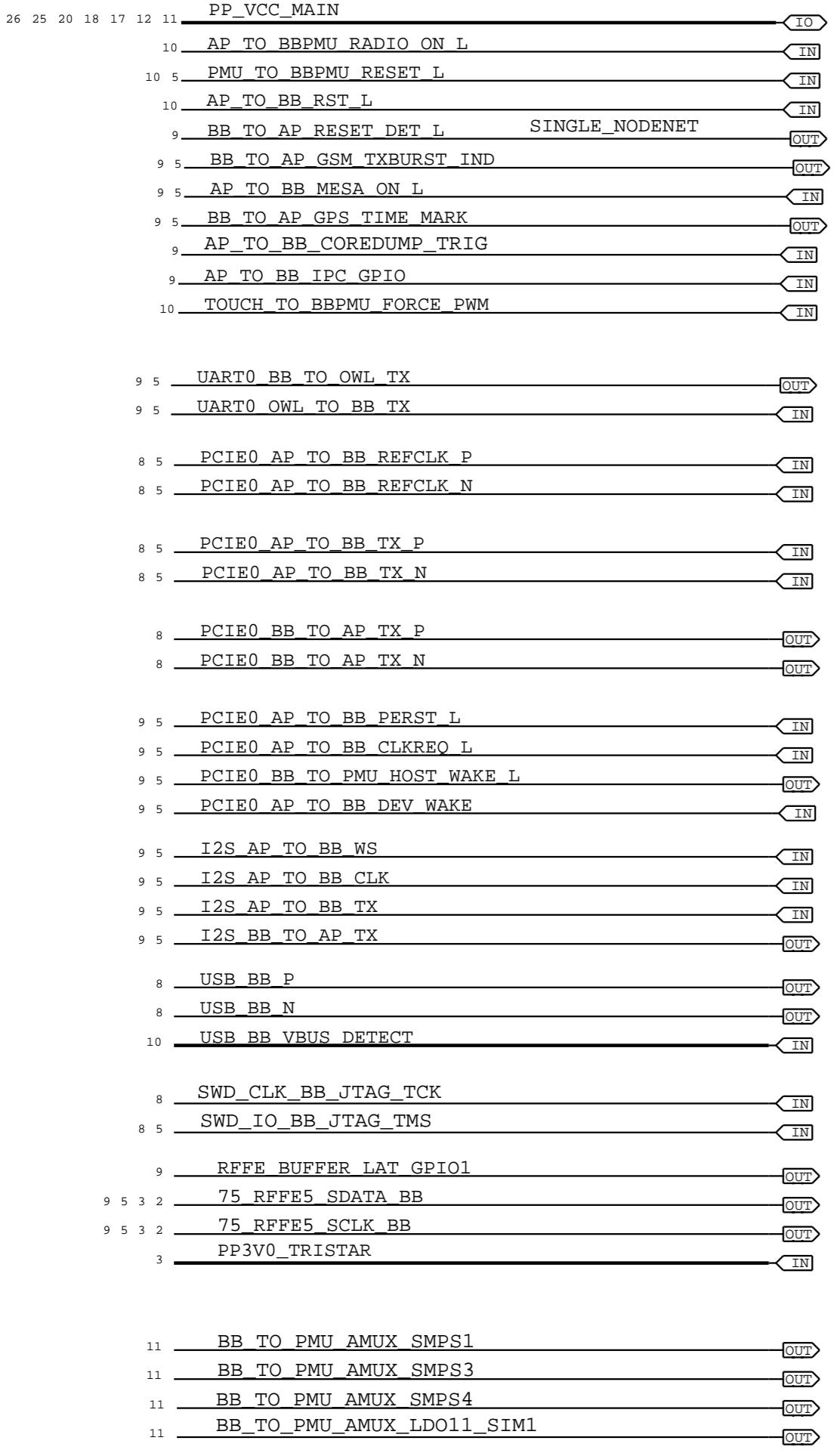


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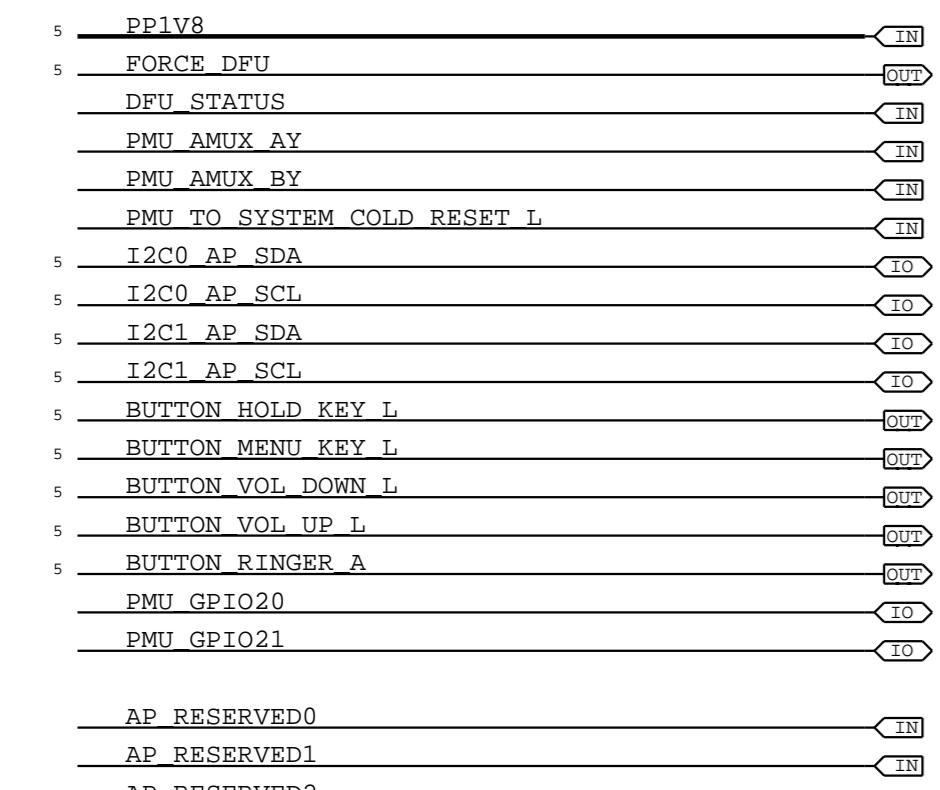
AP TO BB/WLAN/BT/SH CONNECTIONS

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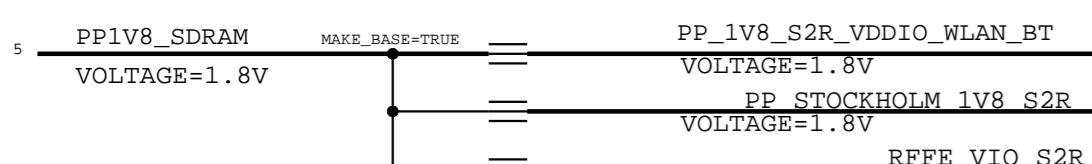
BASEBAND



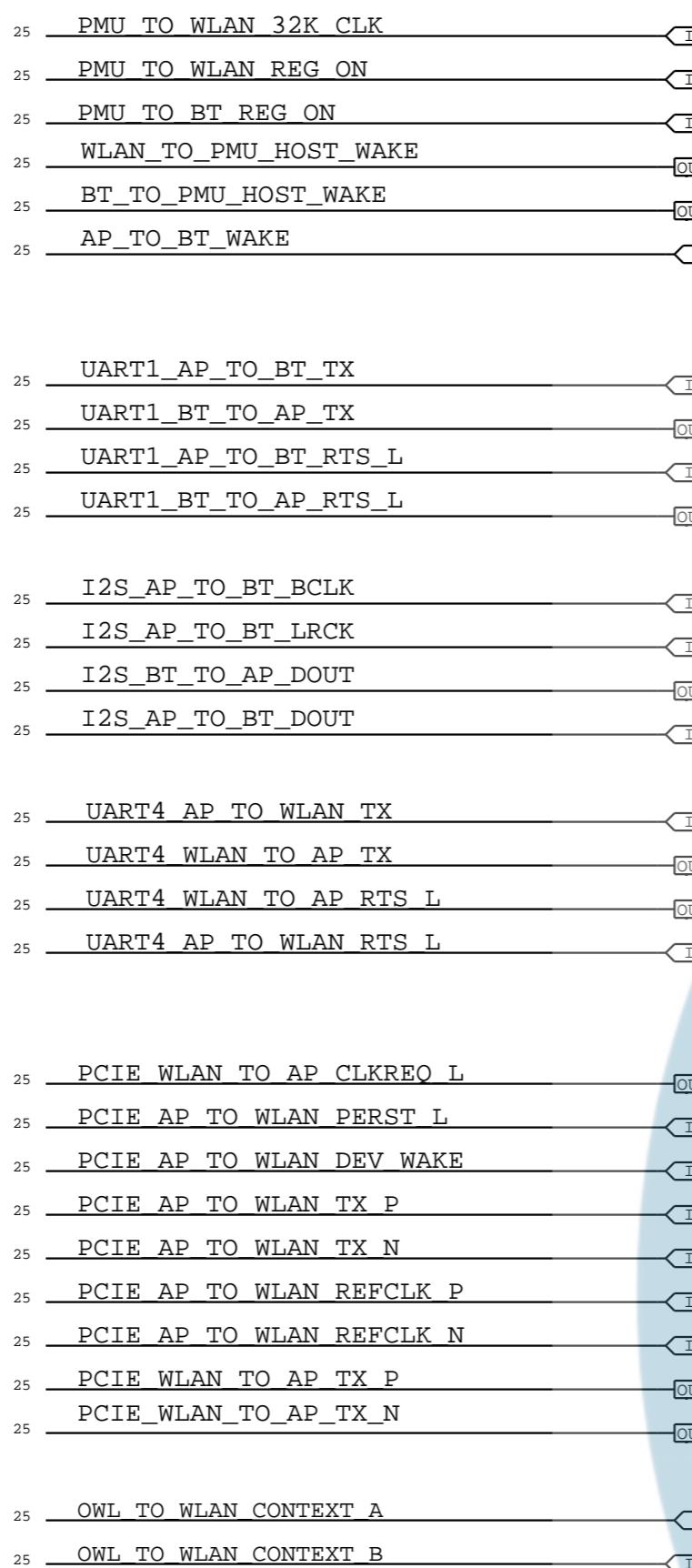
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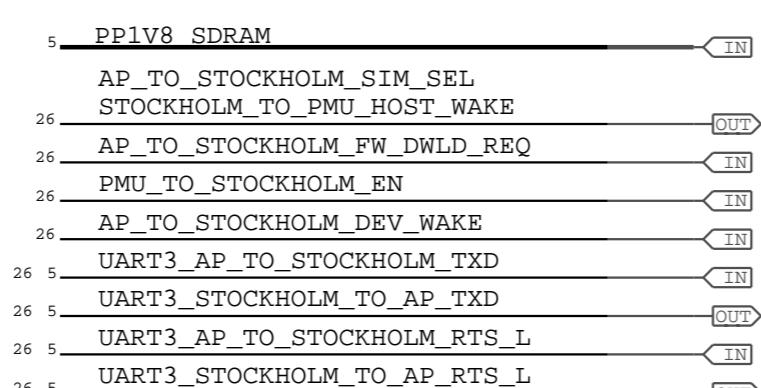
POWER



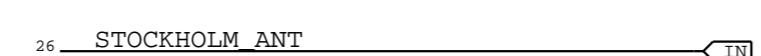
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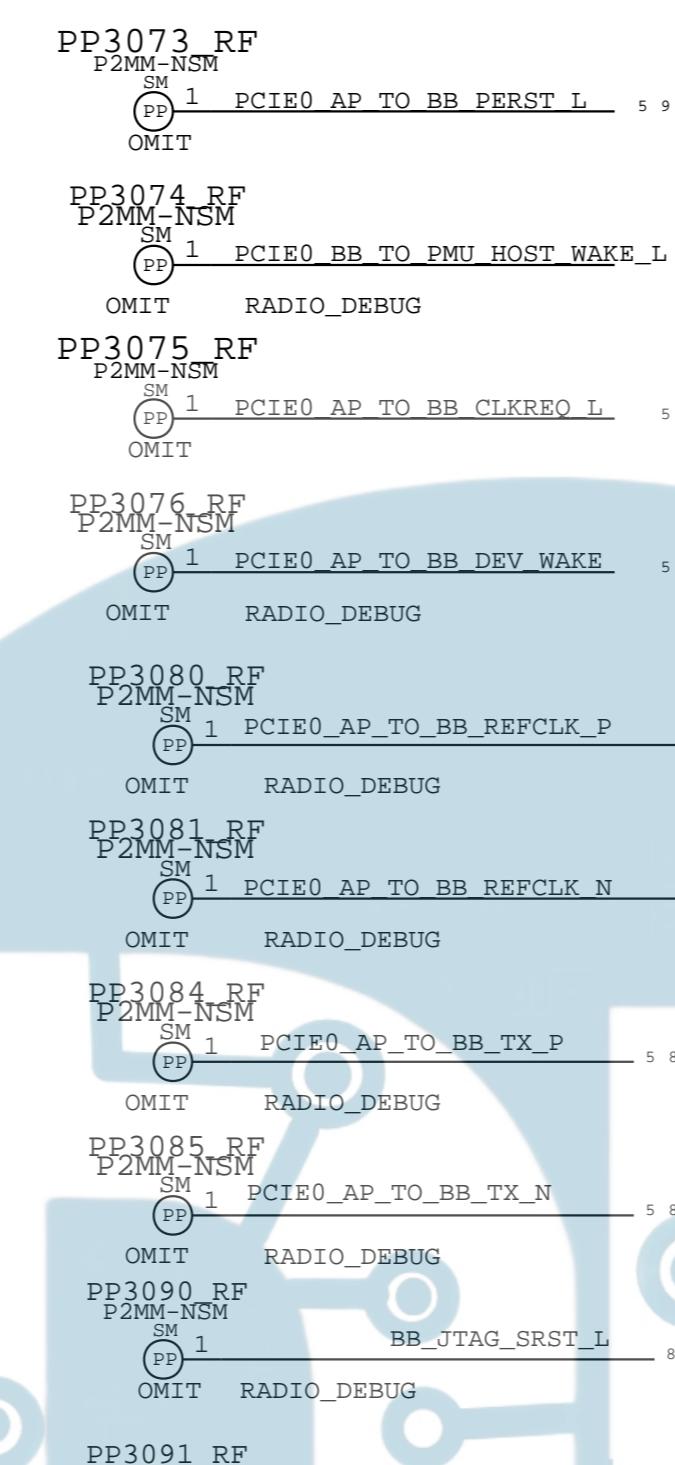
STOCKHOLM



ANT



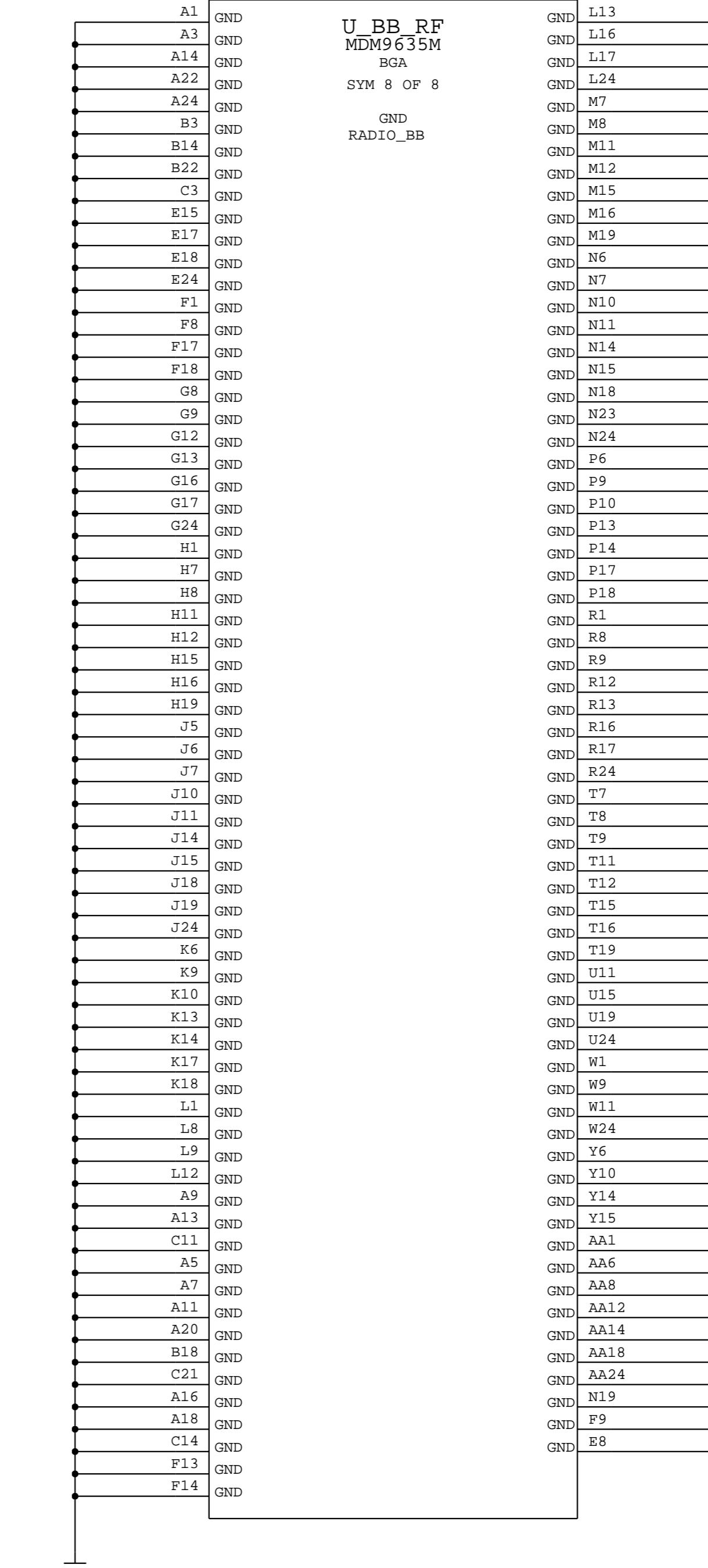
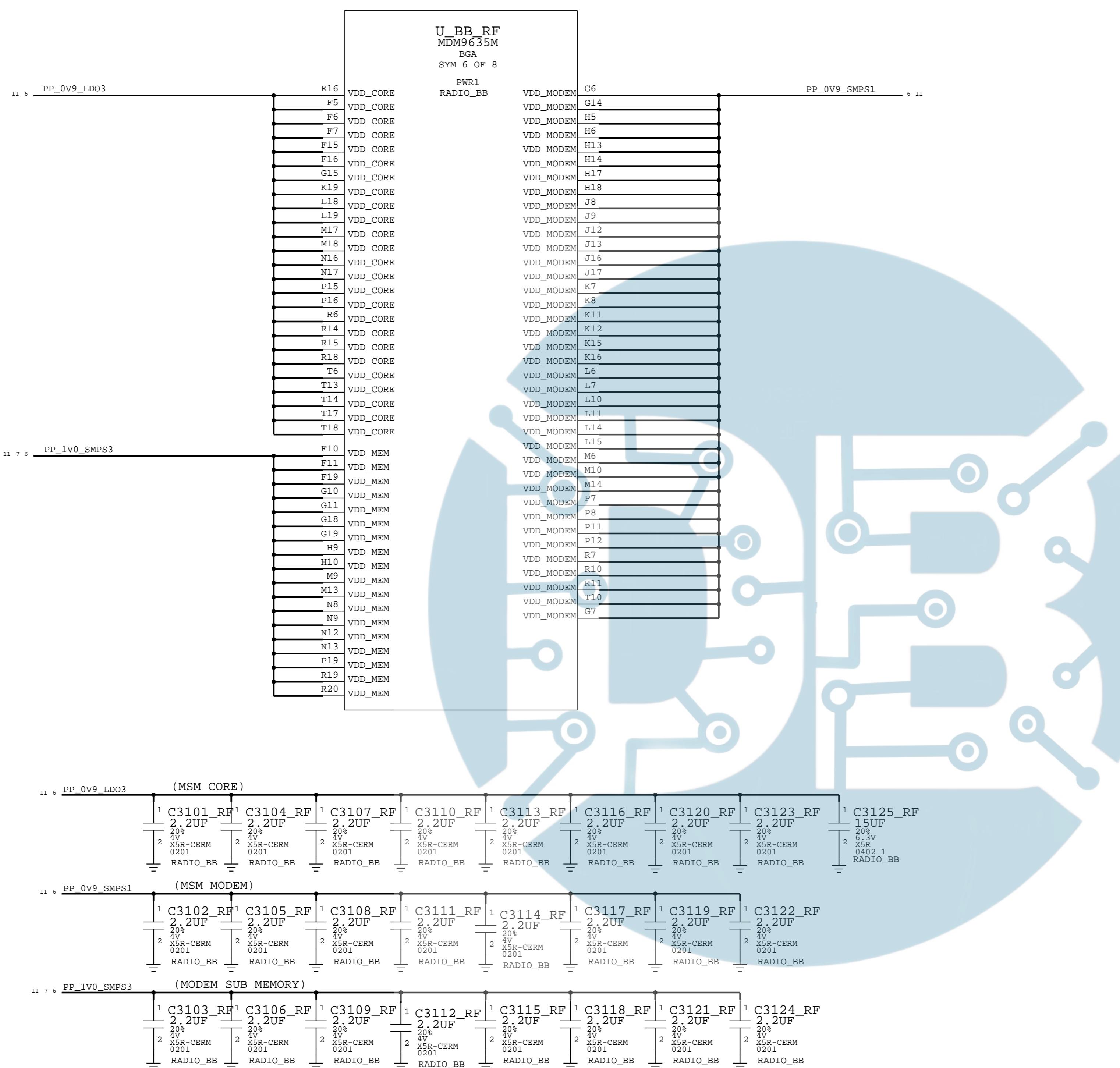
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BASEBAND : POWER 1

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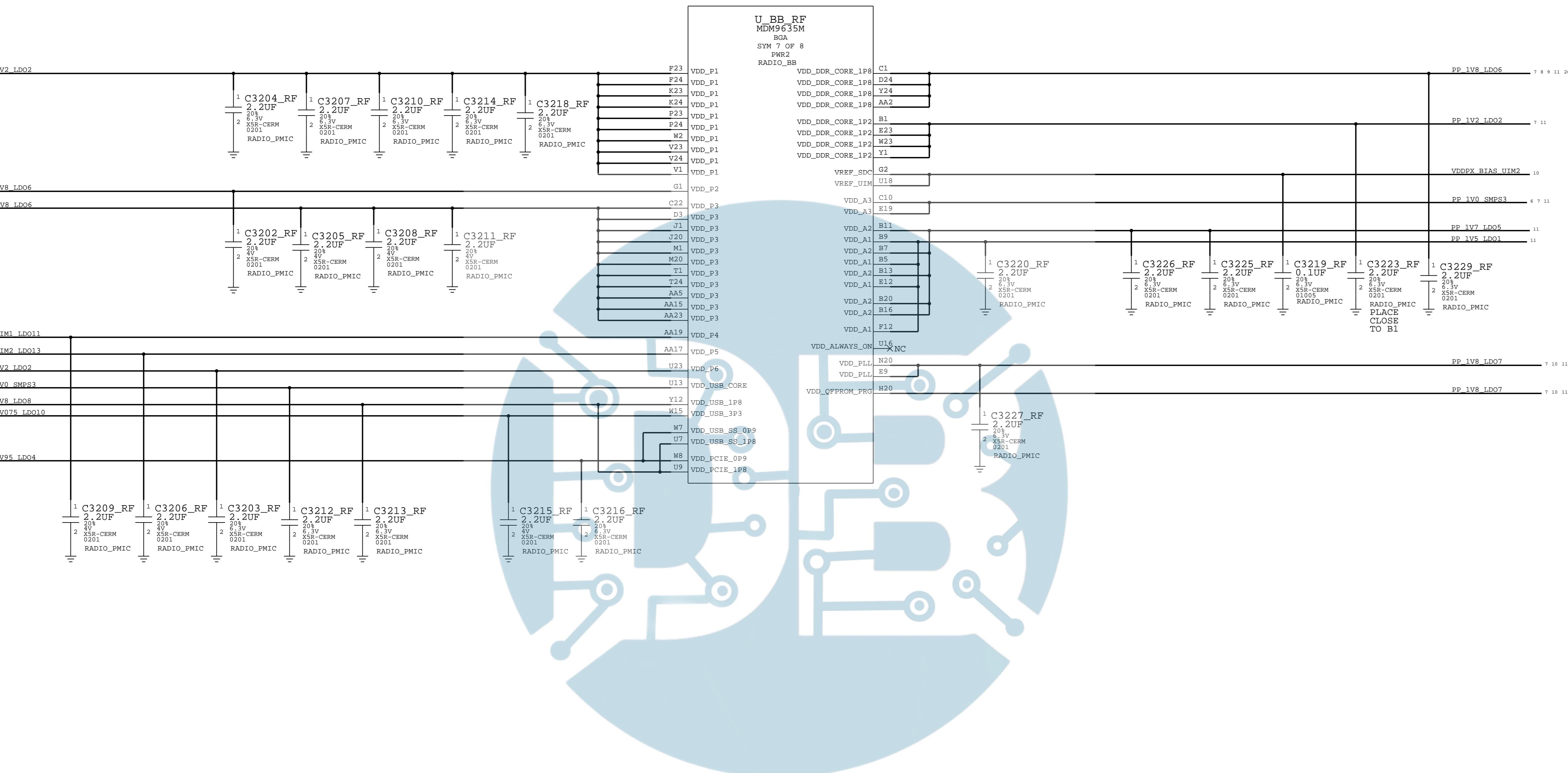
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	SHEET 39 OF 59	

BASEBAND: POWER 2

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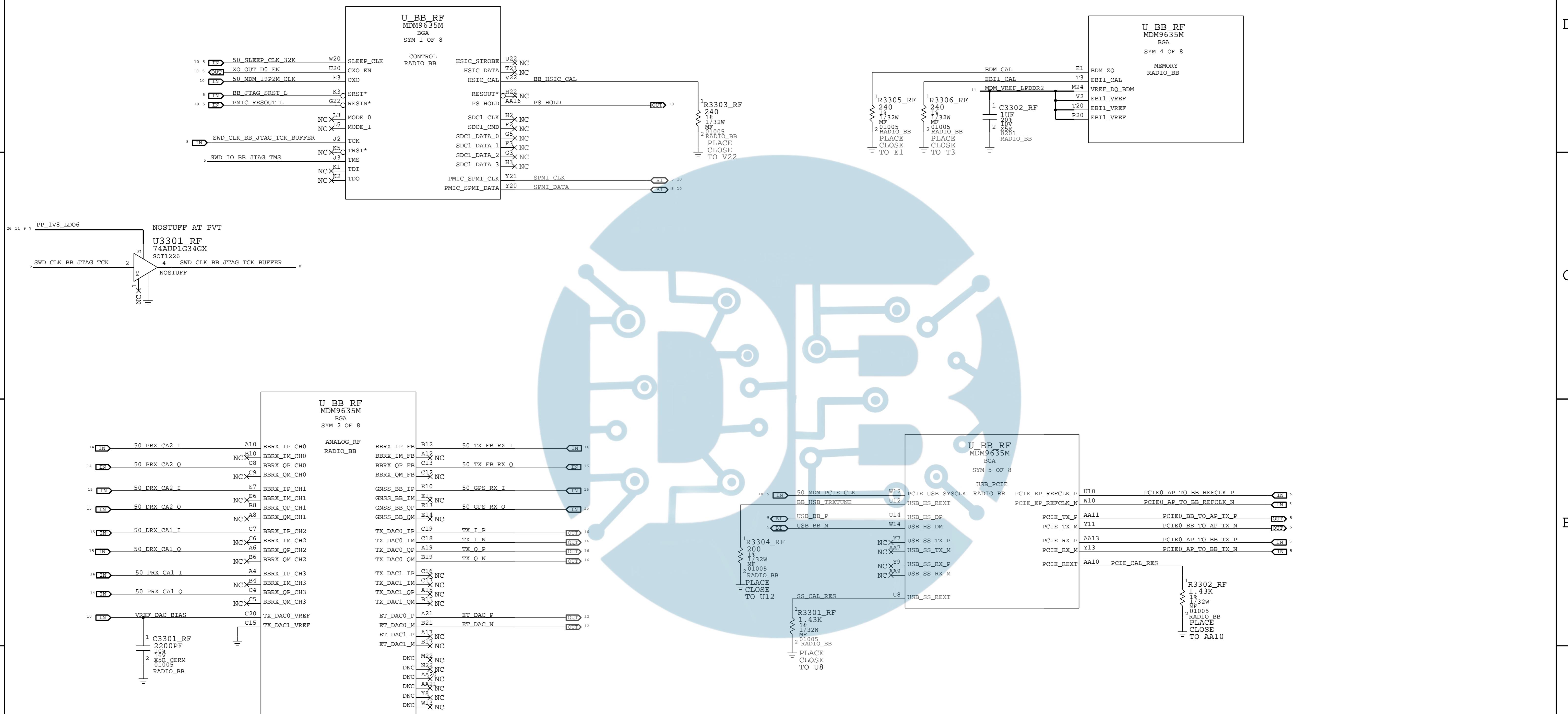
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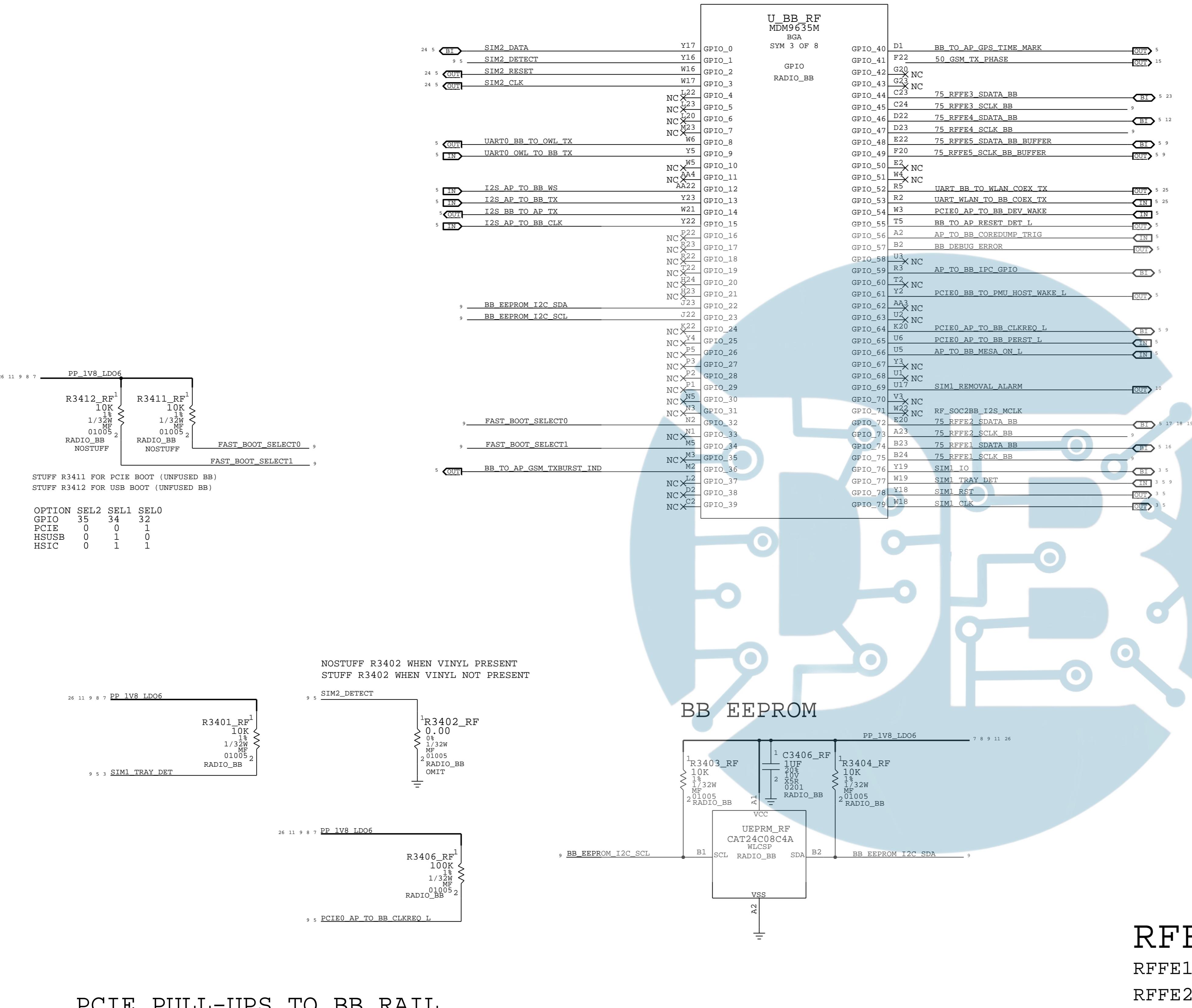
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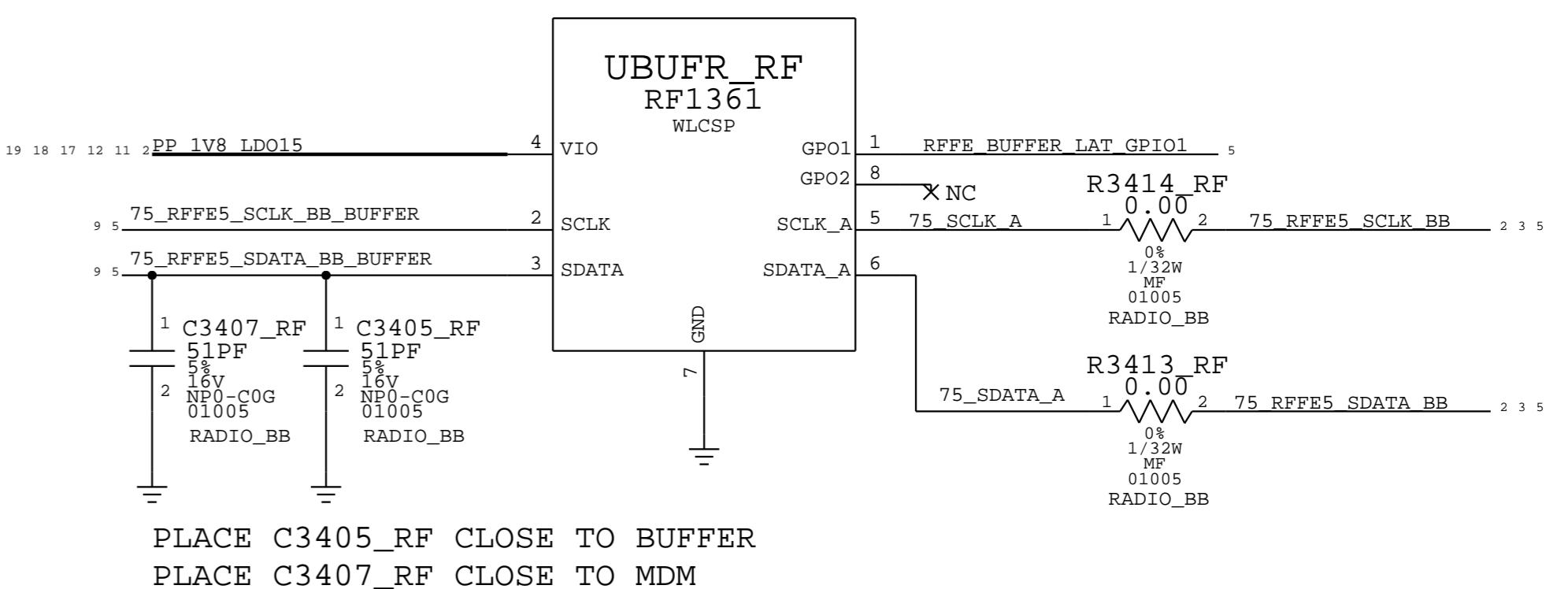
BASEBAND: CONTROL AND INTERFACES



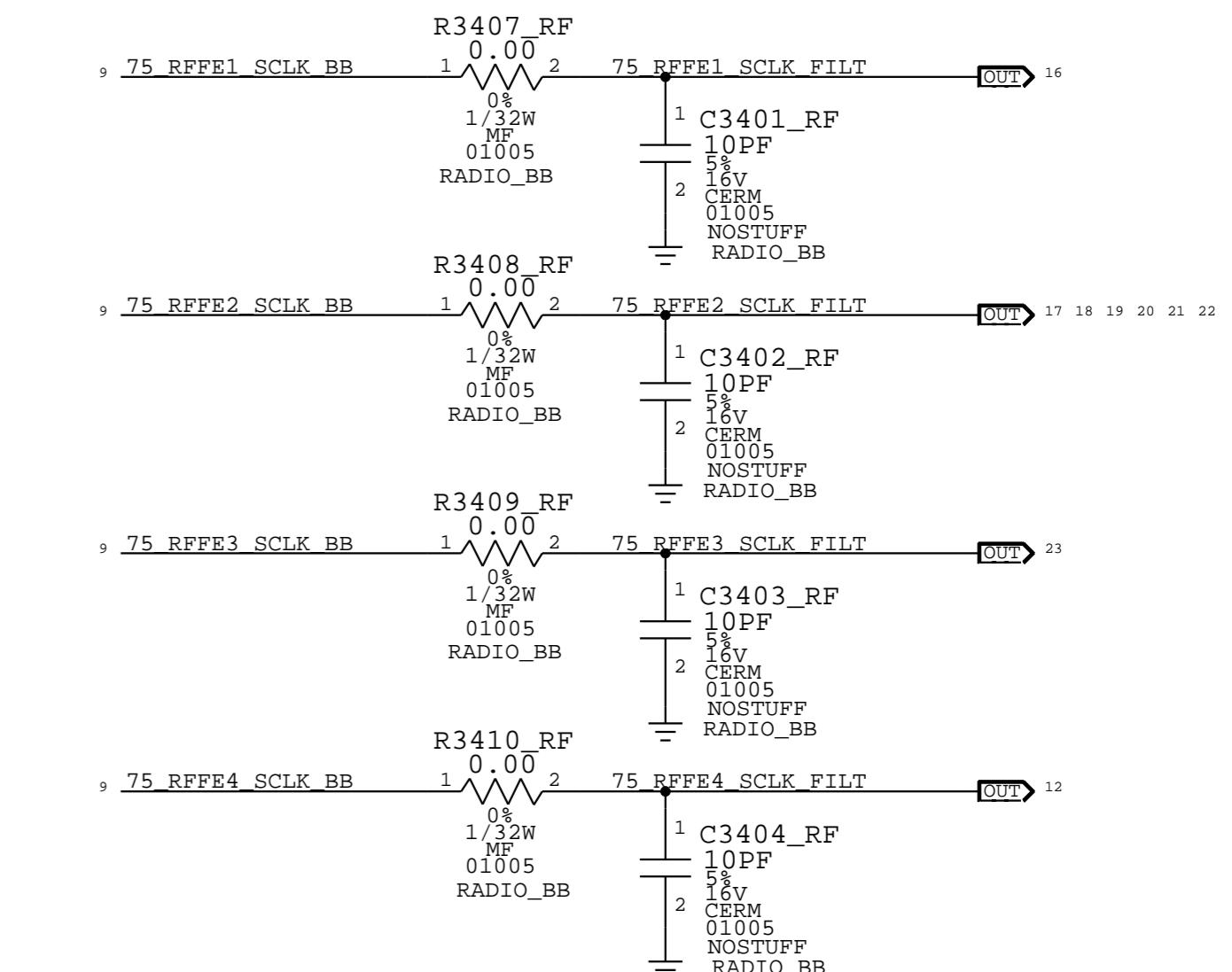
BASEBAND: GPIOS



BUFFER ON RFFE5
SCLK/SDATA_A IS OUTPUT



RFFE CLOCK FILTERS



RFFE USAGE TABLE

RFFE1 WTR

RFFE2 LB/MB/HB PAD, 2G PA, LB/MB/HB ASM

RFFE3 DIV ASM

RFFE4 QPOET

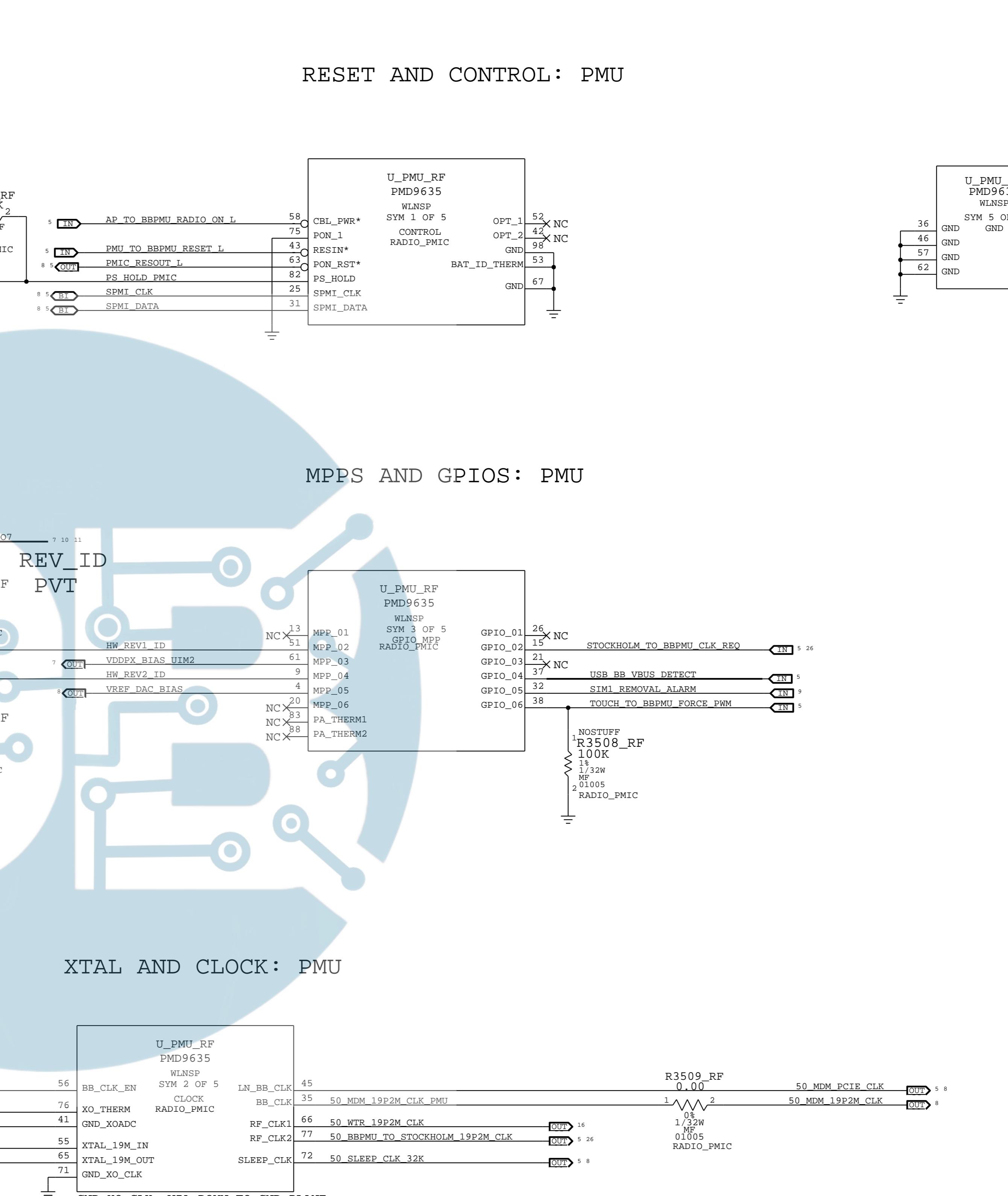
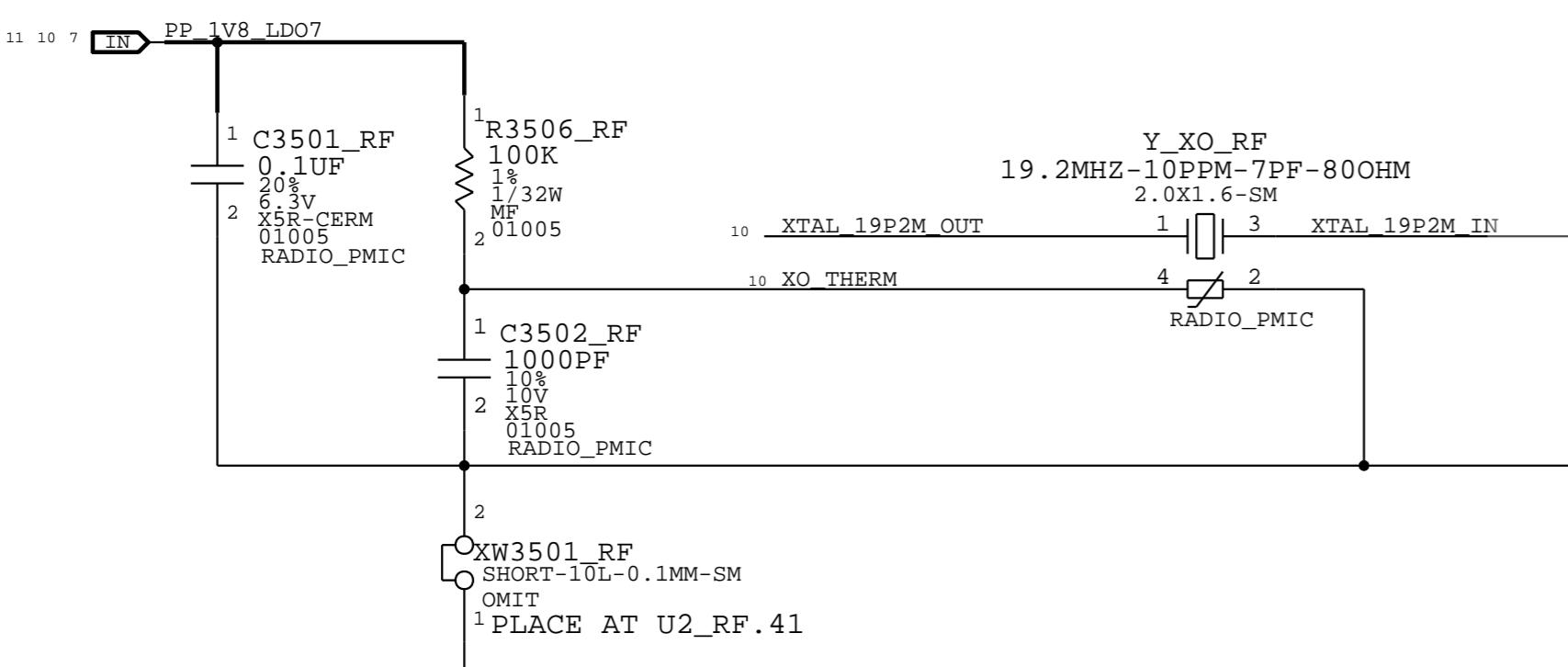
RFFE5 DIV LNA, ANT TUNERS

PAGE TITLE	CELLULAR BASEBAND: GPIOS	
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SHEET	42 OF 59	

PMU: CONTROL AND CLOCKS

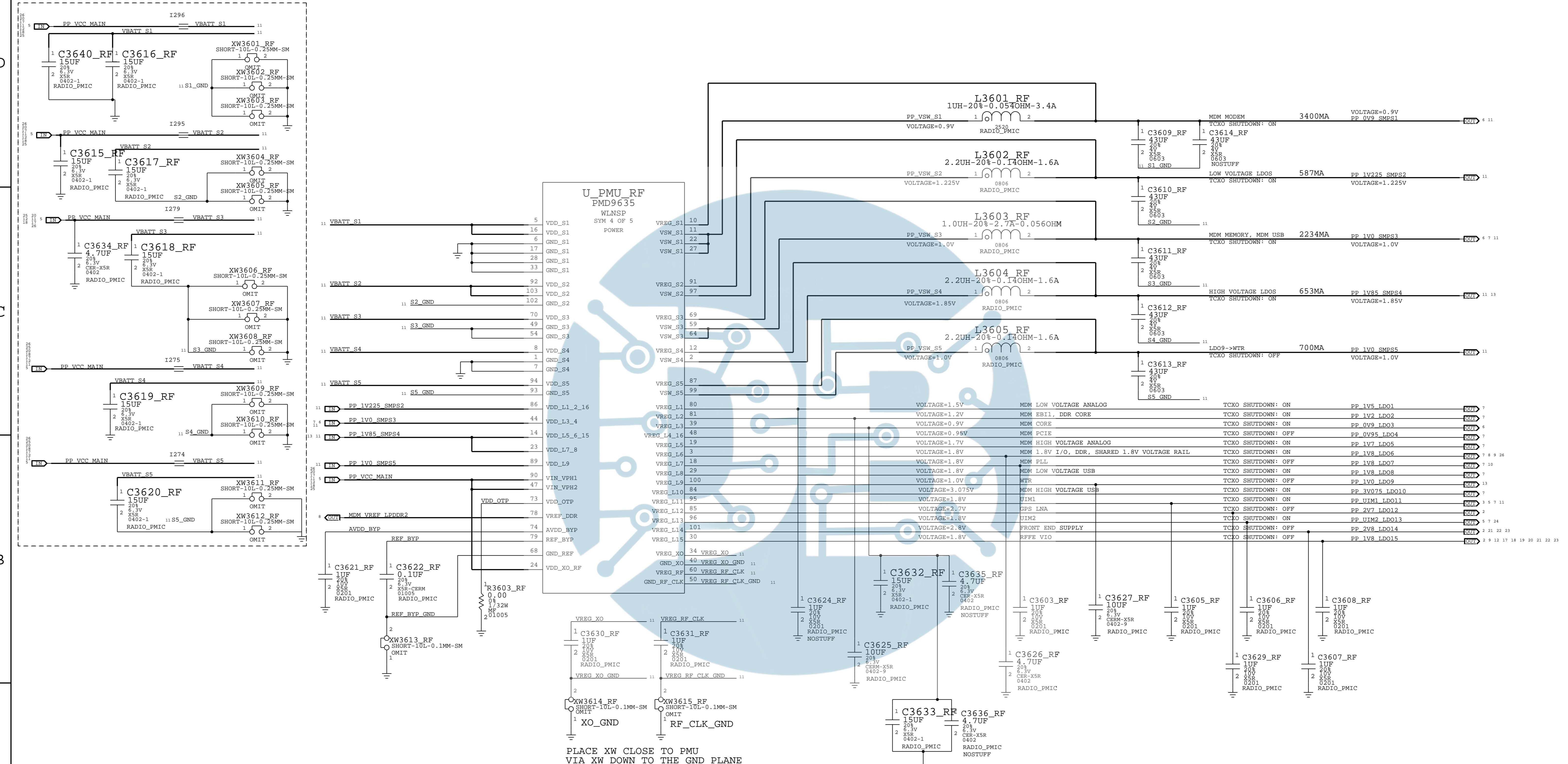
HW_REV2_ID	R3502	R3503	CONFIG
1.80V	698K	-	MLB
0.12V	698K	51.1K	SELF GEN

HW_REV_ID	R3504	R3505	REVISION
0.10V	887K	51.1K	DEV1
0.30V	255K	51.1K	DEV2
0.50V	124K	51.1K	DEV3
0.70V	82.5K	51.1K	DEV4/PROTOMLB1
0.90V	51.1K	51.1K	PROTOMLB2
1.10V	31.6K	51.1K	DEV5/PROTO1
1.20V	50K	100K	PROTO2
1.31V	39K	105K	EVT
1.43V	13.3K	51.1K	EVT_ALT
1.55V	8.25K	51.1K	CARRIER BUILD
1.67V	3.92K	51.1K	DVT
1.80V	10K	-	PVT

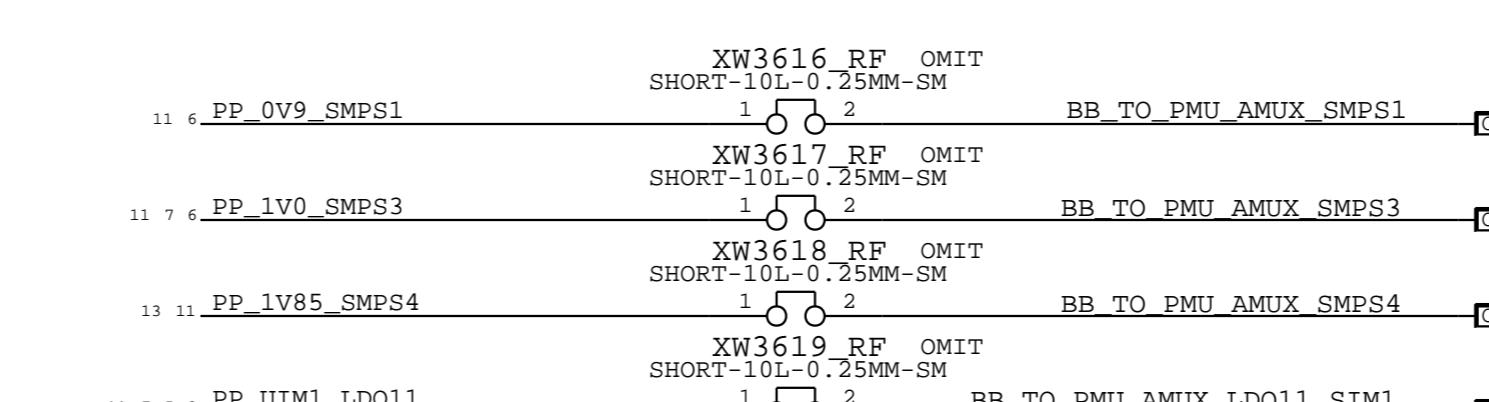


PMU: SWITCHERS AND LDOS

SWITCHERS BULK CAPS



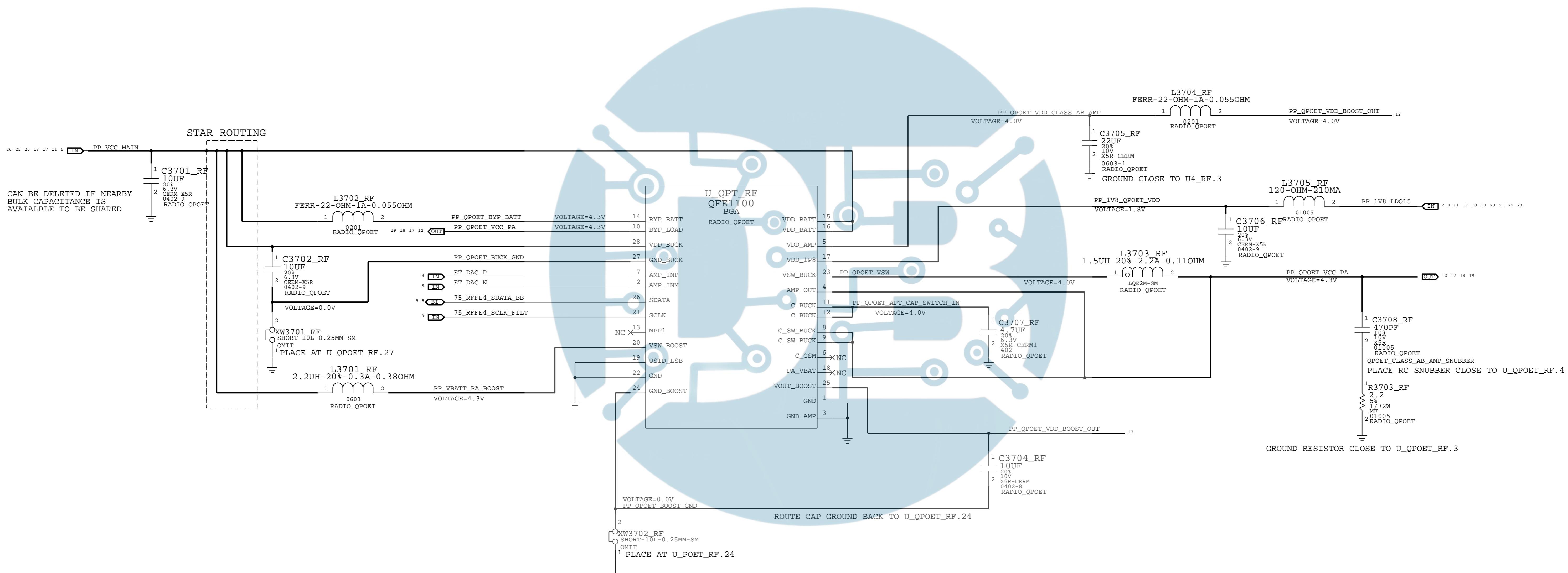
PLACE XW CLOSE TO PMU
VIA XW DOWN TO THE GND PLA



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	PAGE 36 OF 51	
	SHEET 44 OF 59	

PMU: ET MODULATOR



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		BRANCH
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TRANSCEIVER: POWER

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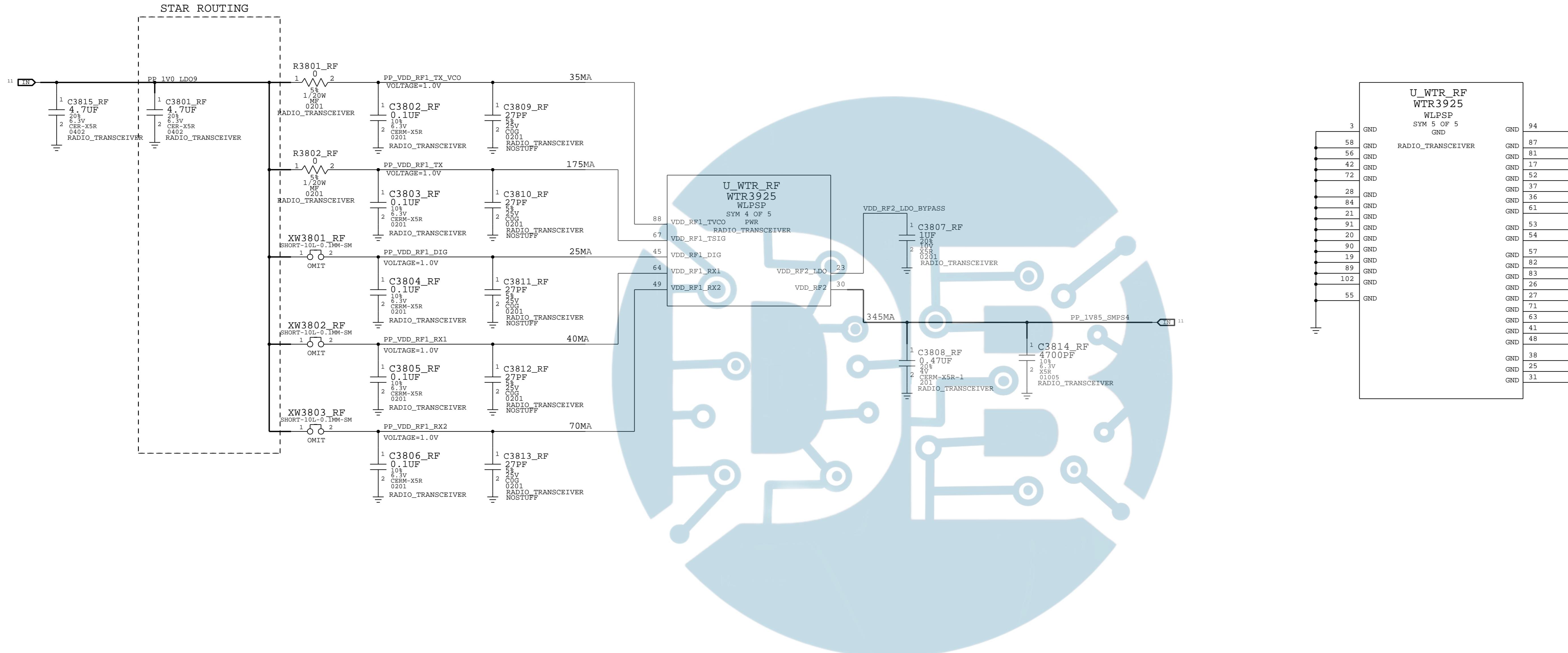
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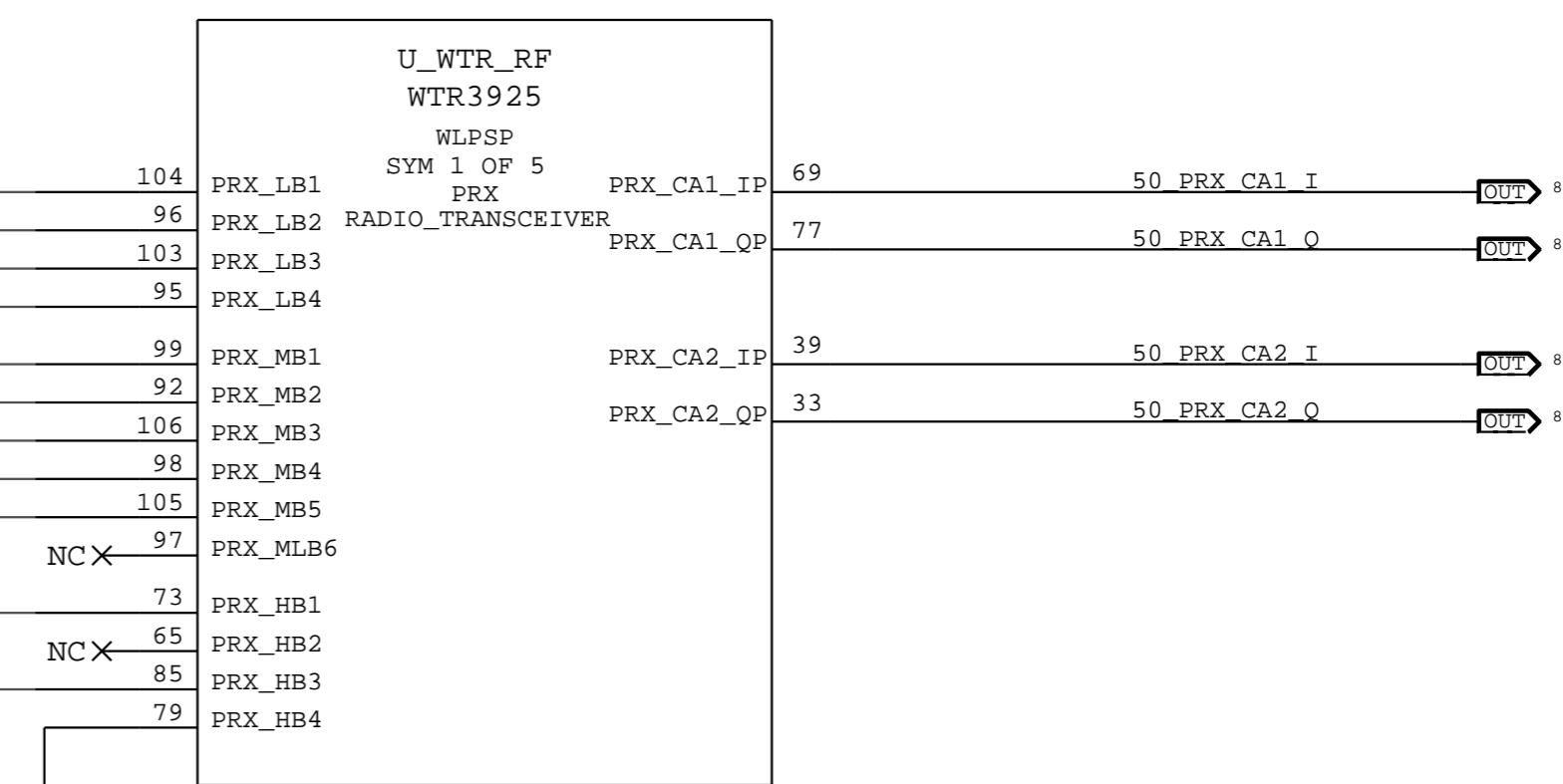
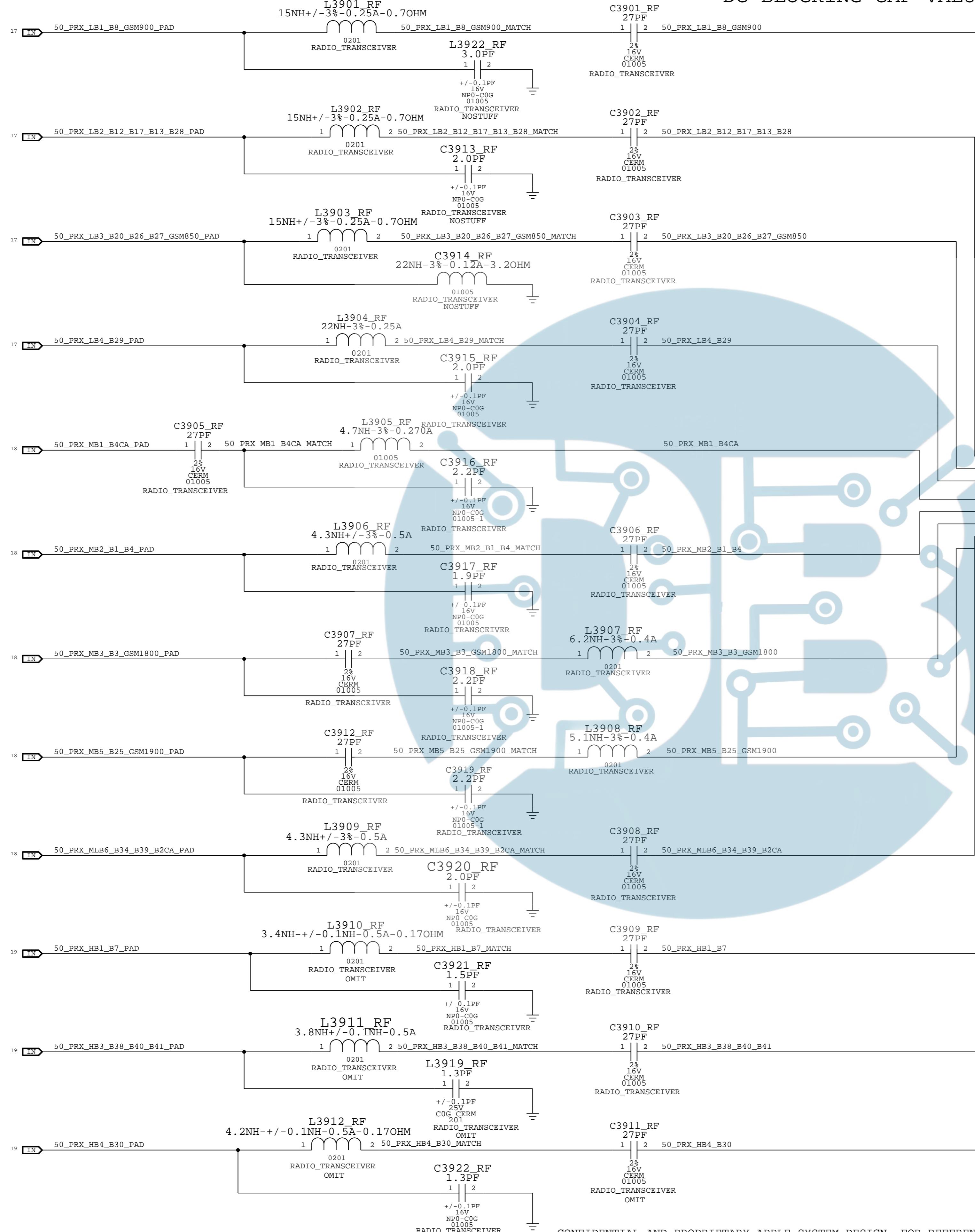


PAGE TITLE	CELLULAR TRANSCEIVER: POWER	
DRAWING NUMBER	051-1902	SIZE D
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PAGE	38 OF 51	
SHEET	46 OF 59	

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TRANSCEIVER: PRX PORTS

DC BLOCKING CAP VALUES CANNOT BE MORE THAN 33PF



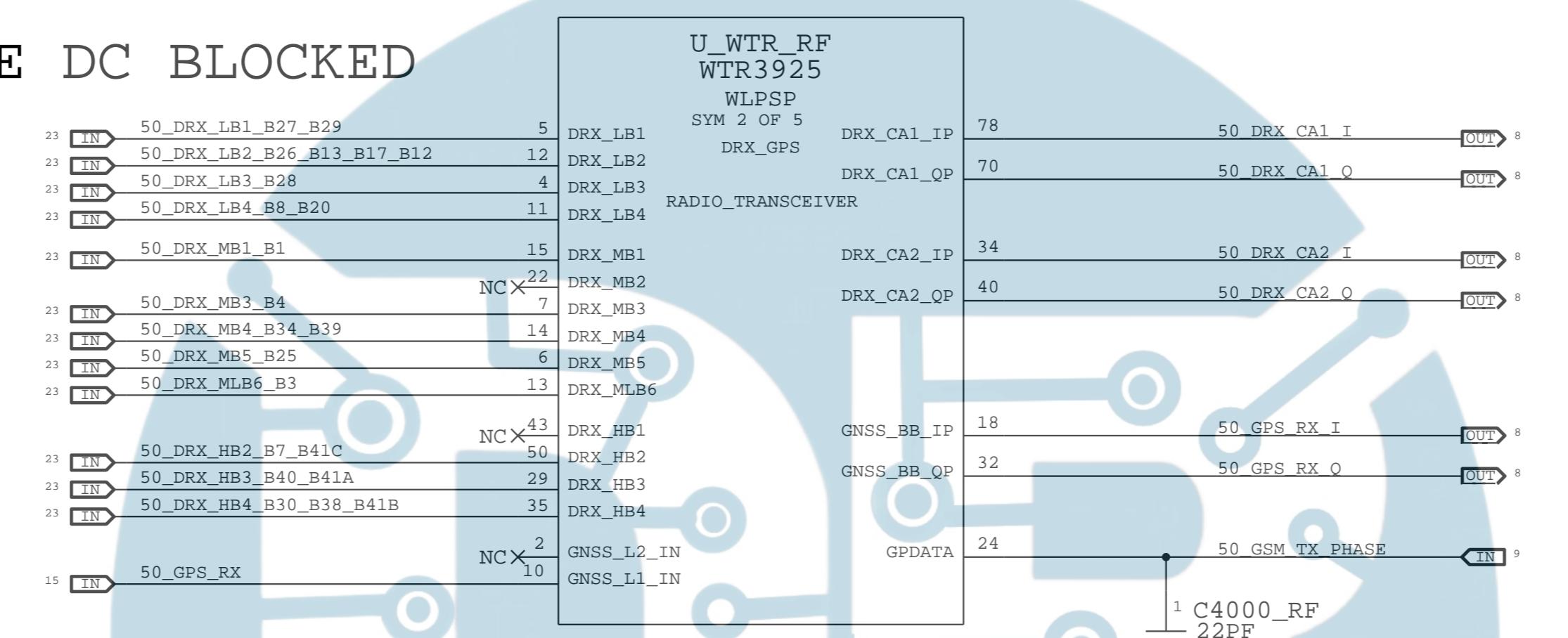
50_PRX_MLB6_B34_B39_B2CA IS ASSIGNED TO MB4

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TRANSCEIVER: DRX/GPS PORTS

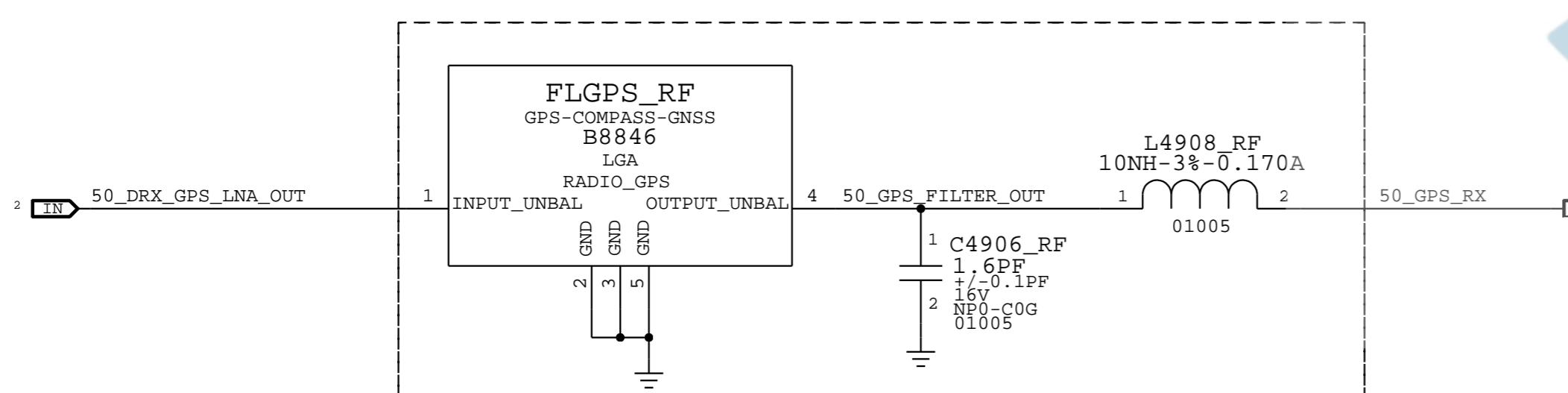
DRX MODULE PORTS ARE DC BLOCKED



PLACE CAP CLOSE TO MDM GPIO14
IMPROVES RXBN BY 4DB

GPS FILTER

PLACE NEAR U_WTR

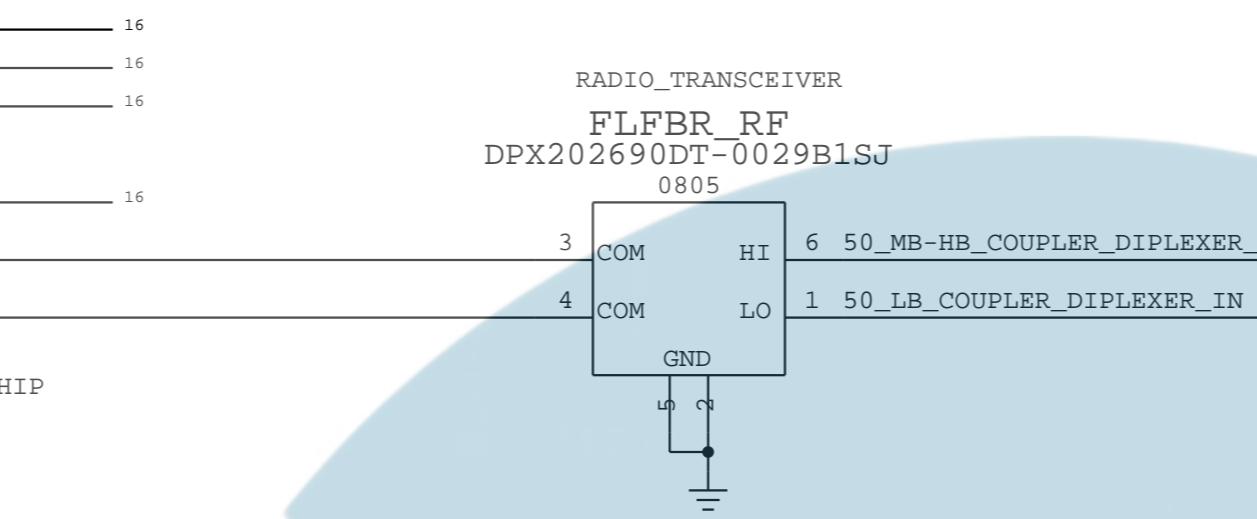
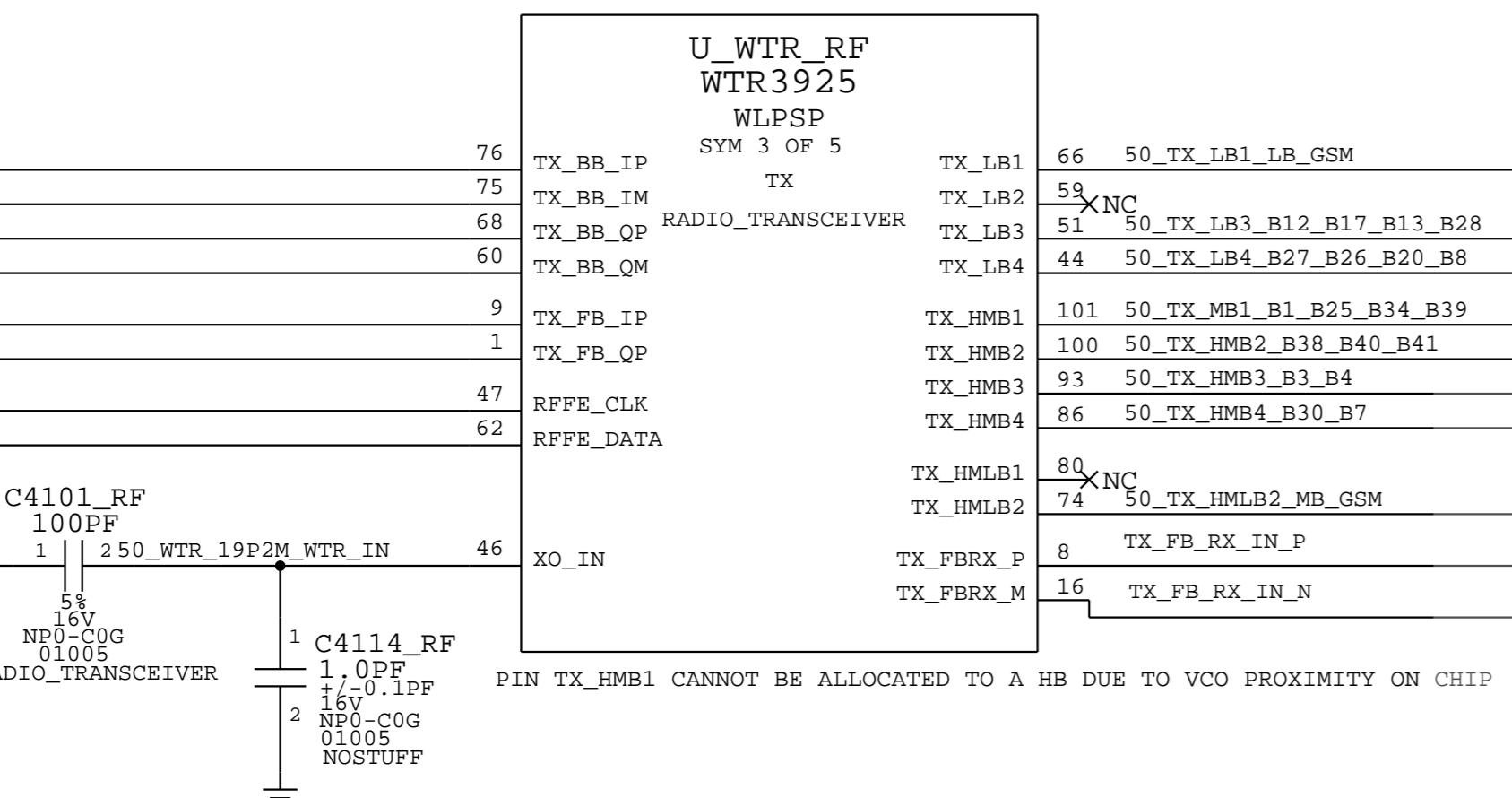


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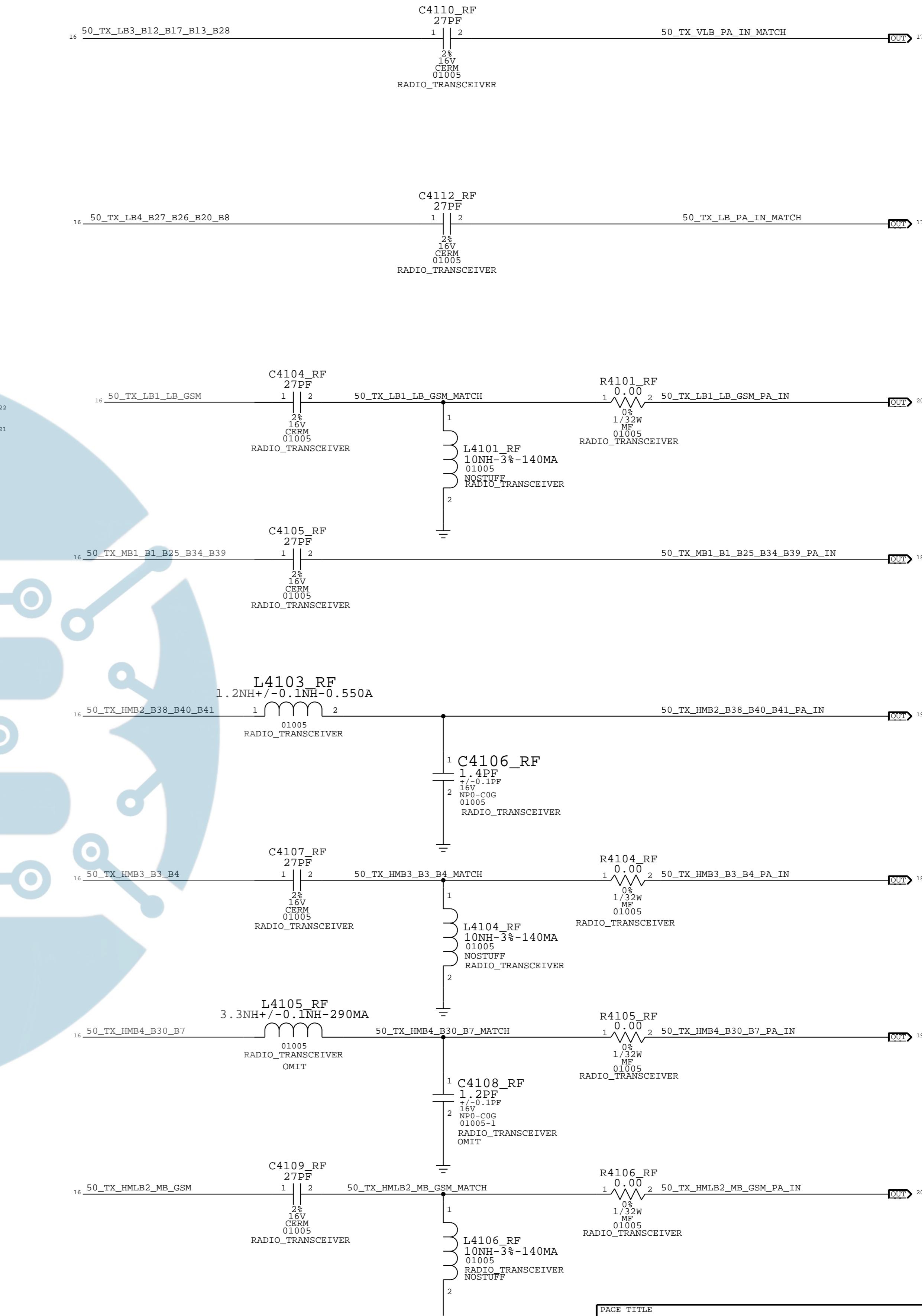
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	C	PAGE	40 OF 51
	D	SHEET	48 OF 59

TRANSCEIVER: TX PORTS

D



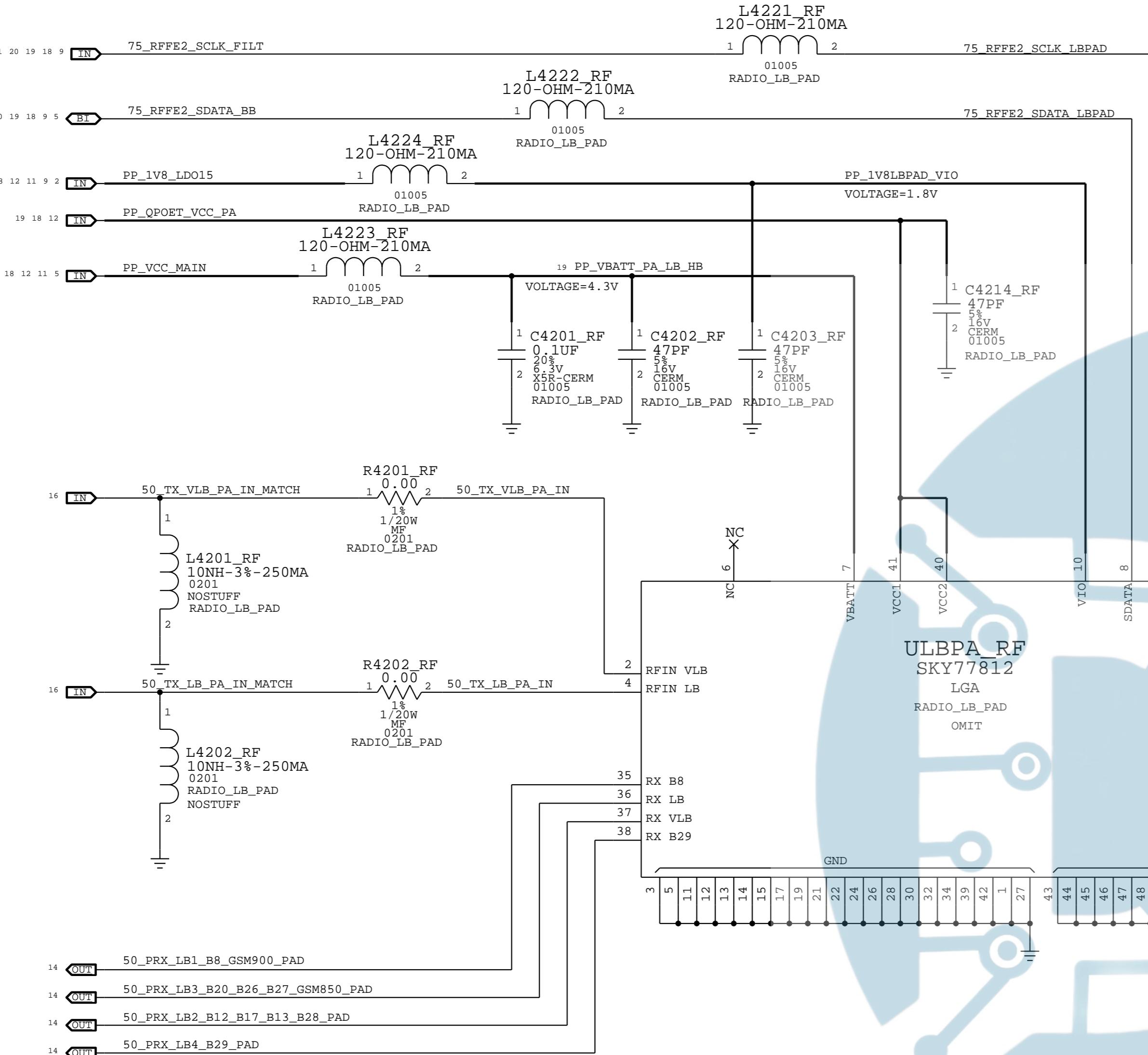
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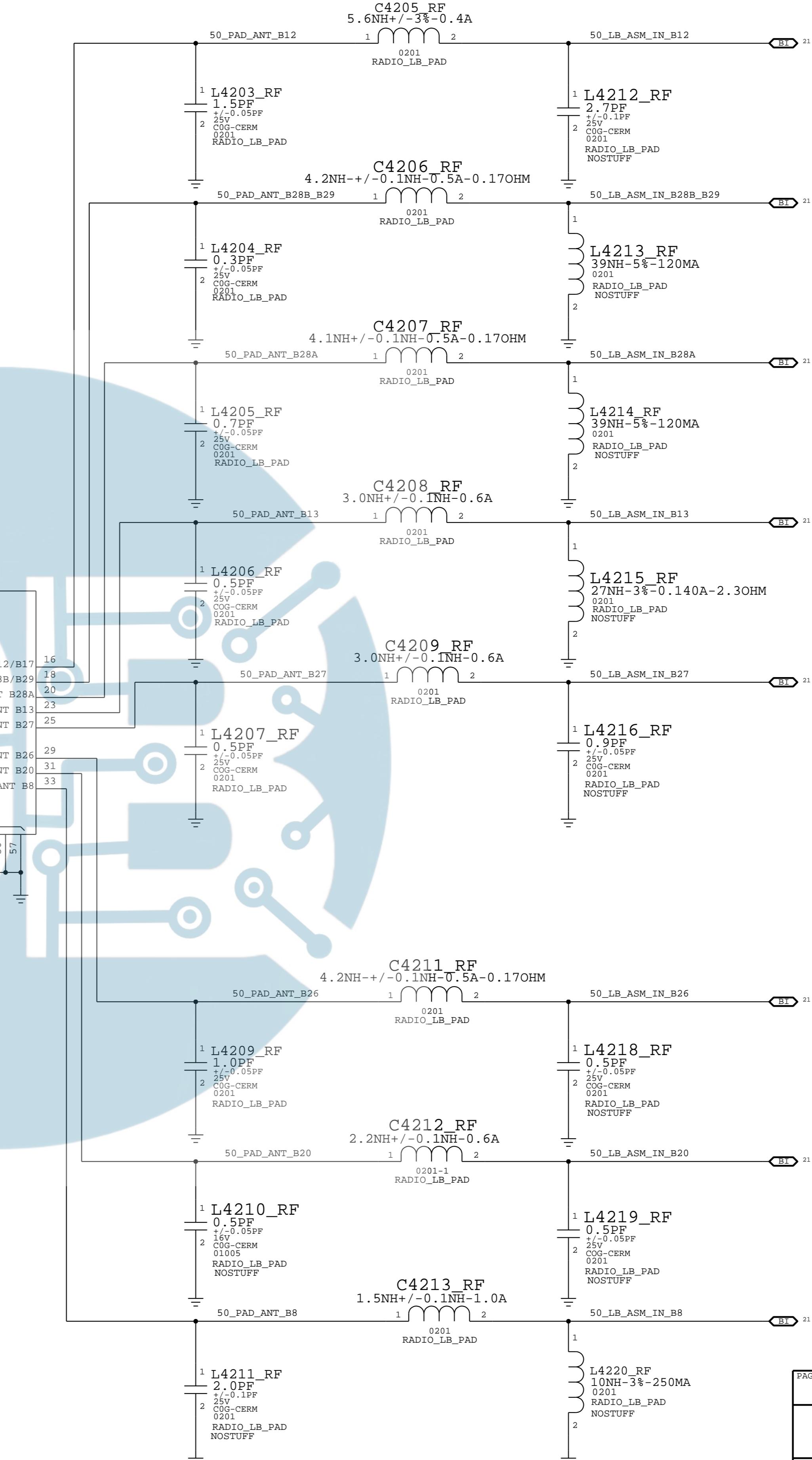
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	SHEET 49 OF 59	

LOW BAND PA+DUPLEXERS

D



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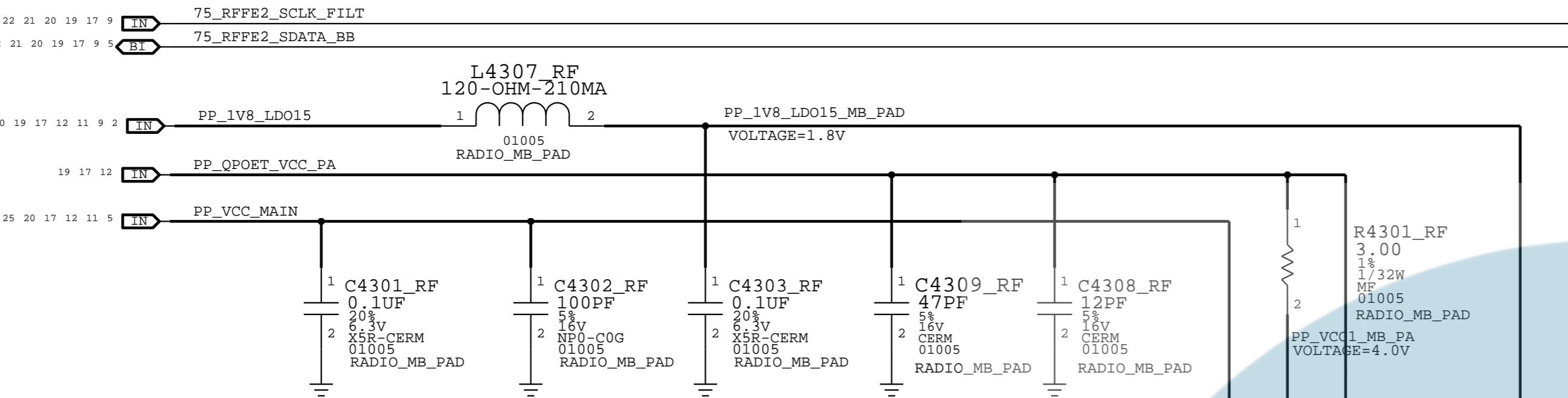


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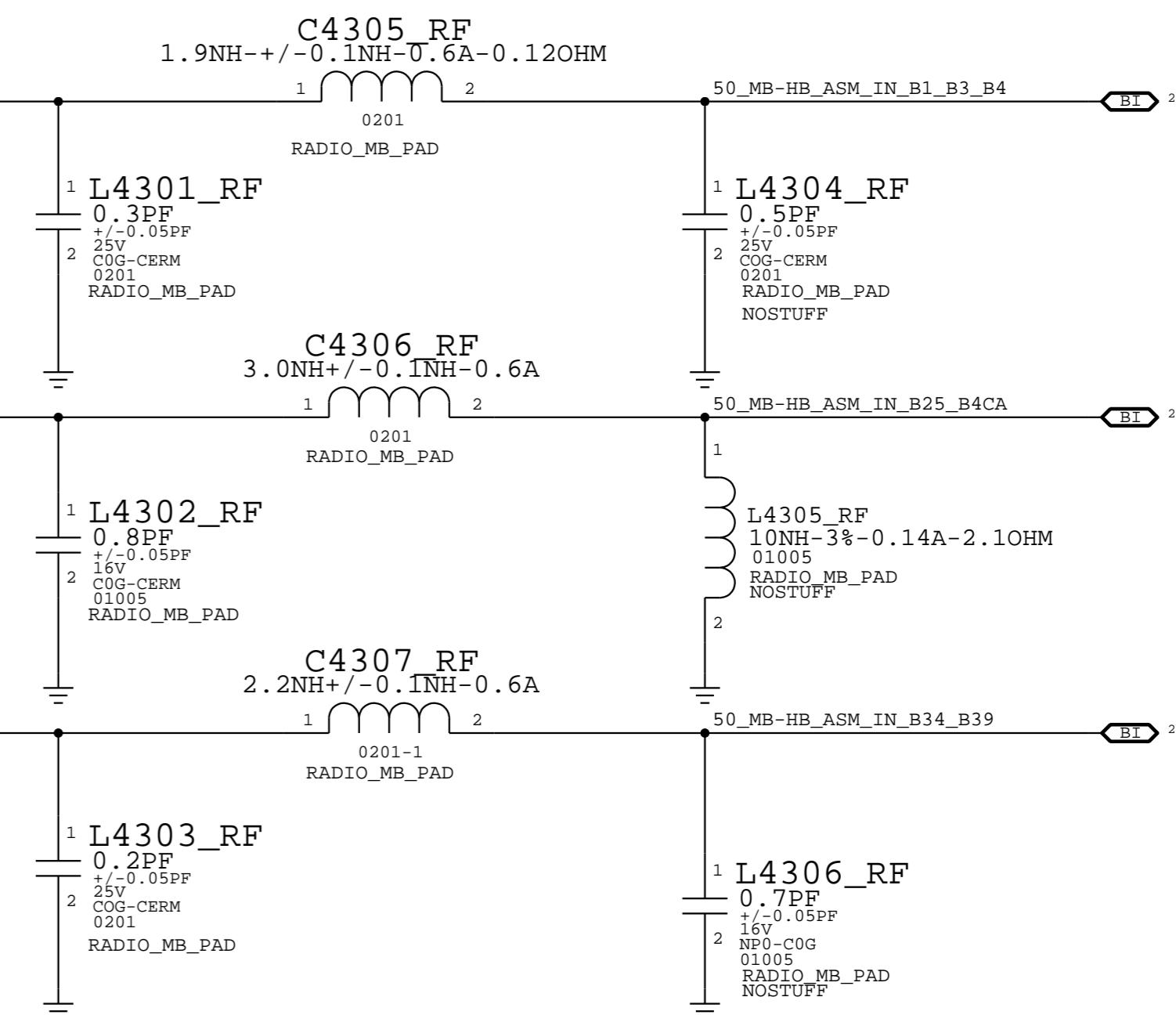
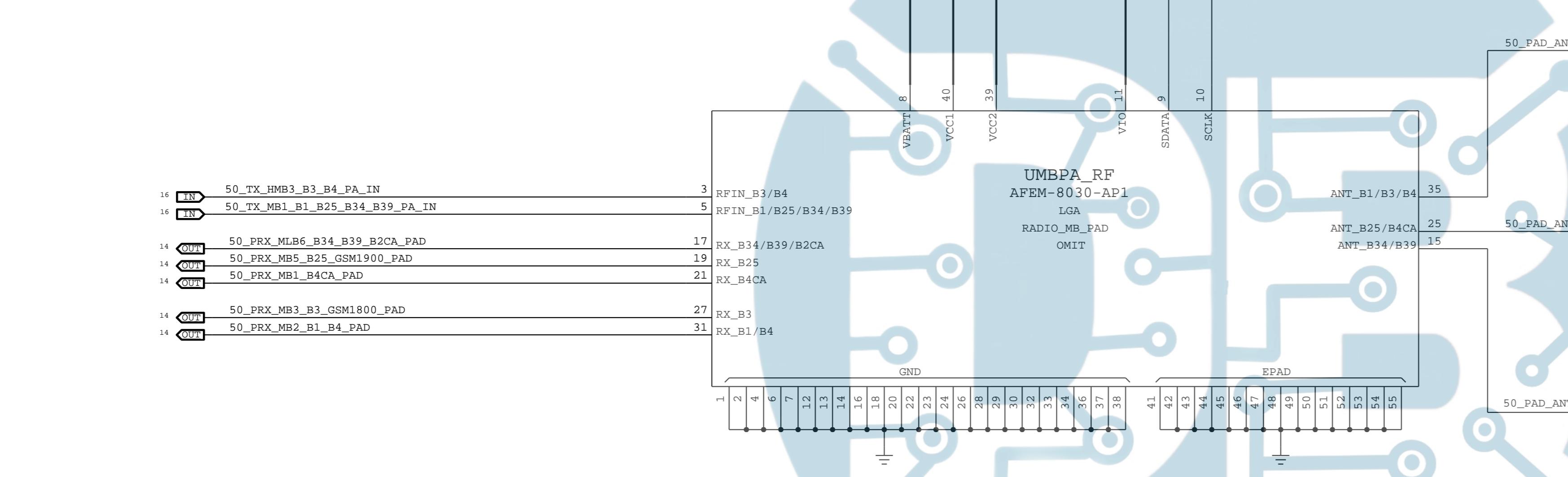
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MID BAND PA+DUPLEXERS

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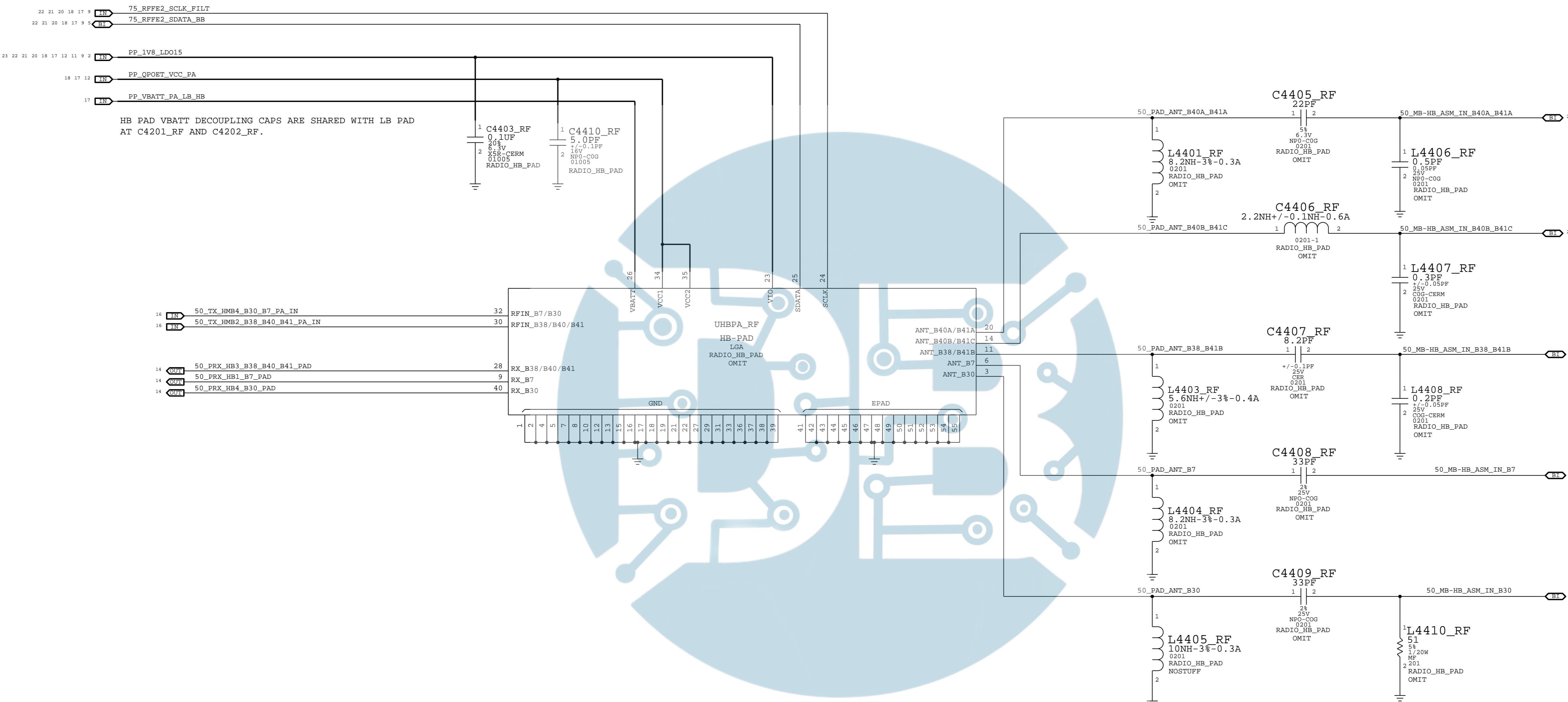
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	SHEET	51 OF 59			

HIGH BAND PA+DUPLEXERS

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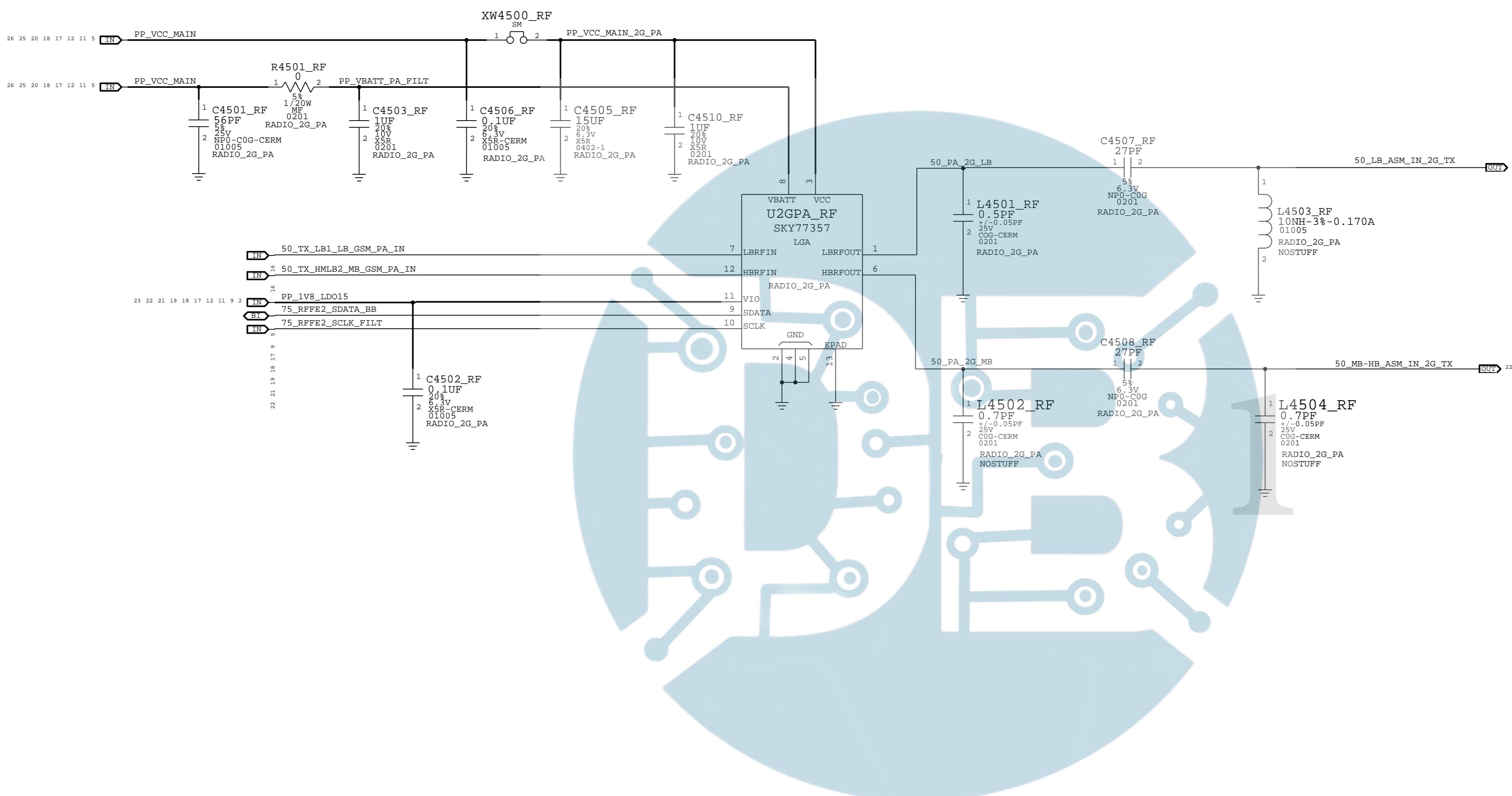
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BRANCH		
PAGE	44 OF 51	
SHEET	52 OF 59	

2G PA



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LOW BAND ANTENNA SWITCH MODULE

D

D

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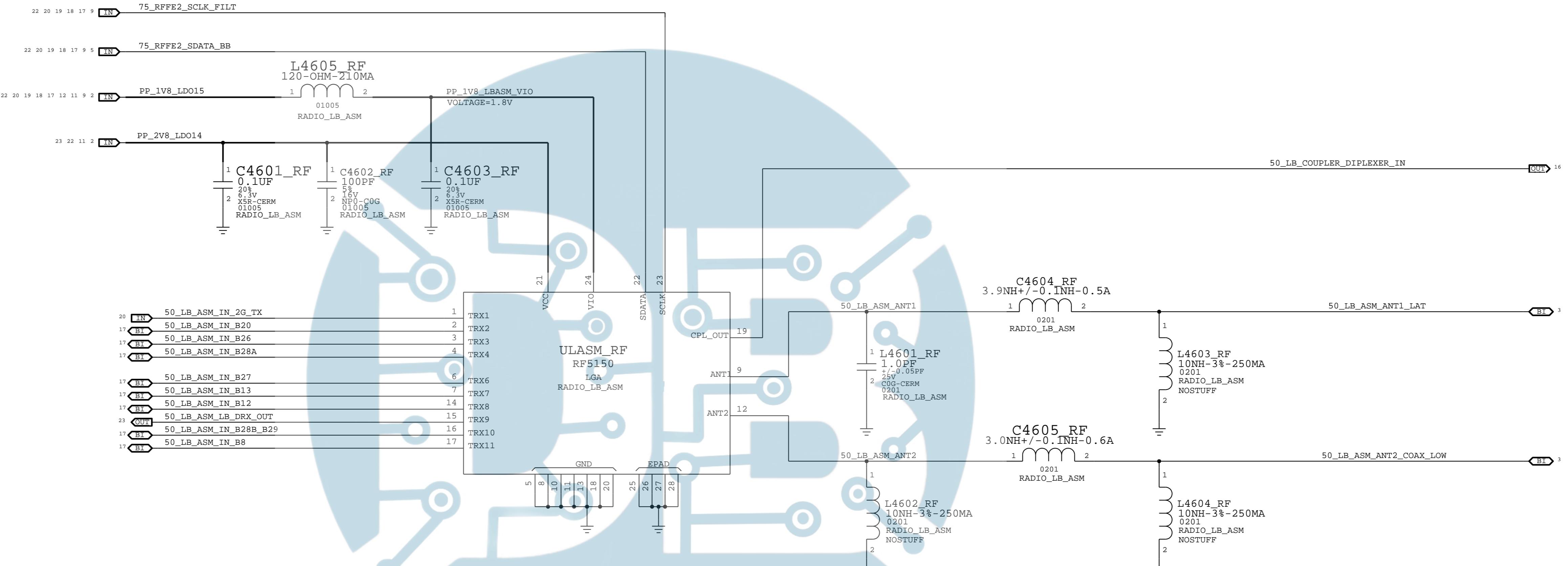
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BRANCH			
PAGE		46 OF 51	
SHEET		54 OF 59	

MID-HIGH BAND ANTENNA SWITCH MODULE

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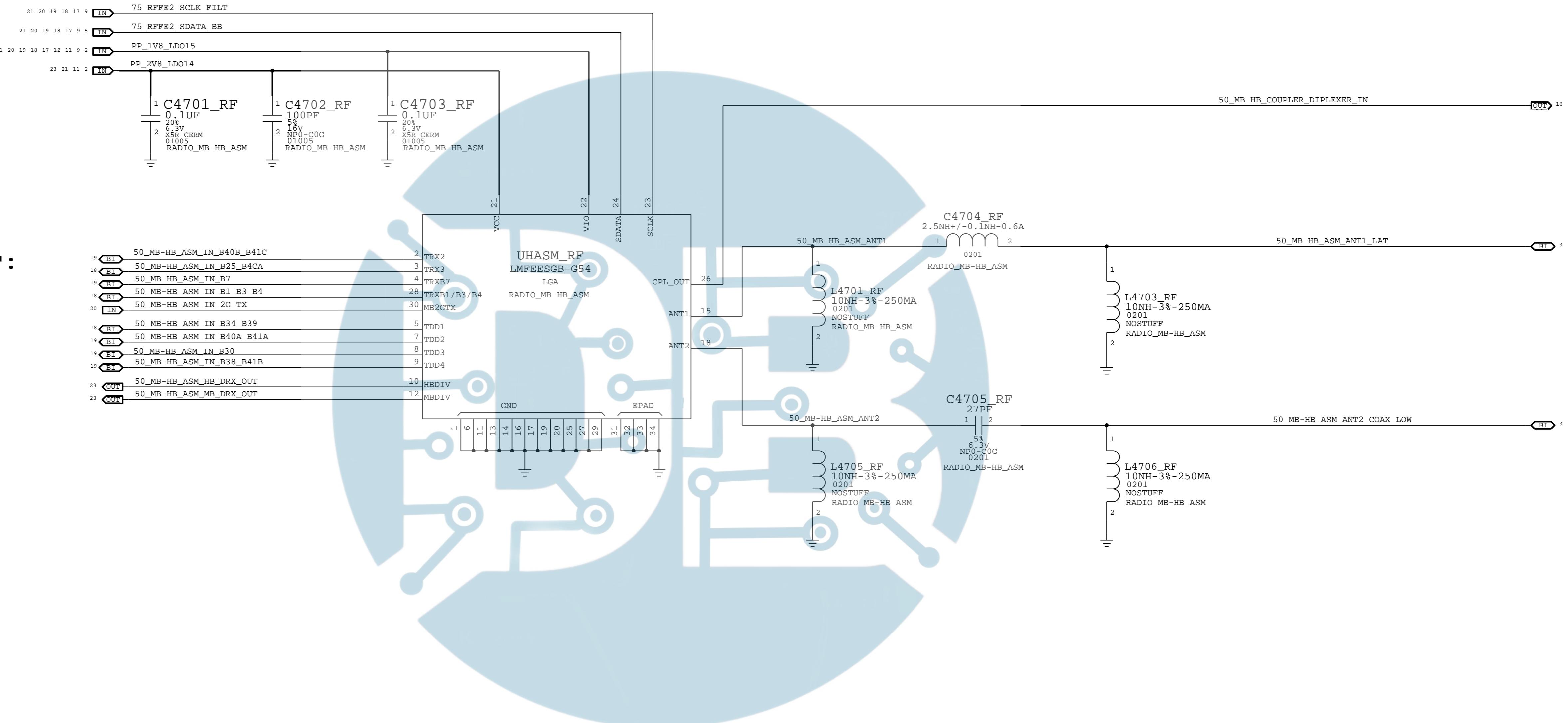
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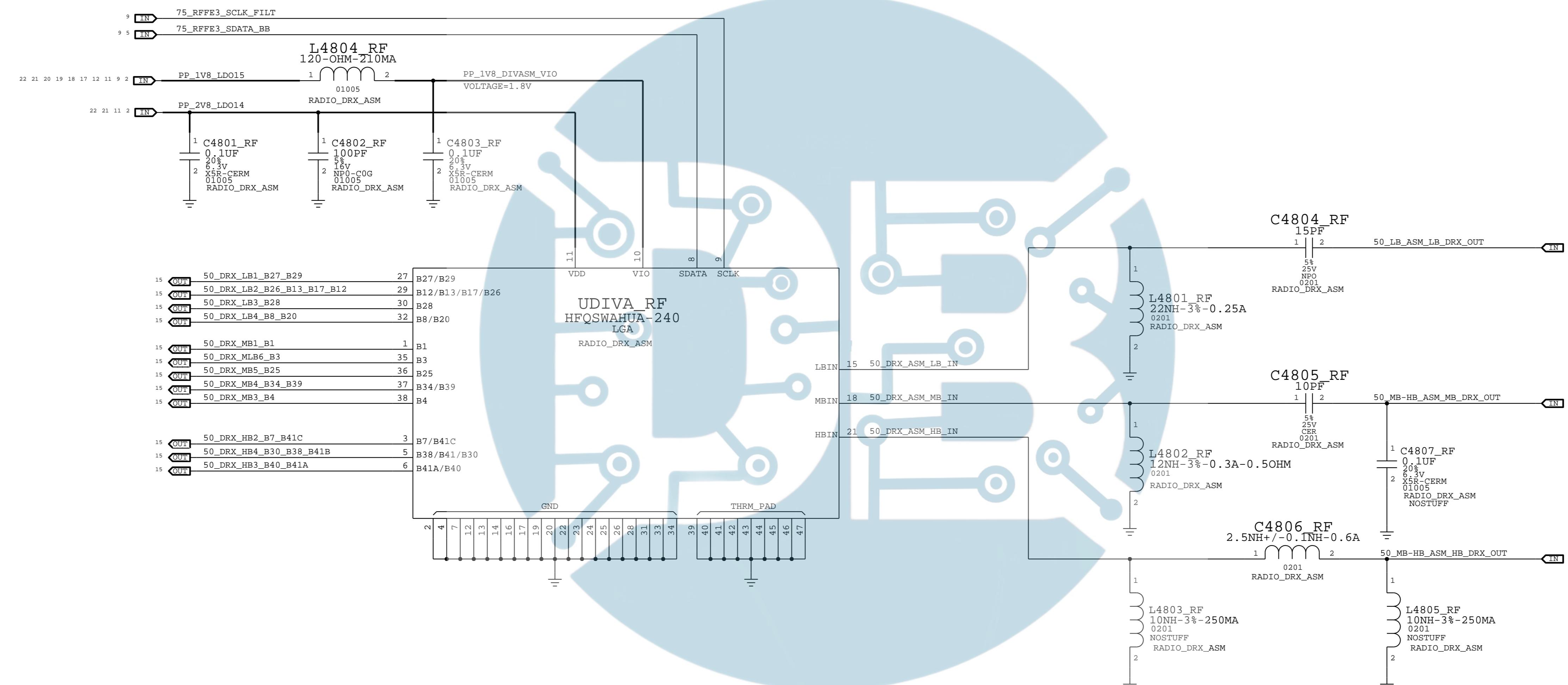
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	SHEET	55 OF 59

DIVERSITY MODULE



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PAGE	48 OF 51	
SHEET	56 OF 59	

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SIM

D

D

C

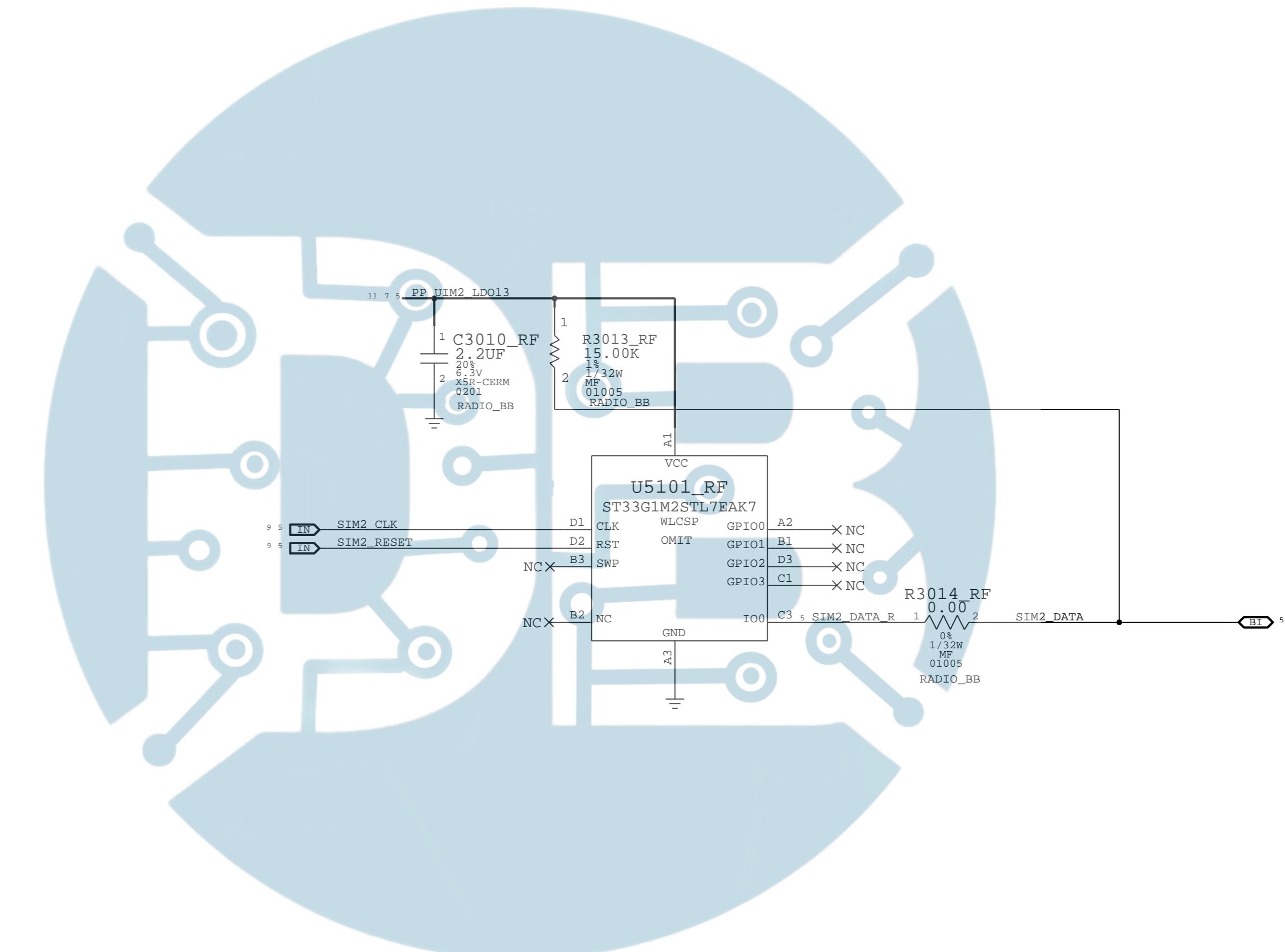
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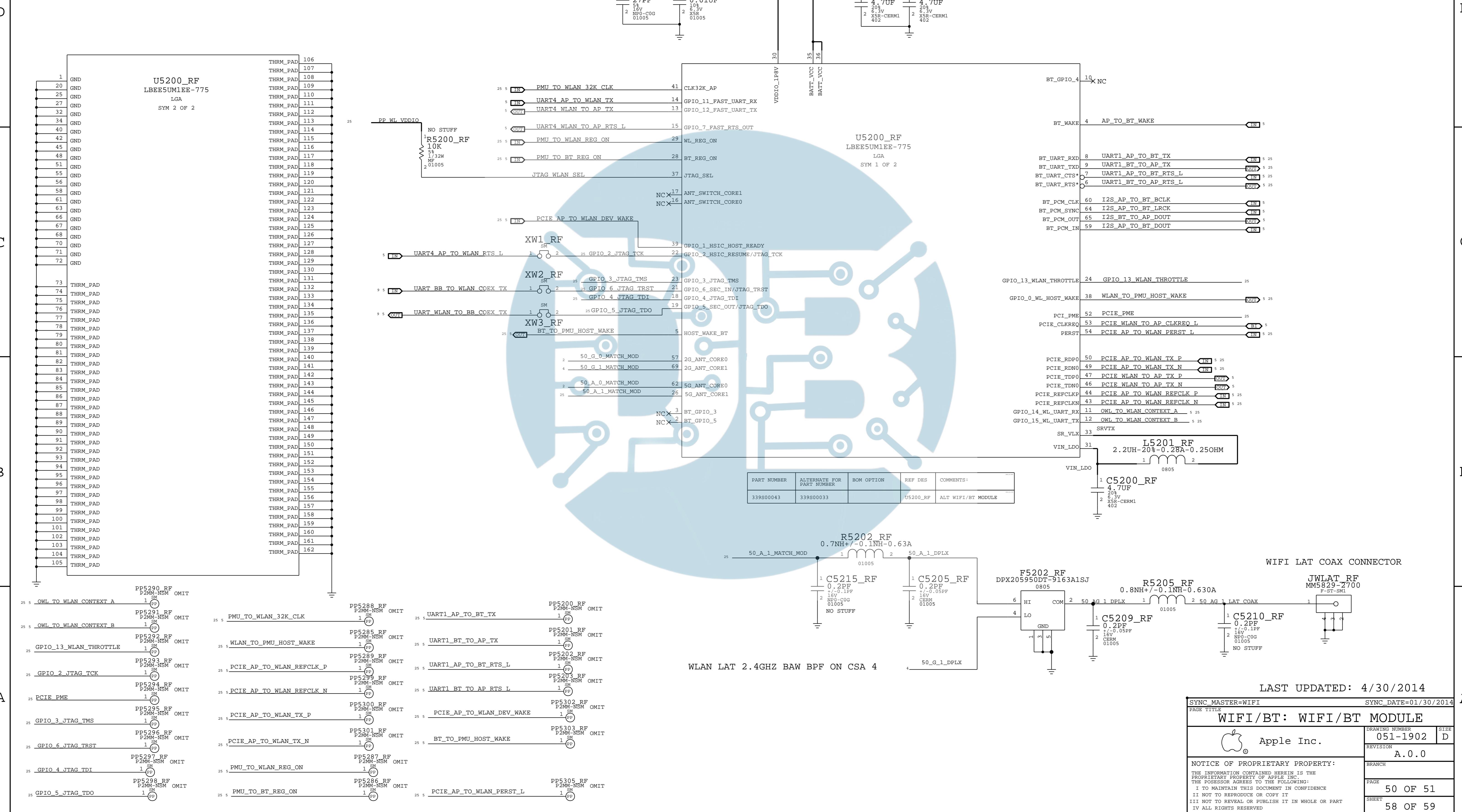
PP5100_RF
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26 5 3 ————— 4FF_SIM_SWP

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BRANCH		
PAGE	49 OF 51	
HEET	57 OF 59	

WIFI / BT



STOCKHOLM

ALL NETNAMES NEED TO BE CHECKED

D

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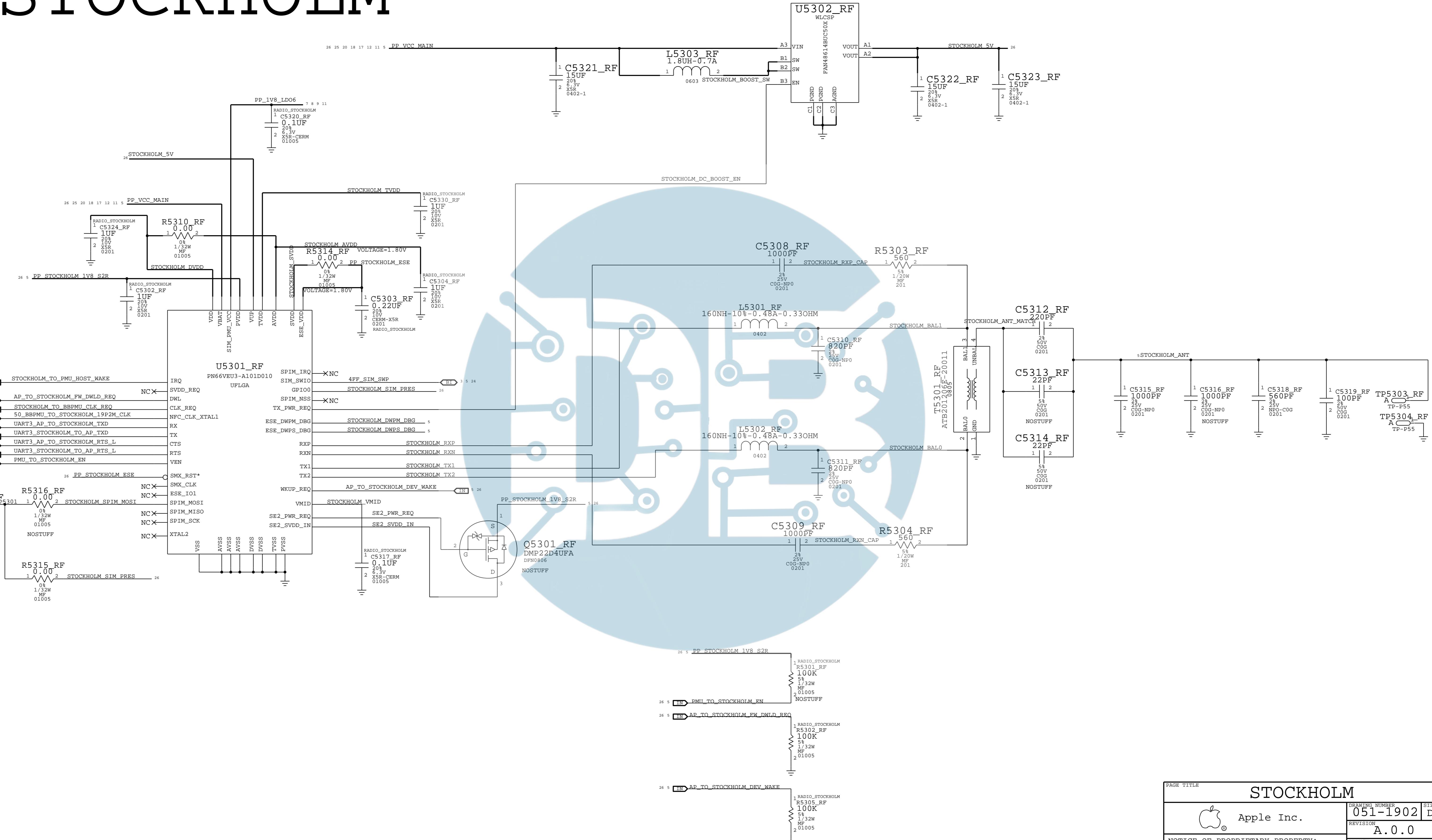
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