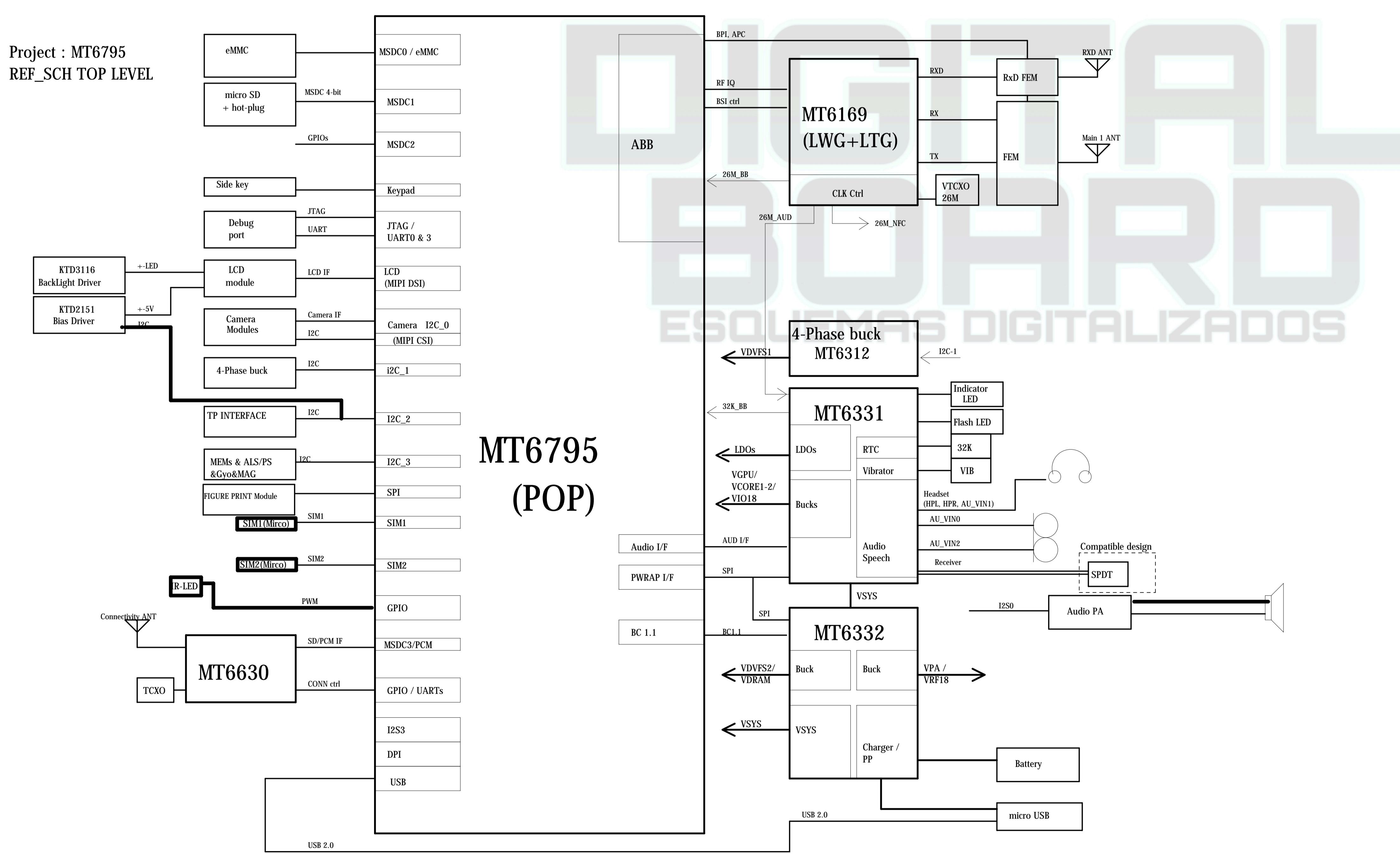
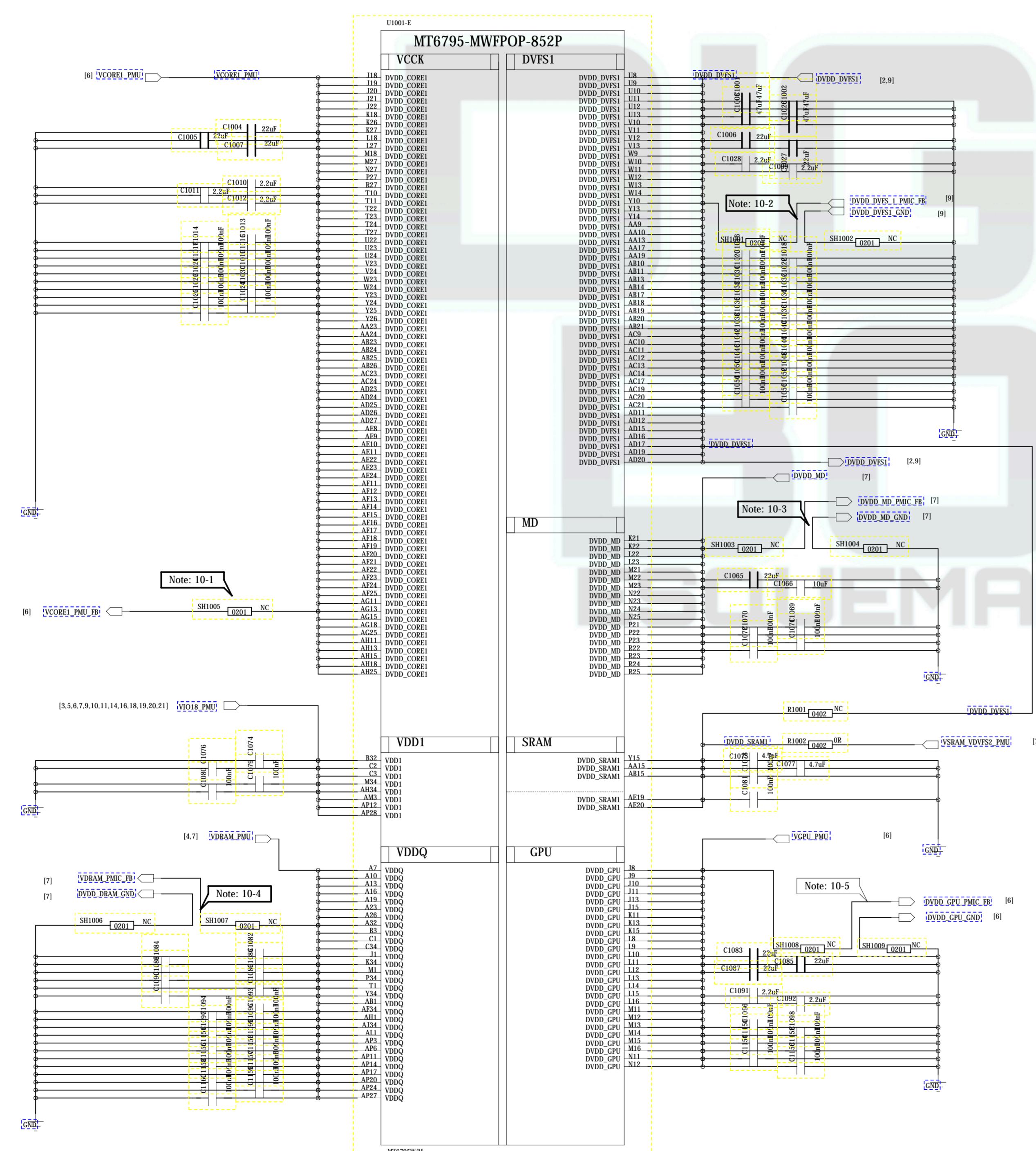


Project : MT6795
REF_SCH TOP LEVEL



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



Schematic design notice of "10_BB_POWER_1" page.

- Note 10-1: VCORE_1 remote sense must be close to MT6795's AG13 ball.
Remote sense trace with GNDshielding to PMIC (Differential)

Note 10-2: Differential pair of DVFS1 remote sense must be close to MT6795's Y10 ball.

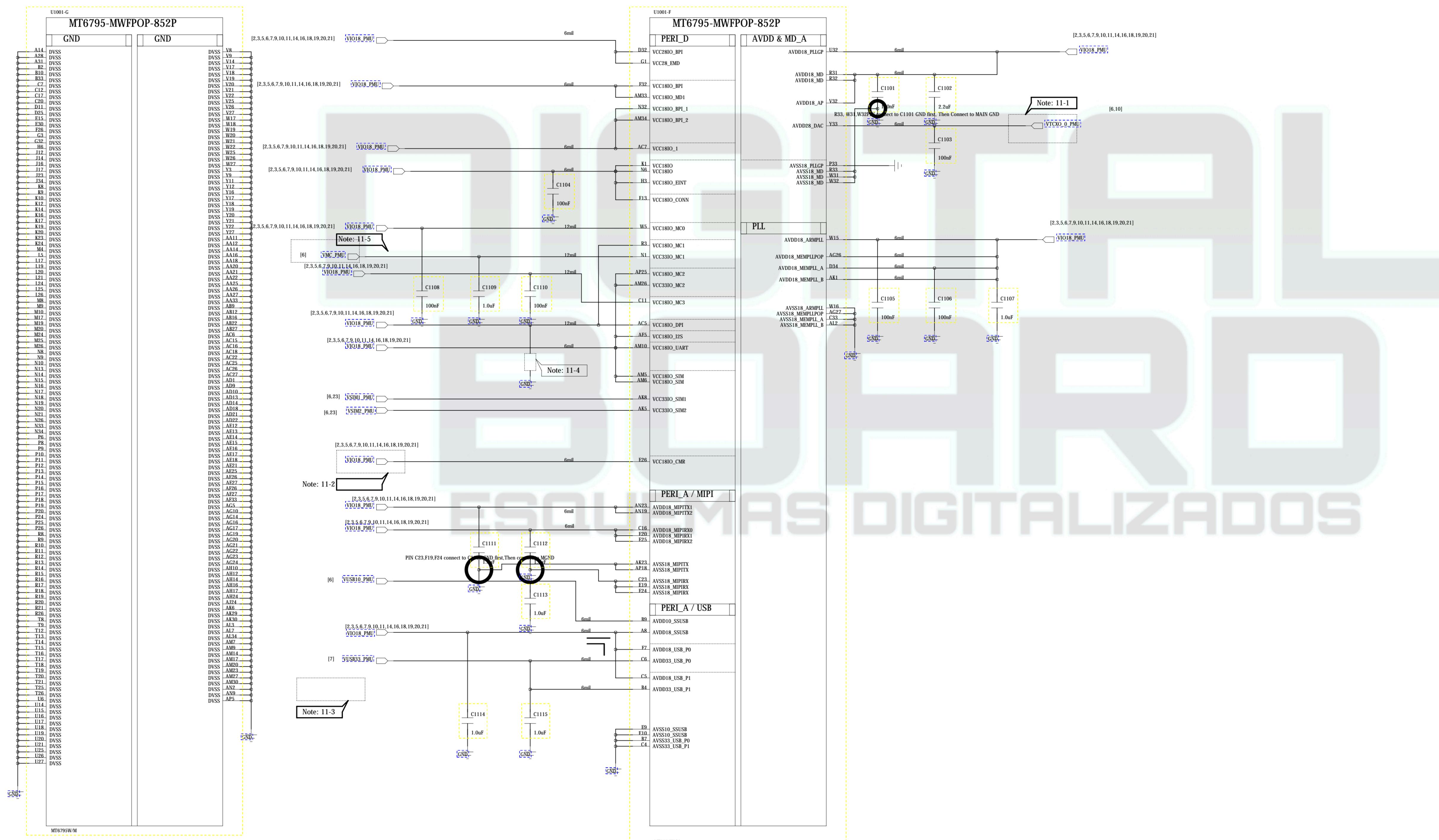
Note 10-3: Differential pair of DVDD_MD remote sense must be close to MT6795's K22 ball.

Note 10-4: VDRAM remote sense must be close to MT6795's A32 ball.

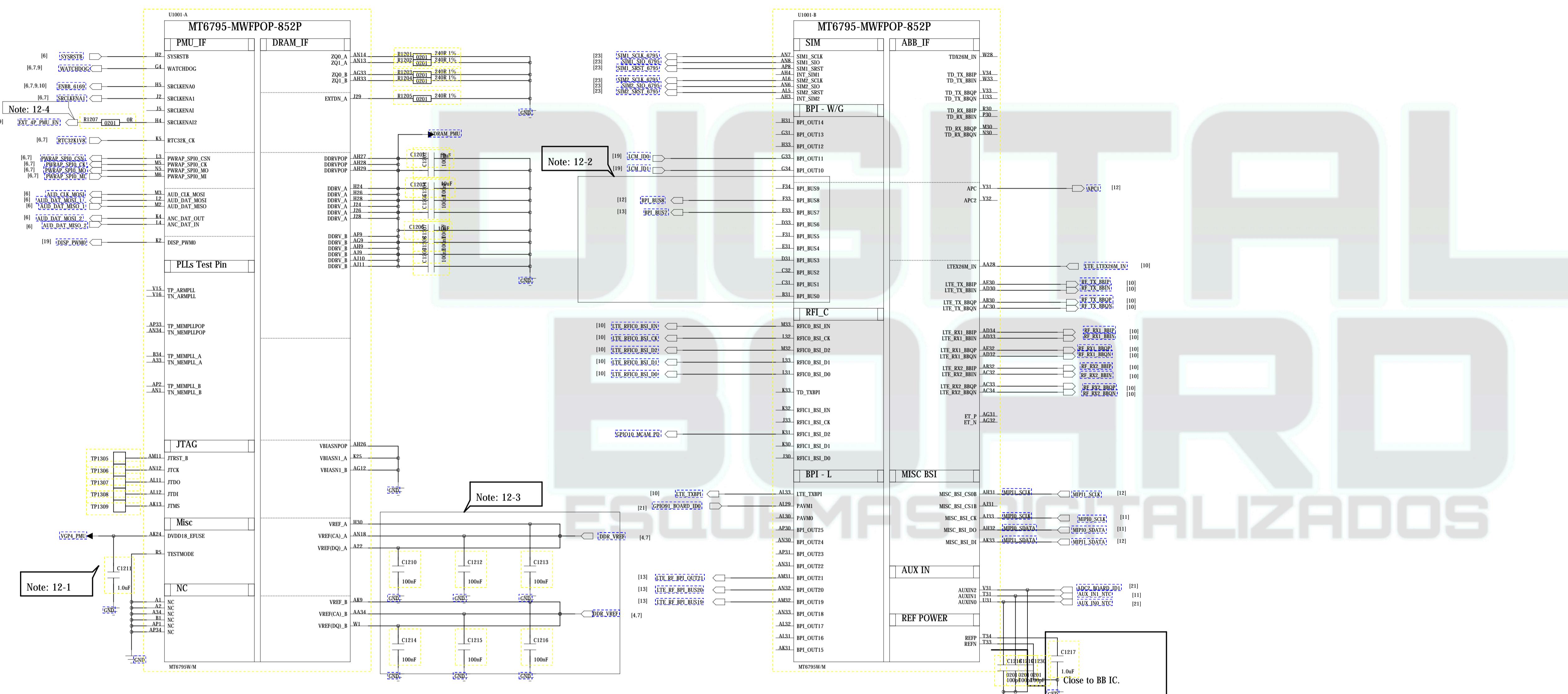
Note 10-5: Differential pair of GPU remote sense must be close to MT6795's J8 ball.

		COMPANY: <Company Name>			
		TITLE: <Title>			
AWN: <Drawn By>	DATED: <Drawn Date>	CODE: <Code>	SIZE: A0	DRAWING NO: <Drawing Number>	REV: <Revision>
ECKED: <Checked By>	DATED: <Checked Date>				
ALITY CONTROL: <QC By>	DATED: <QC Date>				
LEASED: <Released By>	DATED: <Release Date>				
SCALE: <Scale>		SHEET: 2F	23		

REVISION RECORD			
ltr	ECO NO.	APPROVED:	DATE



COMPANY: <Company Name>		
TITLE: <Title>		
DRAWN: <Drawn By>	DATES: <Drawn Date>	
CHECKED: <Checked By>	DATES: <Checked Date>	
QUALITY CHECKED: <QC By>	DATES: <QC Date>	
RELEASED: <Released By>	DATES: <Release Date>	
SCALE: <Scale>	REV: <Rev>	
<Code> A0 <Drawing Number><Revision>		
CODE: <Code>	SIZE: <Size>	DRAWING NO.: <Drawing No>
RELEASED: <Released By>	DATES: <Release Date>	SCALE: <Scale>
SHEET 8f		23



Schematic design notice of "11 BB 11" page

- Note 12-1: Apply 1.8V to DVDD18_EFUSE (AK24) for eFuse programming.

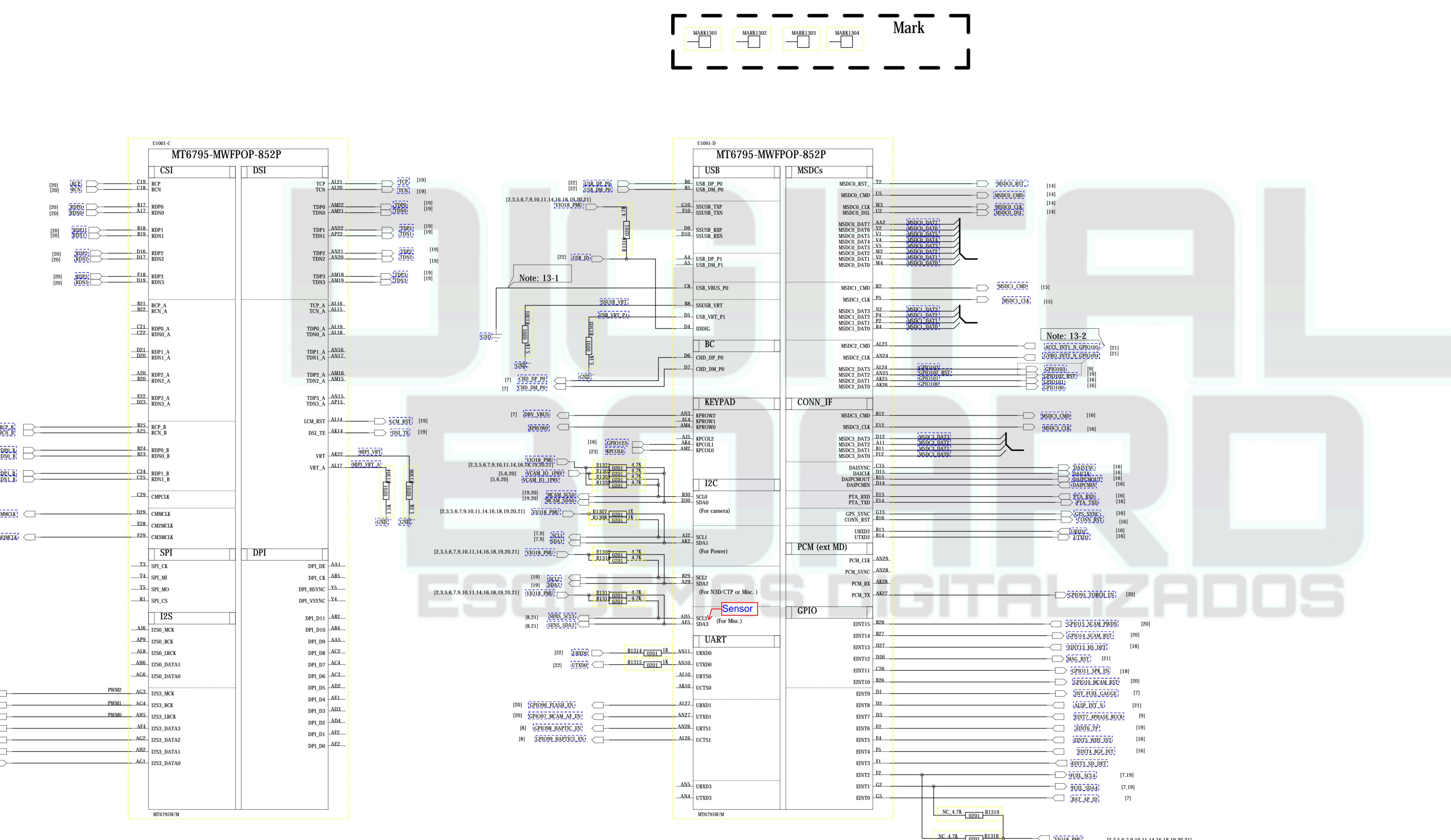
- Note 12-2: The BPI_BUS0~BPI_BUS9 are capable of 2.8V I/O operation.

- Note 12-3: The de-coupling cap. of DRAM VREF have to be placed as close to BB as possible.

- Note 12-4: SRCLKENAI2 features watch dog reset output to reset 4-phase buck.
R1207 BOM option: R1207 = 0R when BOM option of U2401 is DA9210.

COMPANY:	<Company Name>		
TITLE:	<Title>		
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	A0	<Drawing Number>	<Revision>
SCALE: <Scale>	SHEET: 4F	23	

ISSUER RECORD			
LTR	ECO NO.	APPROVED	DATE



Schematic design notice of "12_BB_2" page.

Note 13-1: Connect USB_VBUS_P0 (C8 ball) pin to GND since USB "B-Valid" detection has implemented by MT6332's ADC.

Note 13-2: GPIO103 is dedicated for DA9210 4-phase buck control.

COMPANY:	<Company Name>		
TITLE:	<Title>		
DRAWN:	<Drawn By>	DATED:	<Drawn Date>
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>
CODE:	SIZE:	DRAWING NO.:	REV.:
<Code>		A0 <Drawing Number>	<Revision>

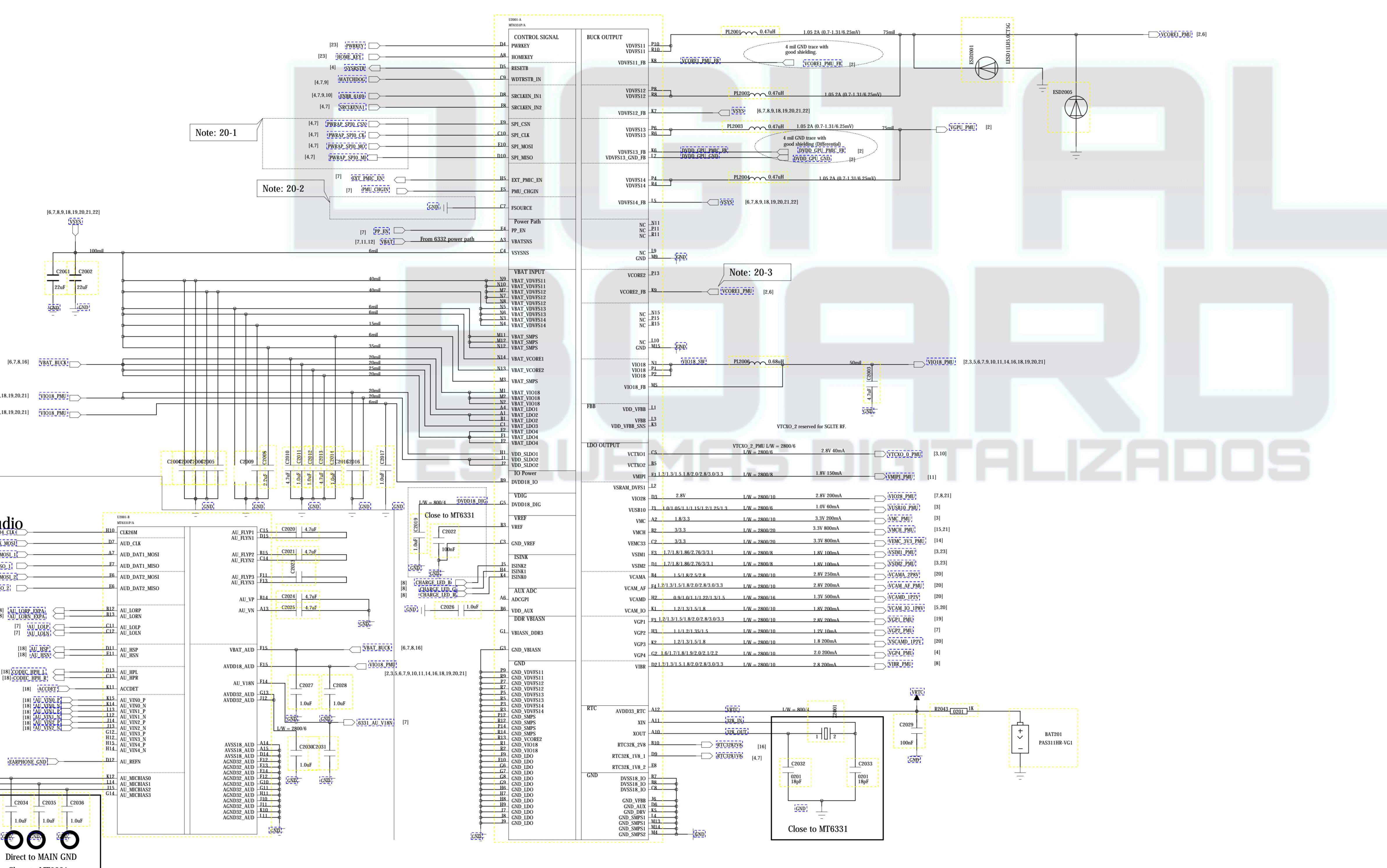
REVISION RECORD			
ltr	ECO NO.	APPROVED	DATE

Schematic design notice of "20_POWER_MT6331" page.

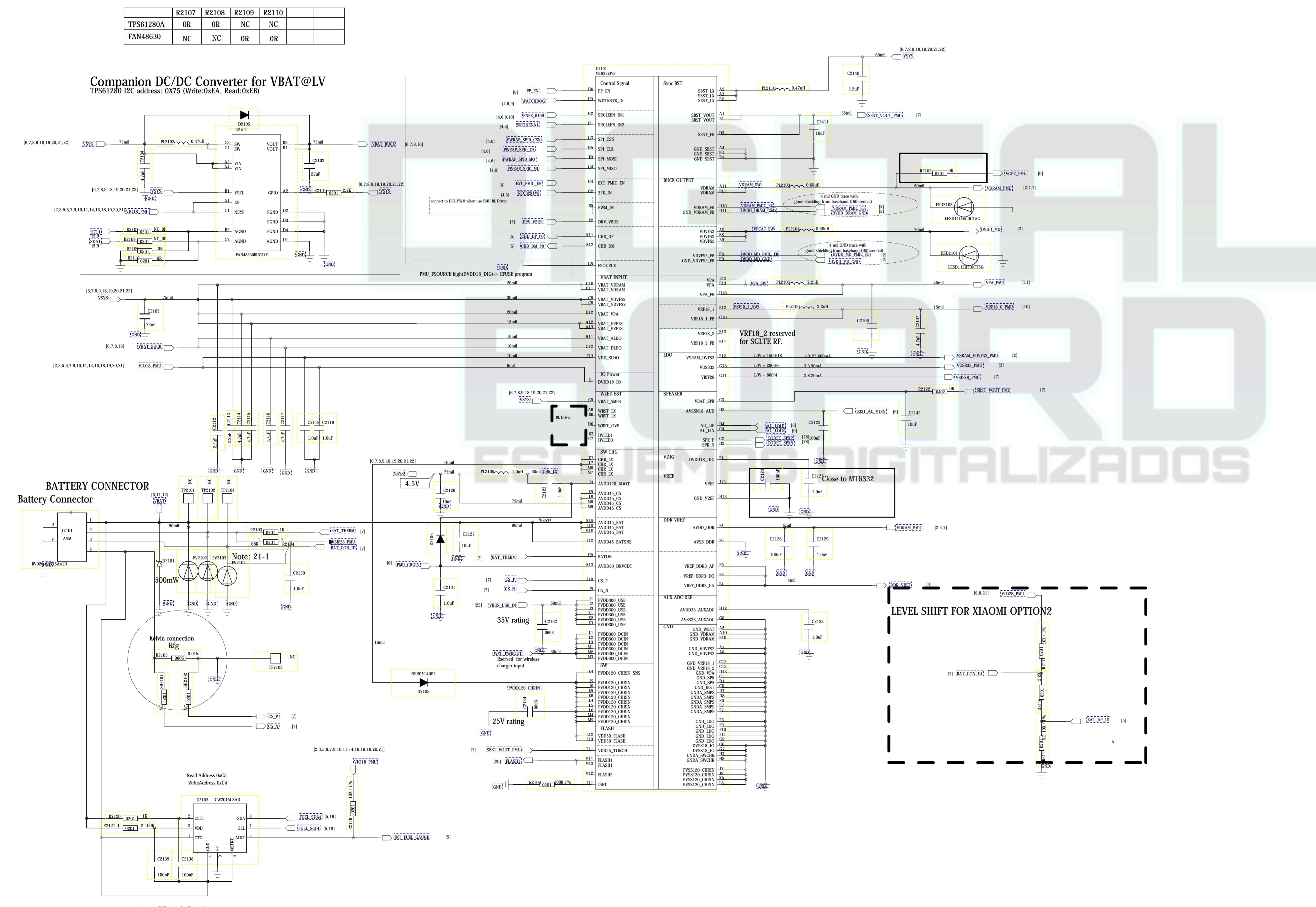
Note 20-1: External pull resistor in PMU SPI interface is not allowed.

Note 20-2: PMU_FSOURCE high(DVDD18_DIG) -> EFUSE program.

Note 20-3: Connect MT6331's K9 pin to "VCORE1_PMU" when VCORE2 is not used.



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



Note: 21-1

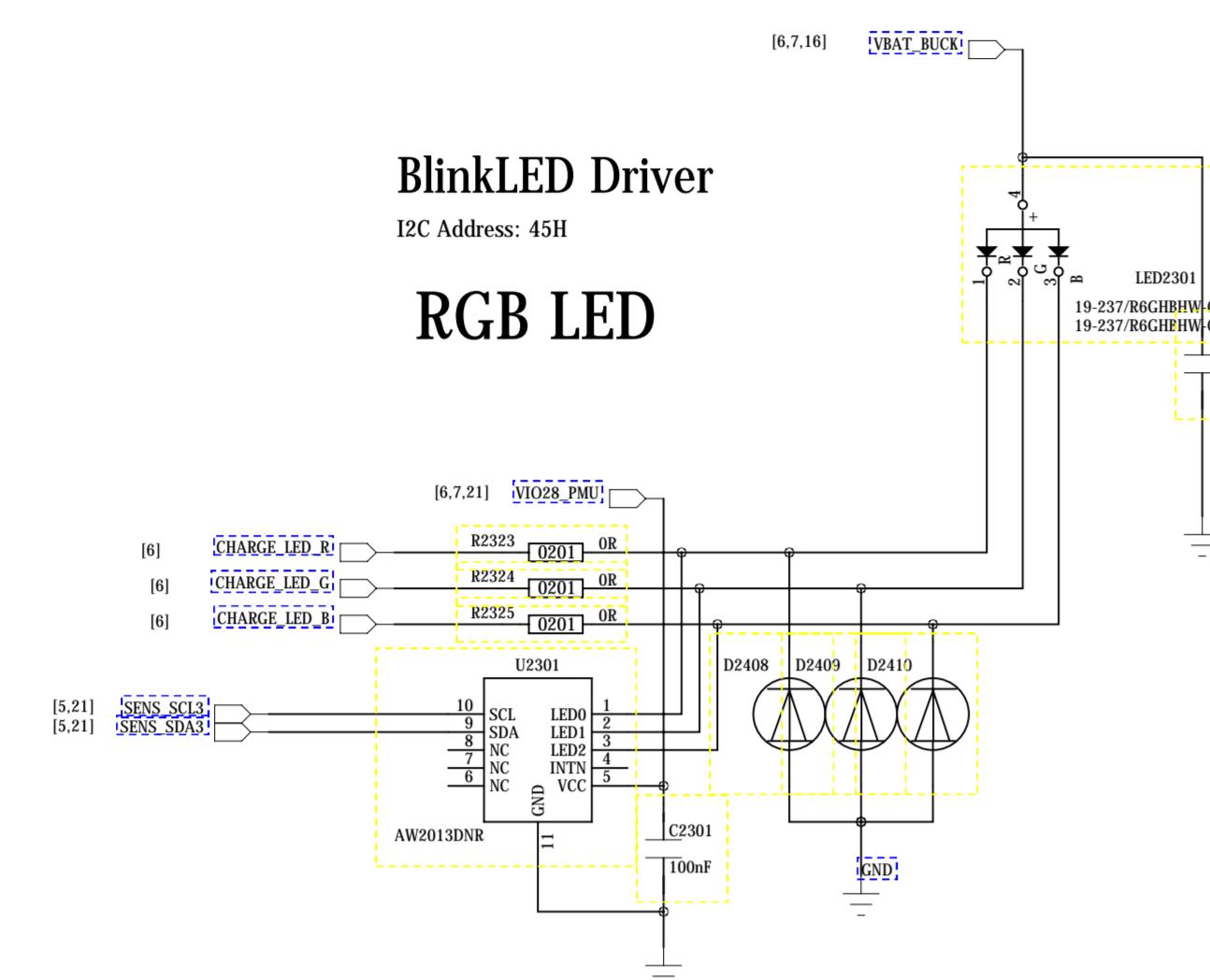
R2104 Change according to BATERRY NTC. R=(R@-10j/8---R@60j*8) Default NTC=6

COMPANY:	<Company Name>		
TITLE:	<Title>		
>	CODE:	SIZE:	DRAWING NO: REV:
te>	<Code>	A0 <Drawing Number><Revision>	
	S.d.		7-22

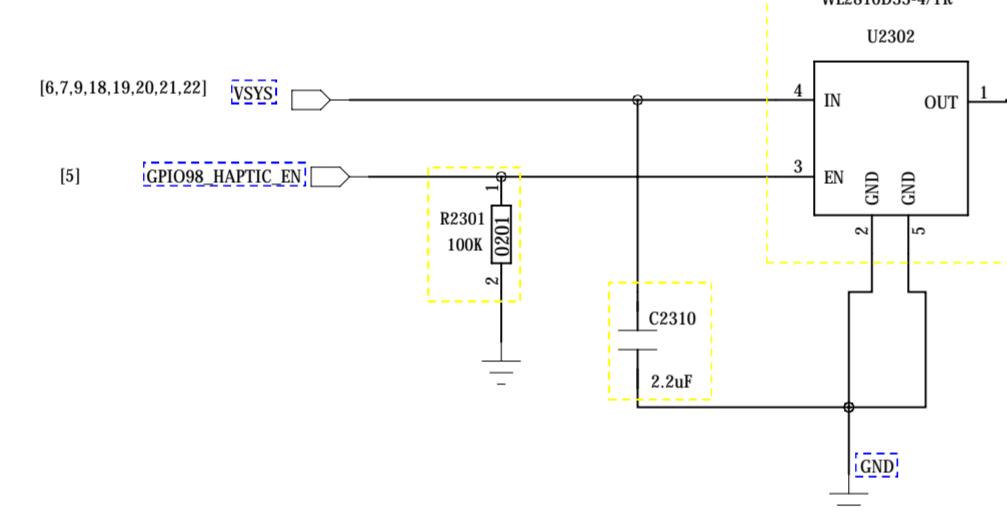
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

0

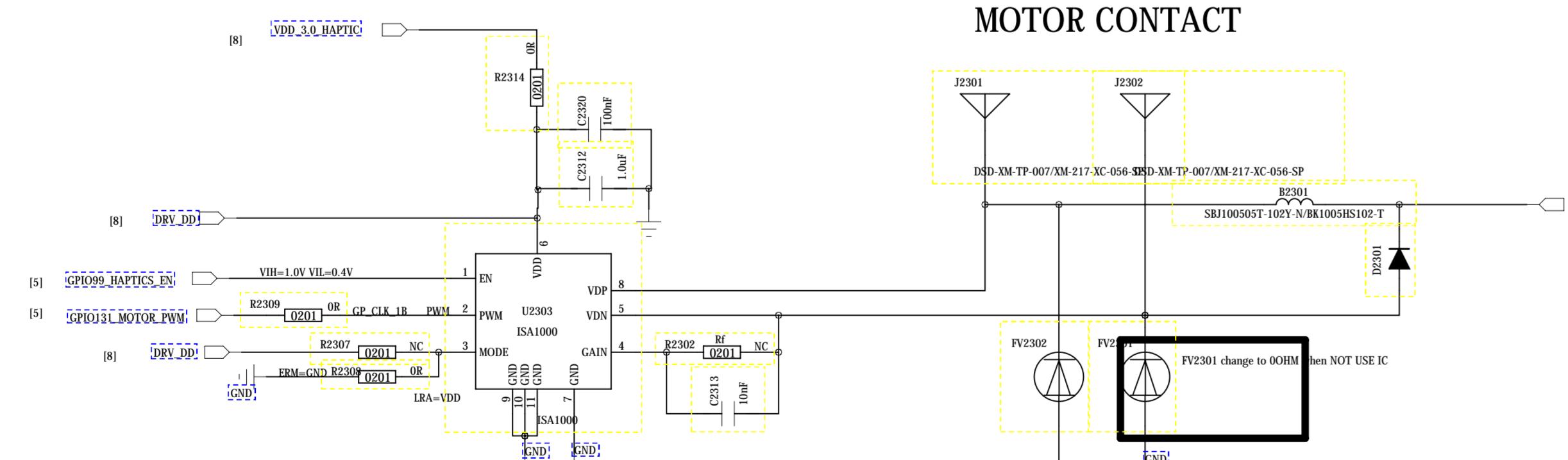
D



MOTOR DRIVER CIRCUIT



MOTOR CONTAN



XM-217-XC-056-~~SPD~~-XM-TP-007/XM-217-XC-056-SP
P2201

COMPANY:	<Company Name>		
TITLE:	<Title>		
te>	CODE:	SIZE:	DRAWING NO:
Date>	<Code>	A0	<Drawing Number><Revision>
ate>	SCALE: <Scale>		SHEET: 8F 23

4-Phase Buck

4-Phase Buck I2C address: 0x68 (Write:0xD0, Read:0xD1)



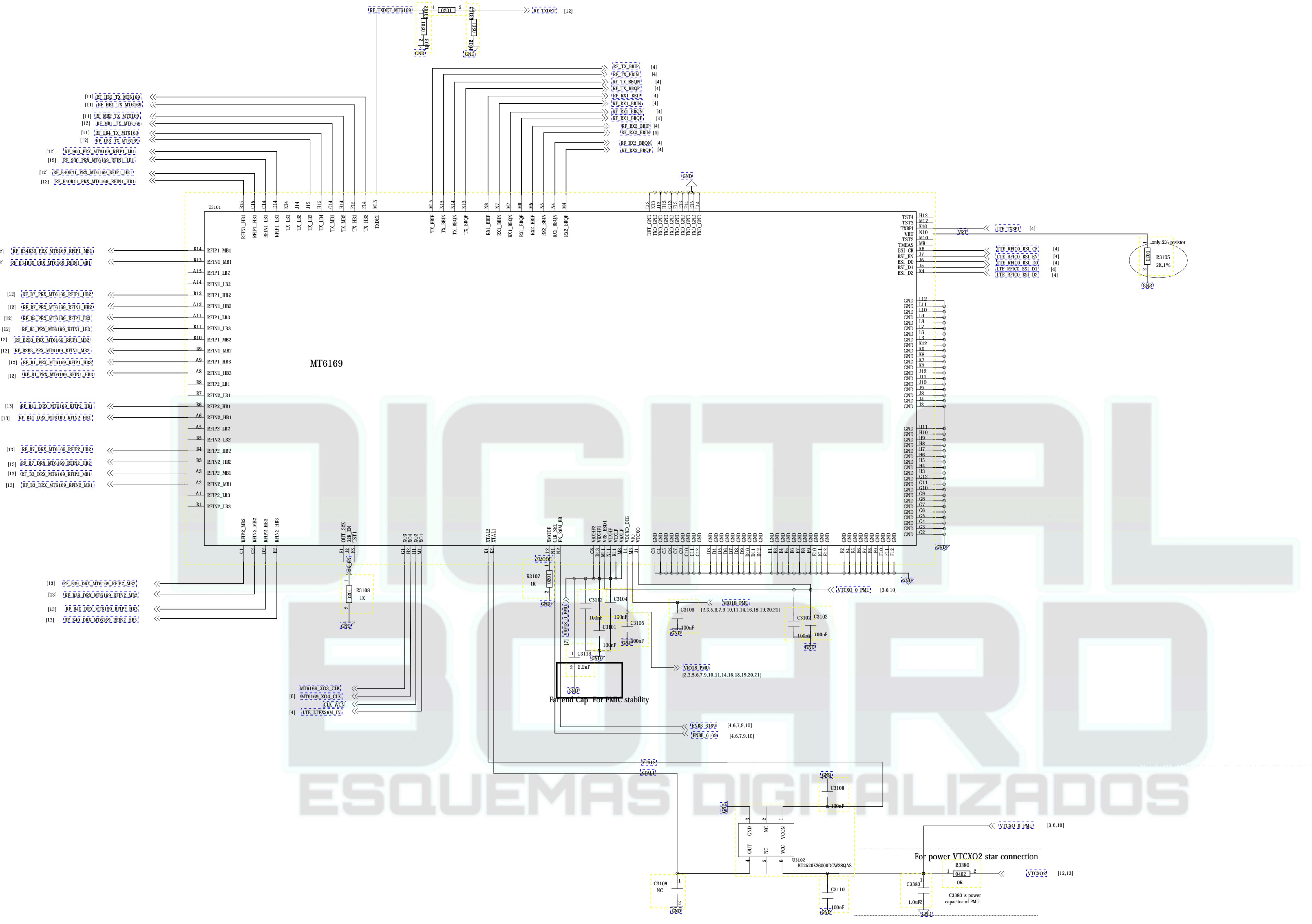
Schematic design notice of "24_POWER_EXT_4PHASE_BUCK" page.

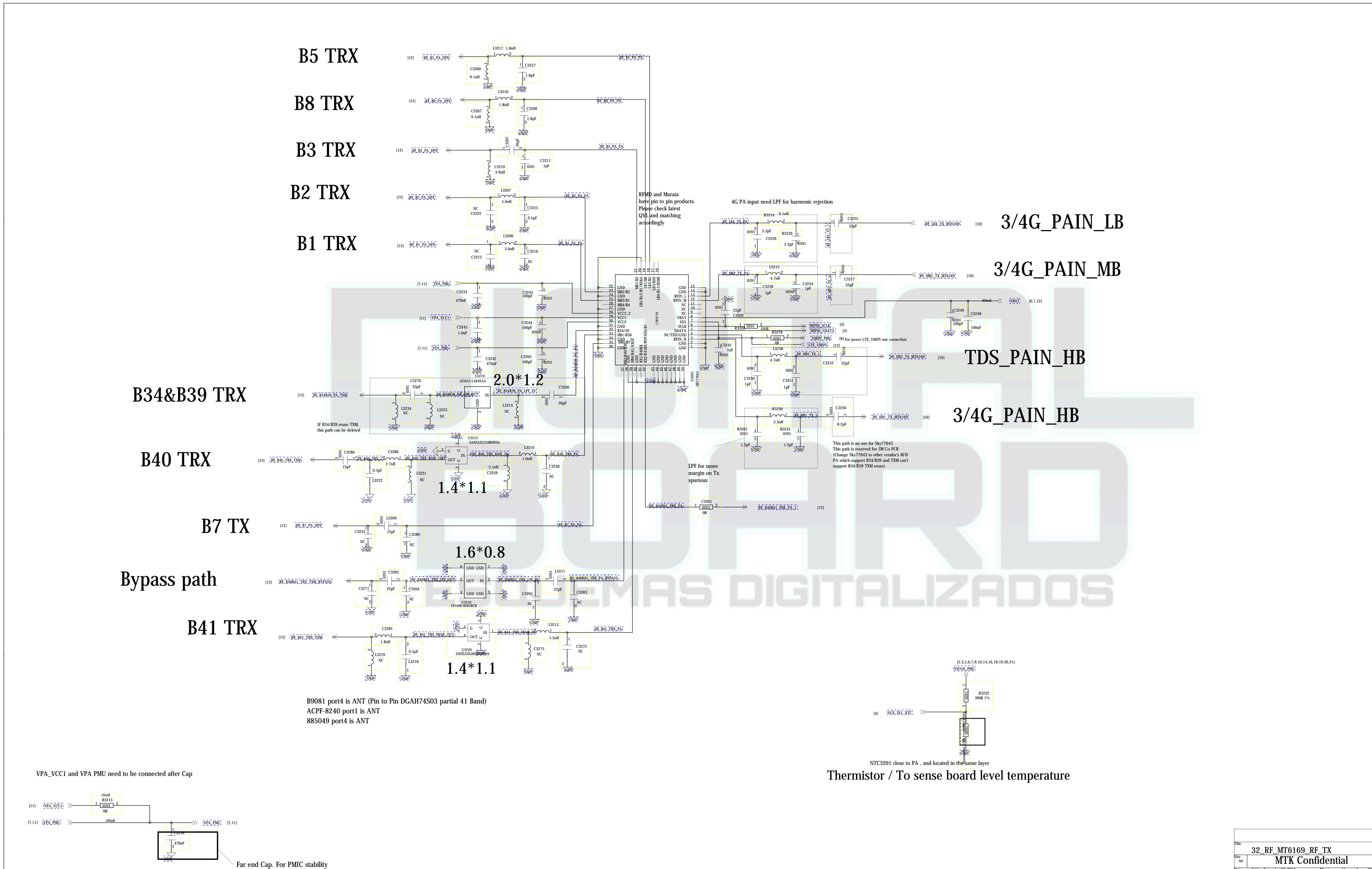
Note 24-1: DA9210's GPIO10 = SRCLKEN0 => Buck EN controlled by SRCLKEN0 or I2C.

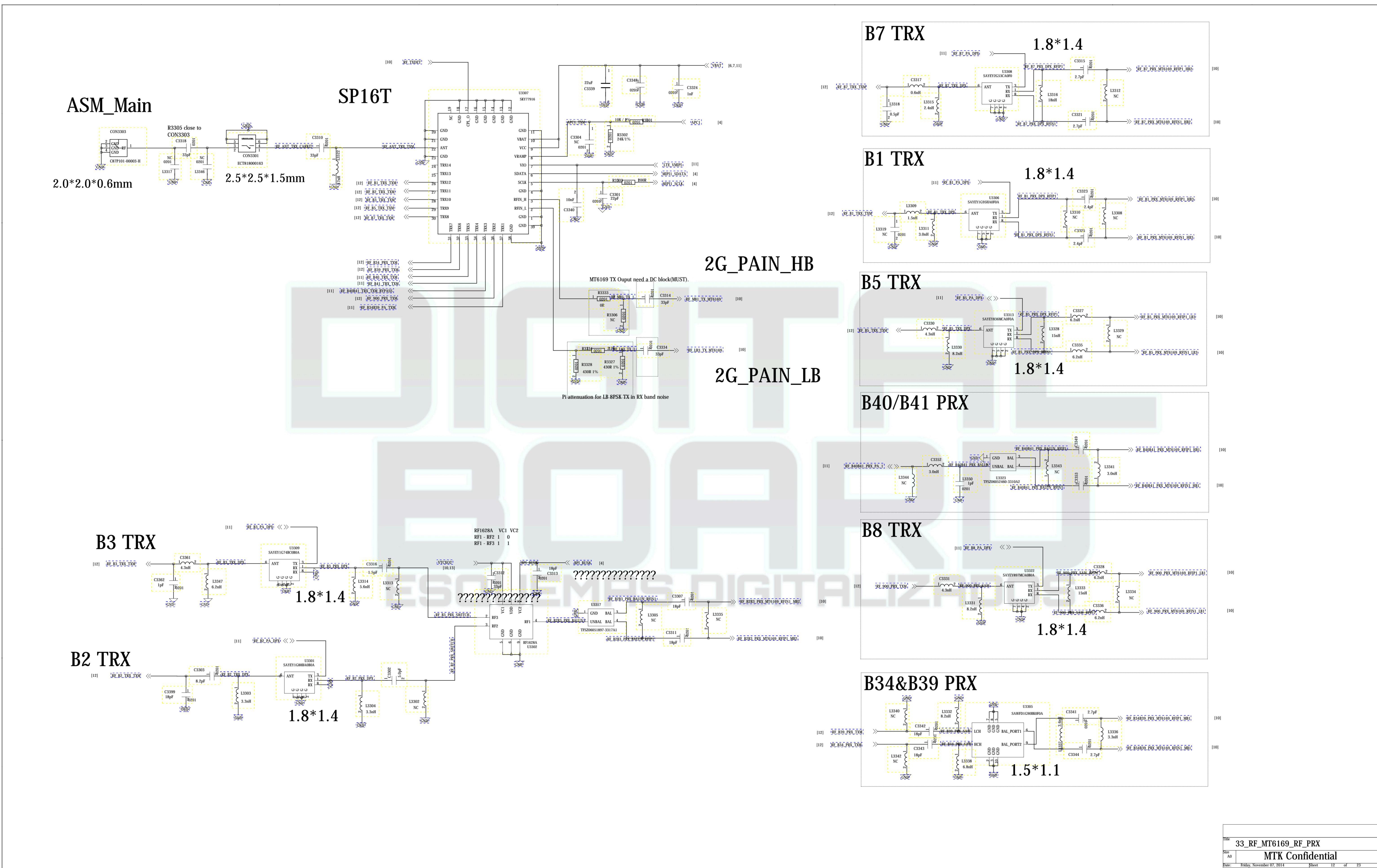
Note 24-2: BOM option to select which 4-phase buck be applied? DA9210 or second source.

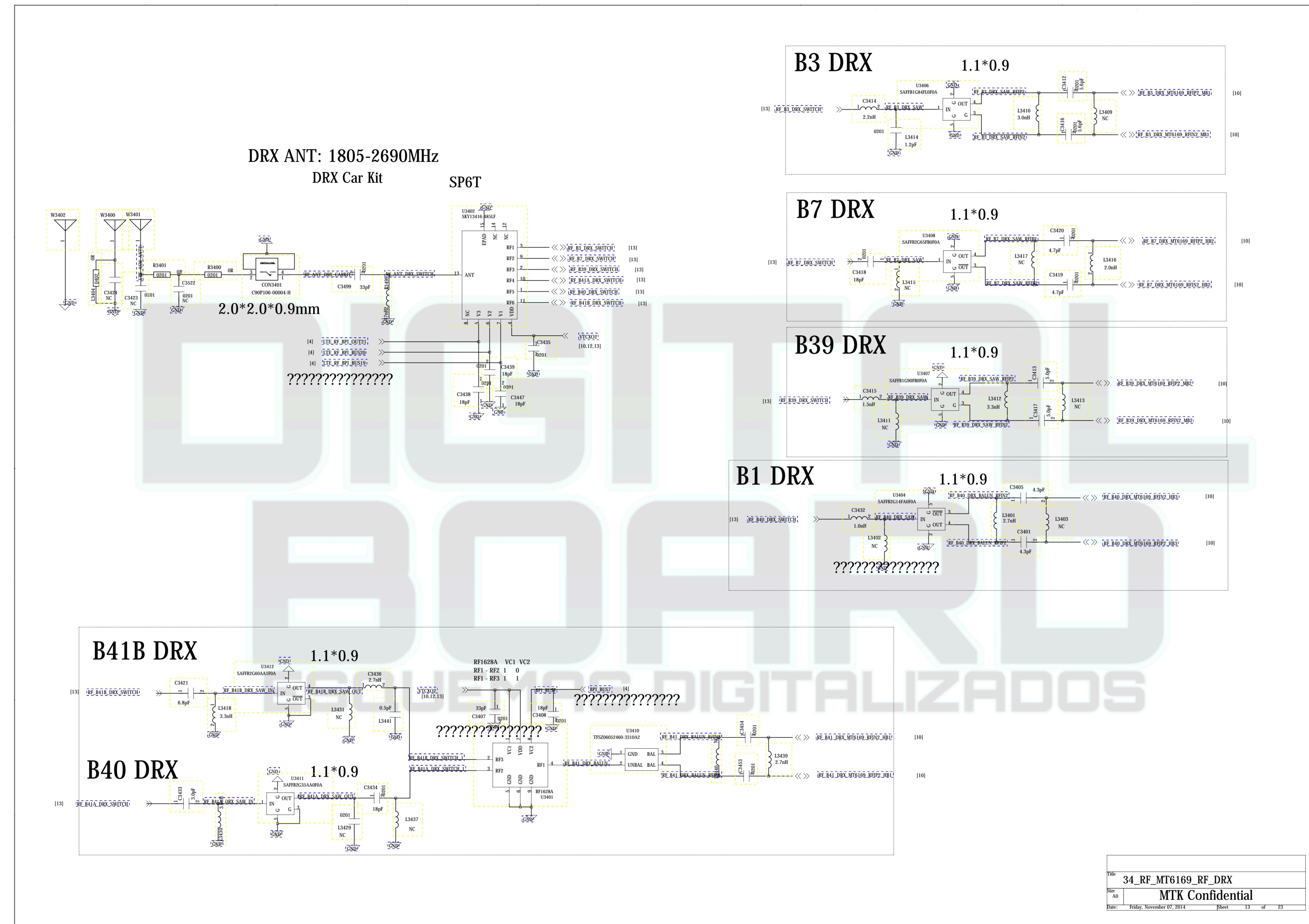
	R2402	R2404	R2405	R2406
DA9210	N/M	N/M	N/M	N/M
2nd source	10K	OR	OR	OR

COMPANY: <Company Name>	
TITLE: <Title>	
DRAWN: <Drawn By>	DATED: <Drawn Date>
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>
SCALE: <Scale>	REV: <Rev>
CODE: <Code> A0 <Drawing Number><Revision>	
SHEET: 0f 23	

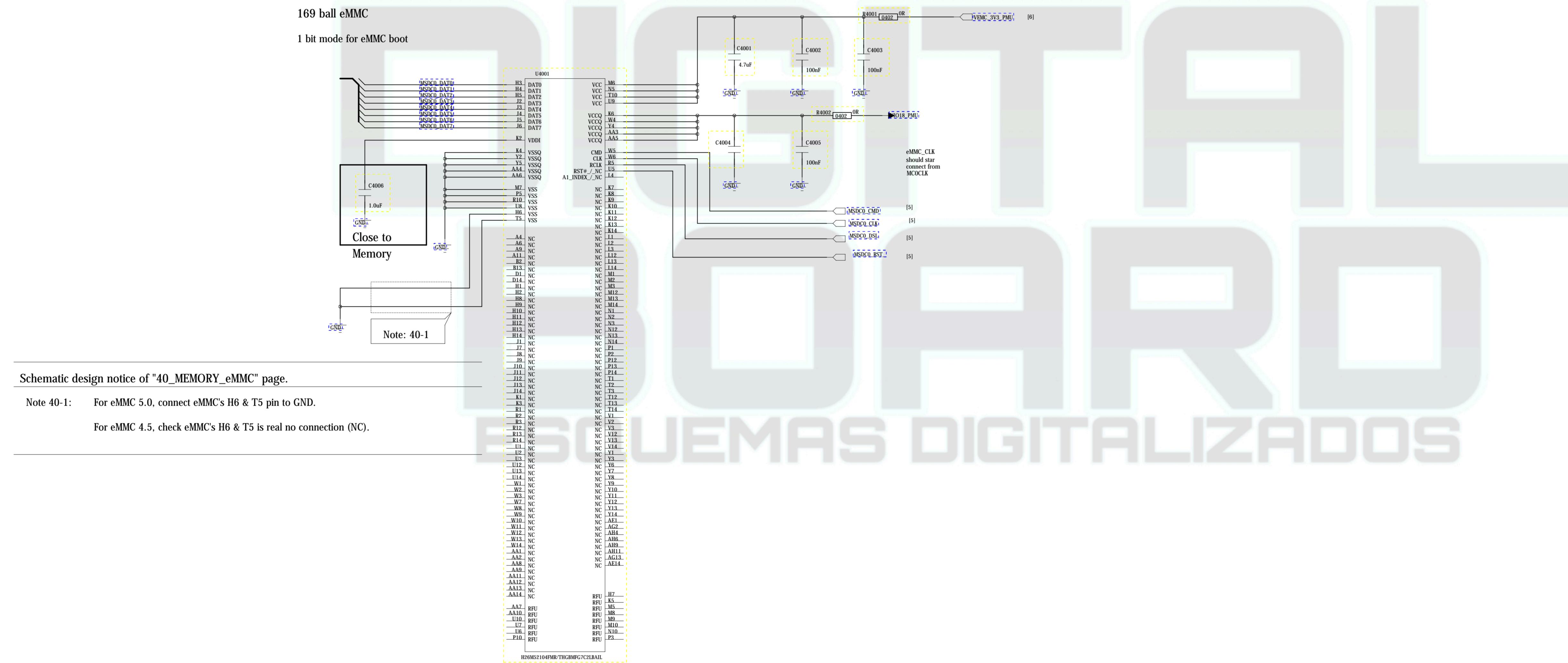




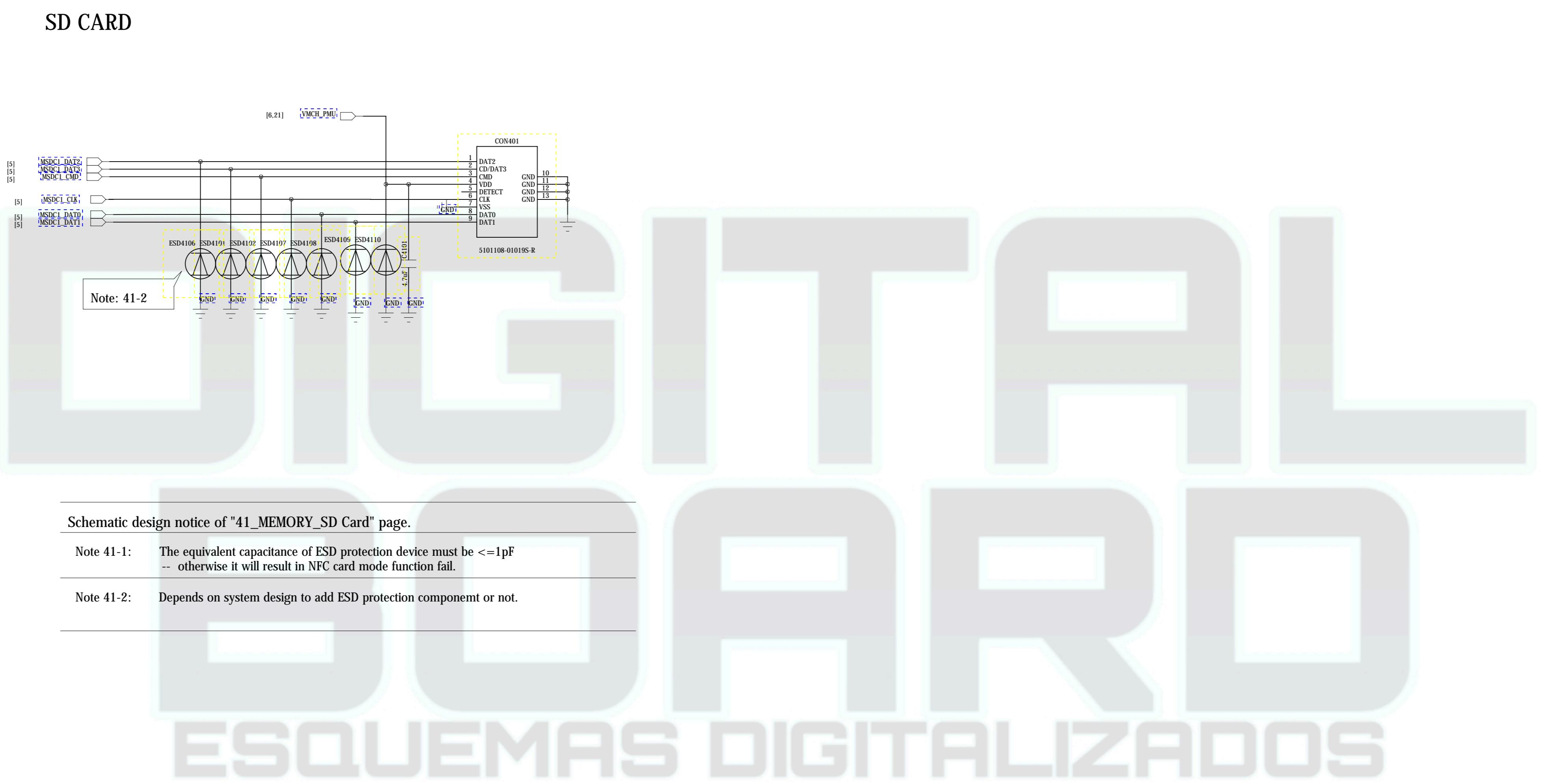




REVISION RECORD			
ITR	ECO NO.	APPROVED:	DATE



DRAWN: <Drawn By>		DATED: <Drawn Date>
CHECKED: <Checked By>		DATED: <Checked Date>
QUALITY CONTROL: <QC By>		DATED: <QC Date>
RELEASED: <Released By>		DATED: <Release Date>
CODE: <Code>	SIZE: <Scale>	DRAWING NO.: <Drawing Number> REV: <Revision>
<Title>		



COMPANY: <Company Name>	
TITLE: <Title>	
DRAWN: <Drawn By>	DATUM: <Drawn Date>
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>
CODE: <Code>	SIZE: <Size>
DRAWING NO: <Drawing Number>	
REV: <Revision>	
SCALE: <Scale>	
SHEET: 45 23	

C

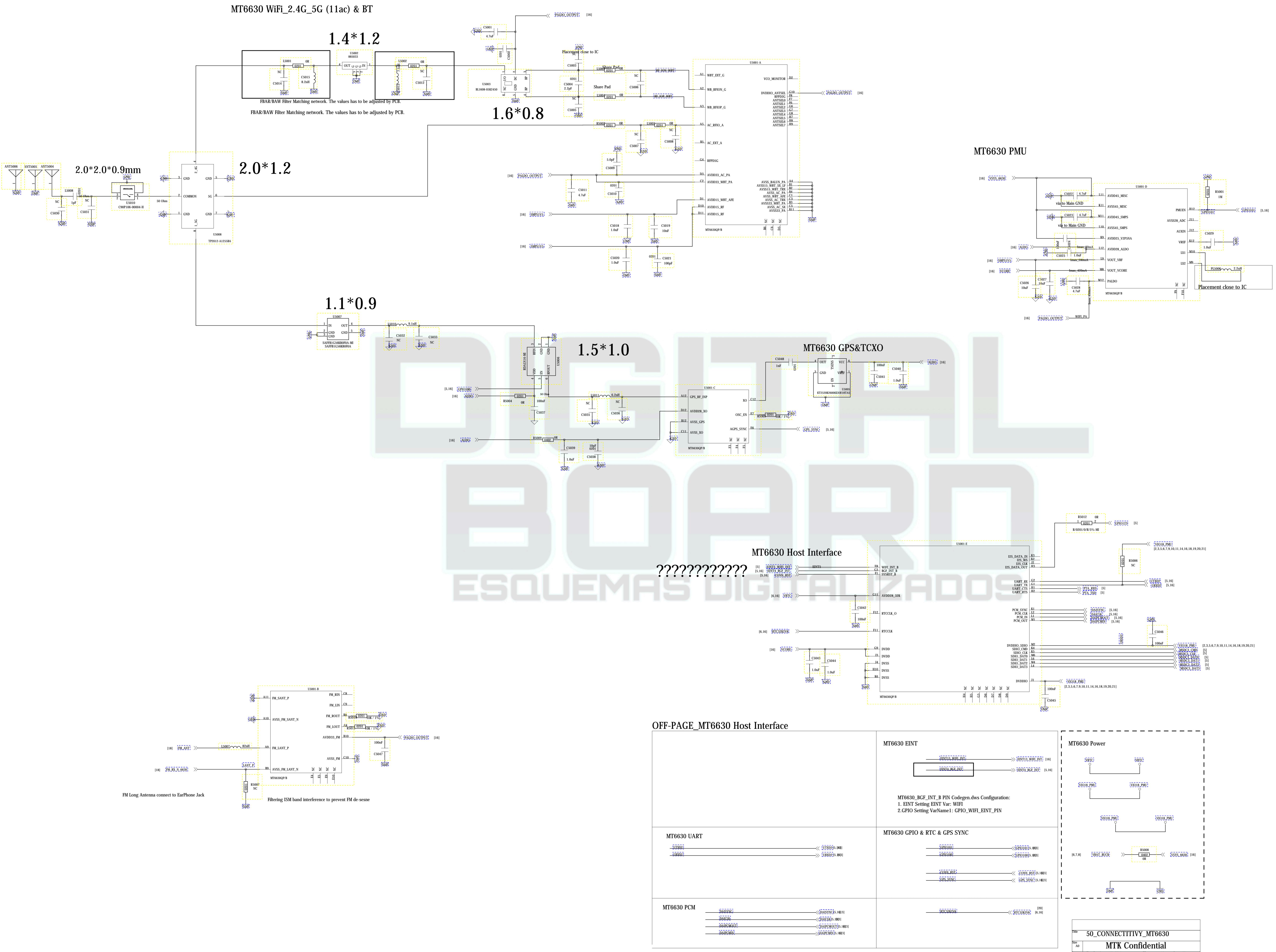
C

B

B

A

A



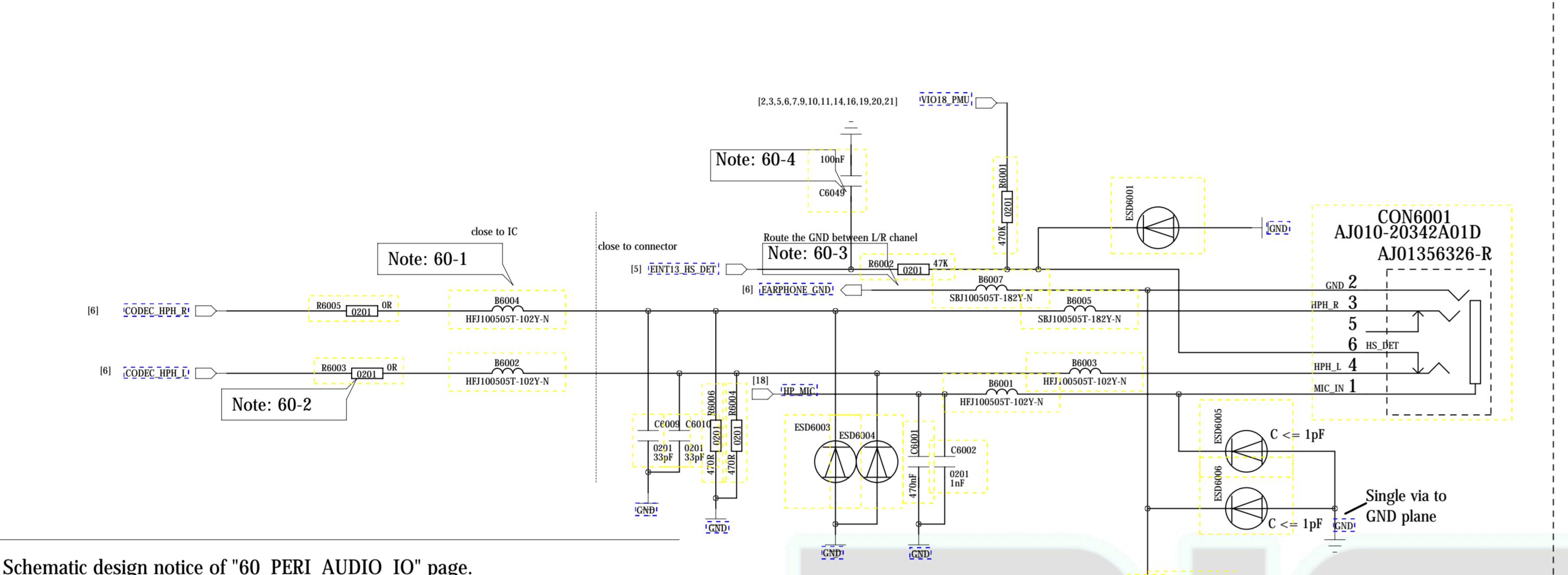
REVISION RECORD			
ITR	ECO NO.	APPROVED:	DATE:

D

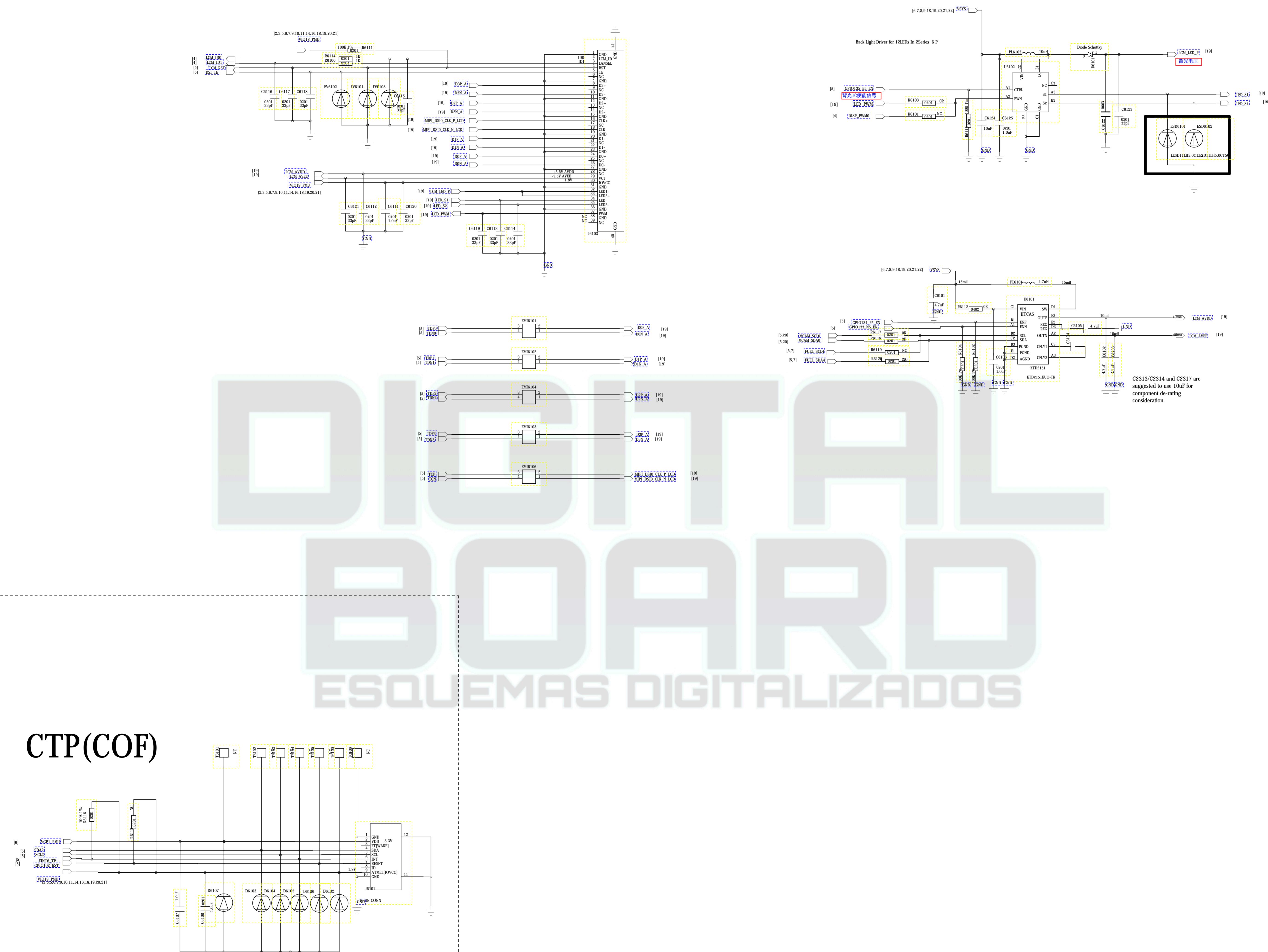
D



Earphone Audio



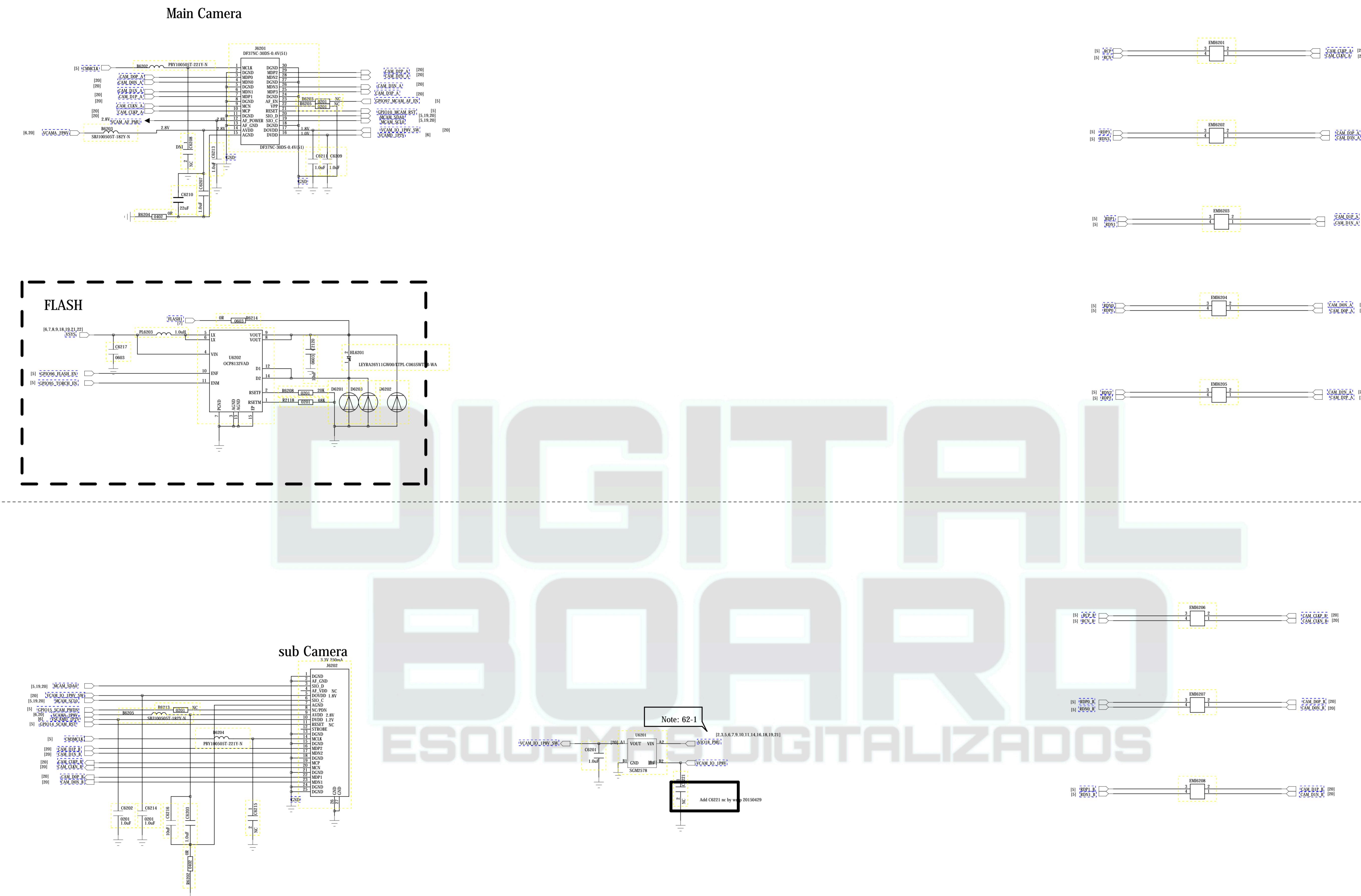
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



CTP(COF)

COMPANY:	<Company Name>		
TITLE:	<Title>		
e>	CODE:	SIZE:	DRAWING NO:
ate>	<Code>	A0	<Drawing Number><Revision>
te>	SCALE: <Scale>	SHEET: 10	23

REVISION RECORD			
ltr	ECN NO.	APPROVED:	DATE



Schematic design notice of "63_PERI_CAMERA_KEYPAD" page.

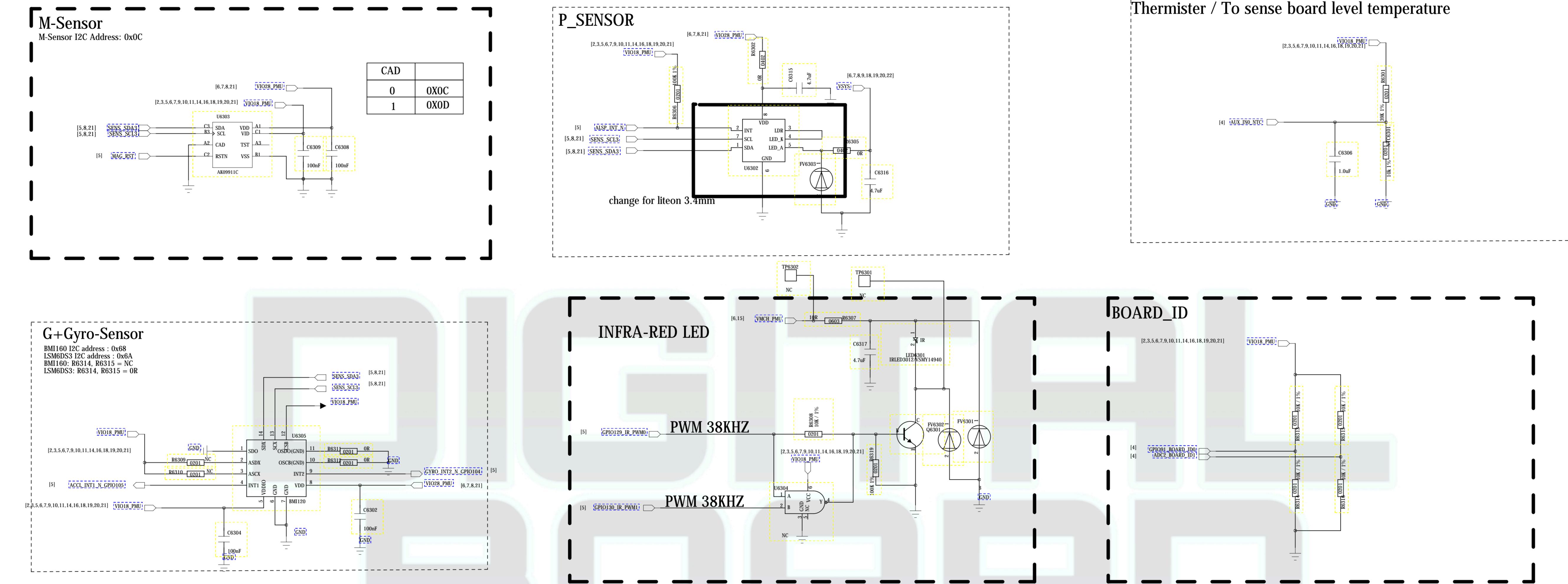
Note 62-1: The VCC of I2C_0 is pulled to "VCAM_IO_PMU".

Note 62-2: I2C control interface of front camera (with AF) must be assigned to I2C-2 bus when PIP/VIV feature is supported.

Note 62-3: Reserve a capacitor (27pF) on camera's MCLK and shunt it to GND to prevent GPS de-sense.

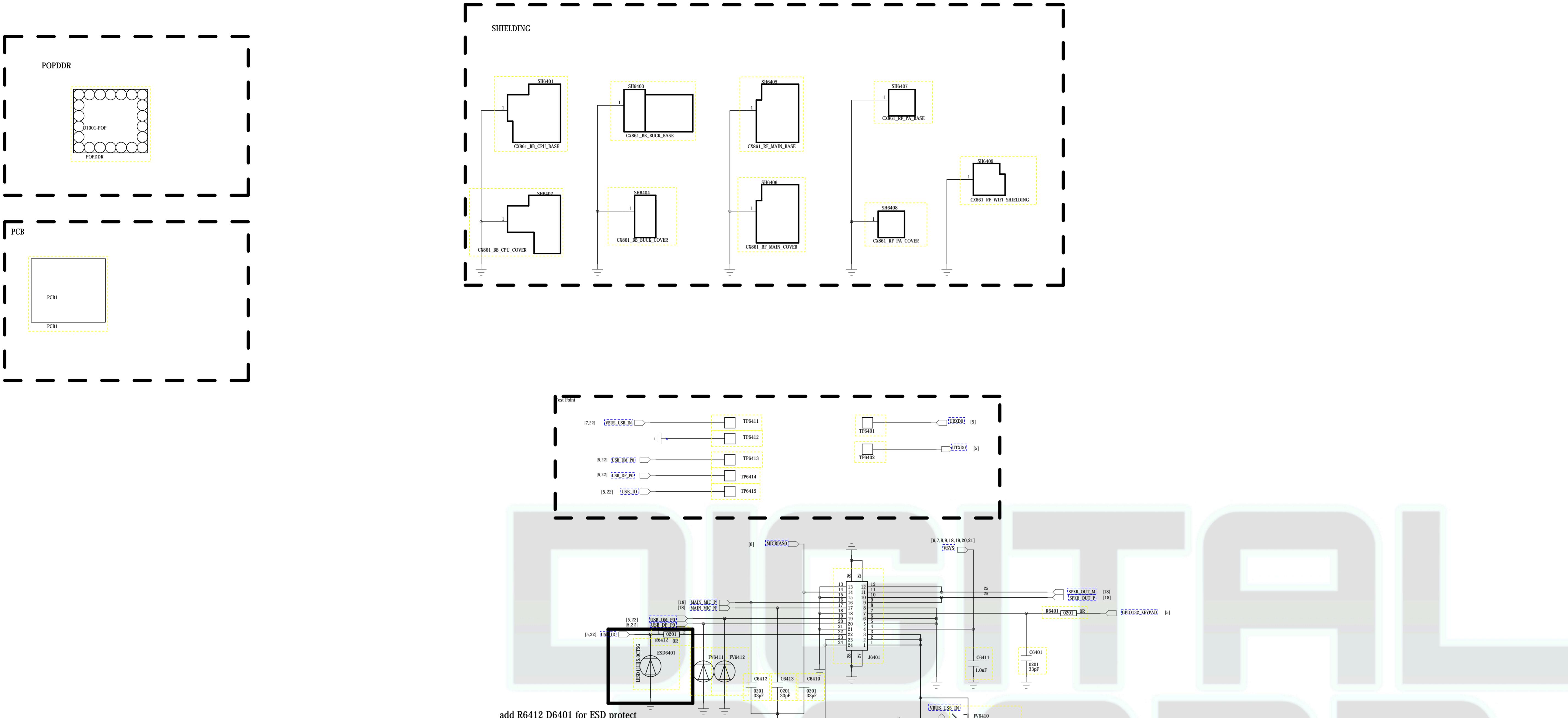
COMPANY: <Company Name>	
TITLE: <Title>	
DRAWN: <Drawn By>	DATED: <Drawn Date>
CHECKED: <Checked By>	DATES: <Checked Date>
QUALITY CONTROL: <QC By>	DATES: <QC Date>
RELEASED: <Released By>	REV: <Release Date>
CODE: <Code>	SIZE: <Size>
DRAWING NO: <Drawing Number><Revision>	
SCALE: <Scale>	SHRIFT: 20 23

REVISION RECORD			
LTN	ECO NO.	APPROVED:	DATE:



COMPANY:	<Company Name>		
TITLE:	<Title>		
DRAWN:	<Drawn By>	SIGNED:	<Drawn Date>
CHEKED:	<Checked By>	CHECKED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATE:	<Release Date>
	SCALE:	<Scale>	REV:
		Sheet:	24 23

REVISION RECORD			
ltr	ECO NO:	APPROVED	DATE:

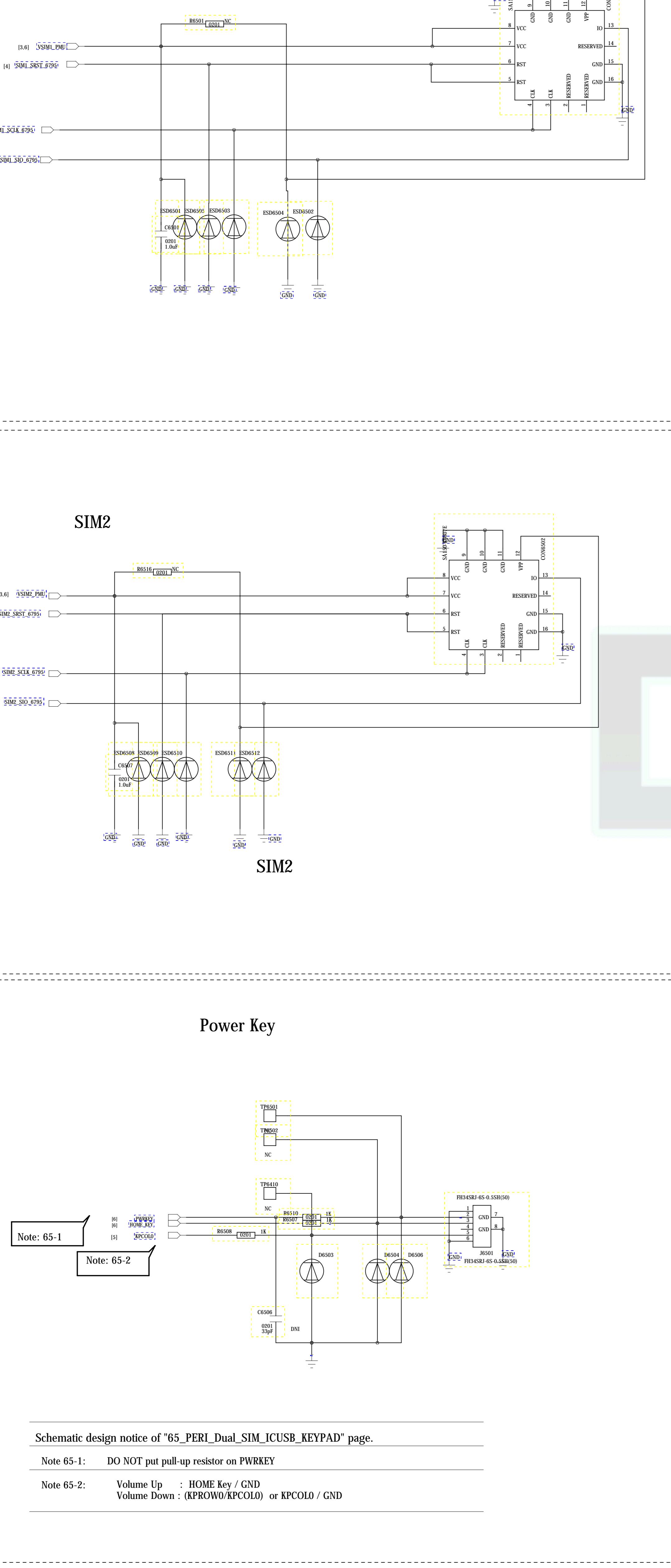


DRAWN:	<Drawn By>	DATED:	<Drawn Date>
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CON:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>

COMPANY:	<Company Name>		
TITLE:	<Title>		
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	A0	<Drawing Number>×Revision>	
SCALE:	<Scale>	SHEET:	22 / 23

DIGITAL BOARD

ESQUEMAS DIGITALIZADOS



Schematic design notice of "65_PERI_Dual_SIM_ICUSB_KEYPAD" page.

Note 65-1: DO NOT put pull-up resistor on PWRKEY

Note 65-2: Volume Up : HOME Key / GND
Volume Down : (KPROW0/KPCOL0) or KPCOL0 / GND

COMPANY:	<Company Name>		
TITLE:	<Title>		
DRAWN:	<Drawn By>	DATED:	<Drawn Date>
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CON:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	A0	<Drawing Number>	<Revision>
SCALE:	<Scale>	SHRFT:	28 23

D

D

C

C

B

B

A

A