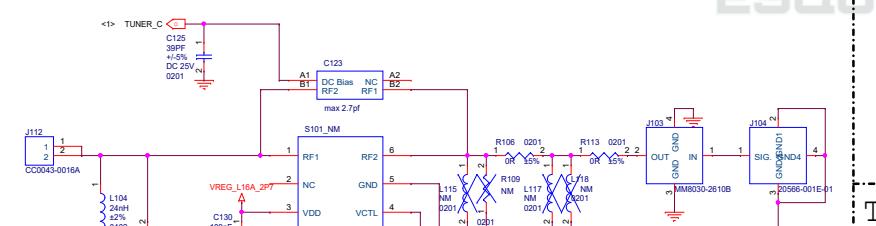
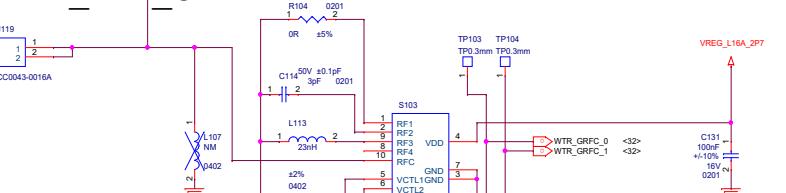


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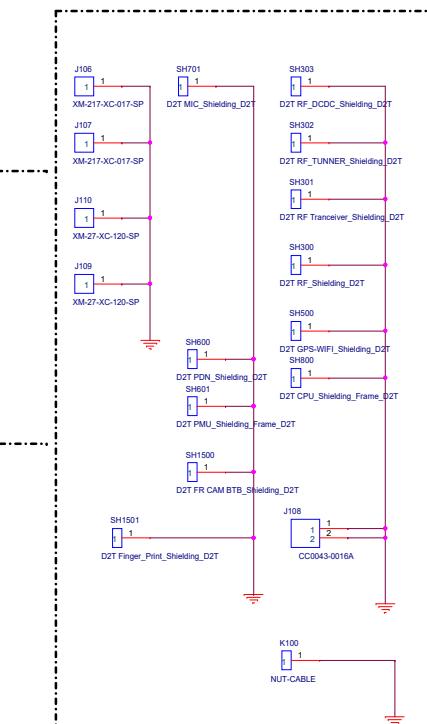
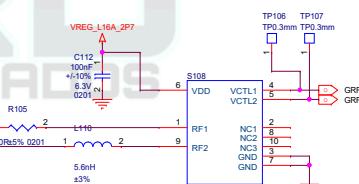
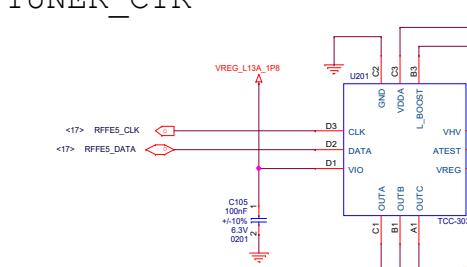


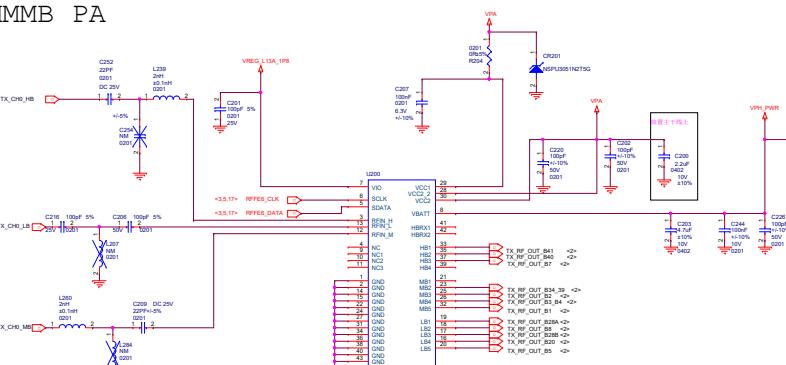
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## DRX\_ANT\_GND

## TUNER\_CTR





B40

TX B41

TX TDS

TX B20

Thermal

PA\_THERM1

PA\_B28B

PA\_B28A

PA\_B41

PA\_B39

PA\_B34

PA\_B41

PA\_B39

PA\_B20

B1&amp;B3

B2

B5

B8

B7

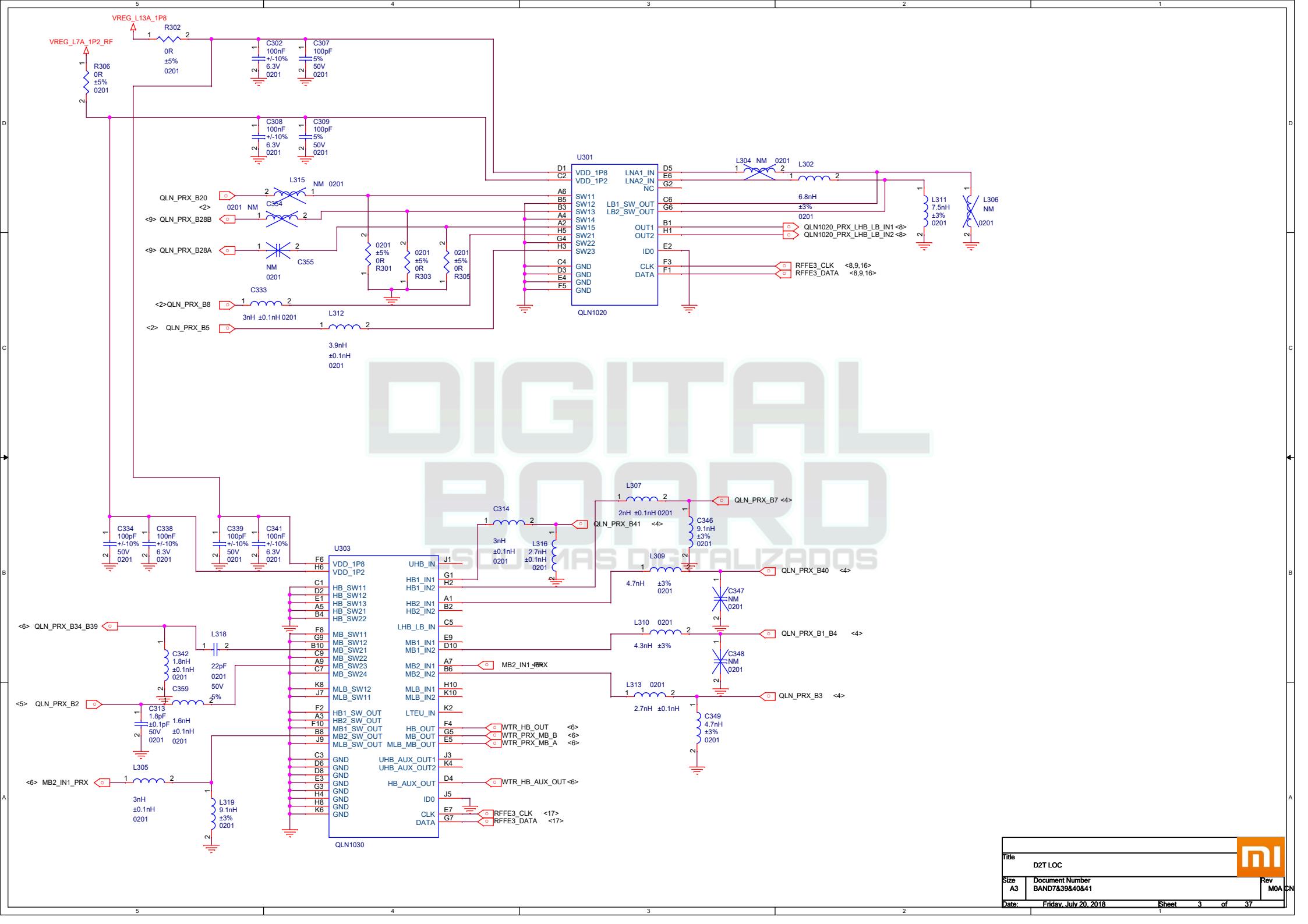
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PRX B34 B39

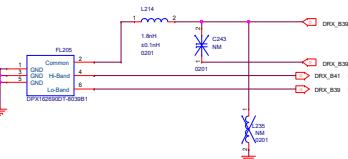
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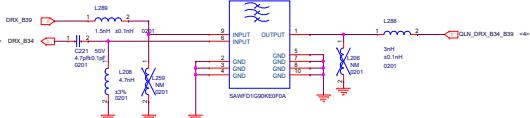
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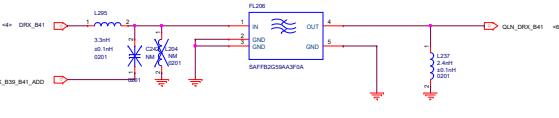
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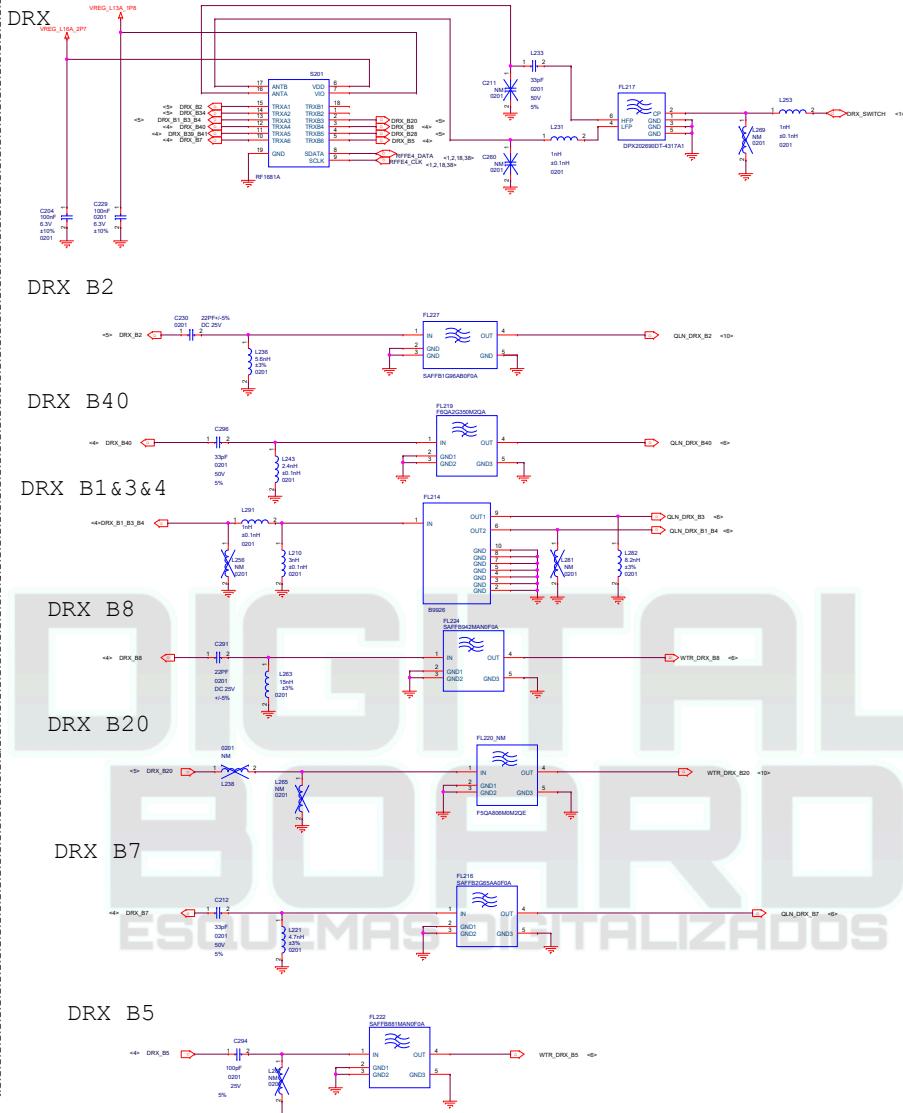
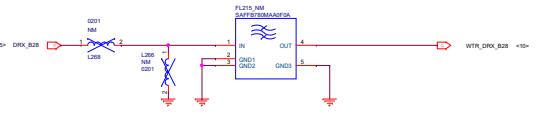
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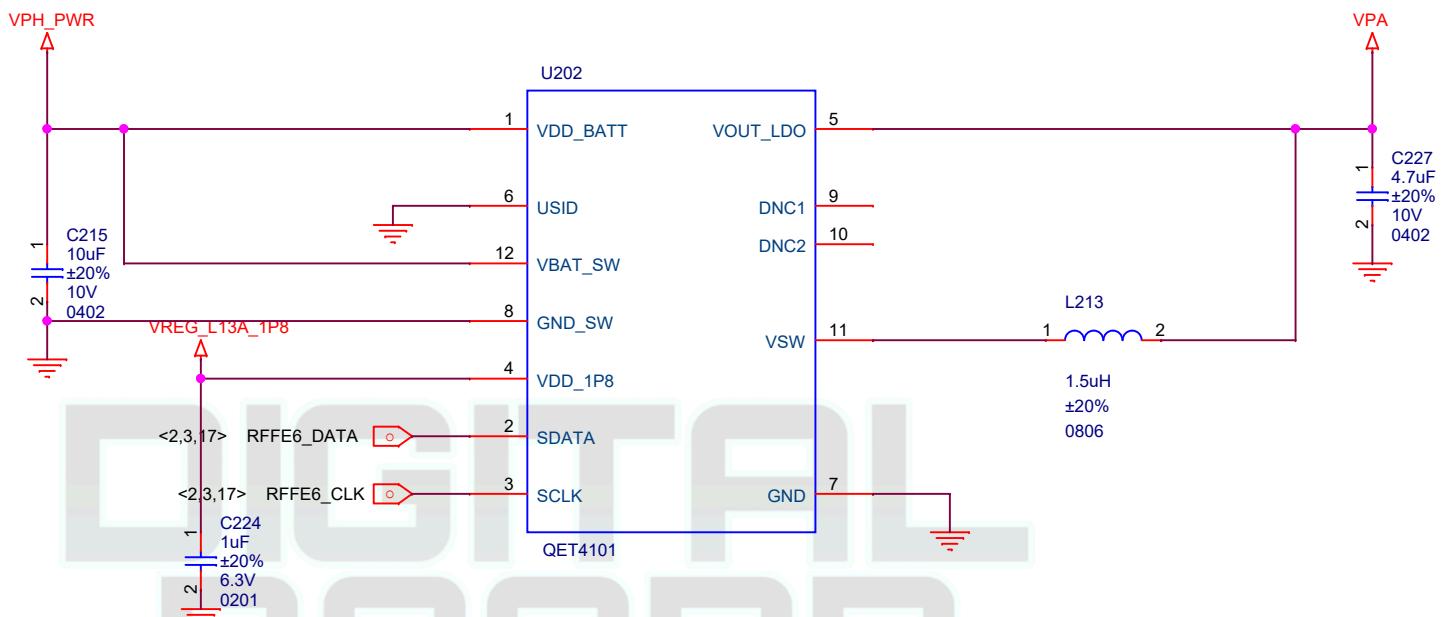


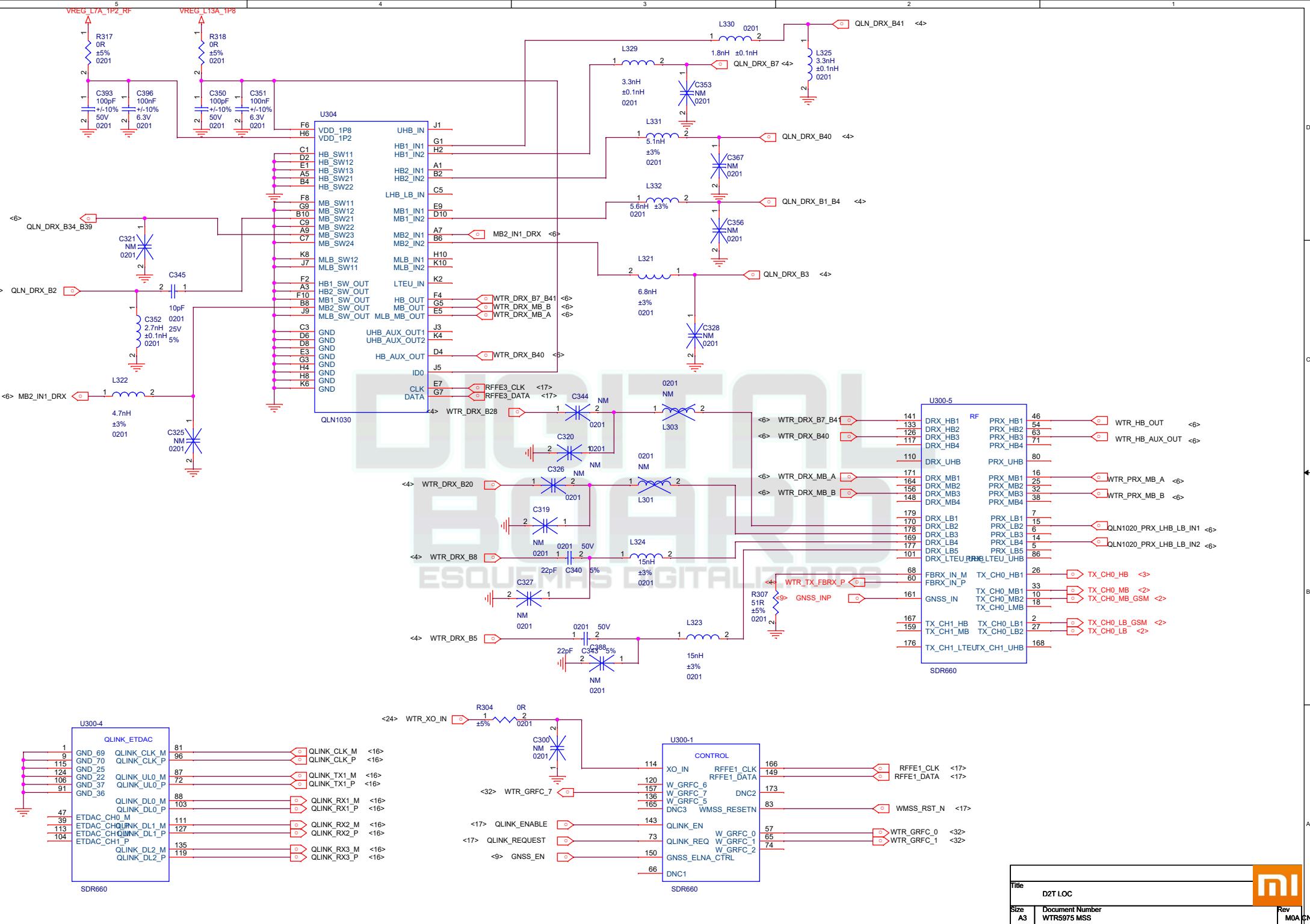
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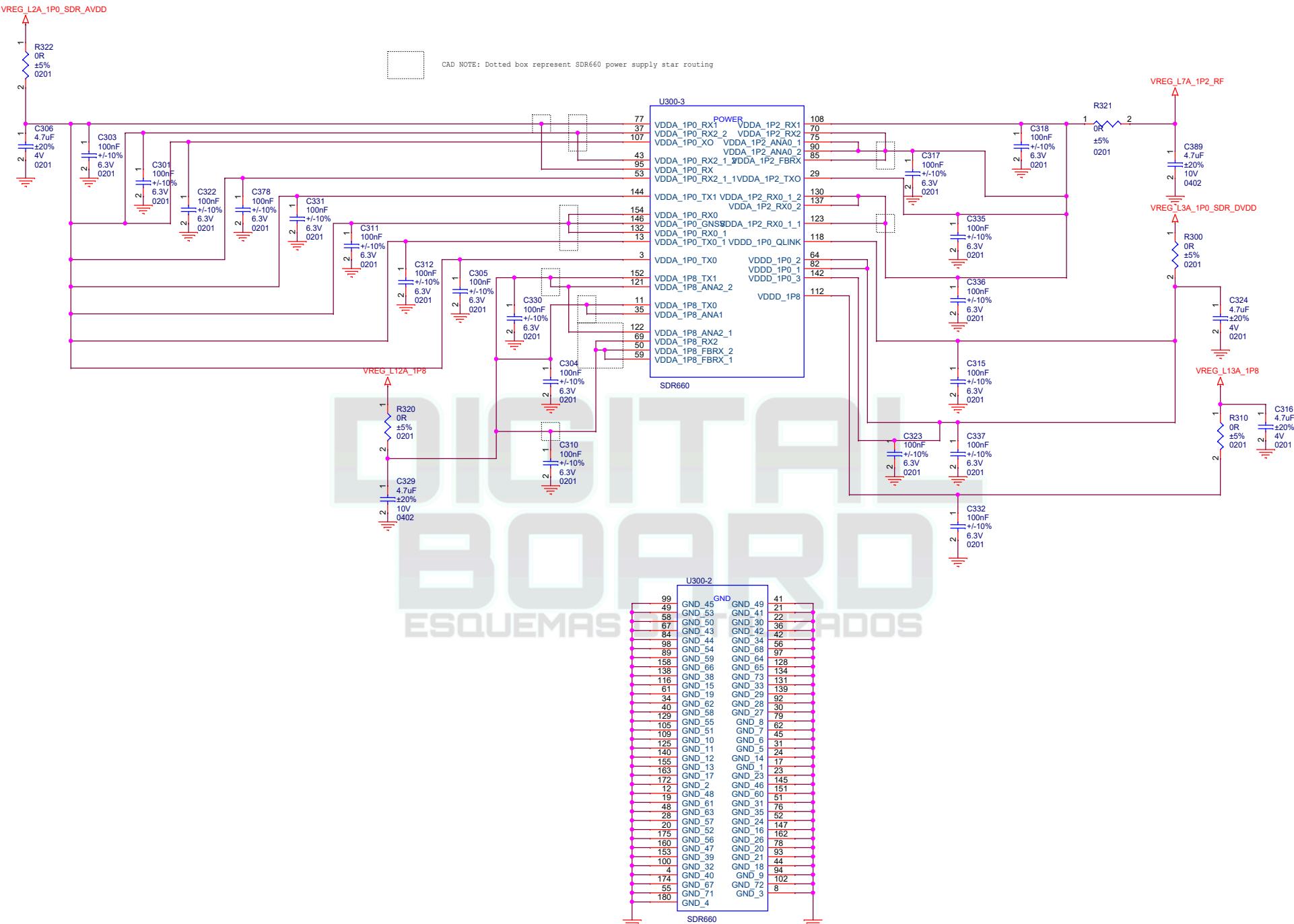


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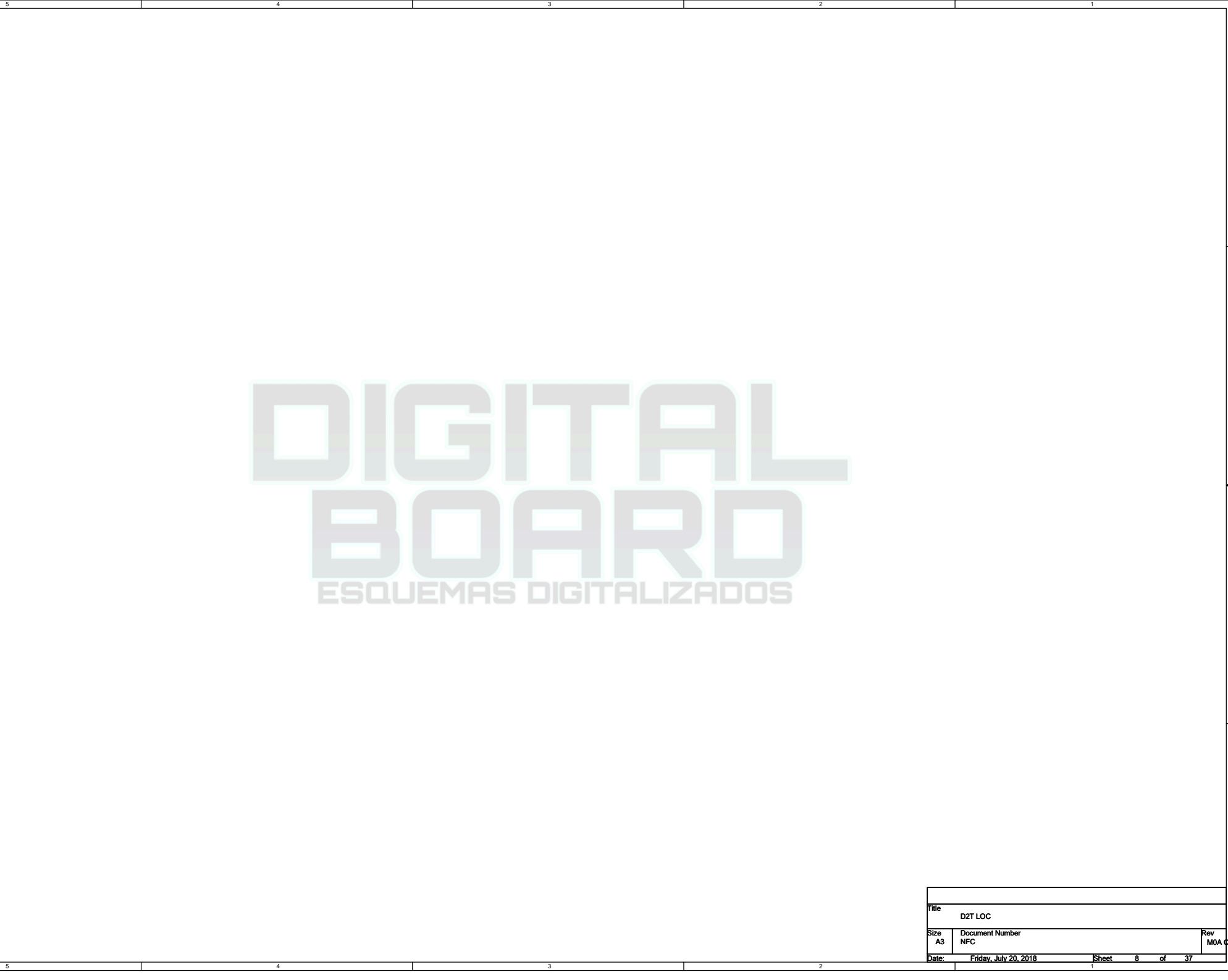


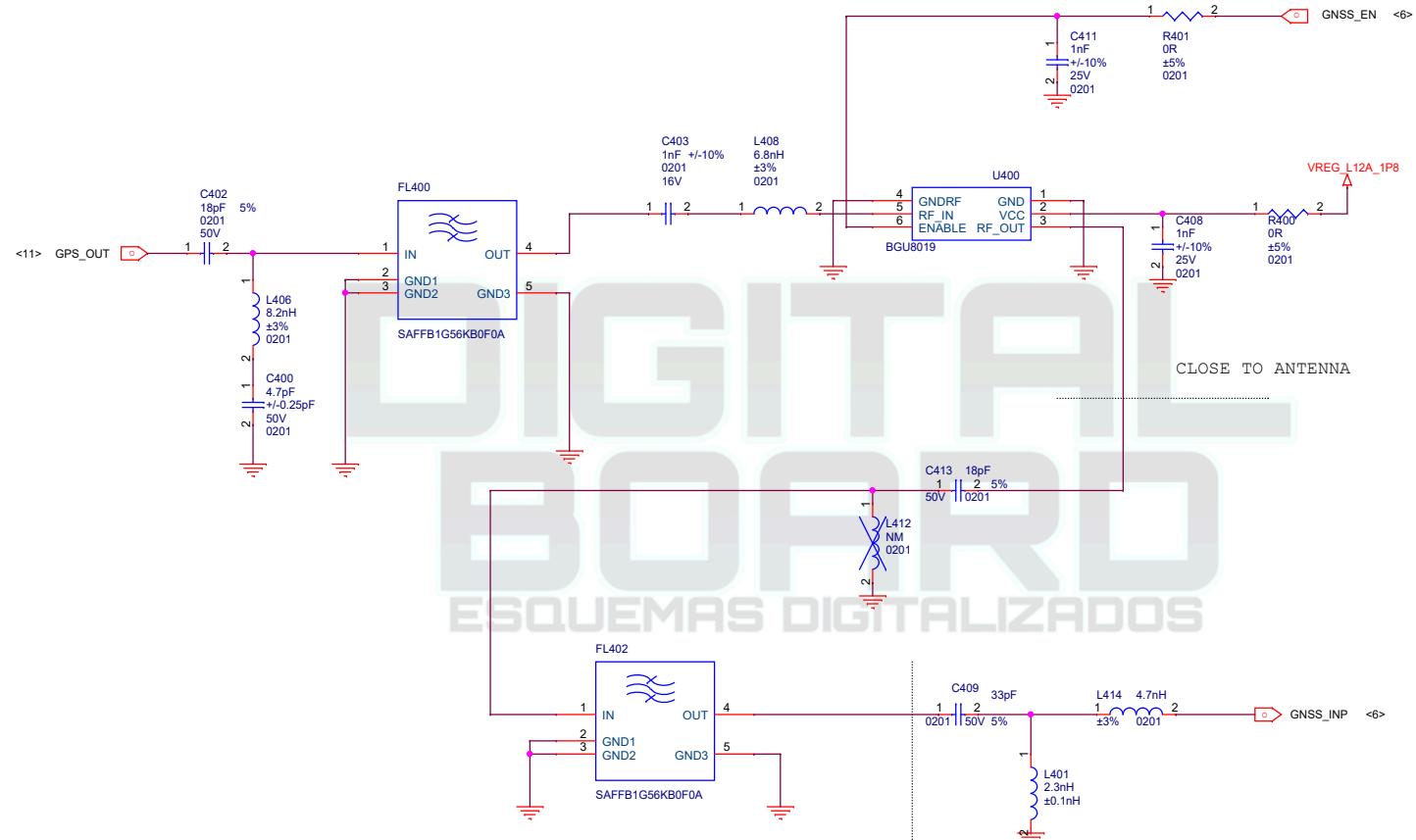


# DIGITAL BOARD

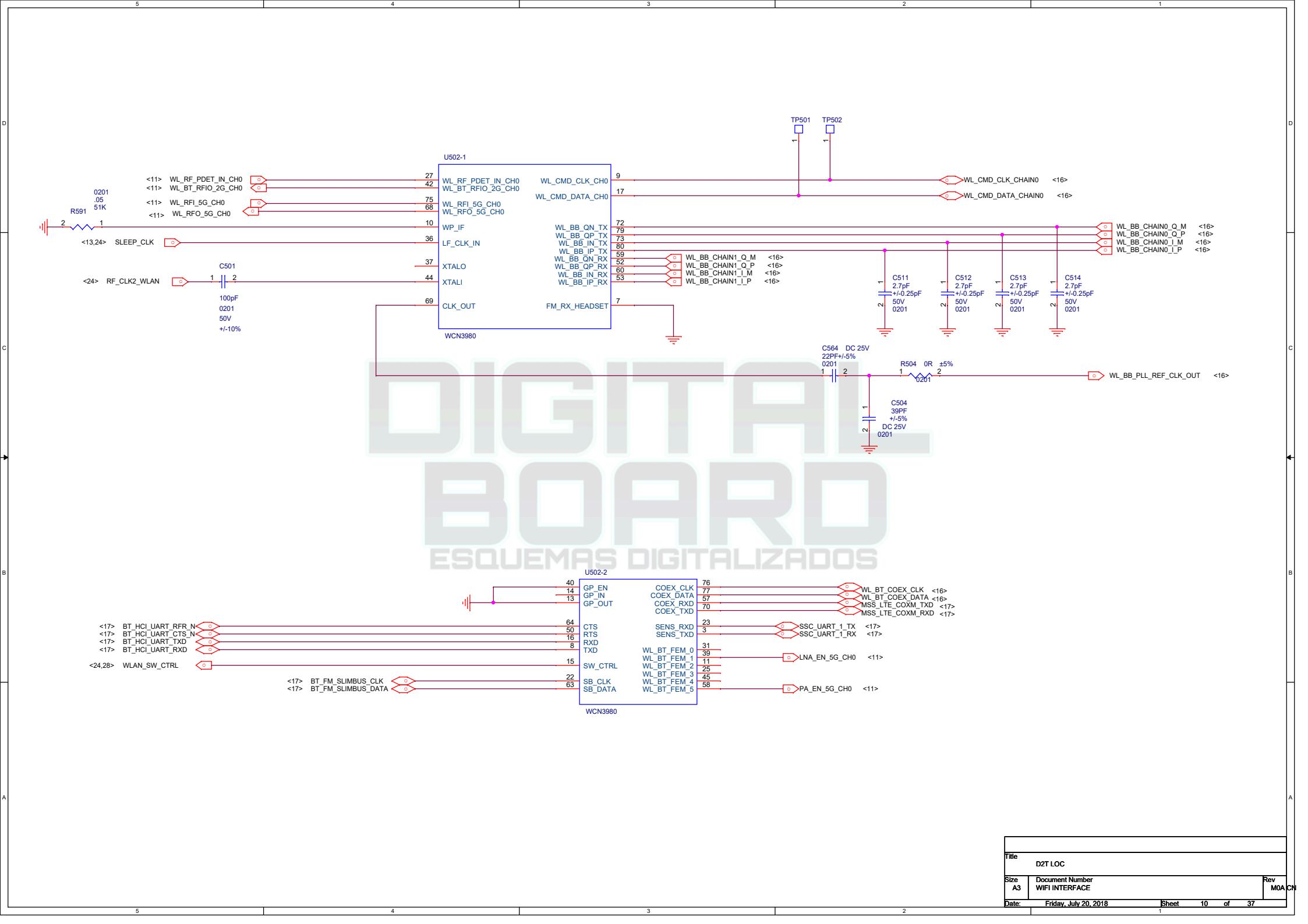
ESQUEMAS DIGITALIZADOS

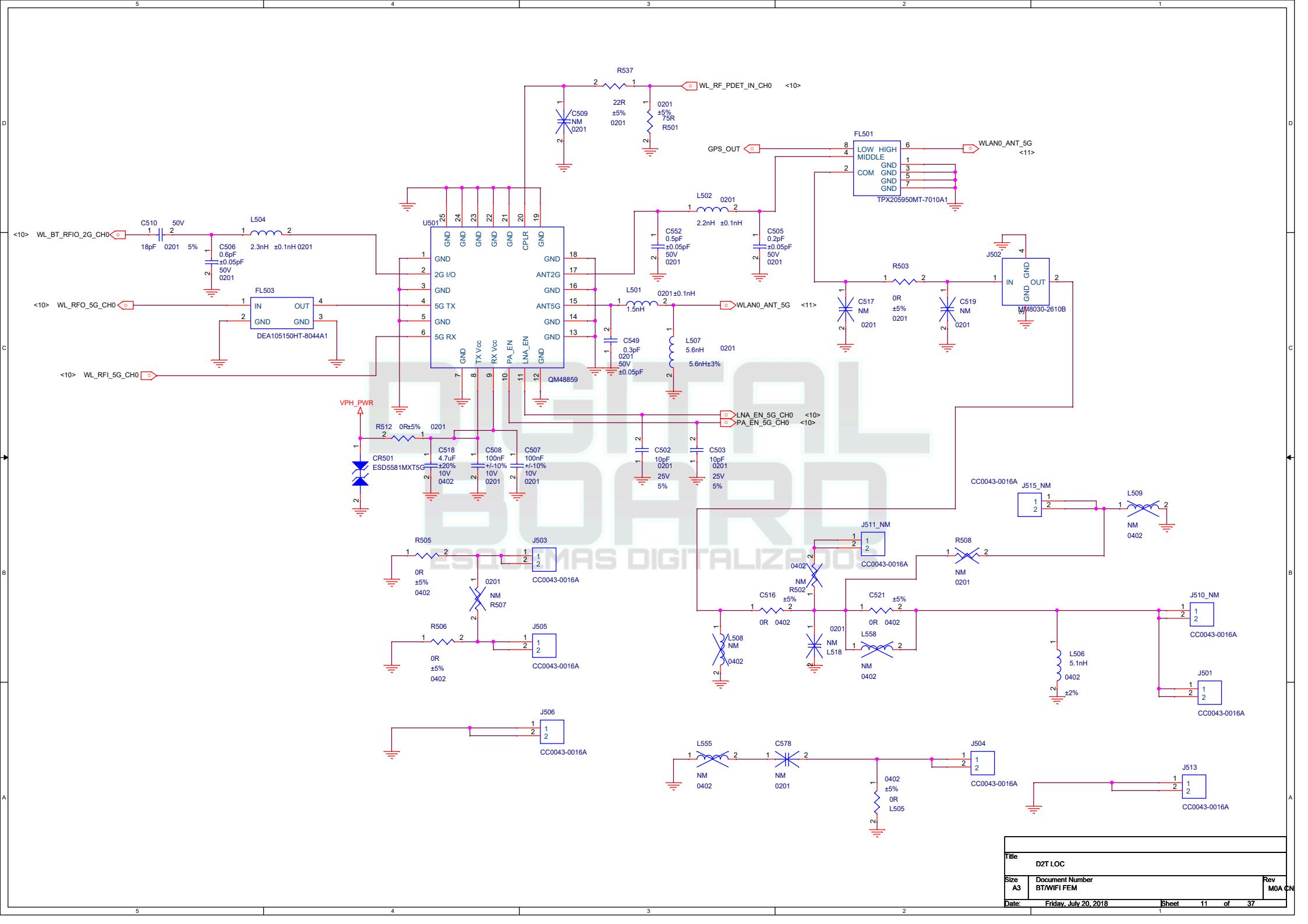
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Size	A3	Document Number	NFC
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			CN

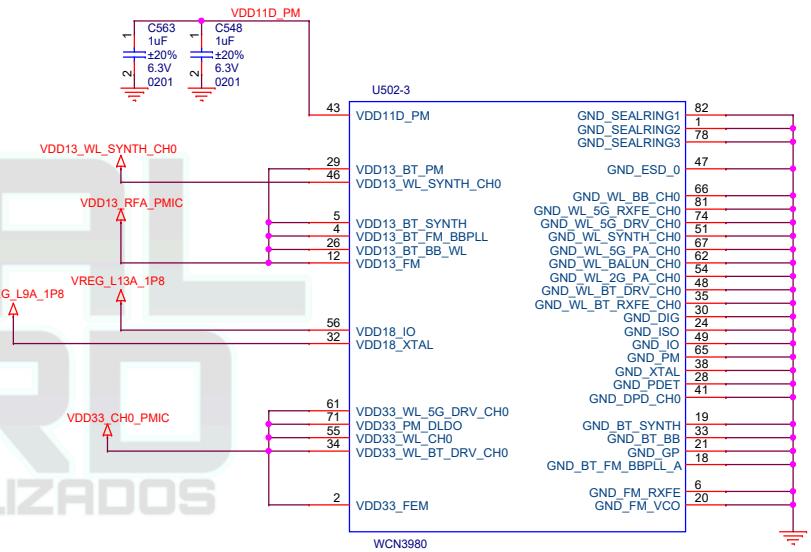
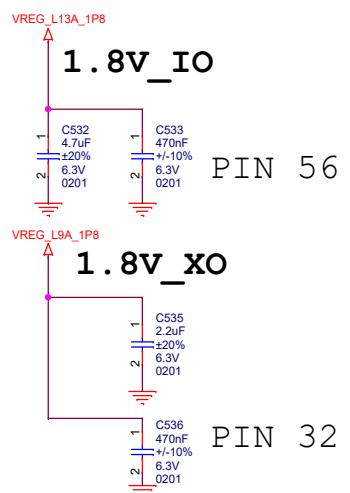
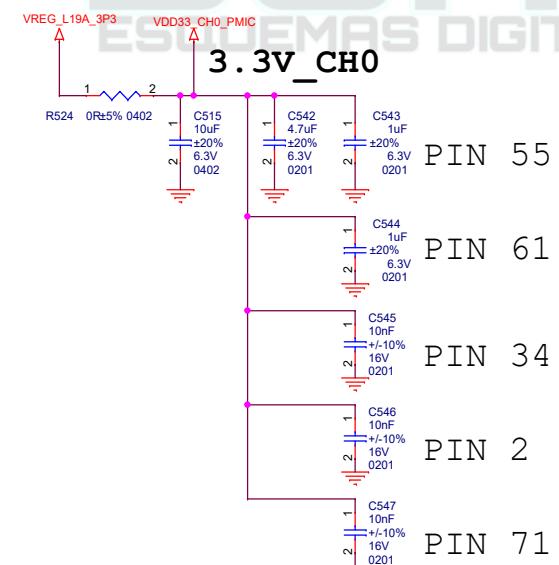
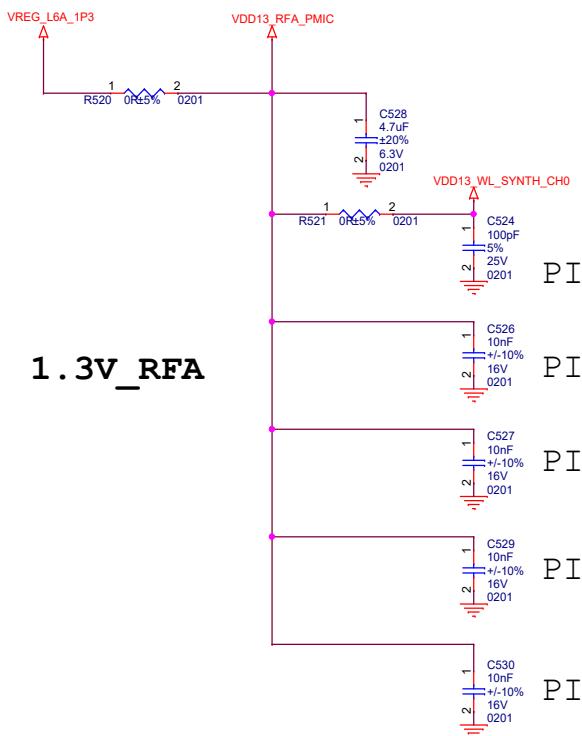


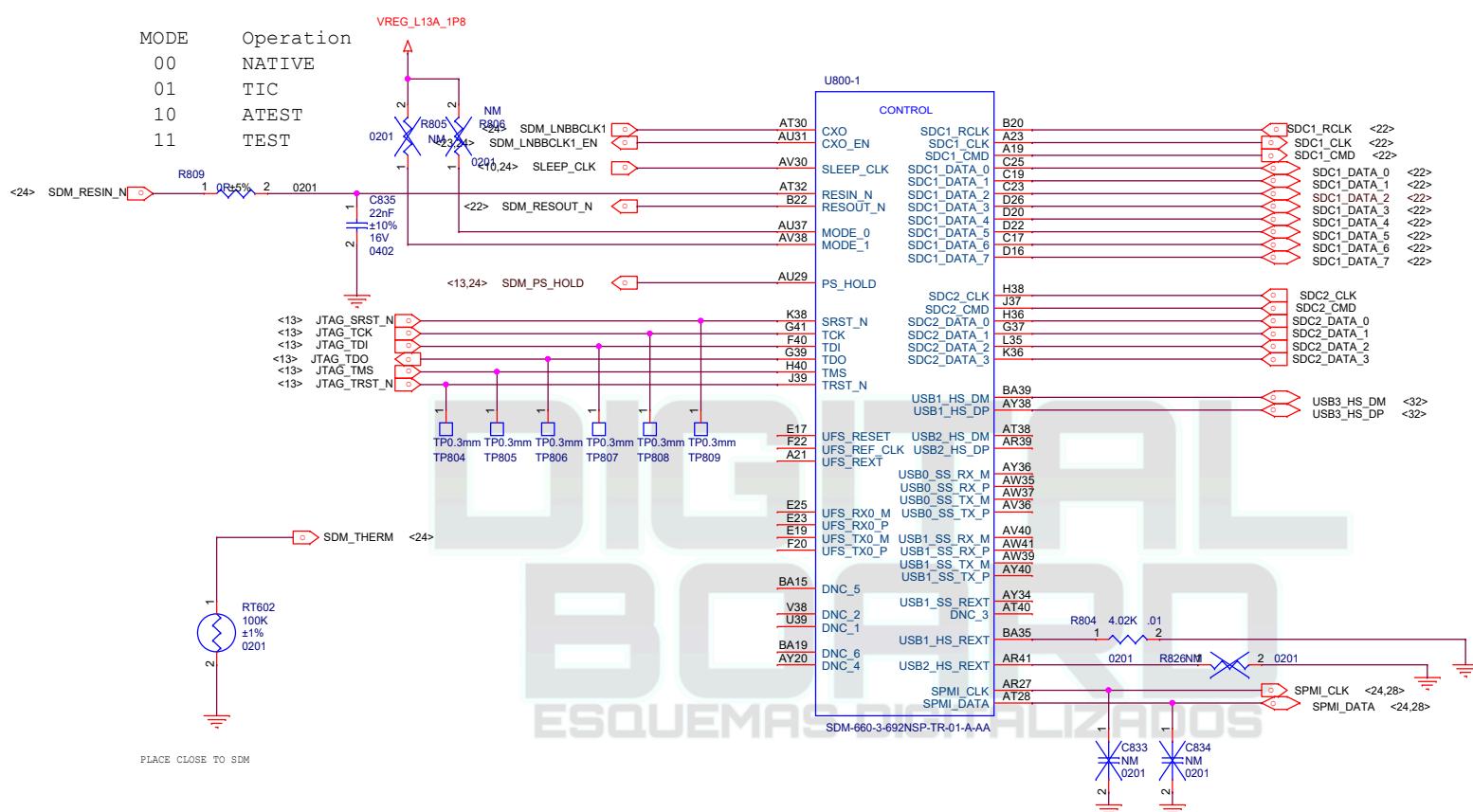


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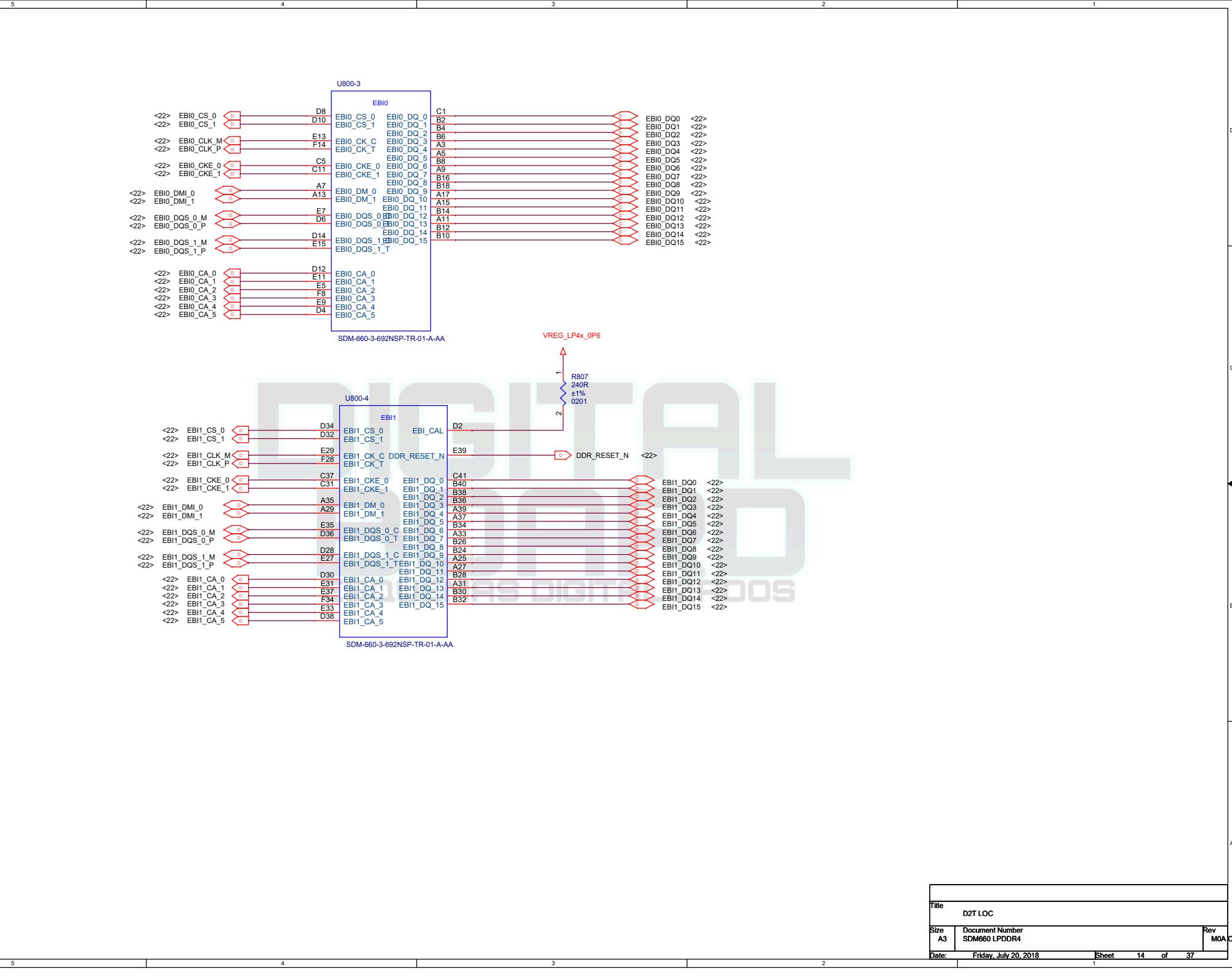


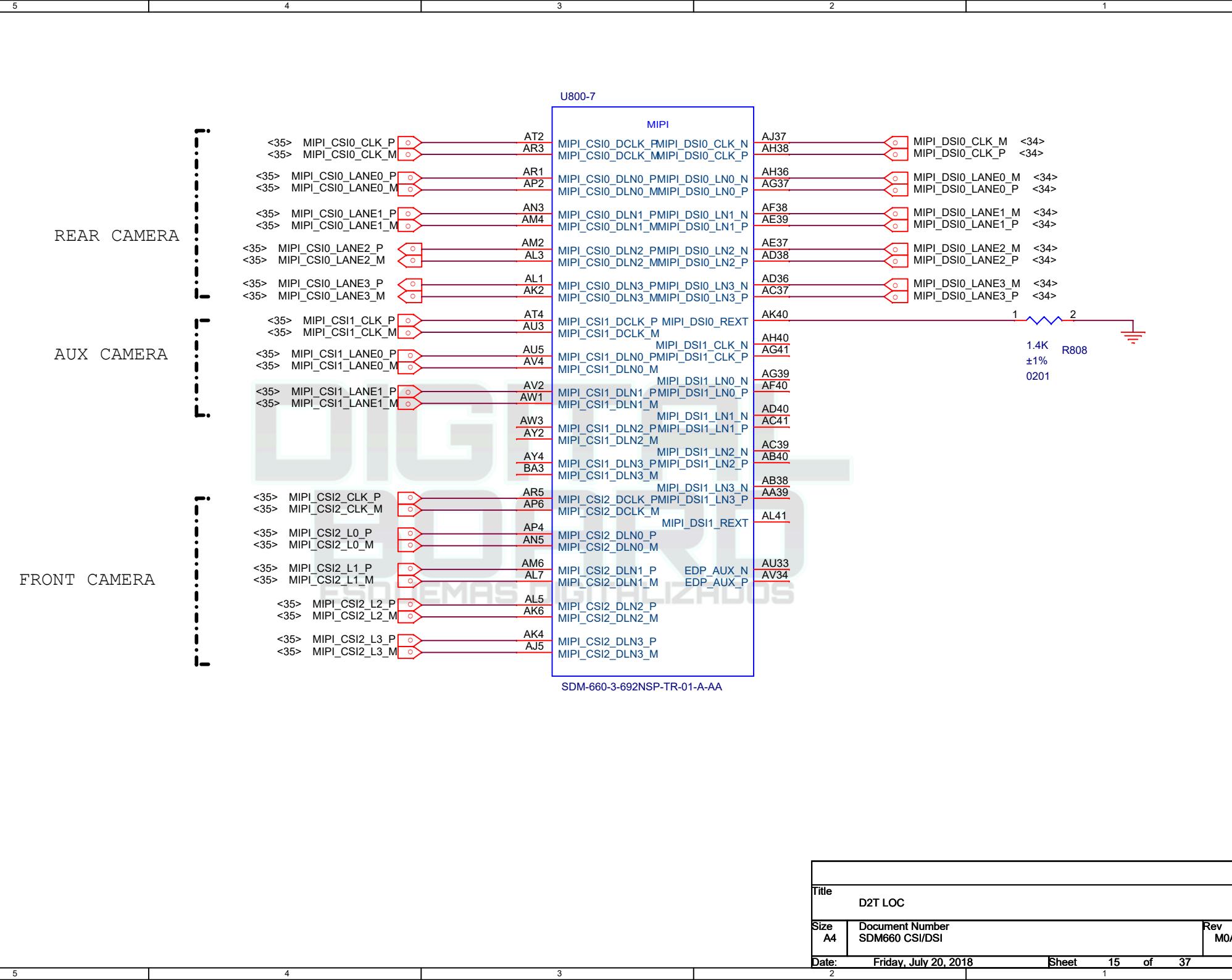


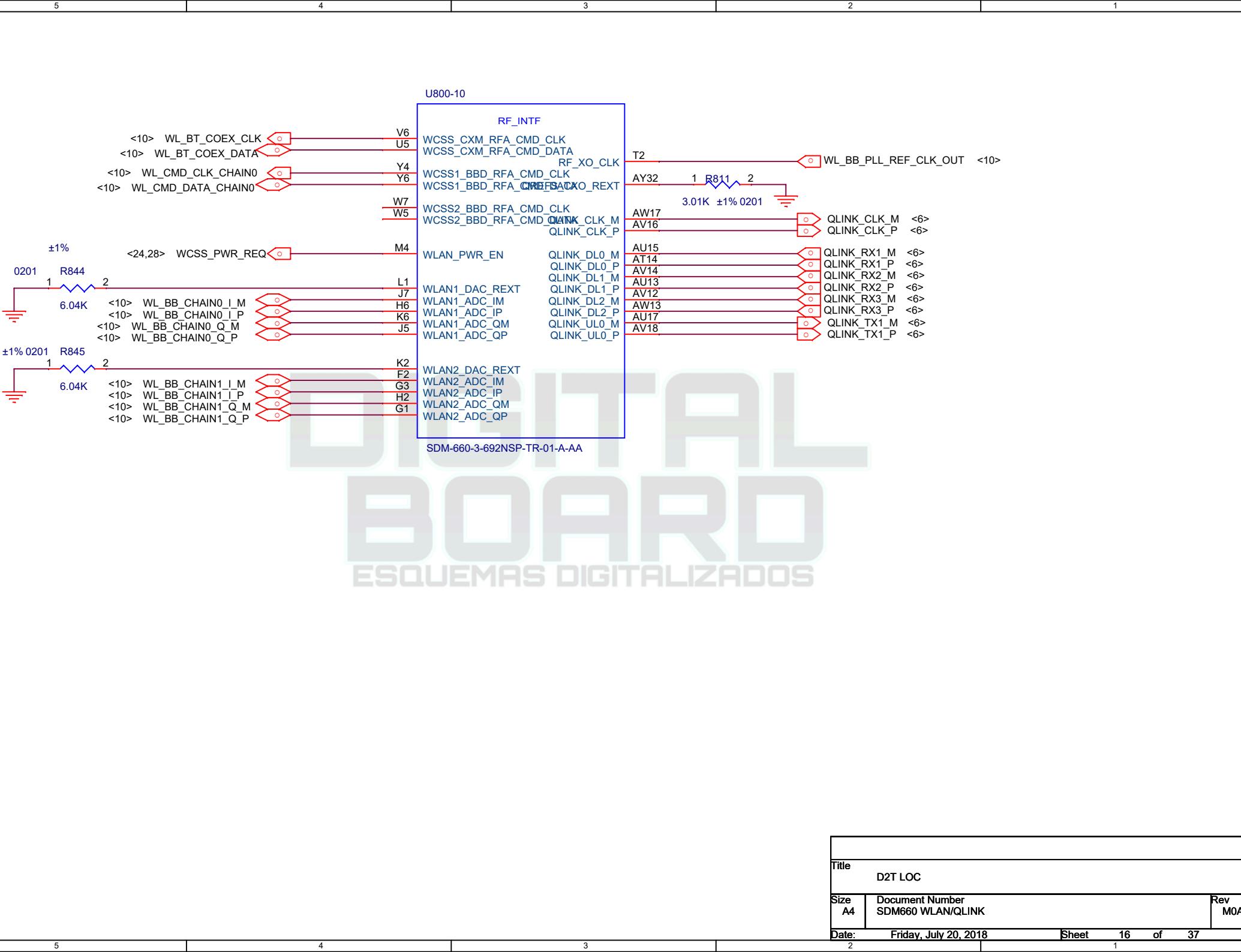


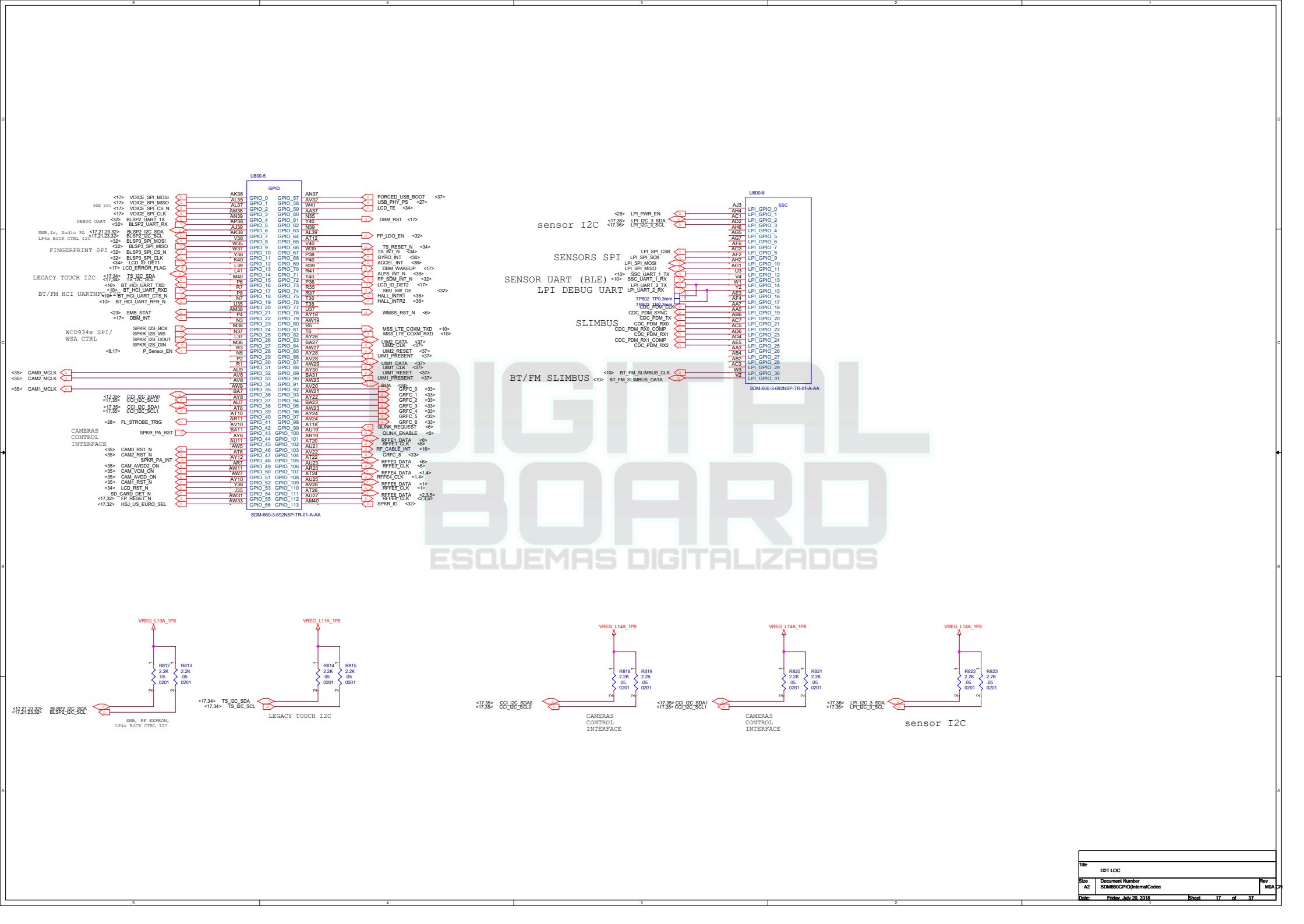


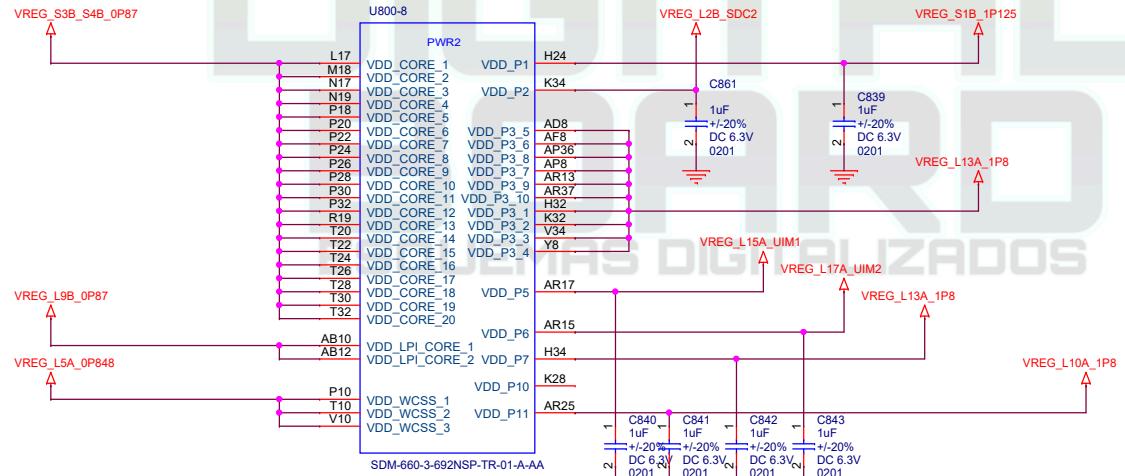
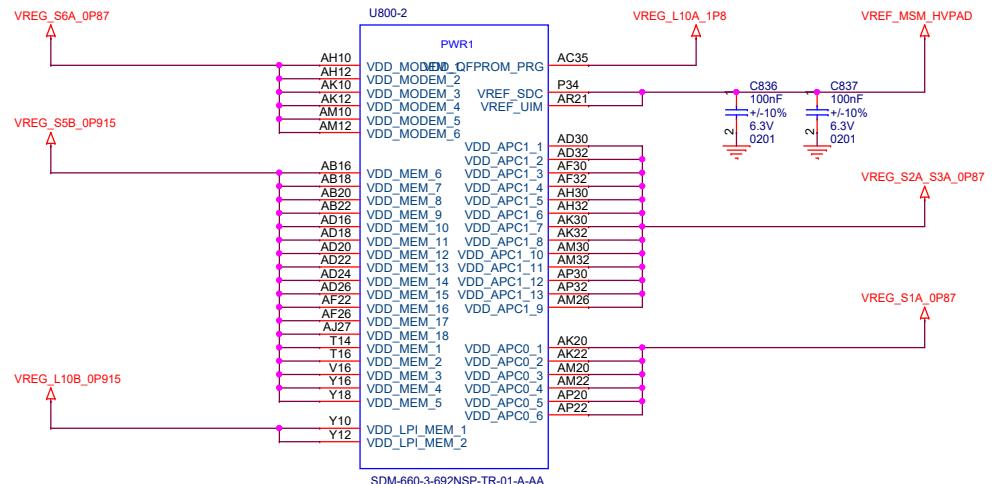
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Size A3	Document Number SDM660 Control
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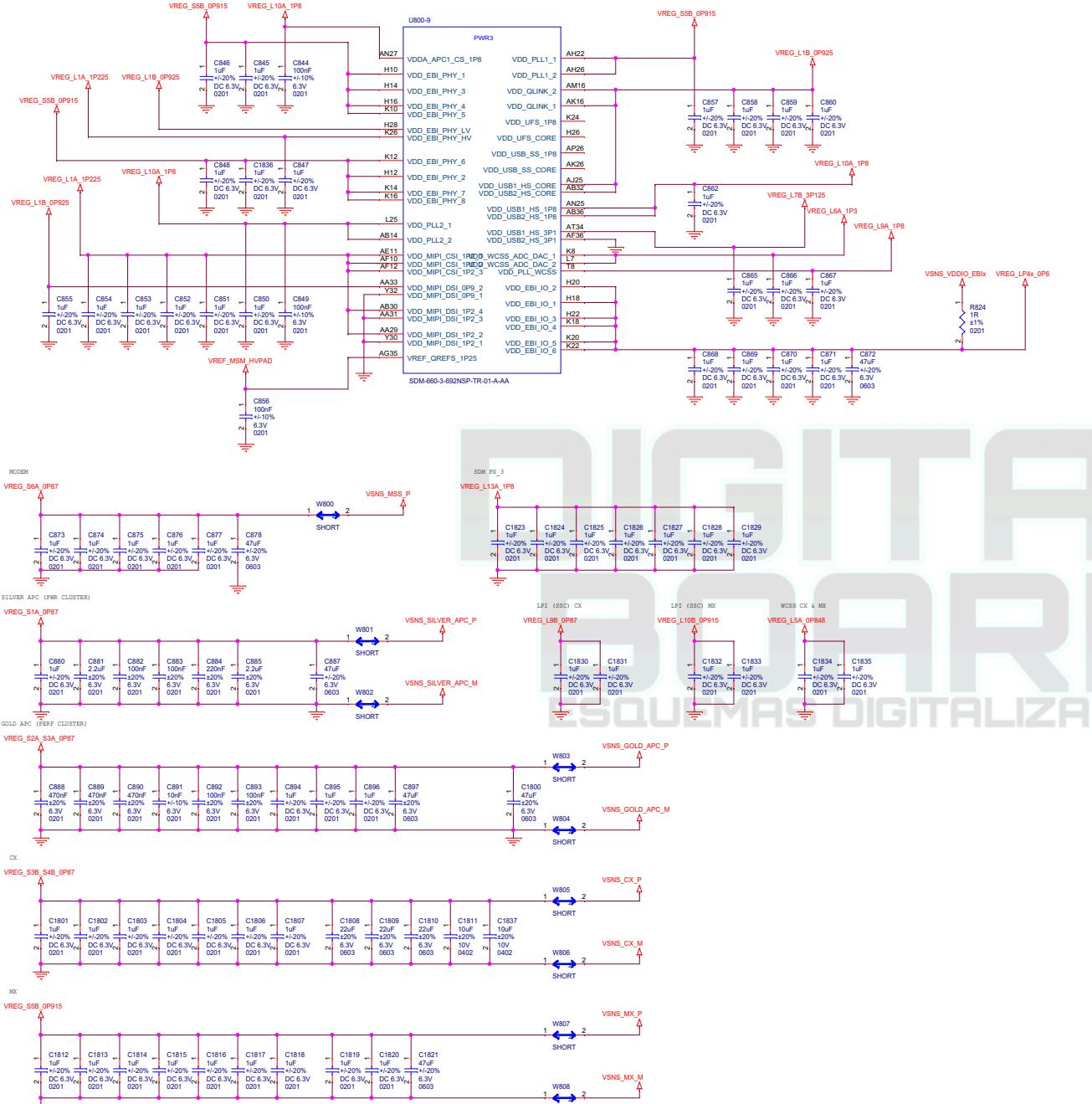


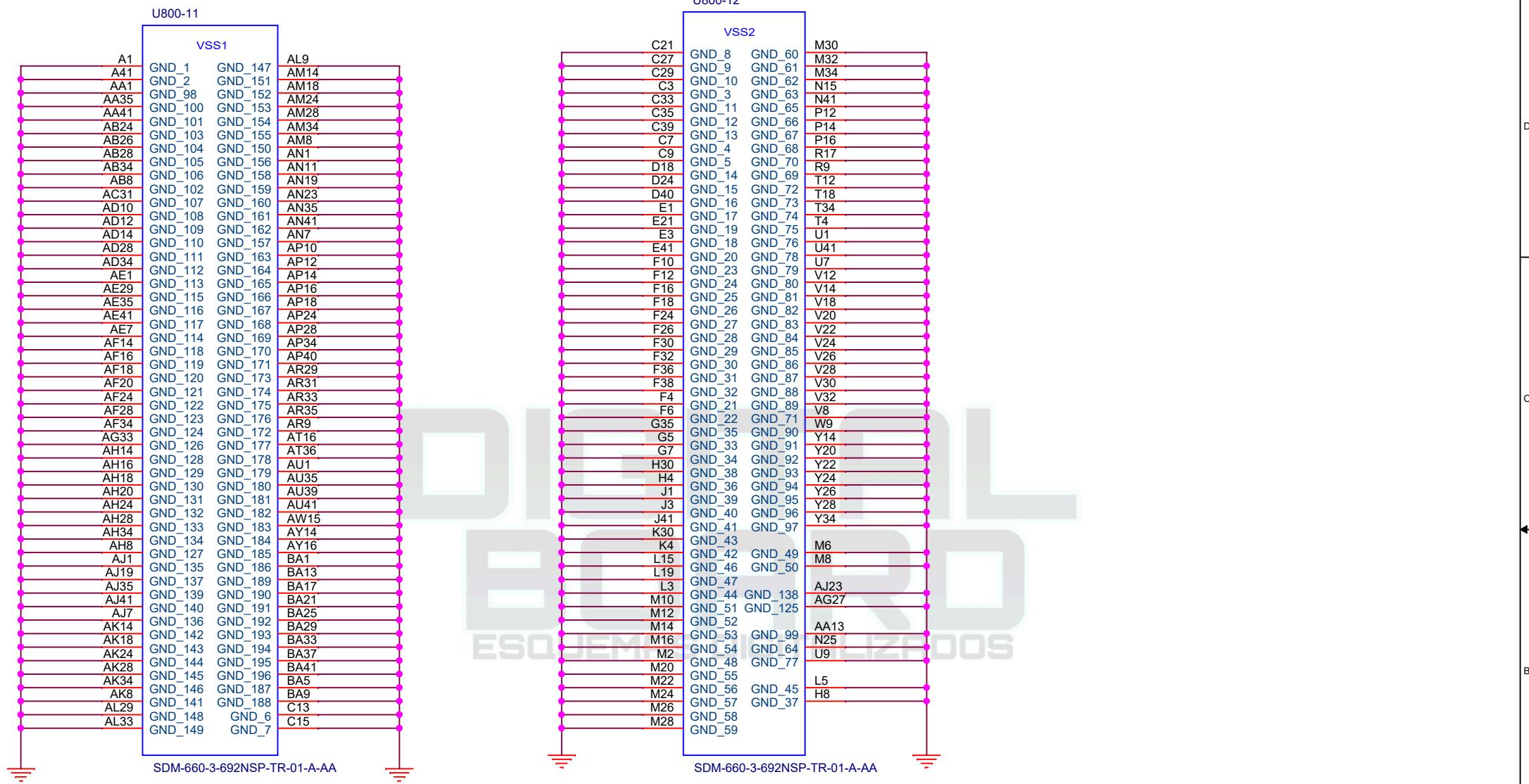






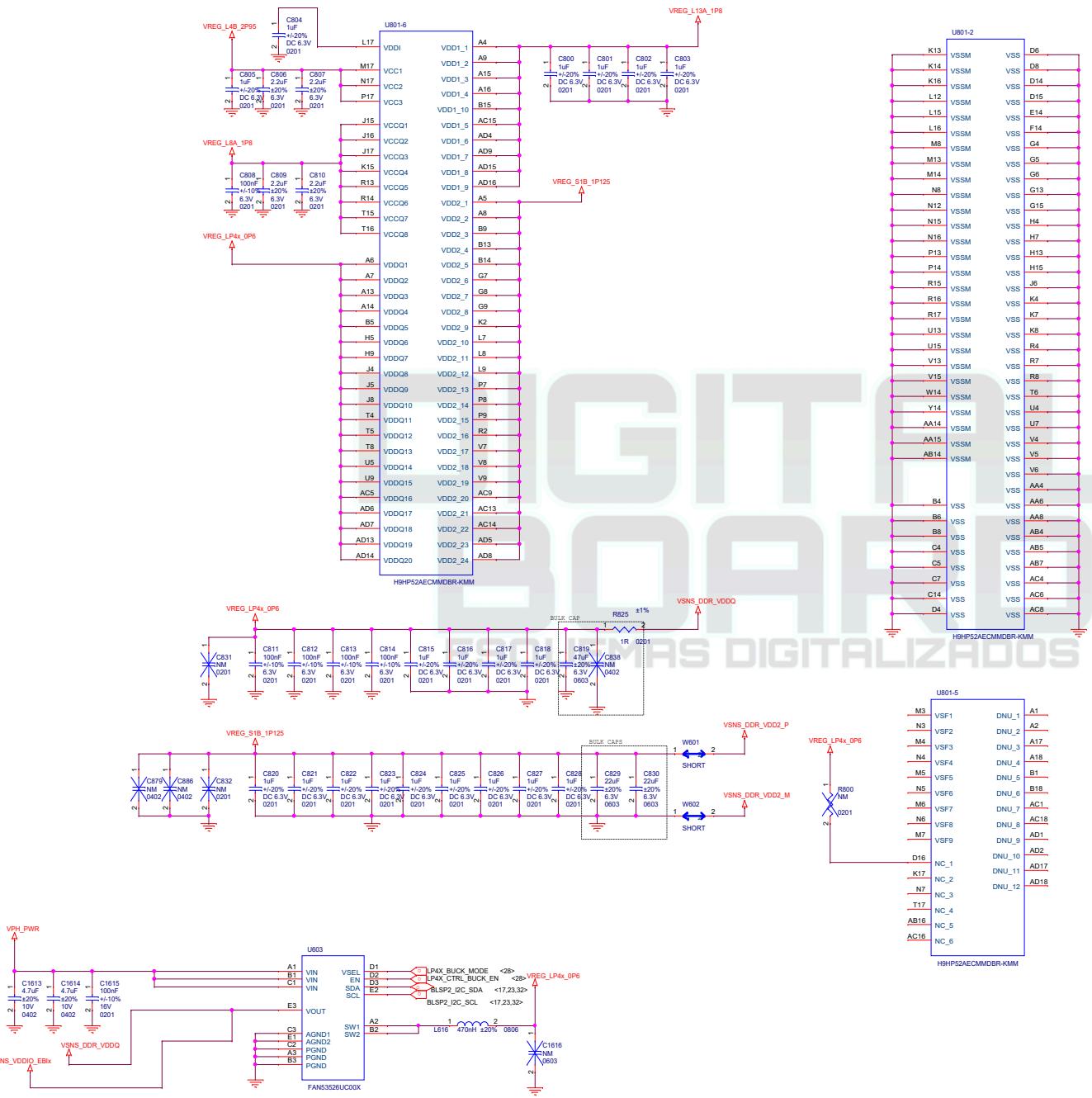
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Size	Document Number	Rev	
A3	SDM660 Power1	MOA/CN	
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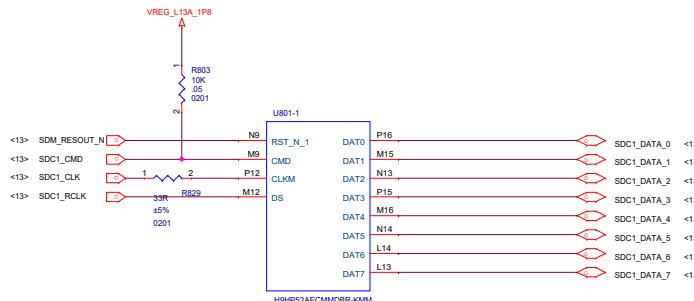




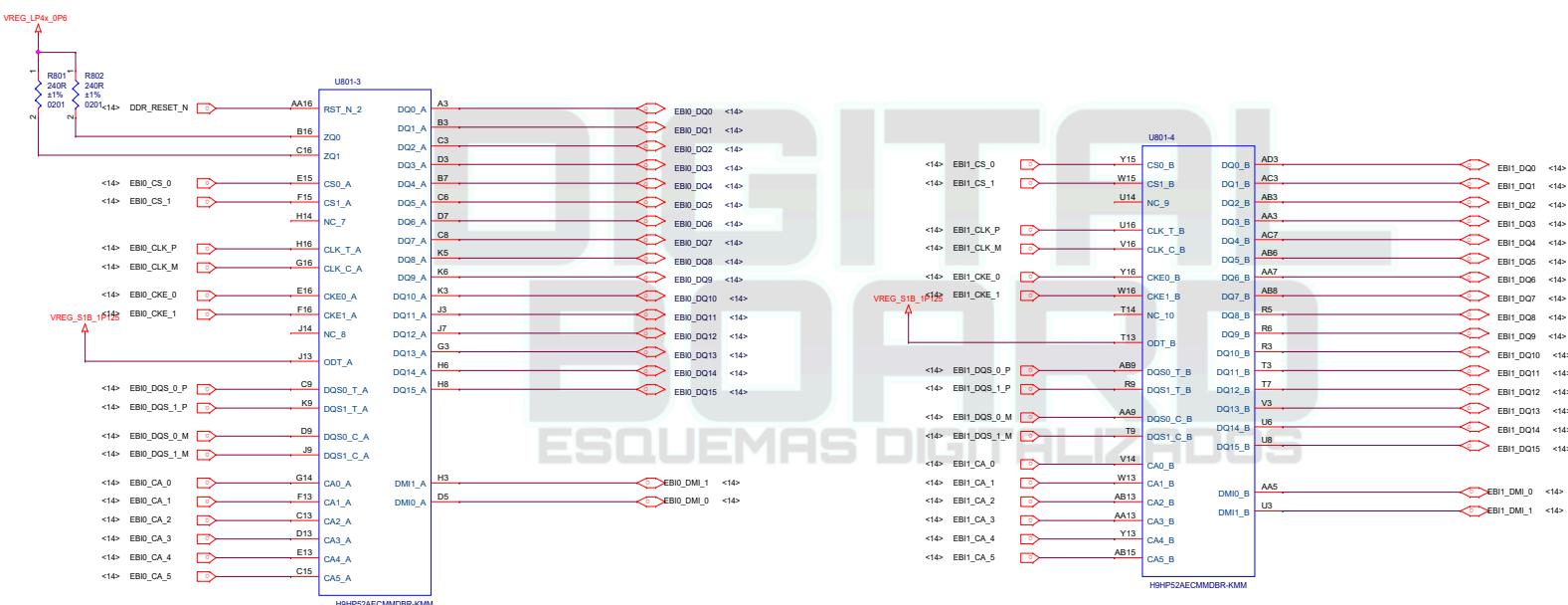
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Size A4	Document Number SDM660 Ground
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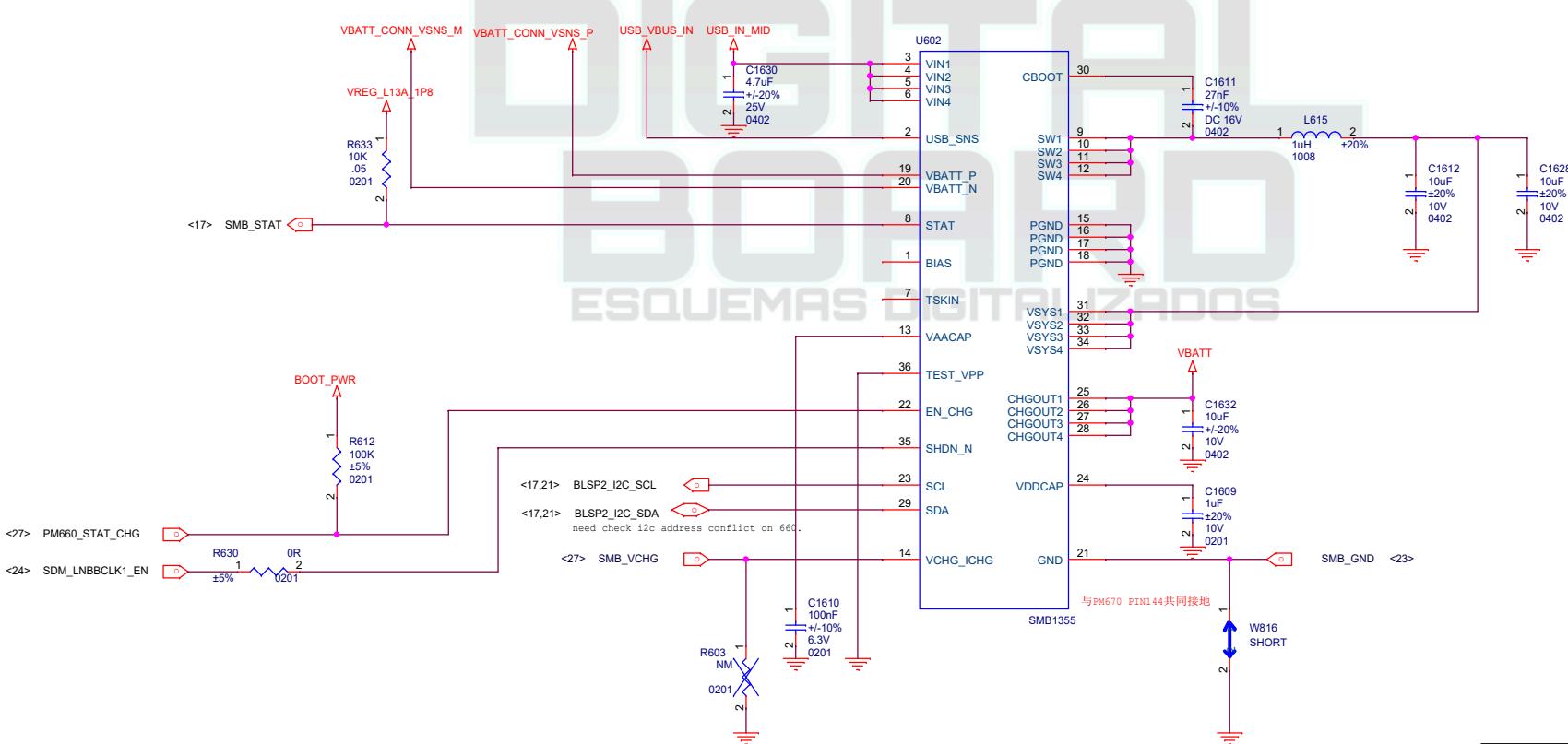
Date: Friday, July 20, 2018





## LPDDR4 / 4x





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A3	SMB1351	MO	ACN
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D

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C

C

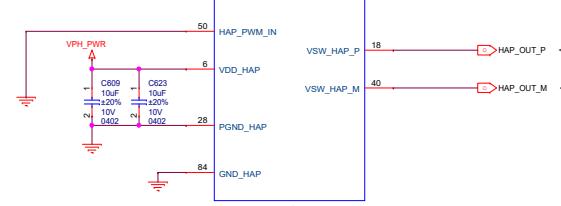
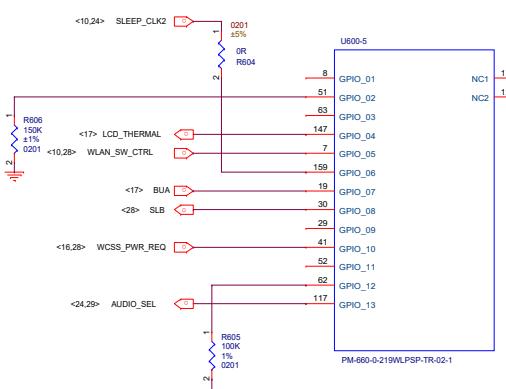
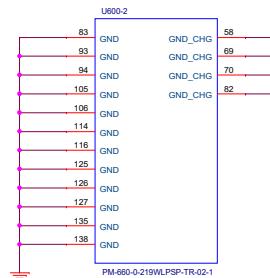
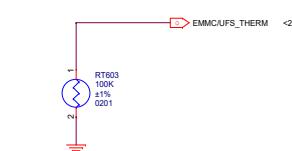
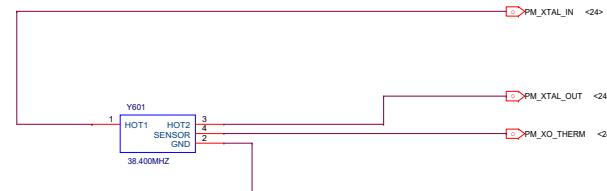
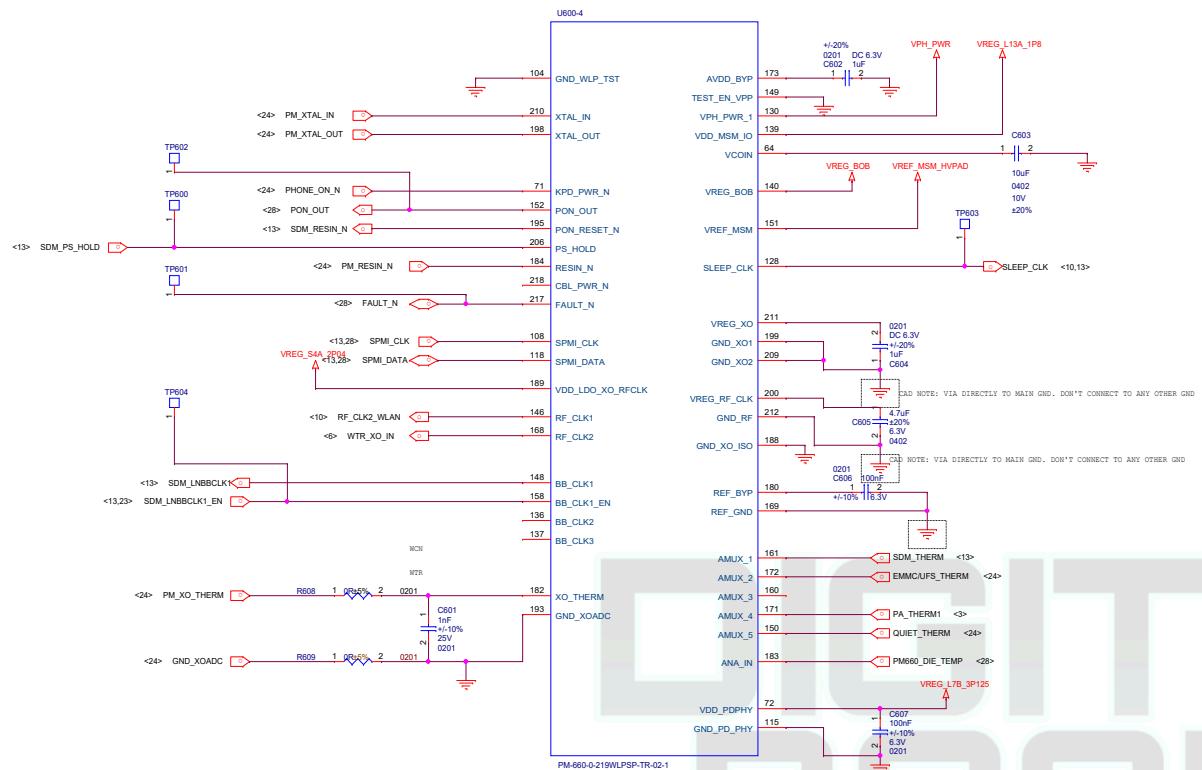
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B

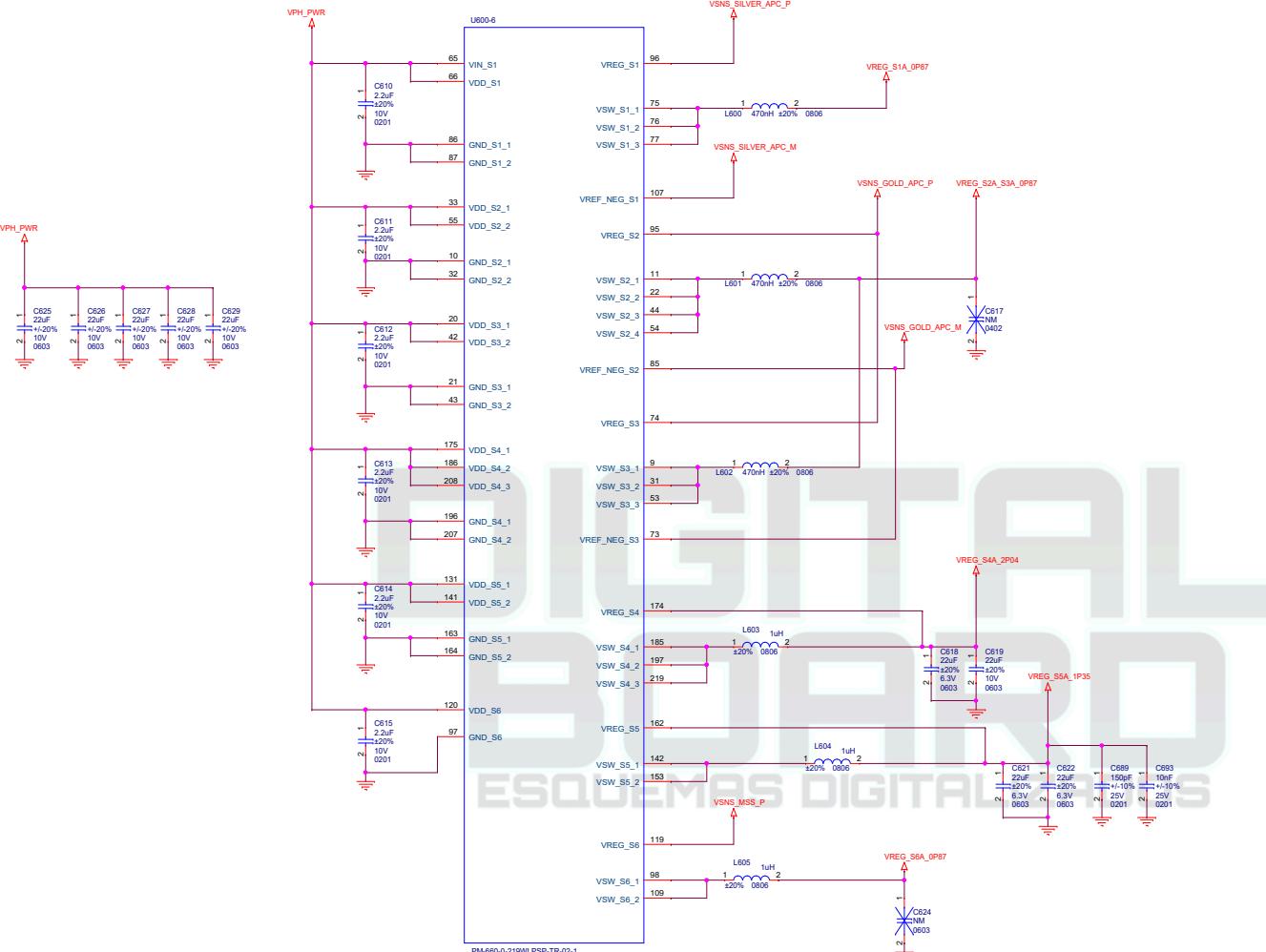
A

A

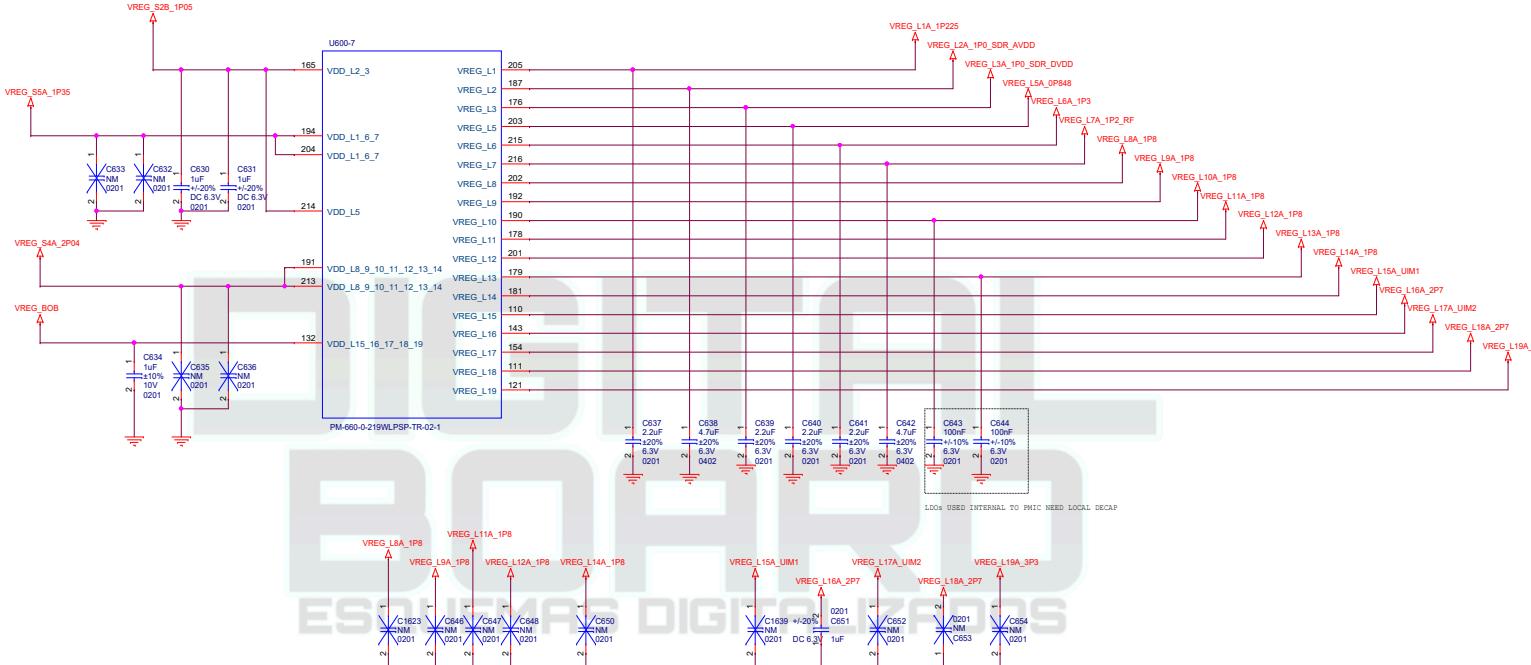
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D2T LOC	
Size A2	Document Number PM660 CLKxControl_GND
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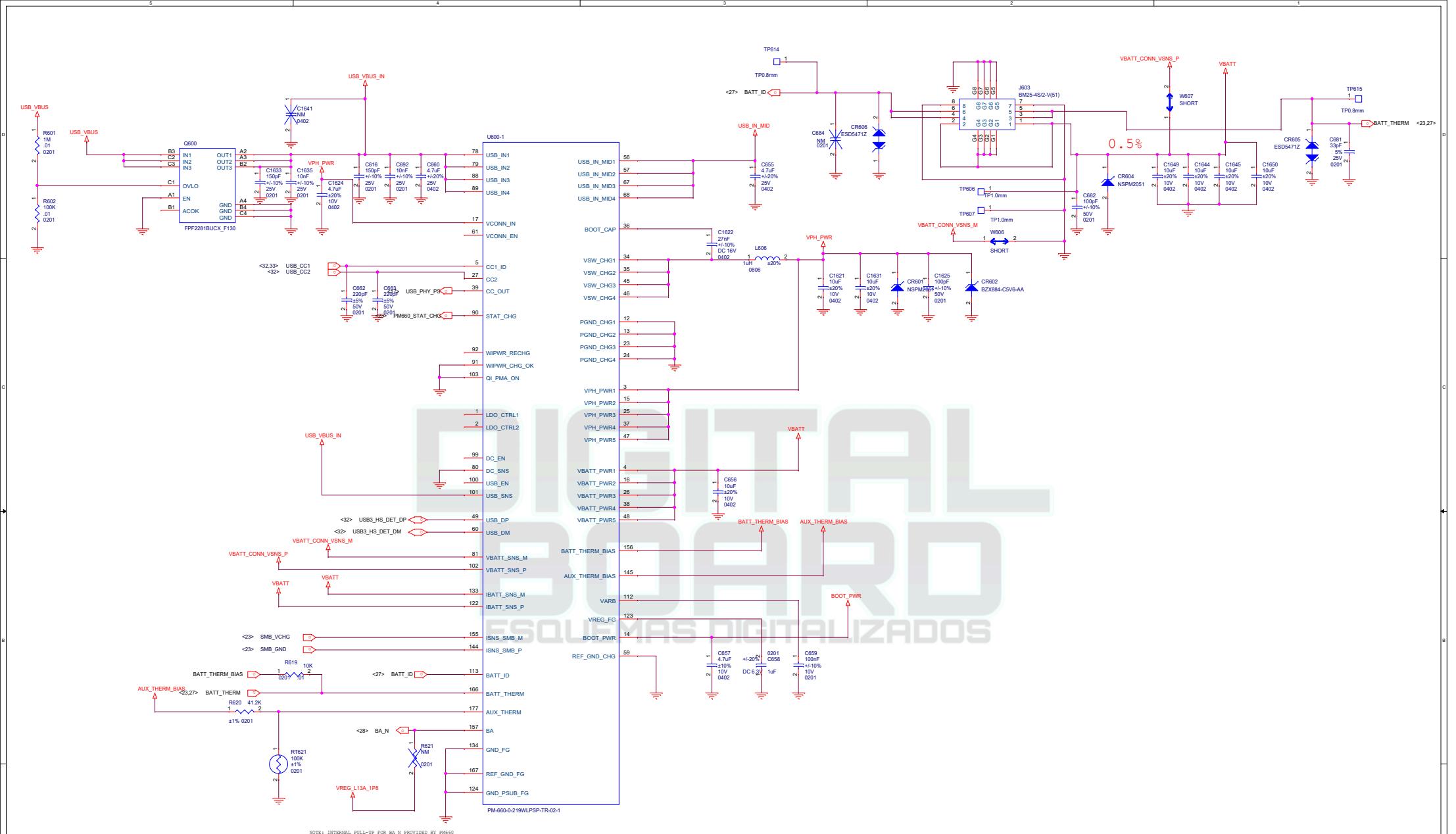


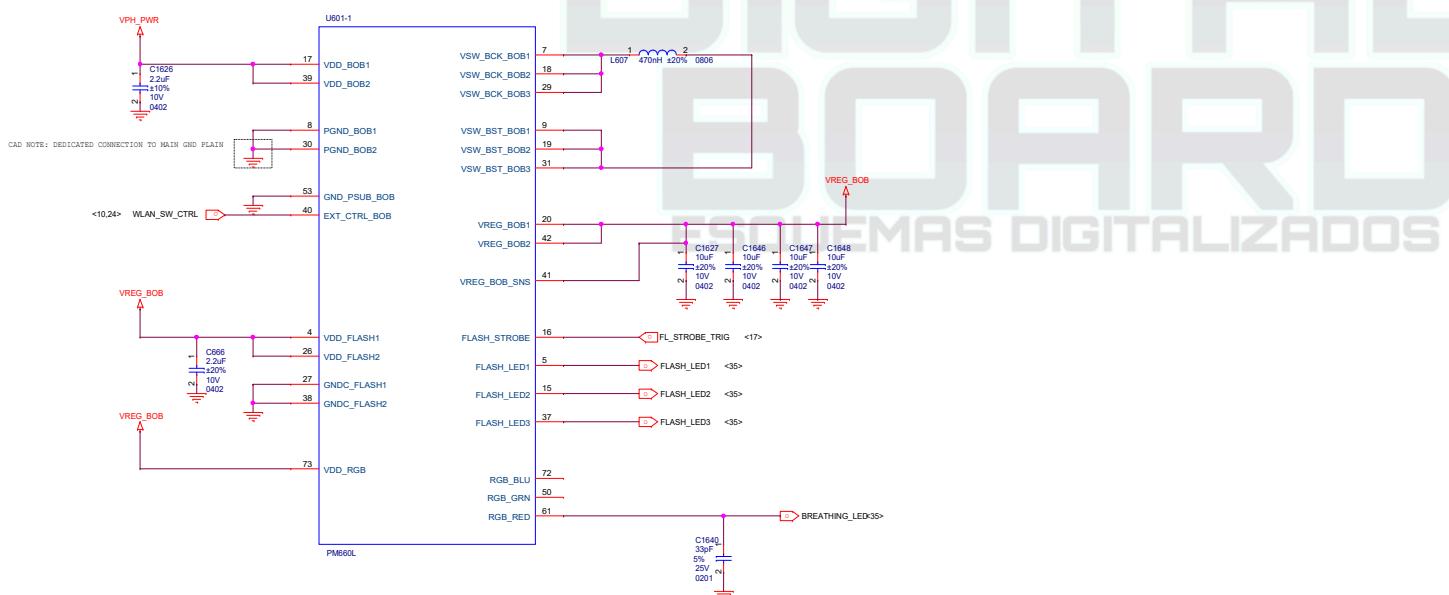
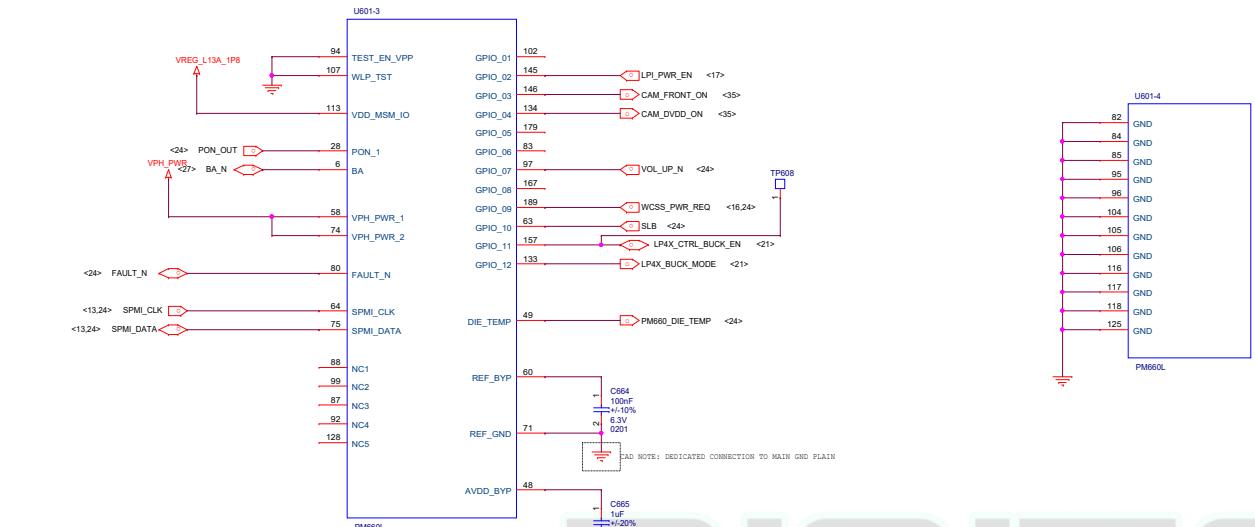
Title		D2T LOC
Size	Document Number	PM660 Buck Converter
A2		
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PSEUDO CAPLESS LDOs  
For CAPLESS LDOs: If decaps on the load side do not  
add up to LDO spec, then install the cap close to the PMIC

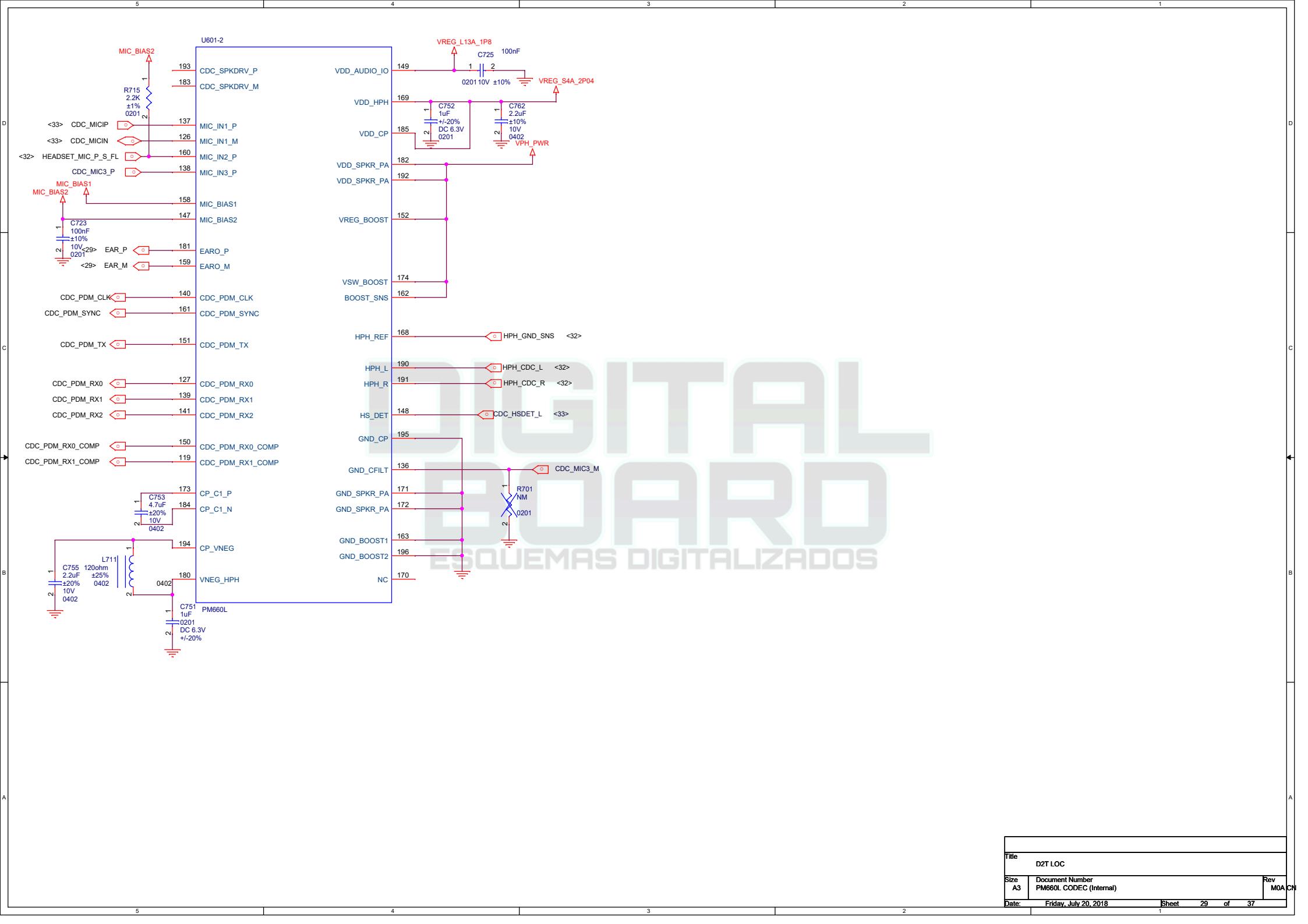
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A2	Document Number	PM660 LDO Circuits
Date:	Friday, July 20, 2018	Rev M0A
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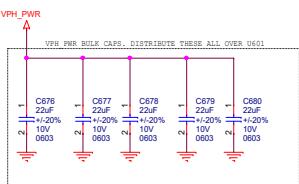
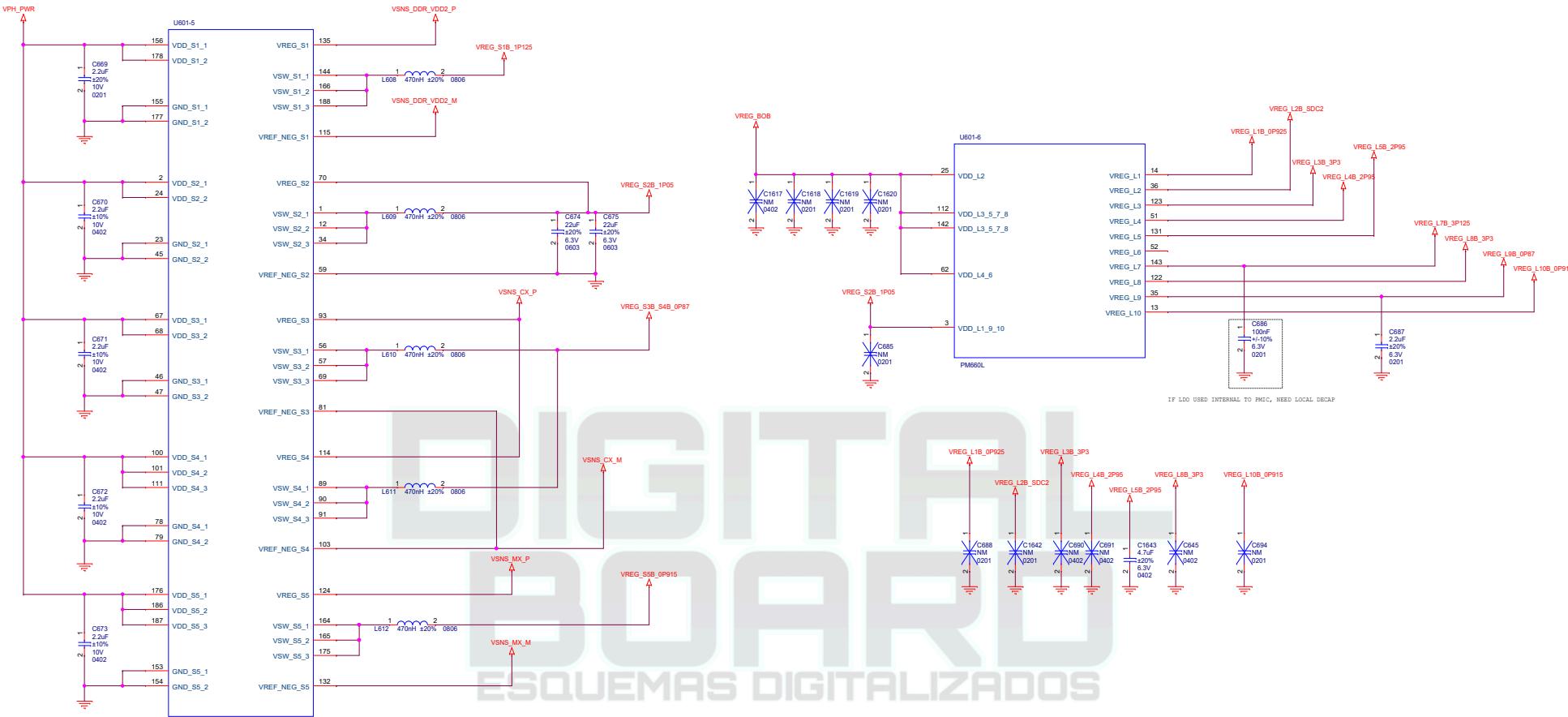


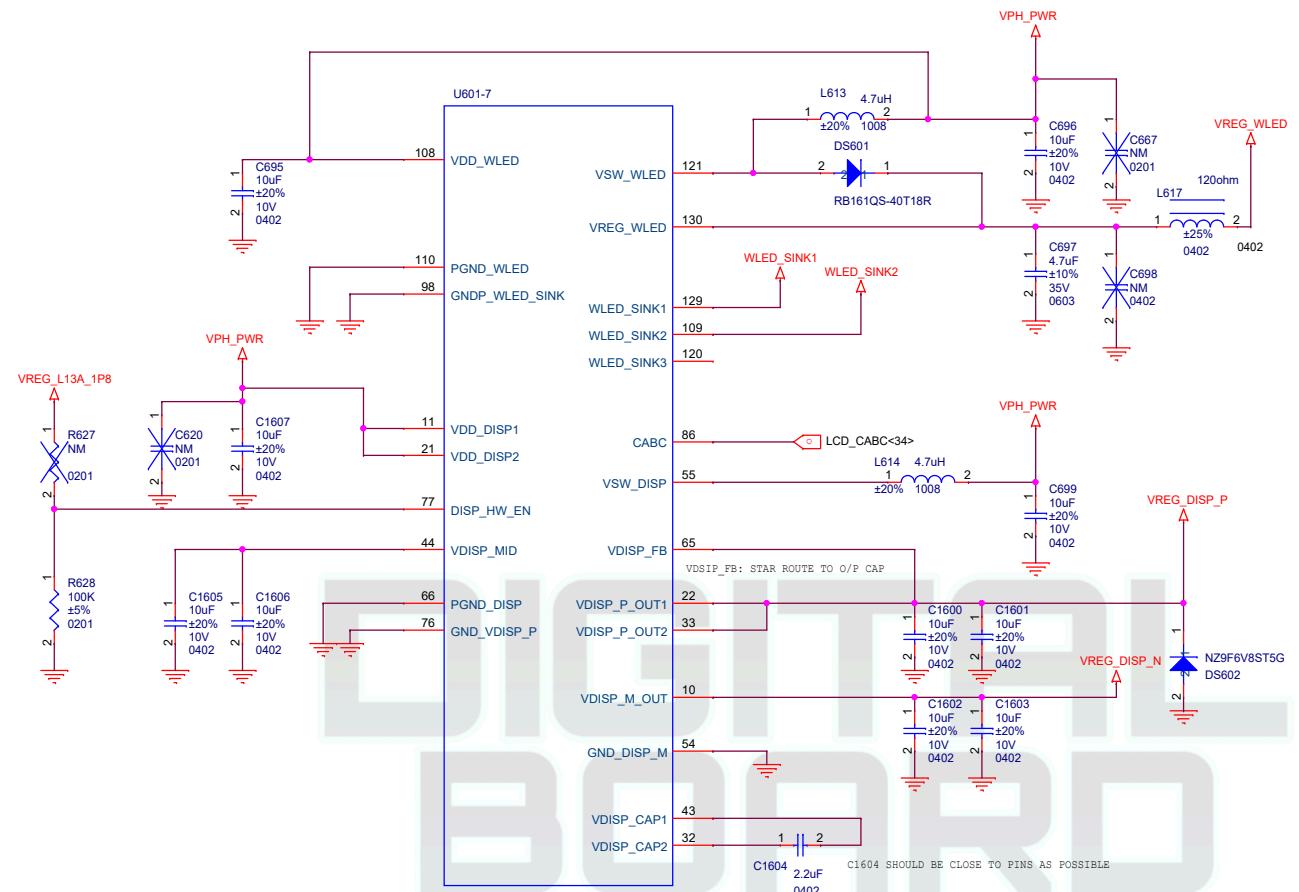


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Size	Document Number
A2	PM660L_ConfAndBobFlash

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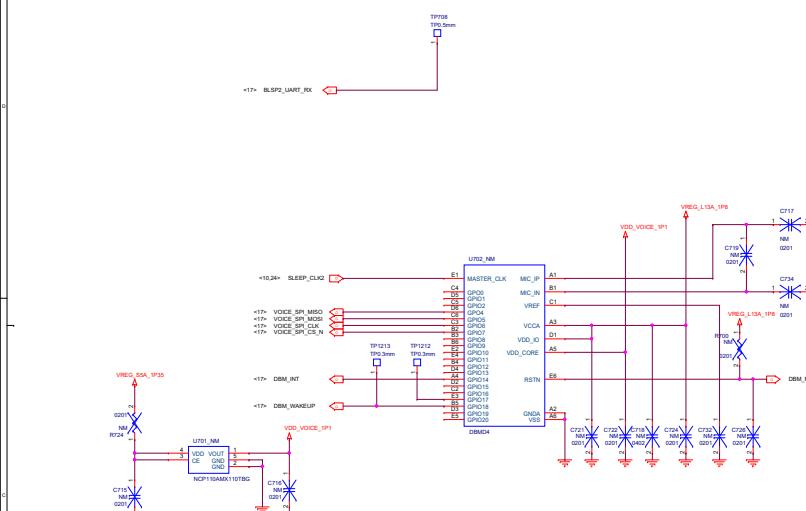




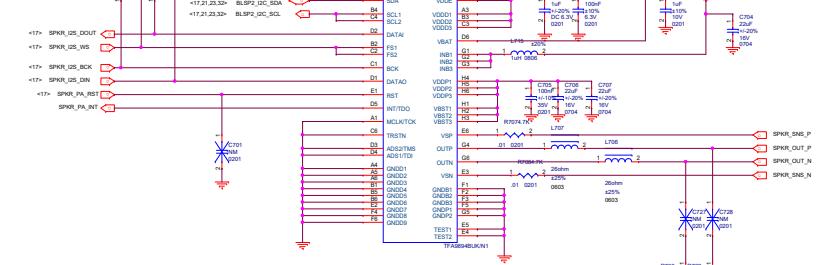


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Size A3	Document Number PM660L.Display (LCD)	Date Friday, July 20, 2018
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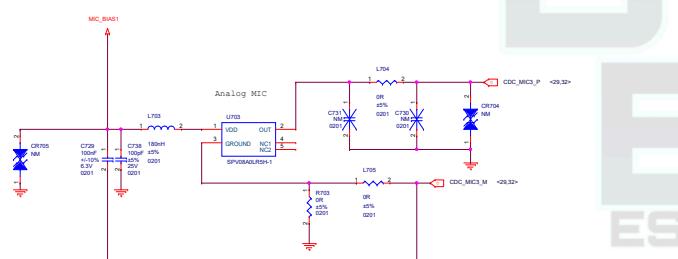
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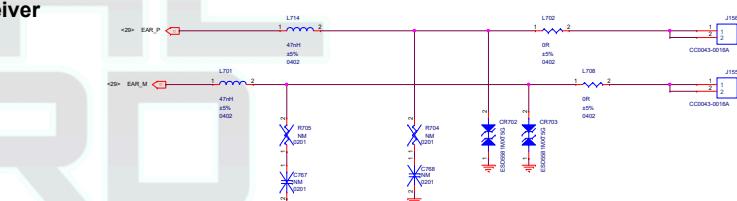
# Speaker PA



# AUX Mic



# Receiver



# Bottom fpc Connector

