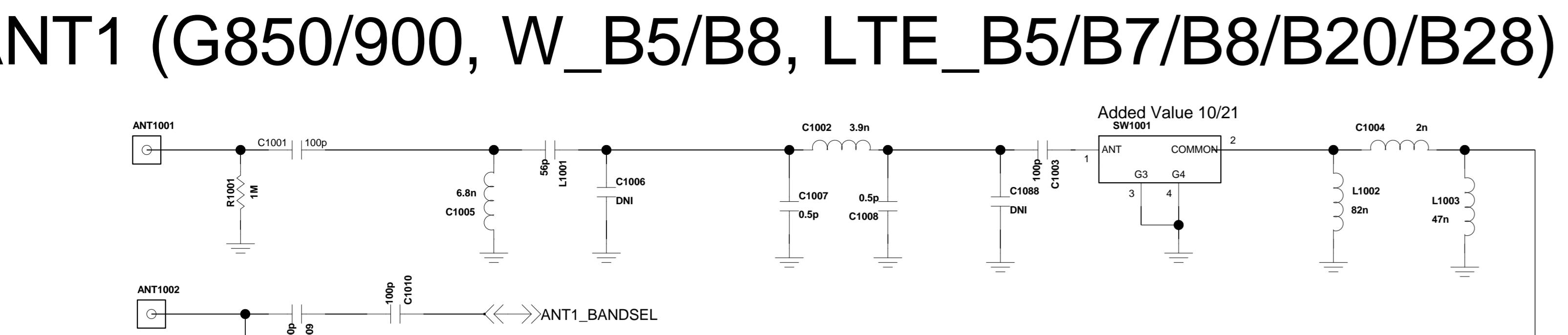


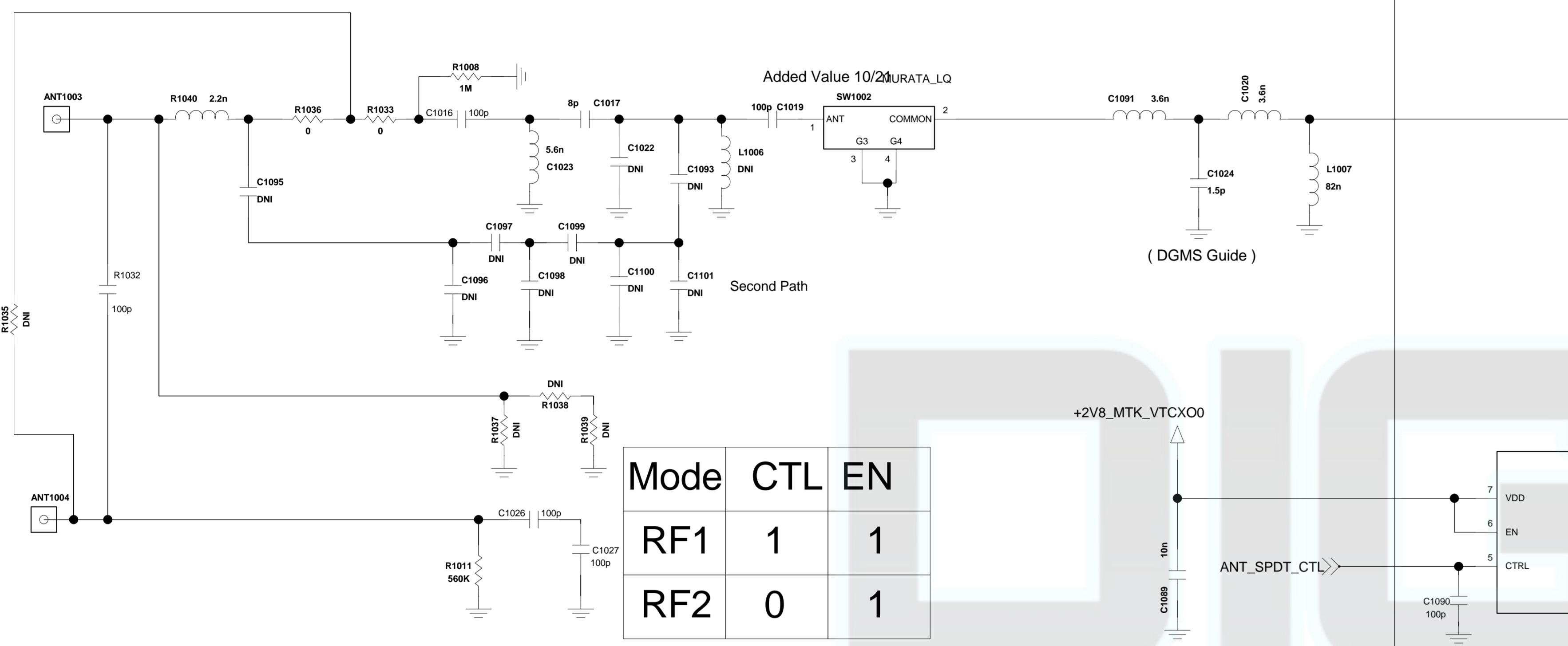
DIGITAL BOARD

ESQUEMAS DIGITALIZADOS

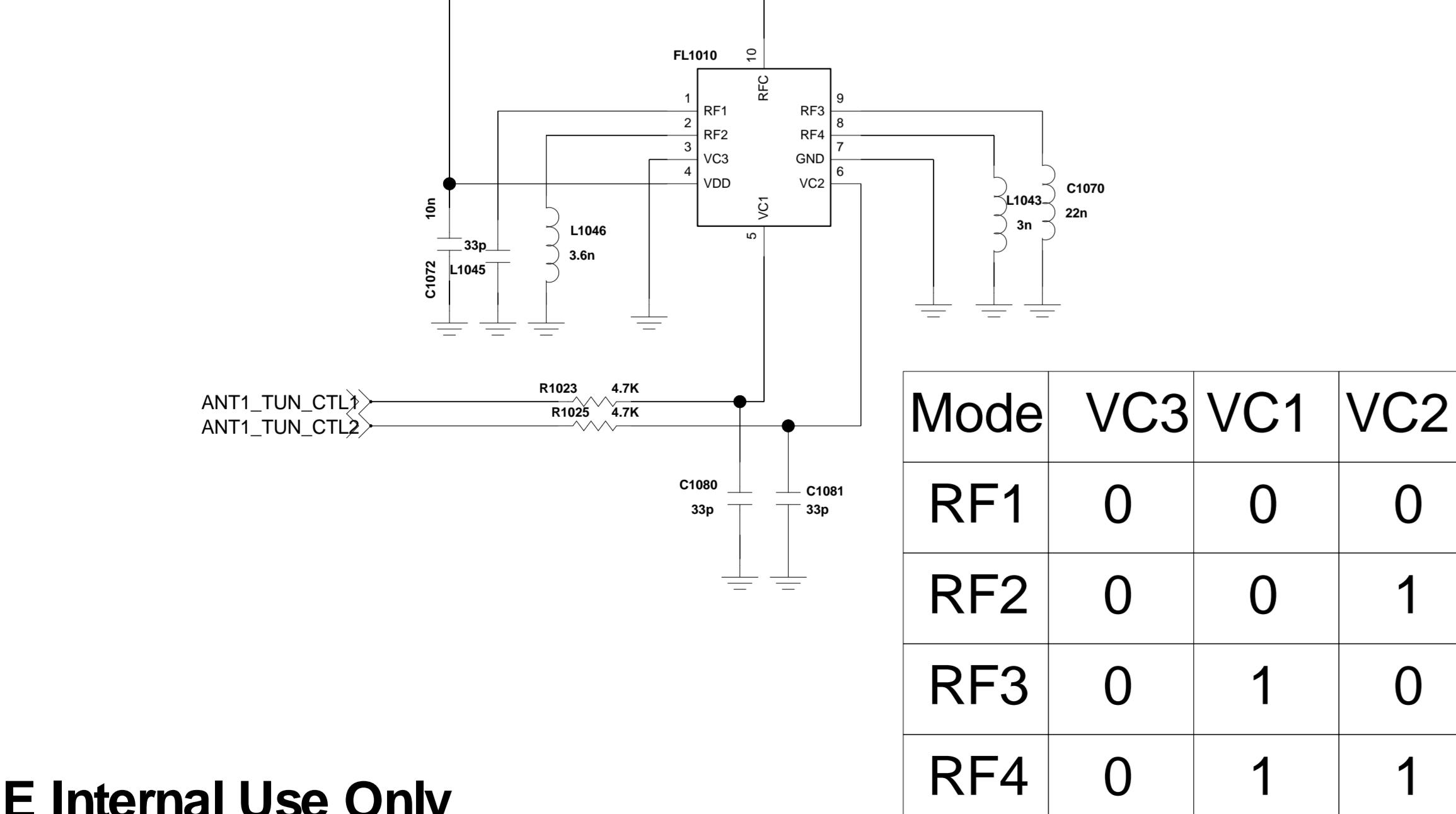
ESQUEMA ELETTRICO



ANT1 (G850/900, W_B5/B8, LTE_B5/B7/B8/B20/B28)



ANT1 TUNER

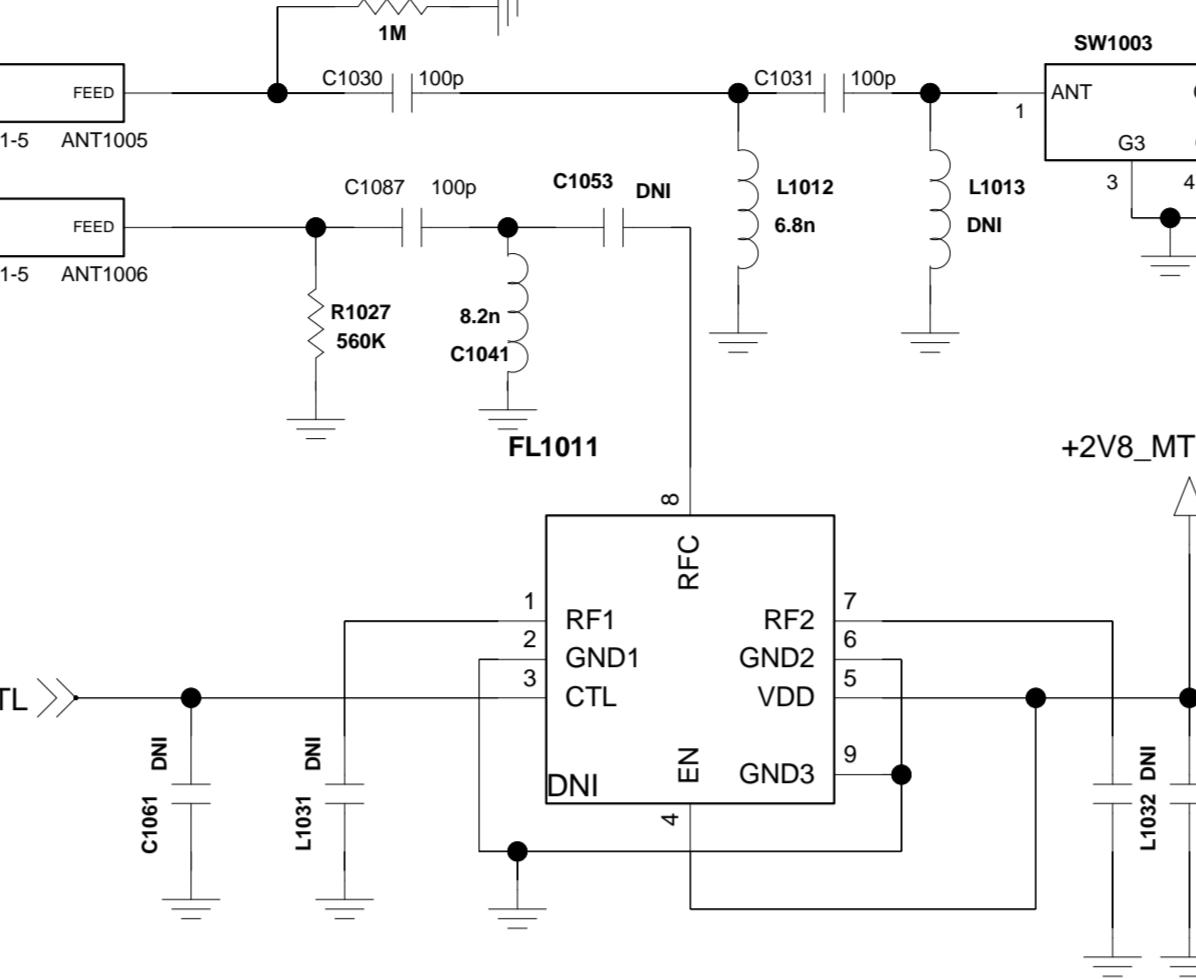


LGE Internal Use Only

ANT6 (B5/B8/B17/B20/B28 2nd)

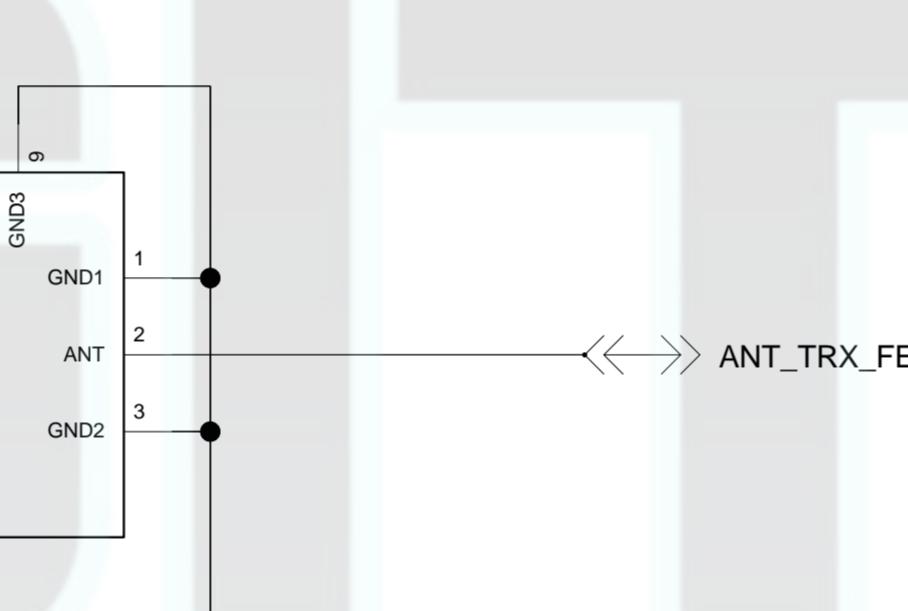


(DGMS Guide)
MC-C00002-14 : ESD Protection (

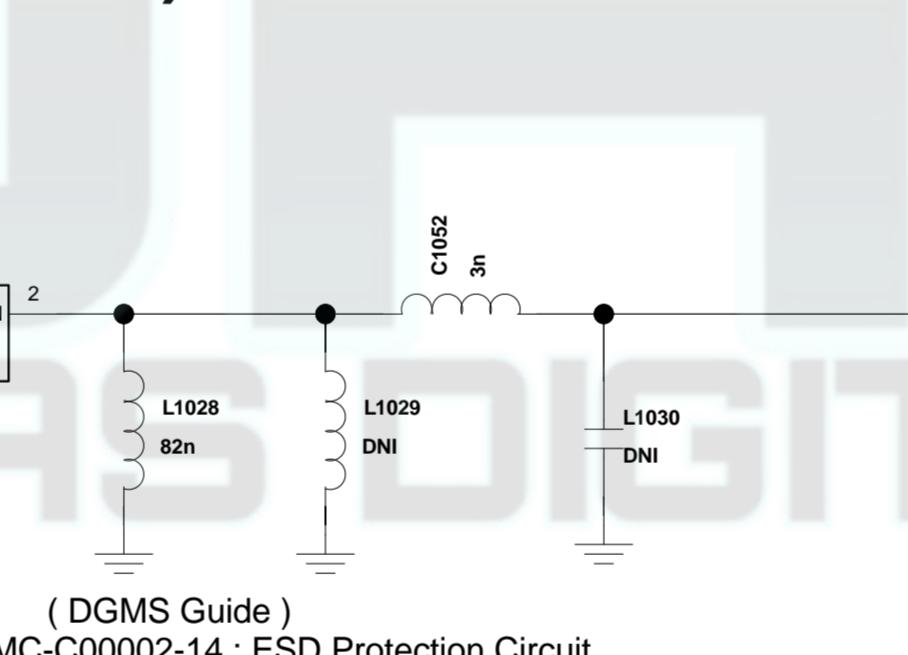


ANT6 TUNE

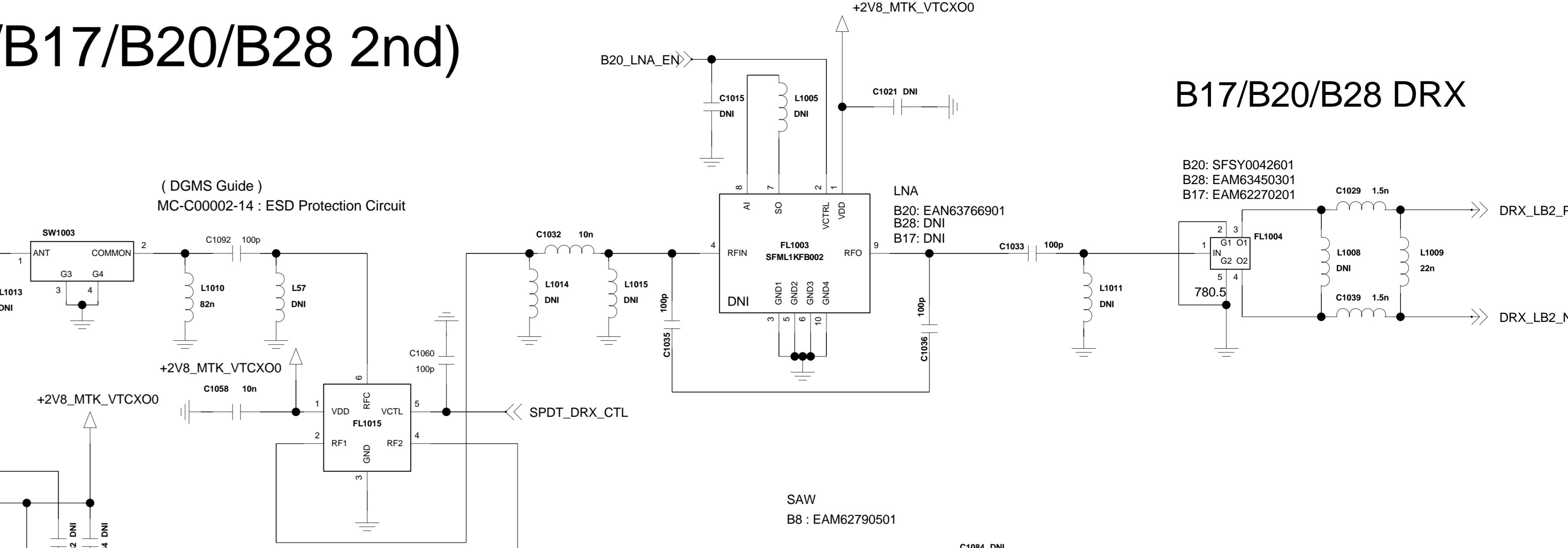
Mode	EN	CT
RF1	1	0
RF2	1	1



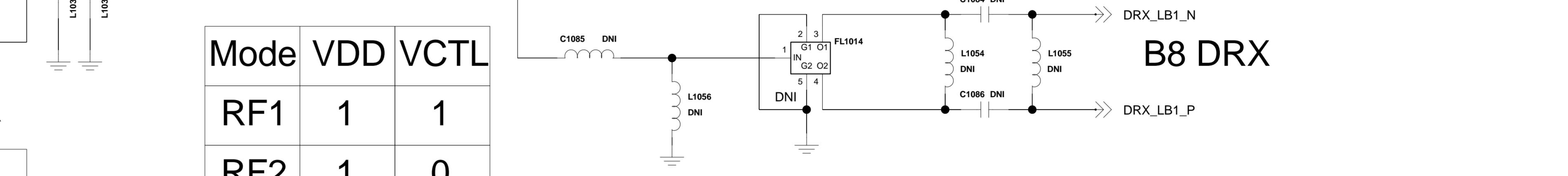
ANT5 (B1/B2/B3/B4/B7 2nd)



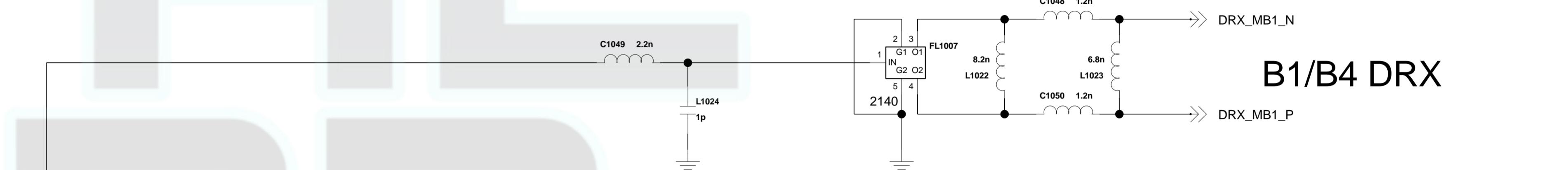
(DGMS Guide)
MC-C00002-14 : ESD Protection



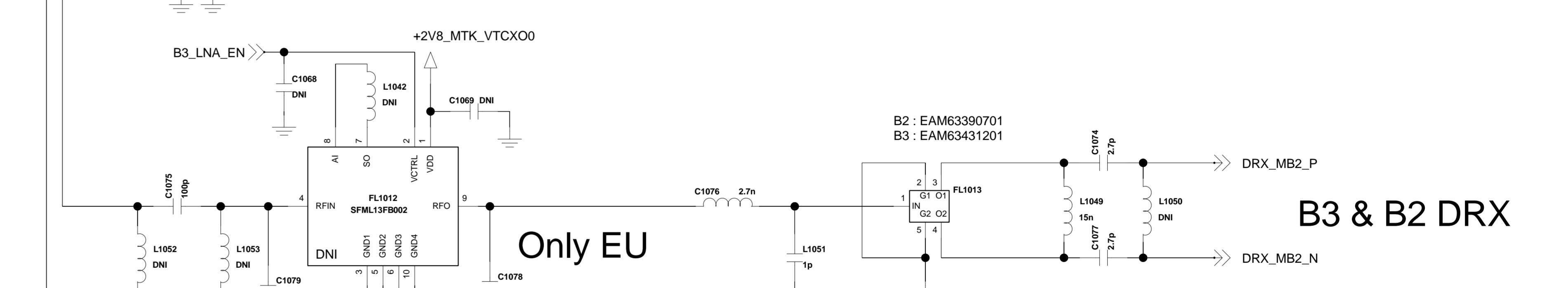
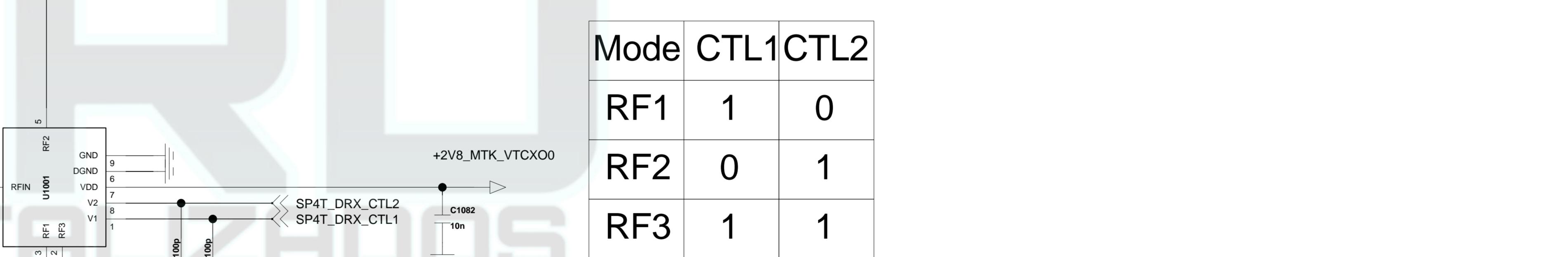
B17/B20/B28 DRX



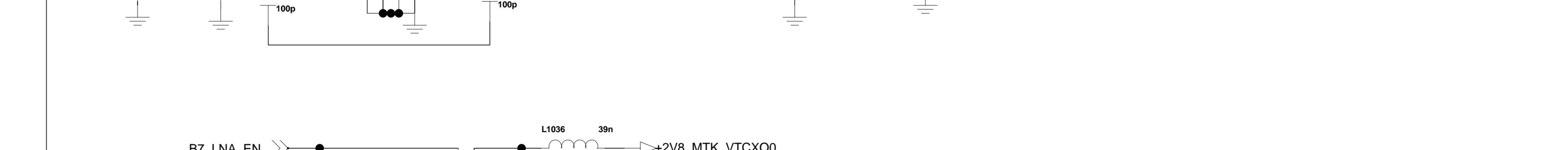
B8 DRX



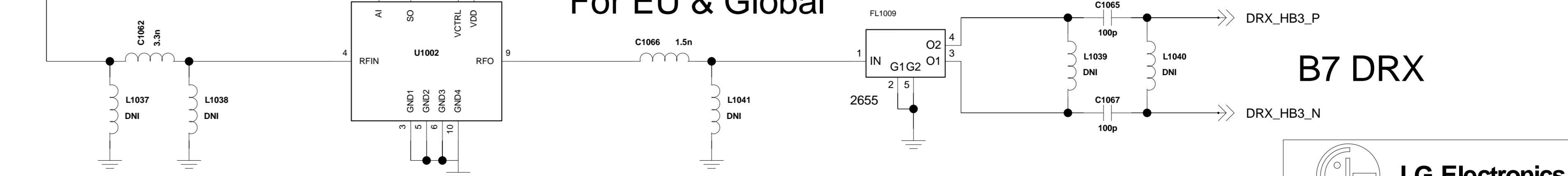
B1/B4 DRX



by EU



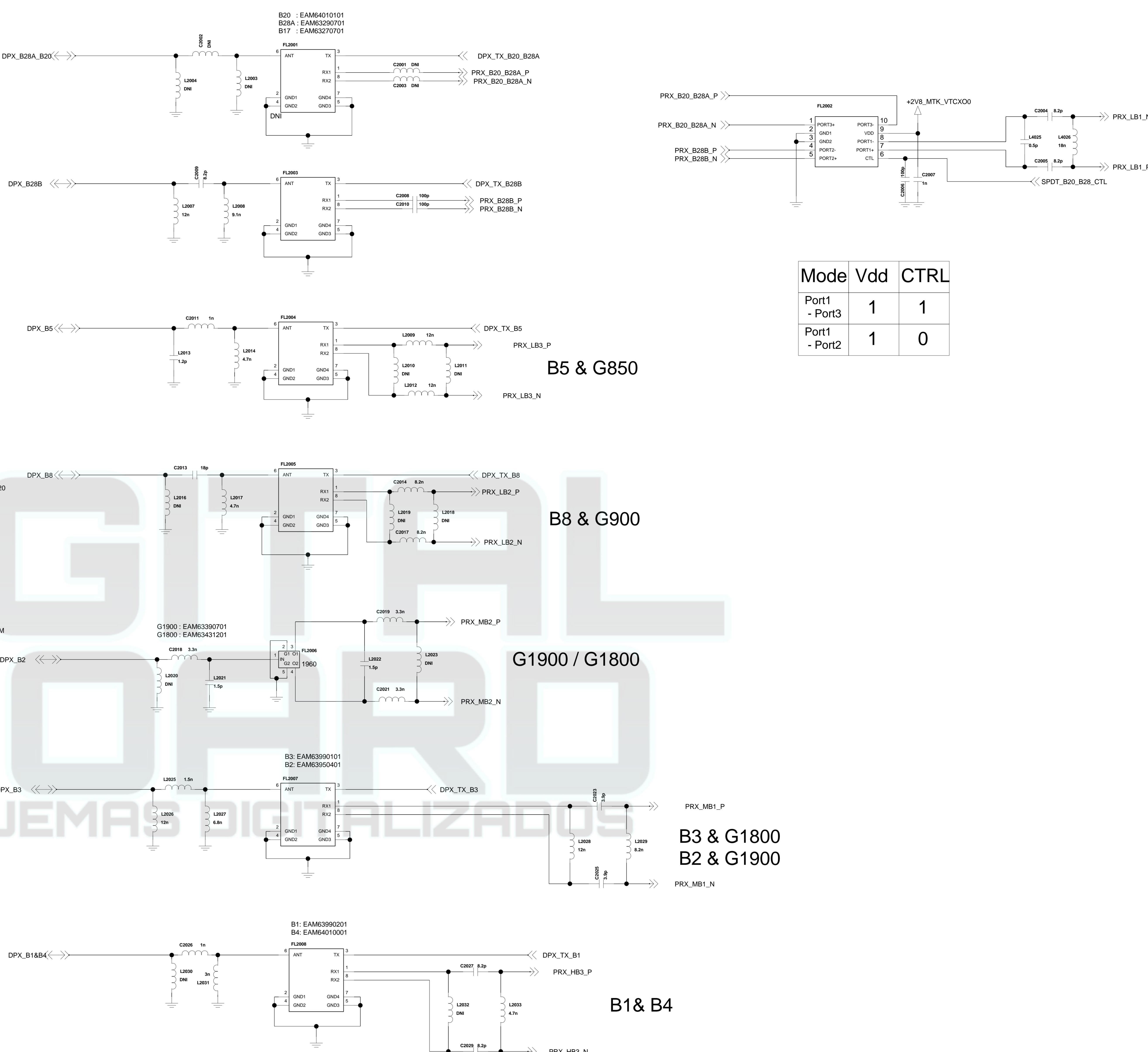
For EU & Global



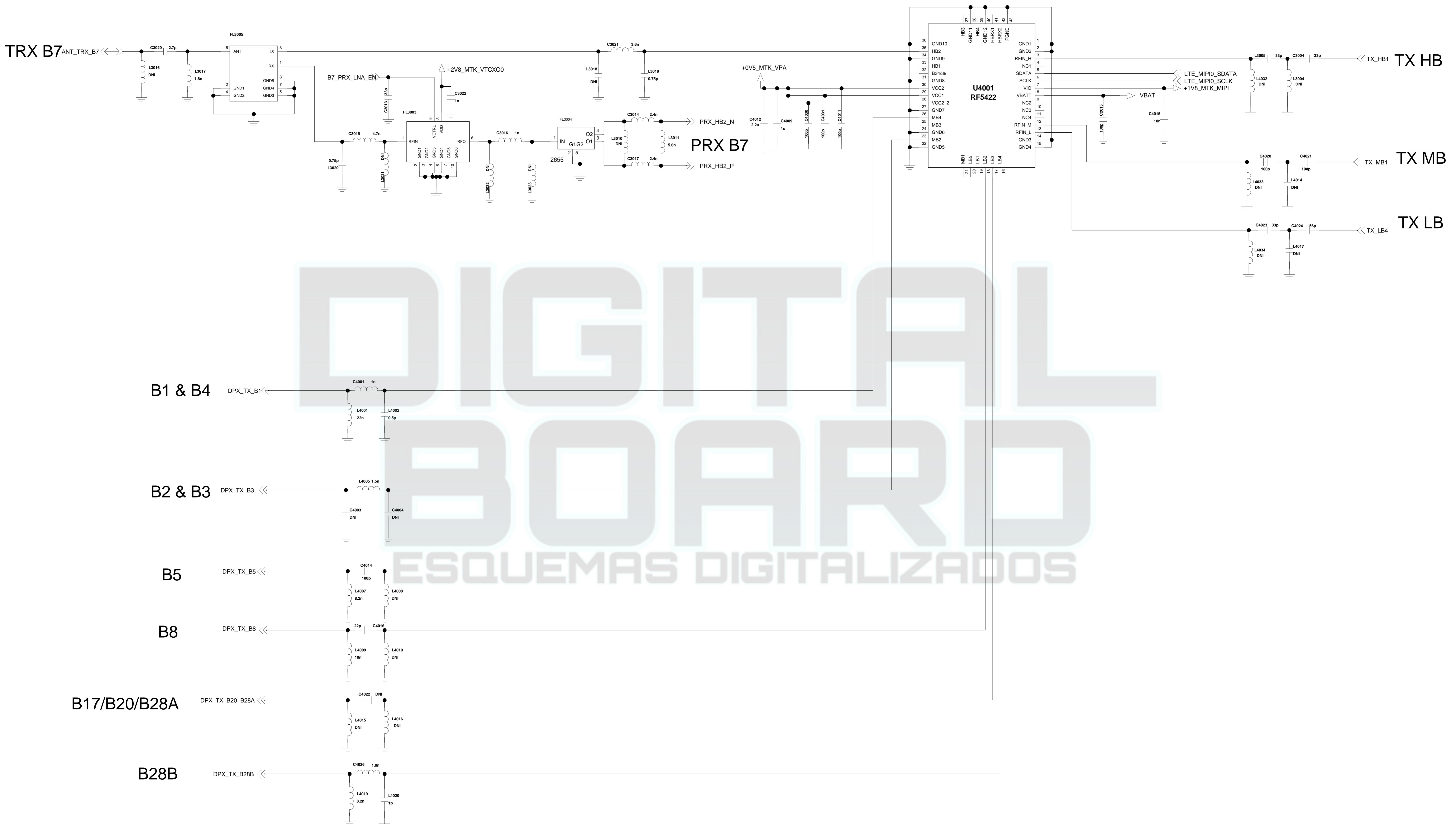
B7 DRX

16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1

< 1-x-2-1_ASM > Rev_1.0



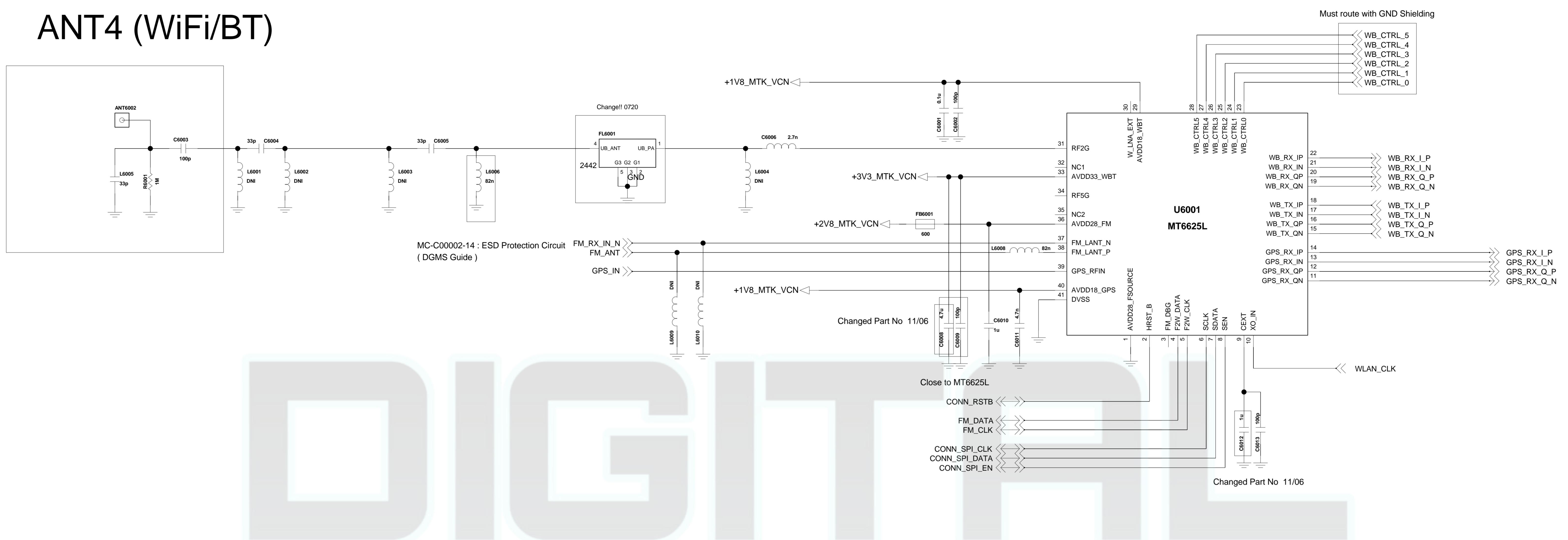
<HCPA_SKY77643>



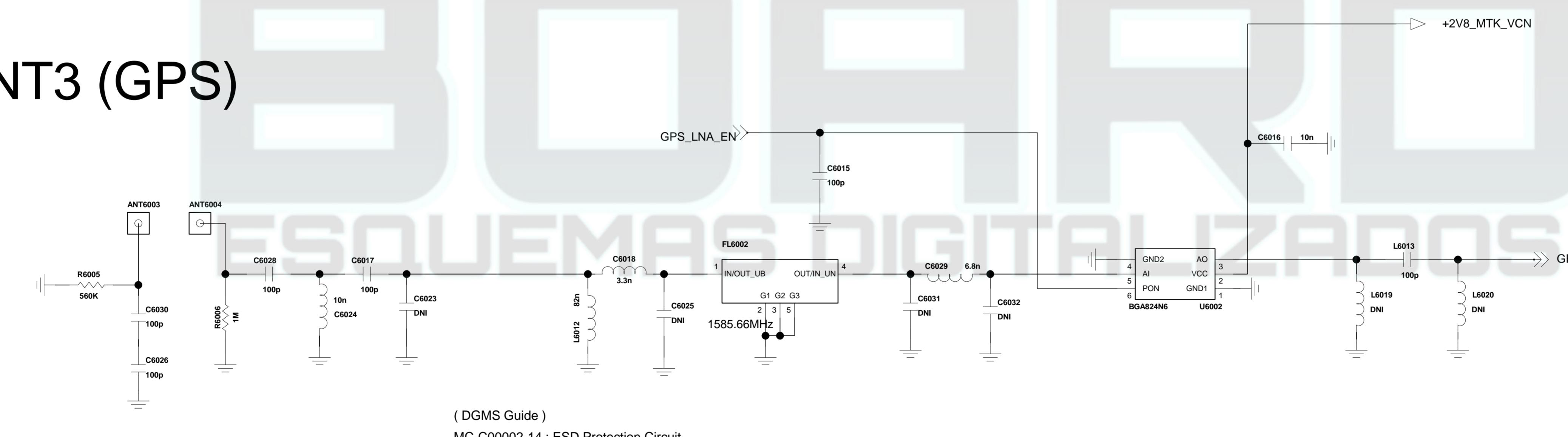
<5-1-1-18_MT6625L_WiFi_BT_GPS_FM>

Rev_0.3

ANT4 (WiFi/BT)



ANT3 (GPS)

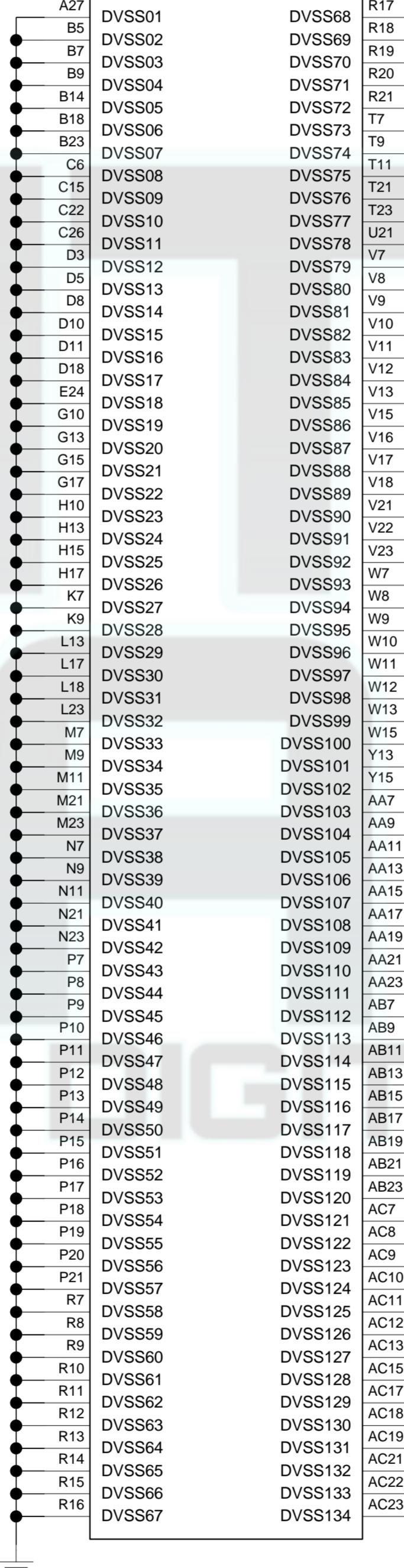


< MT6735M_GND >

U2100
MT6735M

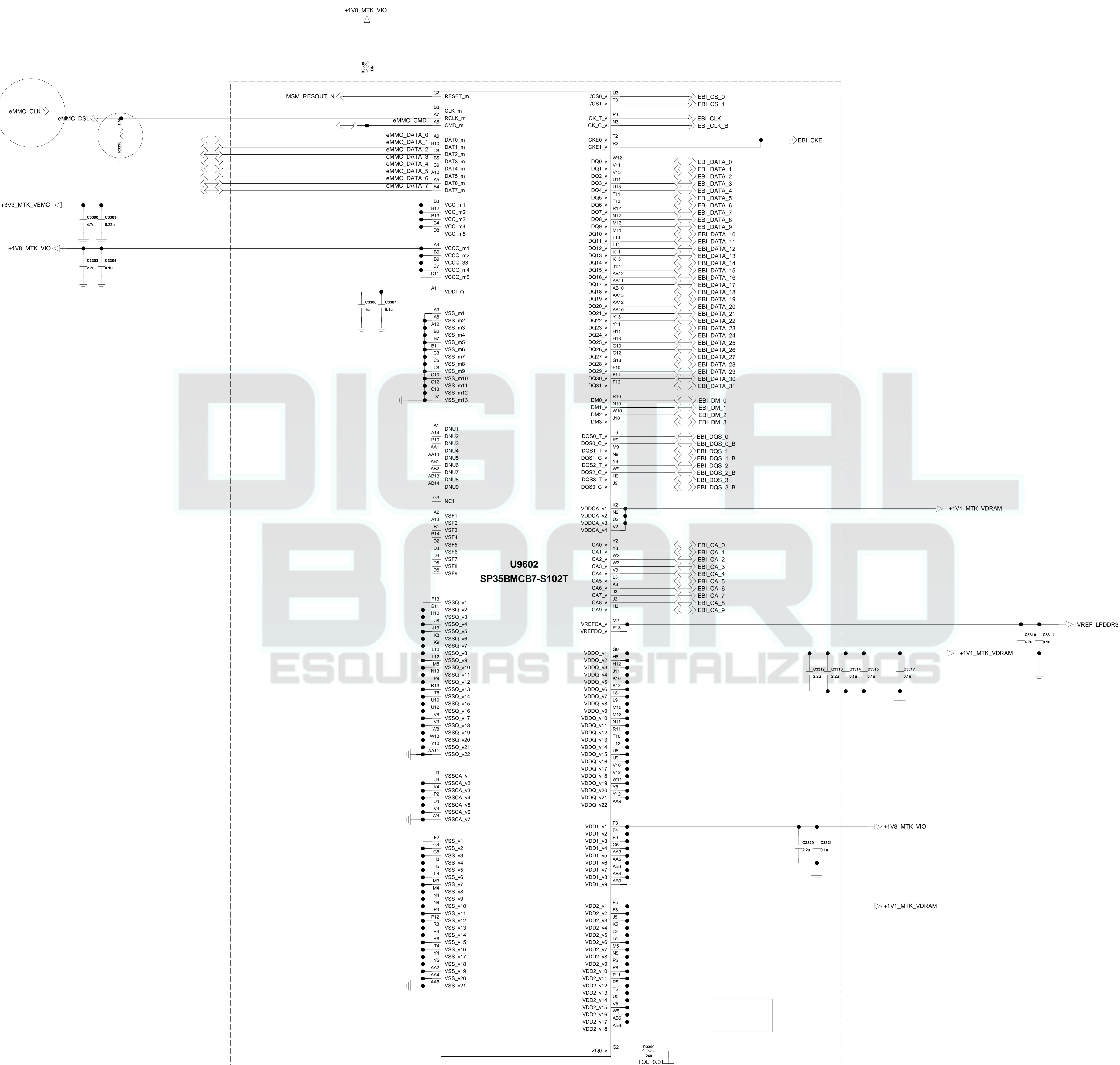
A1	NC1	NC7	AK1
A2	NC2	NC8	AL1
A30	NC3	NC9	AL2
A31	NC4	NC10	AL30
B1	NC5	NC11	AL31
B31	NC6		

U2100
MT6735M



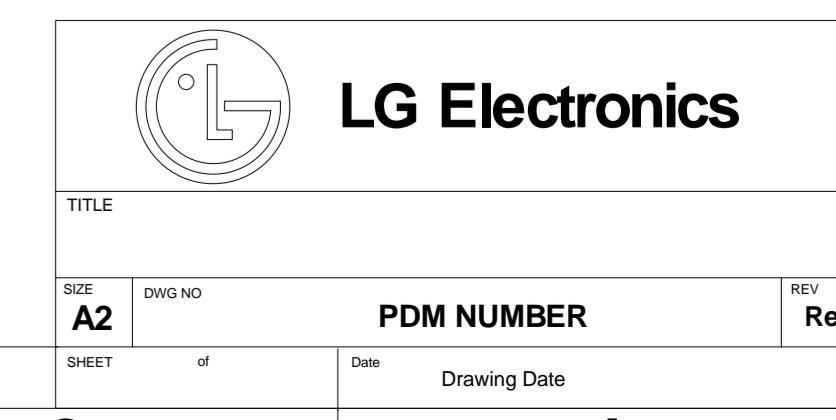
16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1

< 3-3-3-2-1_MCP_eMMC_5_0_8G_DDR3_8G_SMART1>



Schematic1

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16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1

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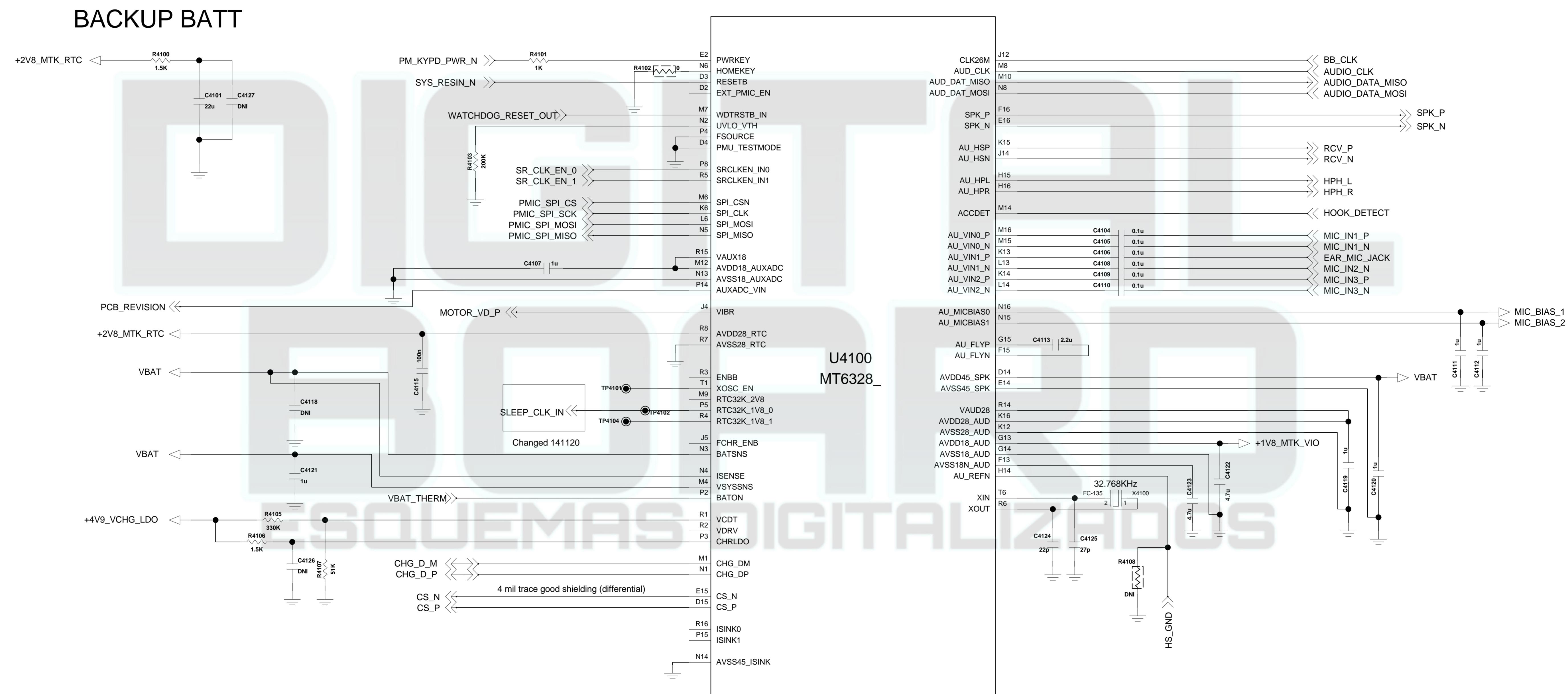
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< MT6328_DATA >



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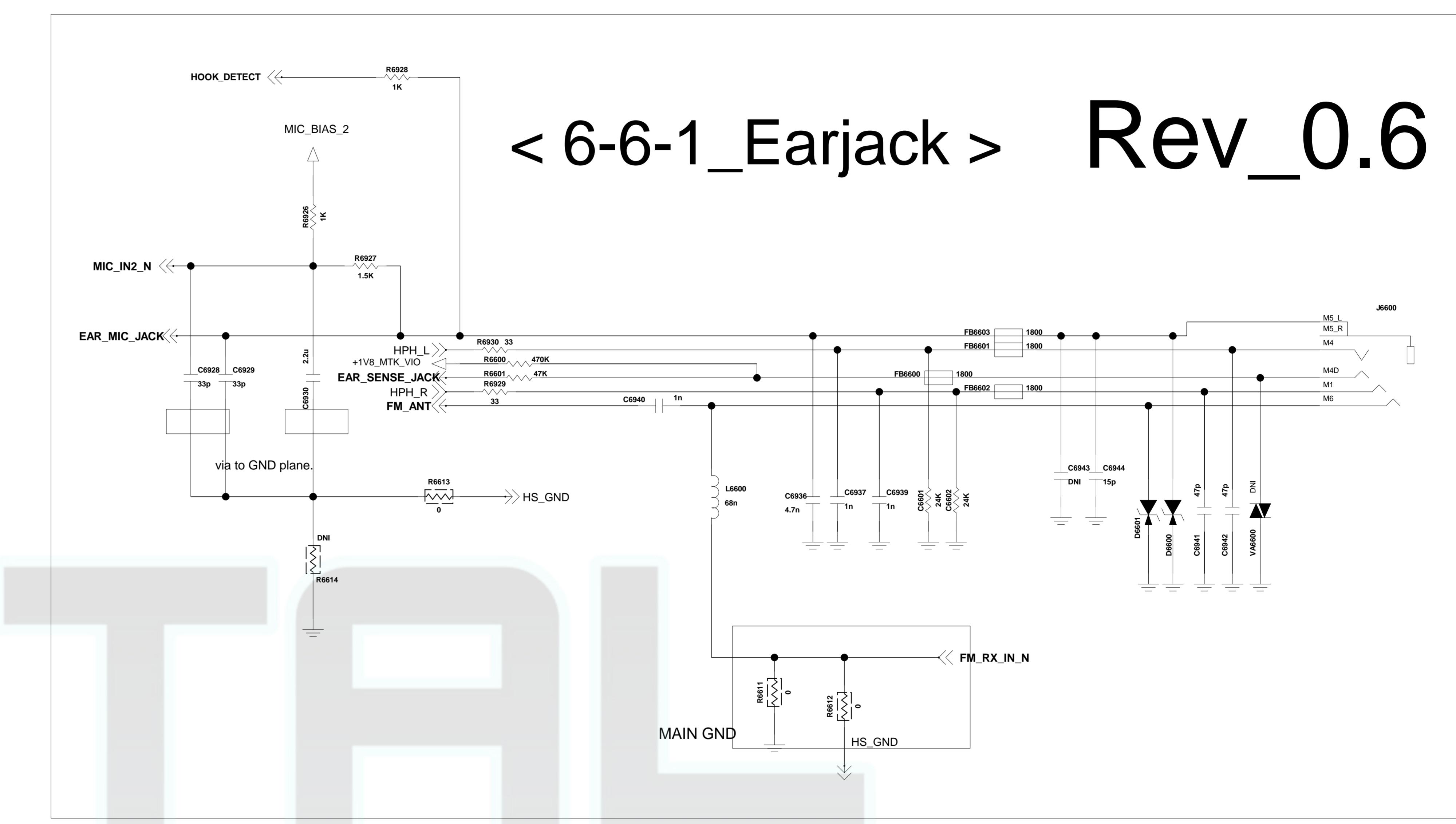
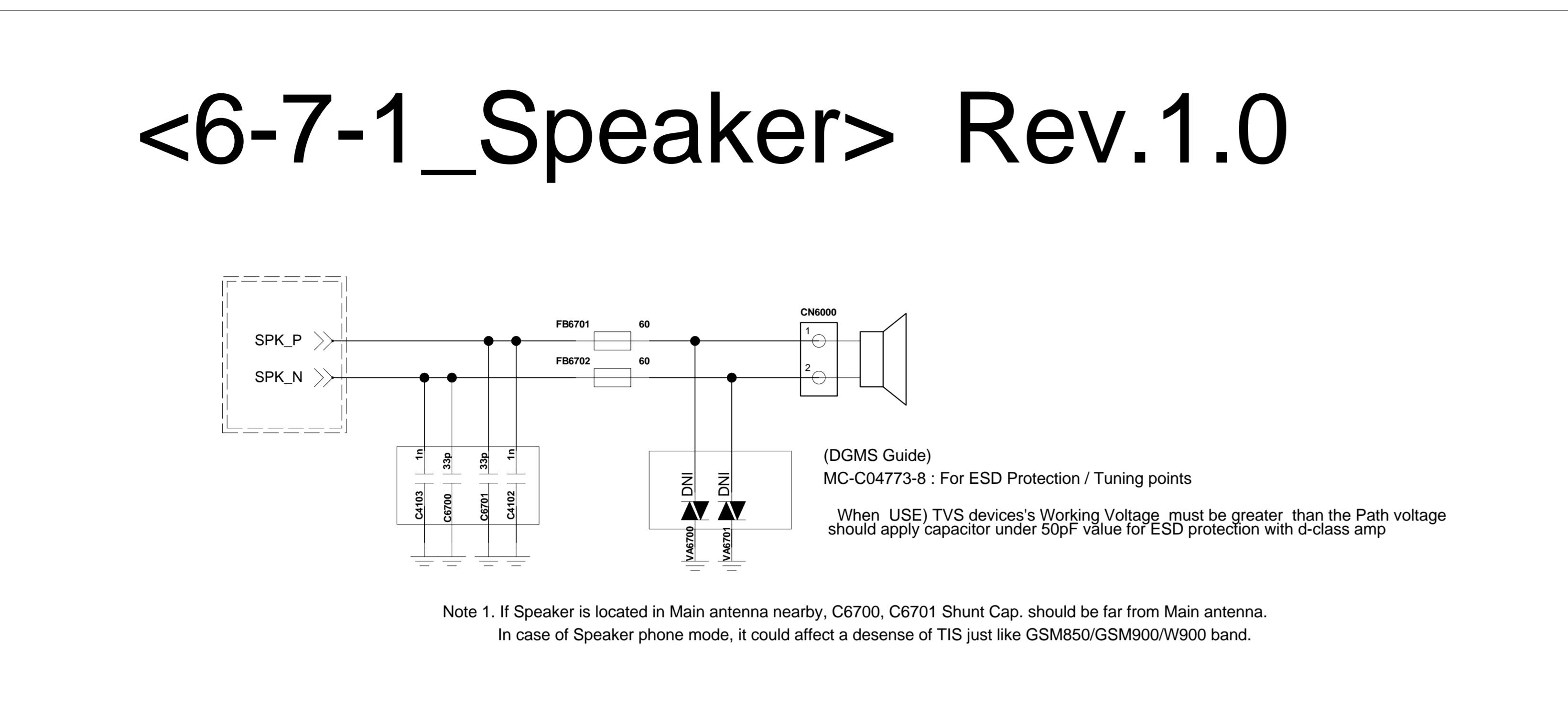
C

C

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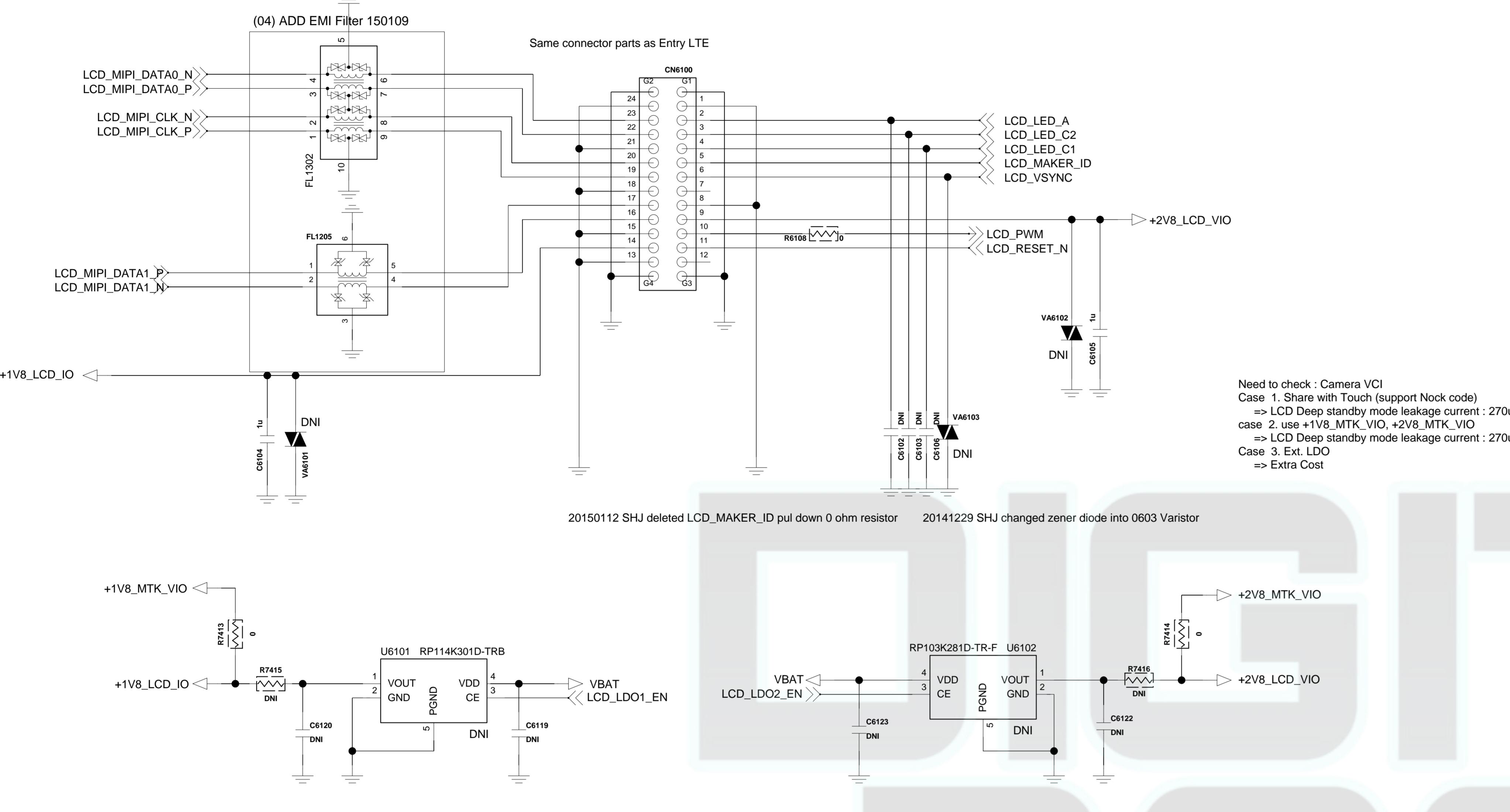
B

LGE Internal Use Only



< 7-1-3_LCD_FWVGA > Rev_1.0

LCD 4.5 Inch

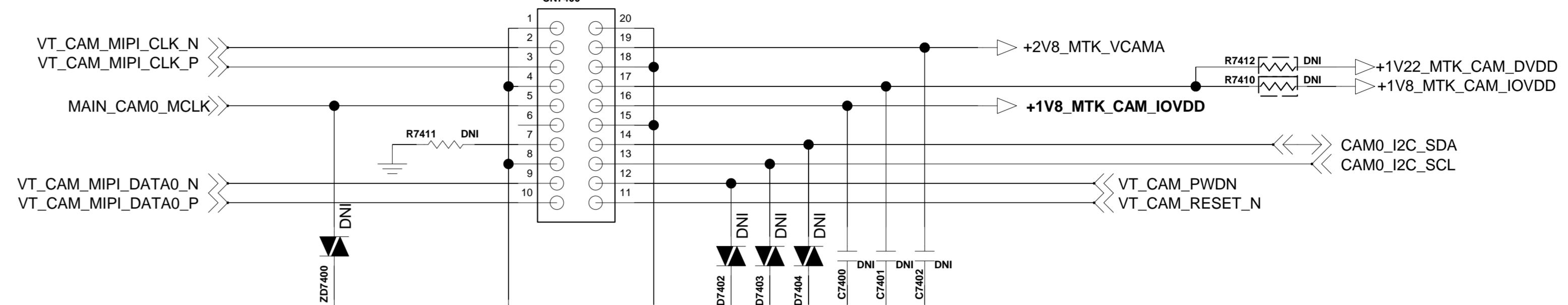


< 7-4_VT_Camera 1. 20pin_VGA_1.26M_1.3M_2.4M >

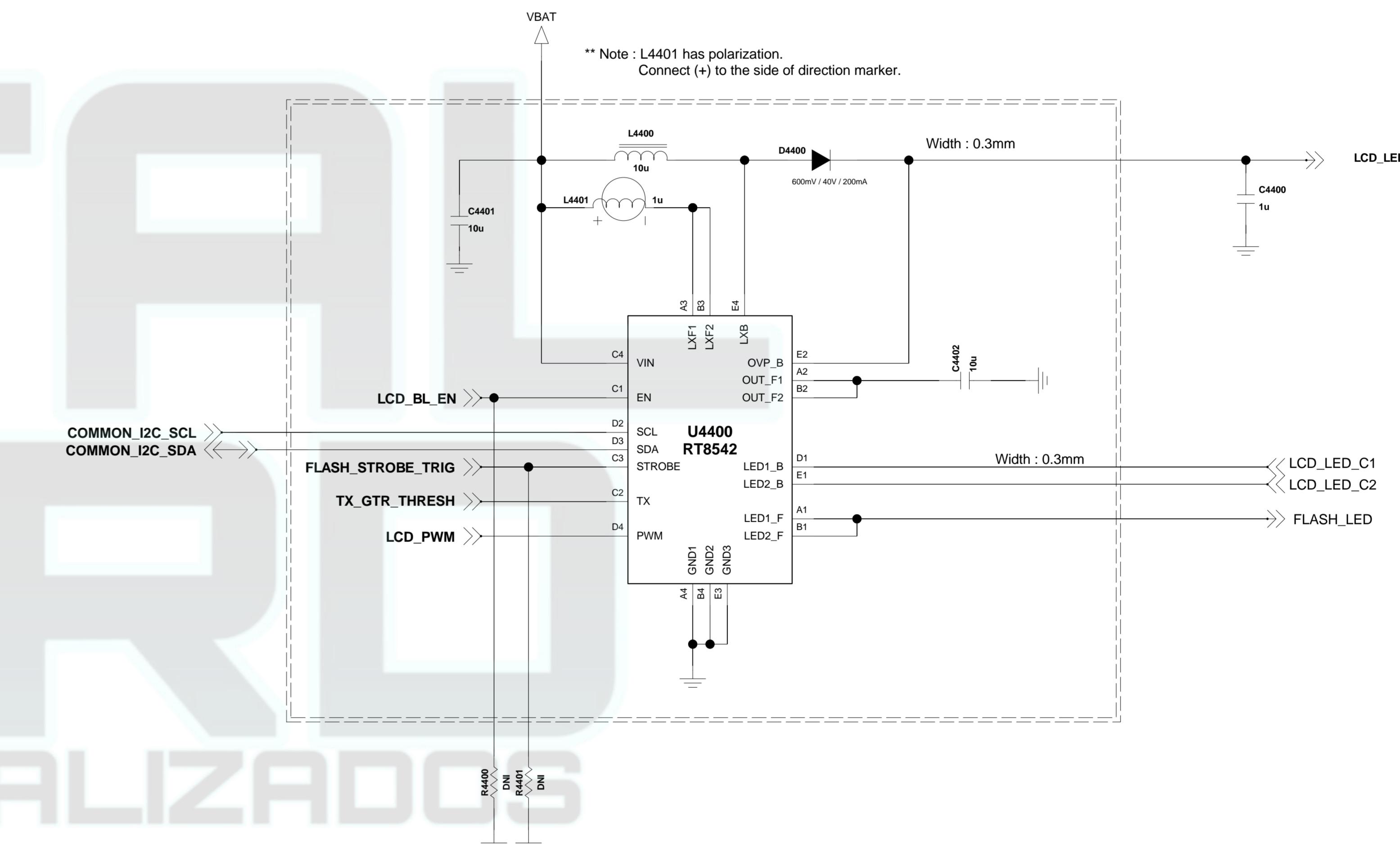
Rev_1.1

MC-C00081-17: 1. Add ESD Protection circuit for Camera signal lines
2. Use under 100ohm/15pF ESD protection for over 2MP Camera

[DGMS Guide]



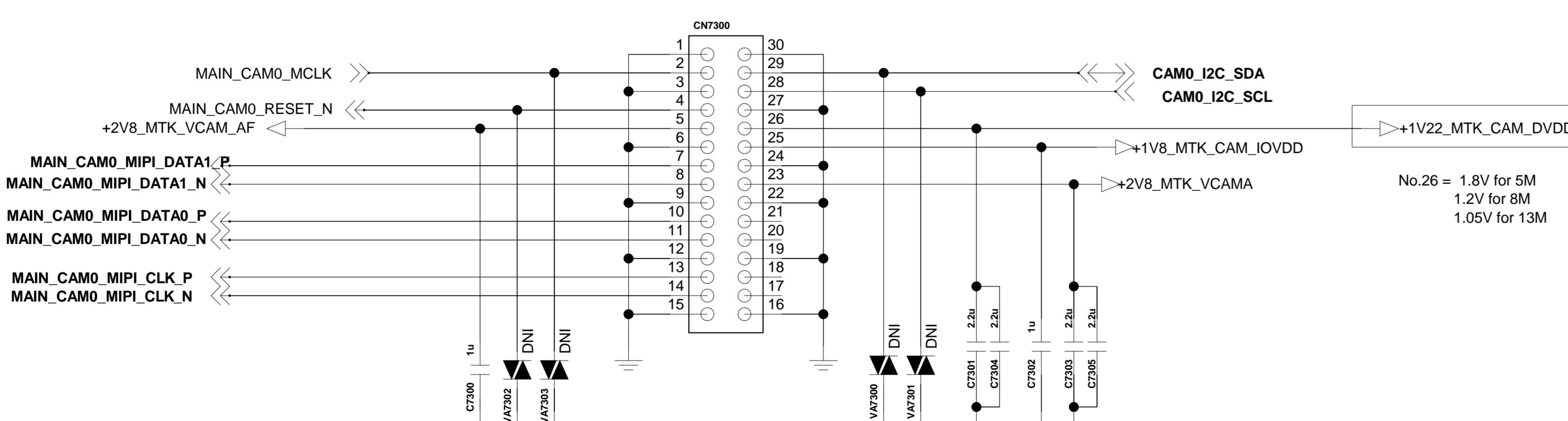
<4-4-3_LCD_Backlight_IC_RT8542> Rev_1.1



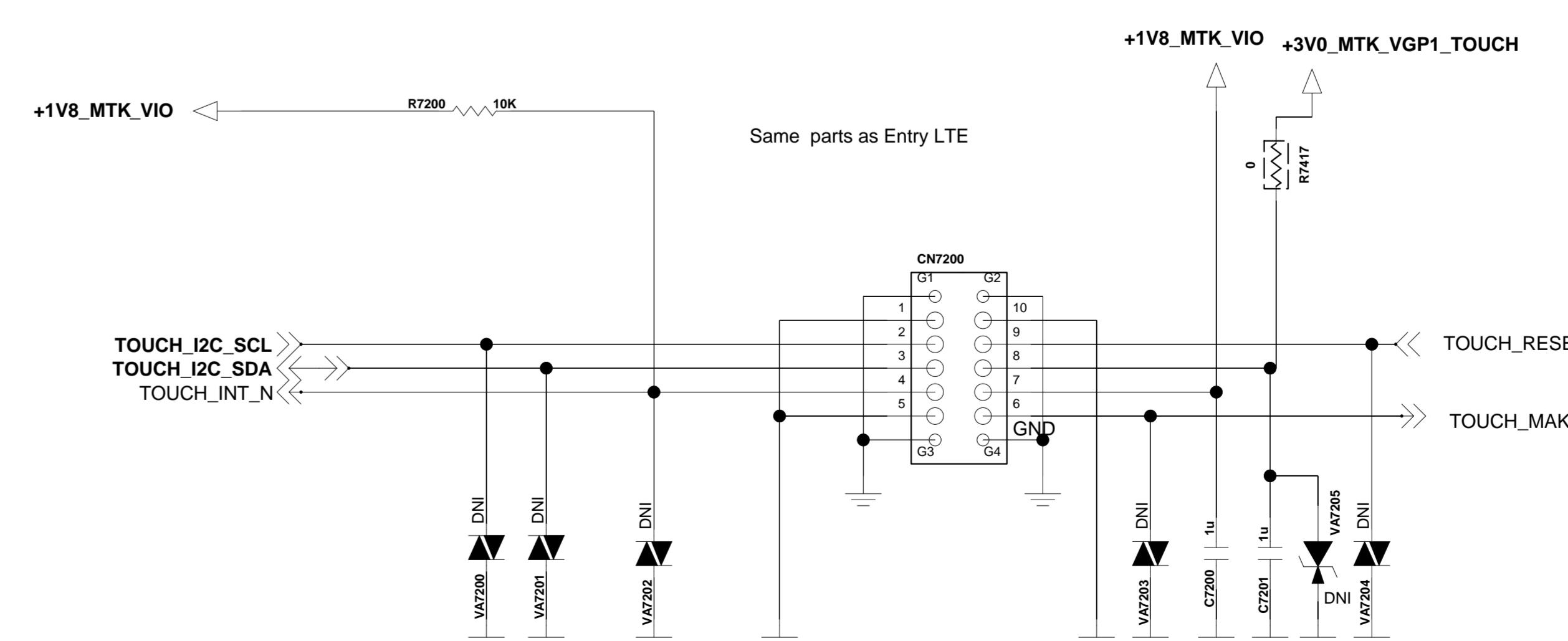
<7-3-5-1_30pin_5M_8M_AF> Rev_1.1

Check the pin map

Rev_0.3



< 7-2-1-1_Touch_Connector >



16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1

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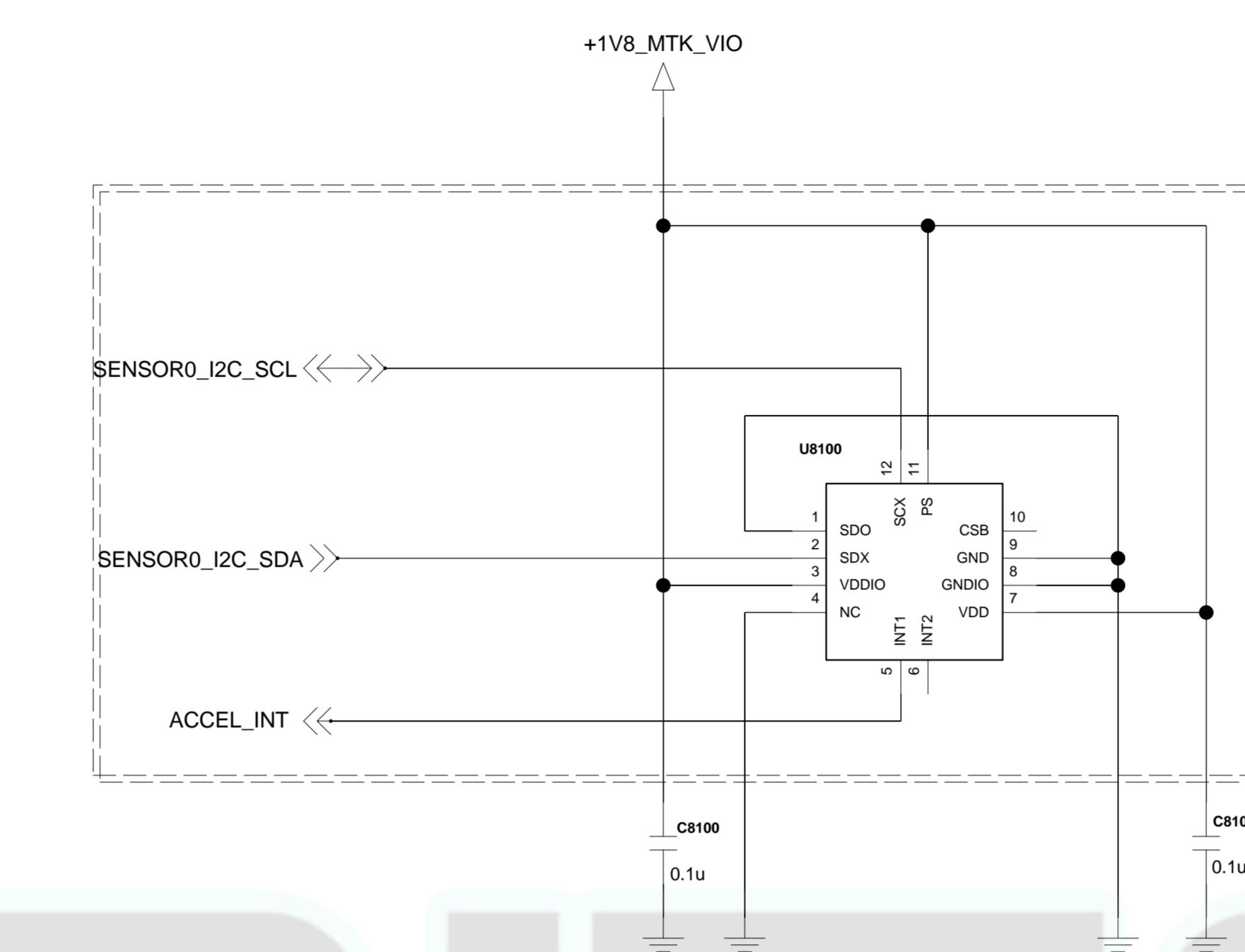
D

C

B

LGE Internal Use Only

<8-1-1-1 ACCEL BMA255 > Rev_0.3

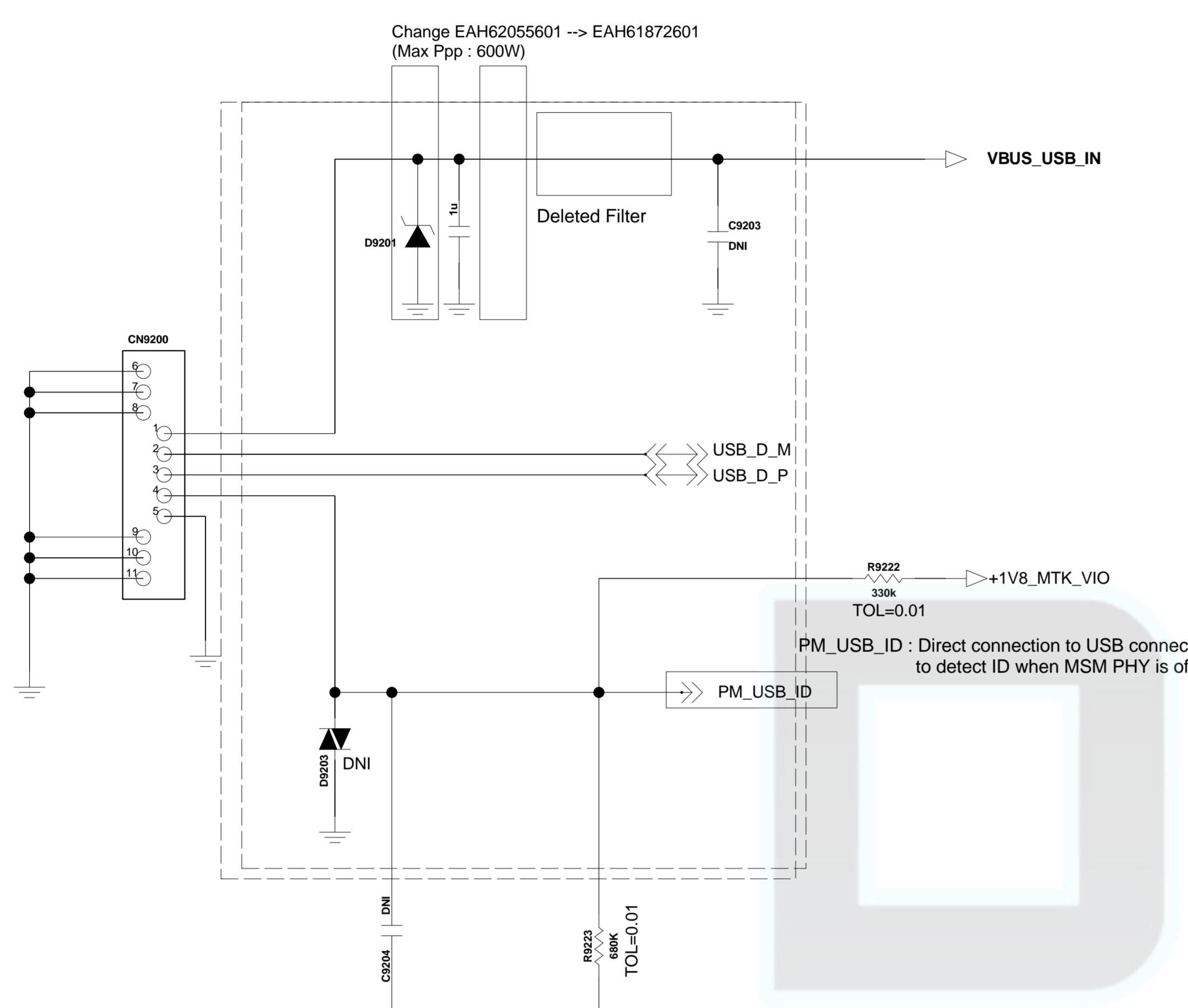


<8-4-1-x_Proximity_APDS-9930-100R>

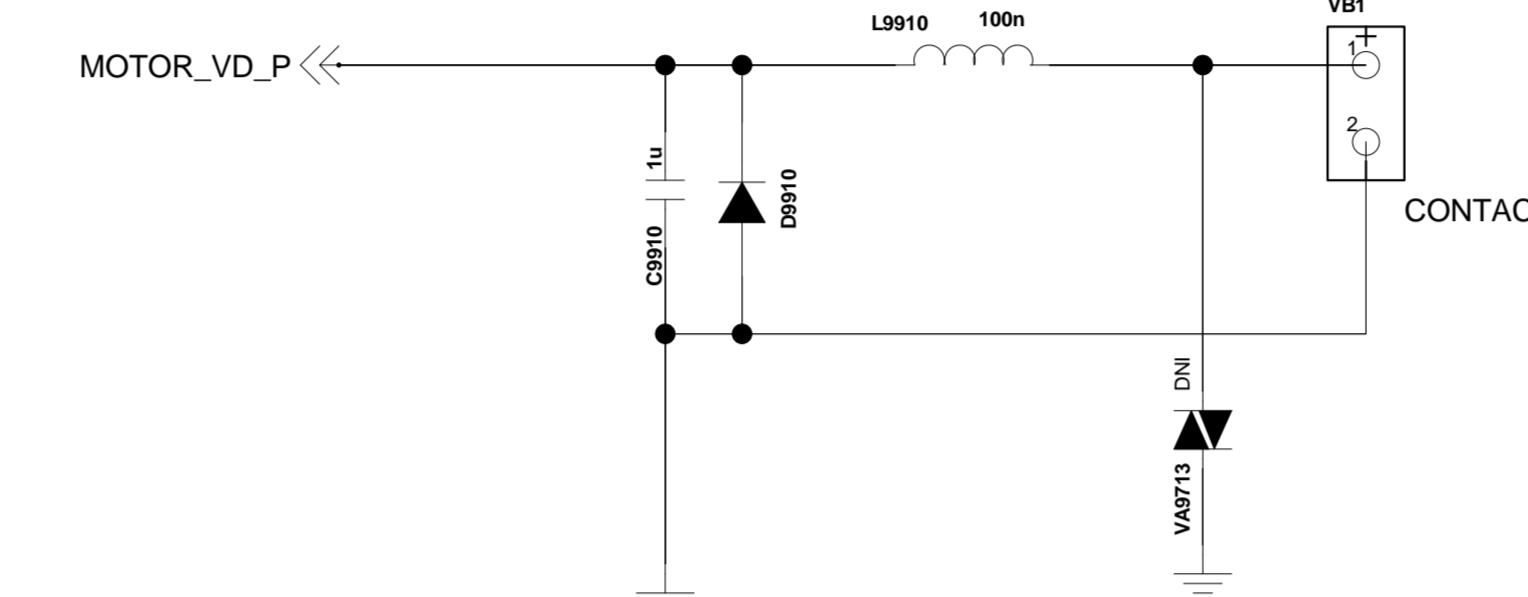
16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1

<9-2-1_USB_IO> Rev.1.0

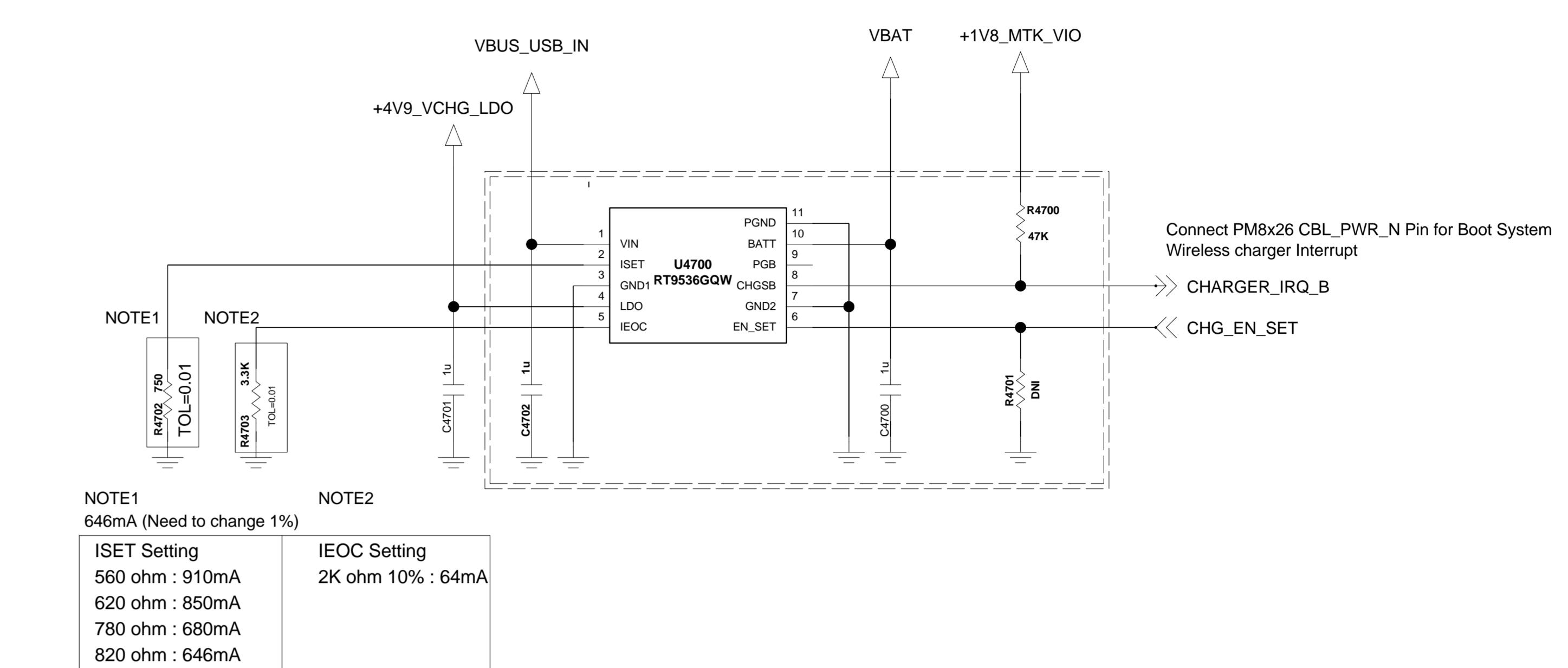
Connector 1. USB 2.0



< 9-9-1-1_Motor > Rev_0.3

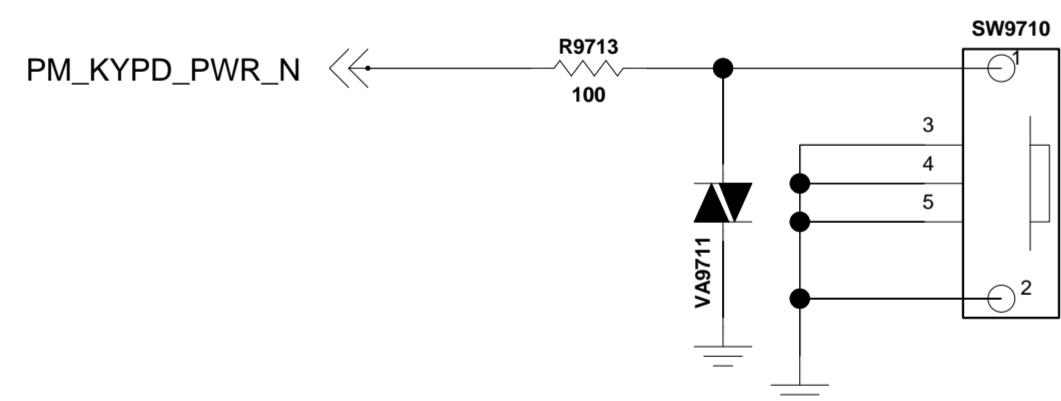


<4-7-1-1_Linear Charger_RT9536> Rev_1.0

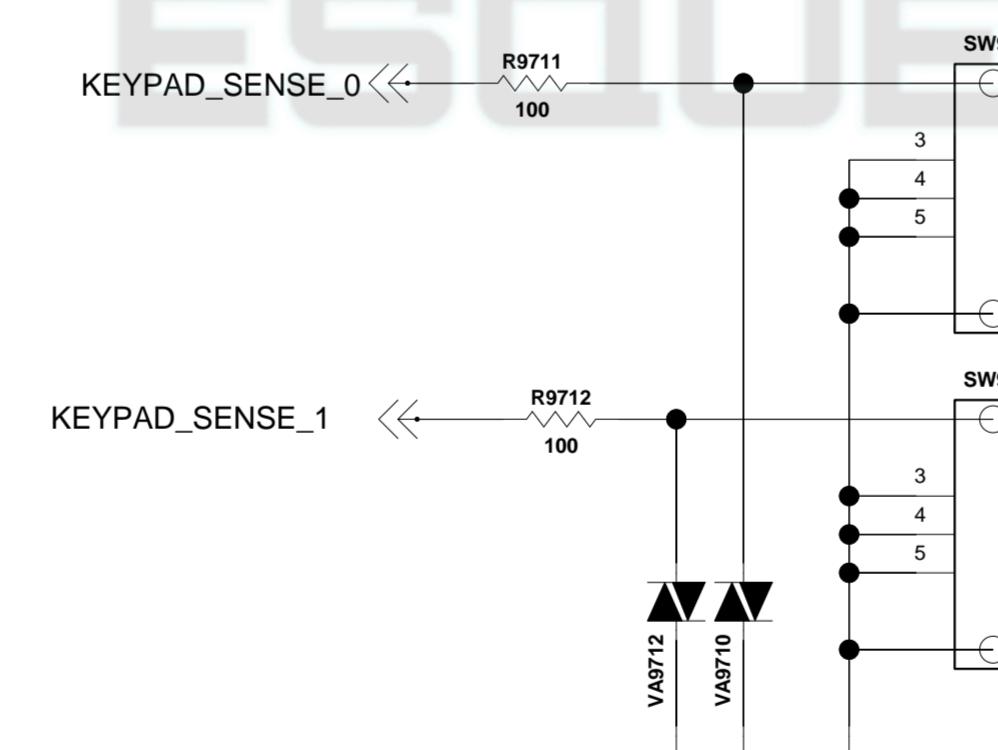


< 9-7-1_Power_Lock_Volume_Key > Rev_0.5

POWER & LOCK KEY



Vol. UP/DOWN



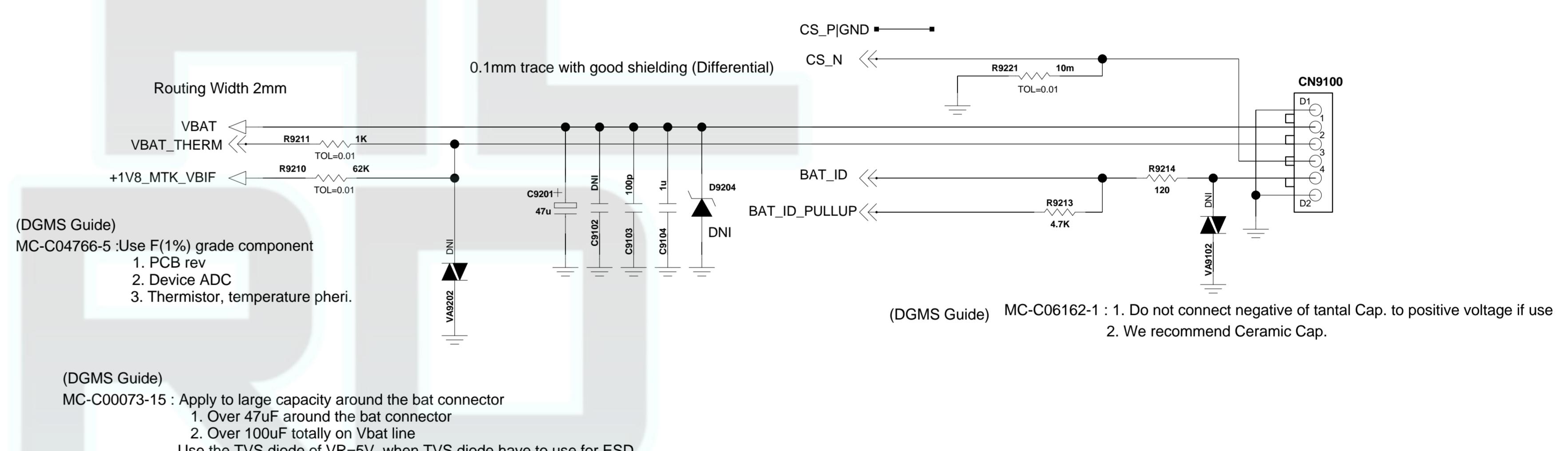
(DGMS Guide)
MC-C00052-11 : All items must be satisfied
1. GND pattern is applied to the Side Key
2. Signal line are placed center of PCB

(DGMS Guide)
MC-C05710-3 : All items must be satisfied
1. Reset Line of BB Chip do not overlap Key pad
2. Placement of Oder : Keypad - Capacitor(varistor, TVS Diode) - Resistor

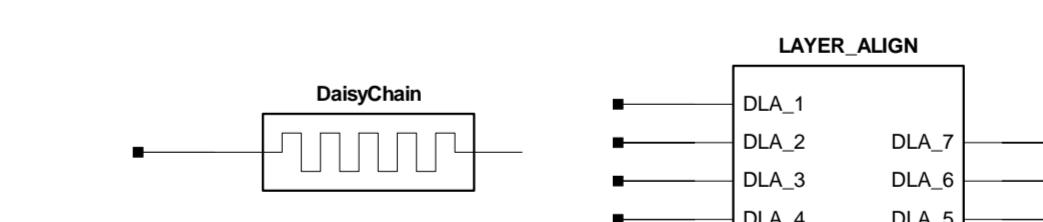
(DGMS Guide)
MC-C00050-9 : ESD prevention Shunt Cap Value of key for all, considering the case of pressing the keys same time, Cap value of connecting one key sensing Line is less than 1nF

(DGMS Guide)
MC-C00069-11 : Key ESD protection on each signal applied DNI Pad

<9-1_Battery_Connector_4Pin> Rev_1.4



Daisy Chain





DIGITAL BOARD

ESQUEMAS DIGITALIZADOS

P/N : FLXXXXXXXXXX(X₅X)