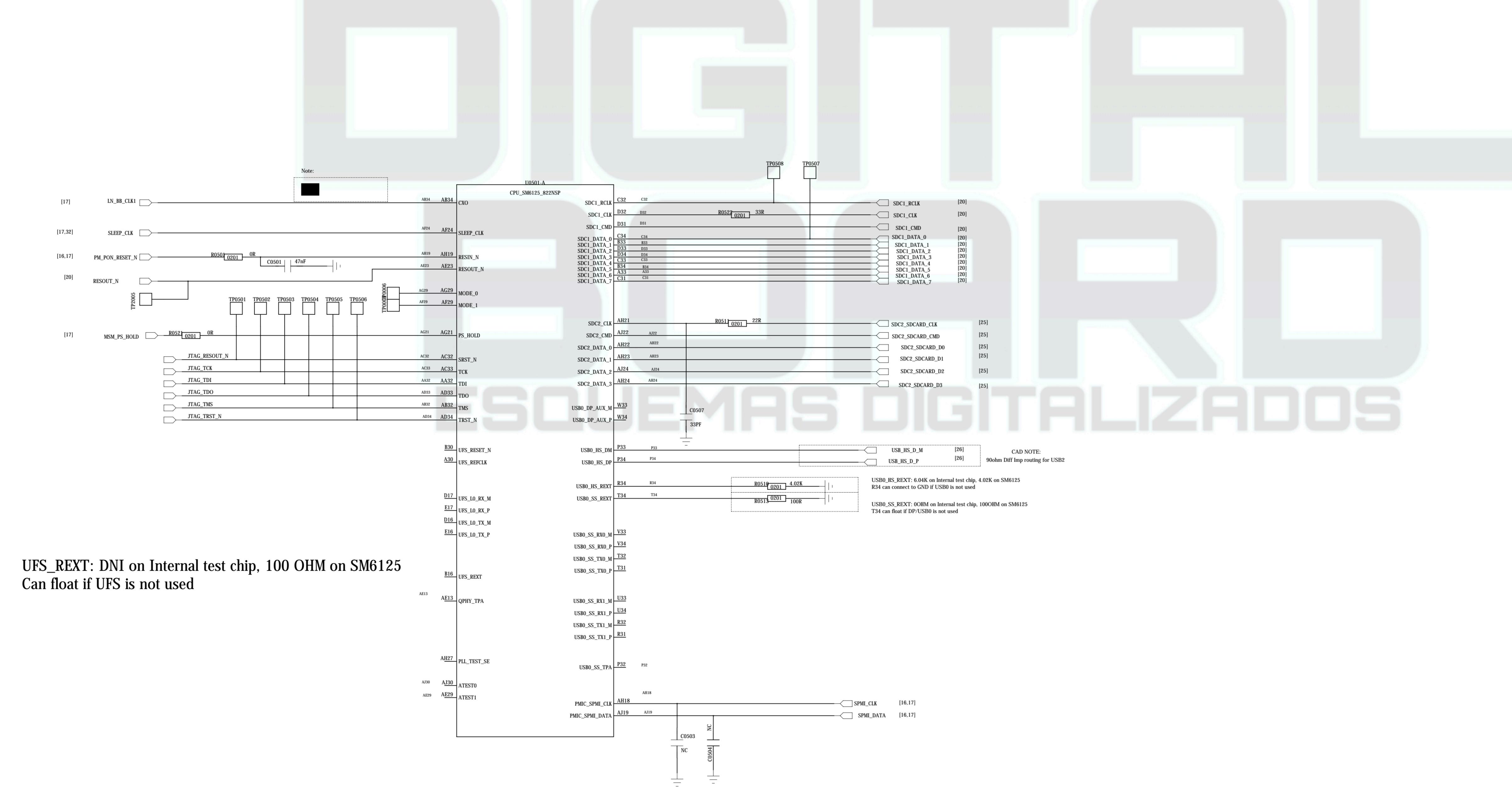


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

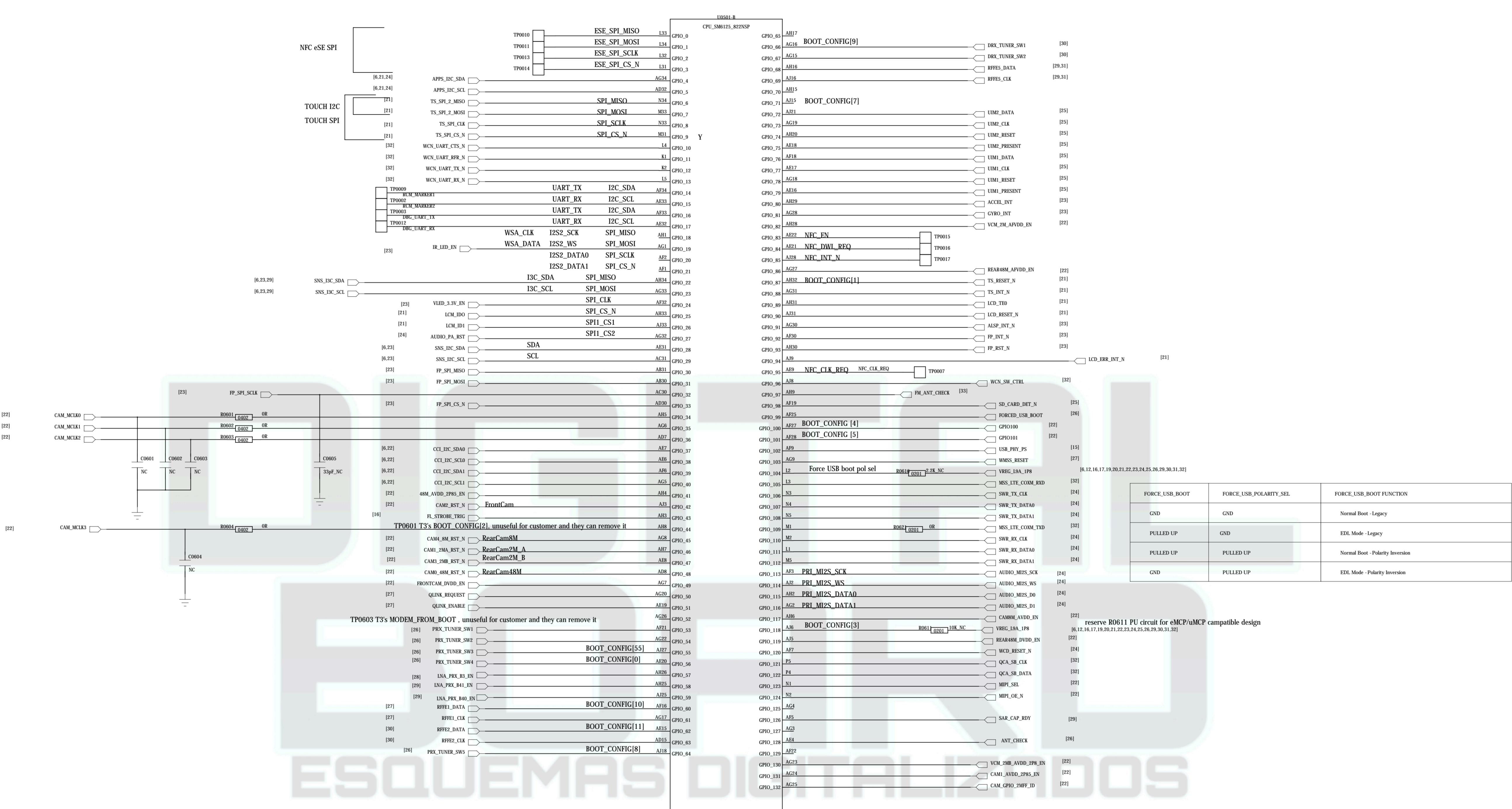


**UFS_REXT: DNI on Internal test chip, 100 OHM on SM612
Can float if UFS is not used**

	COMPANY: <Company Name>			
	TITLE: <Title>			
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ID: ecked Date>				
ID: C Date>				
ID: lease Date>	SCALE: <Scale>	SHEET: 5F		34

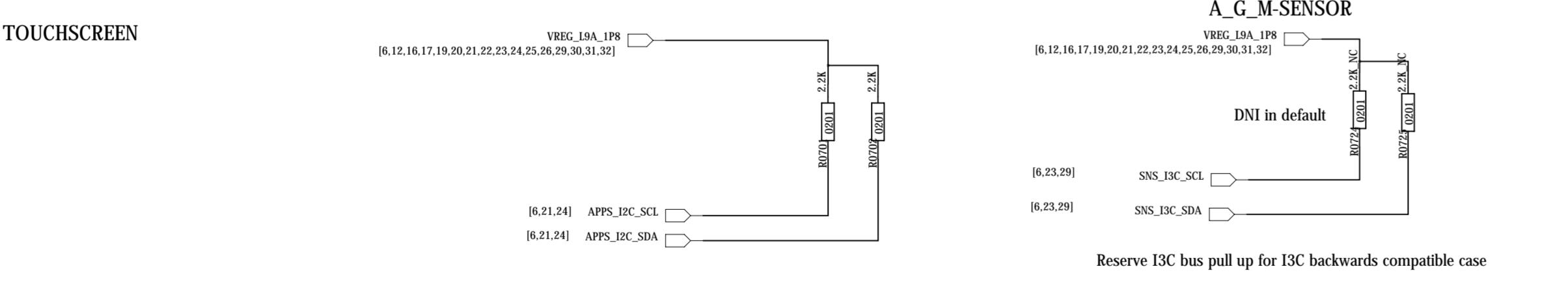
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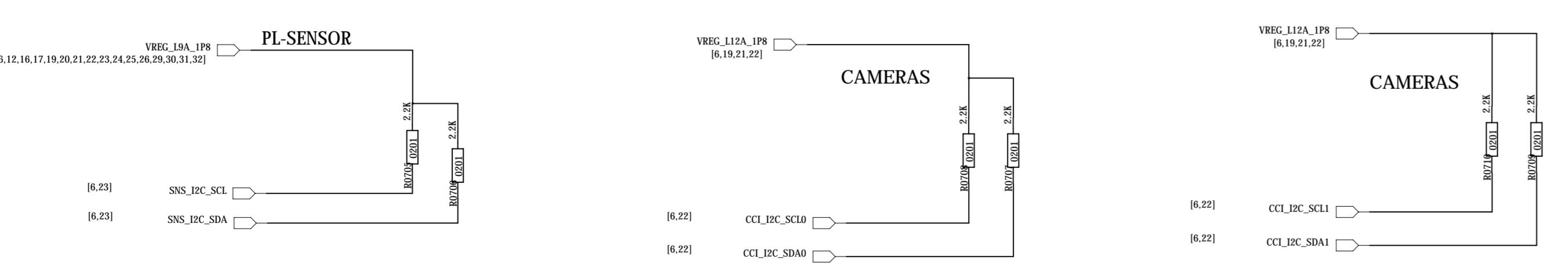
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B



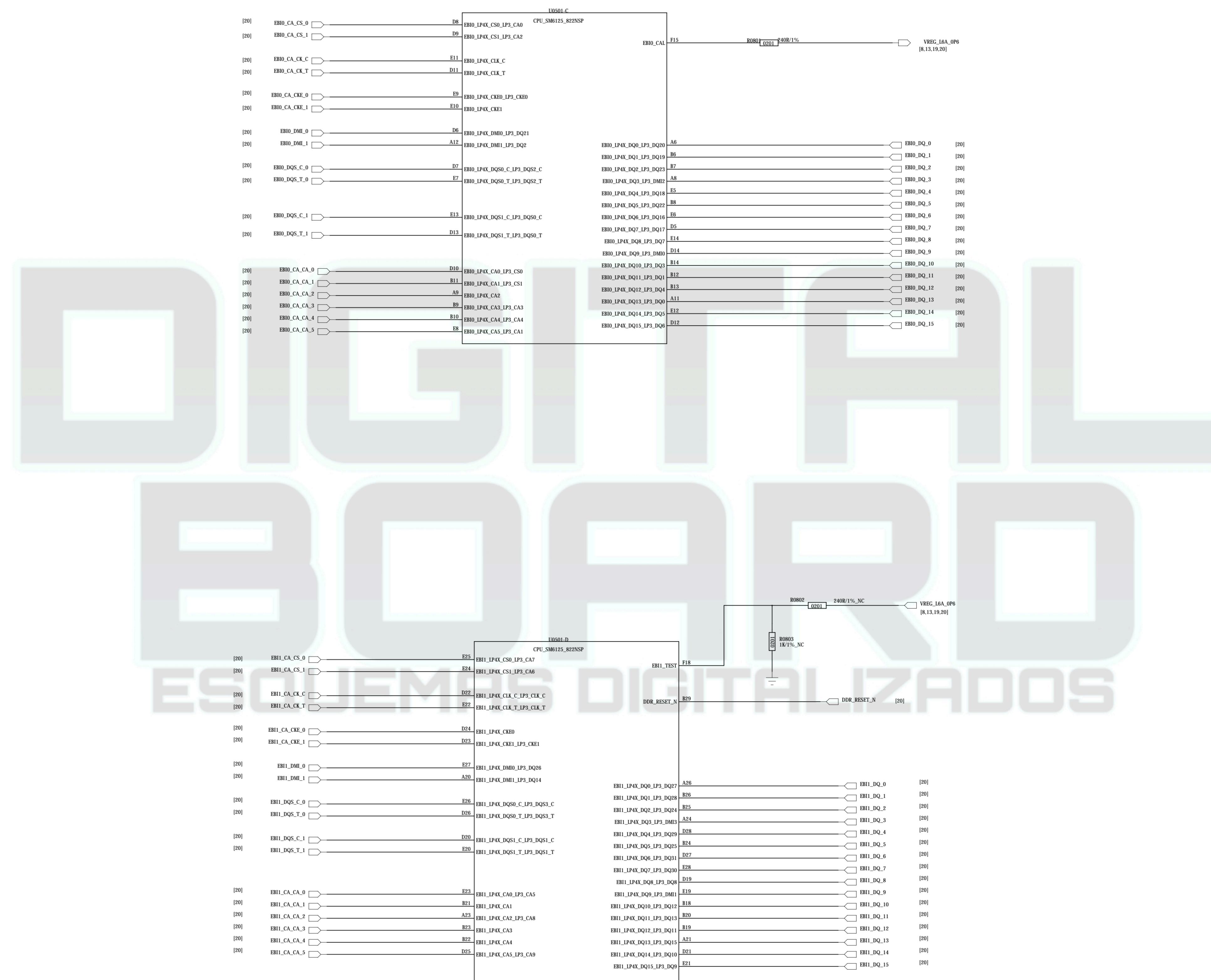
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DRAWING NO:	REV:
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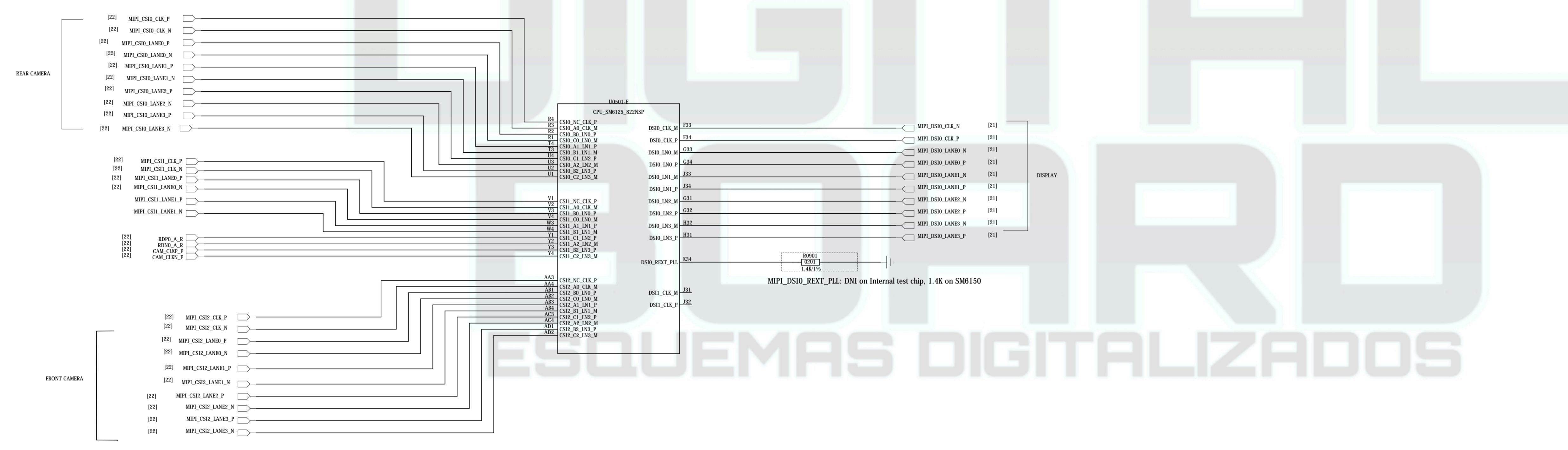


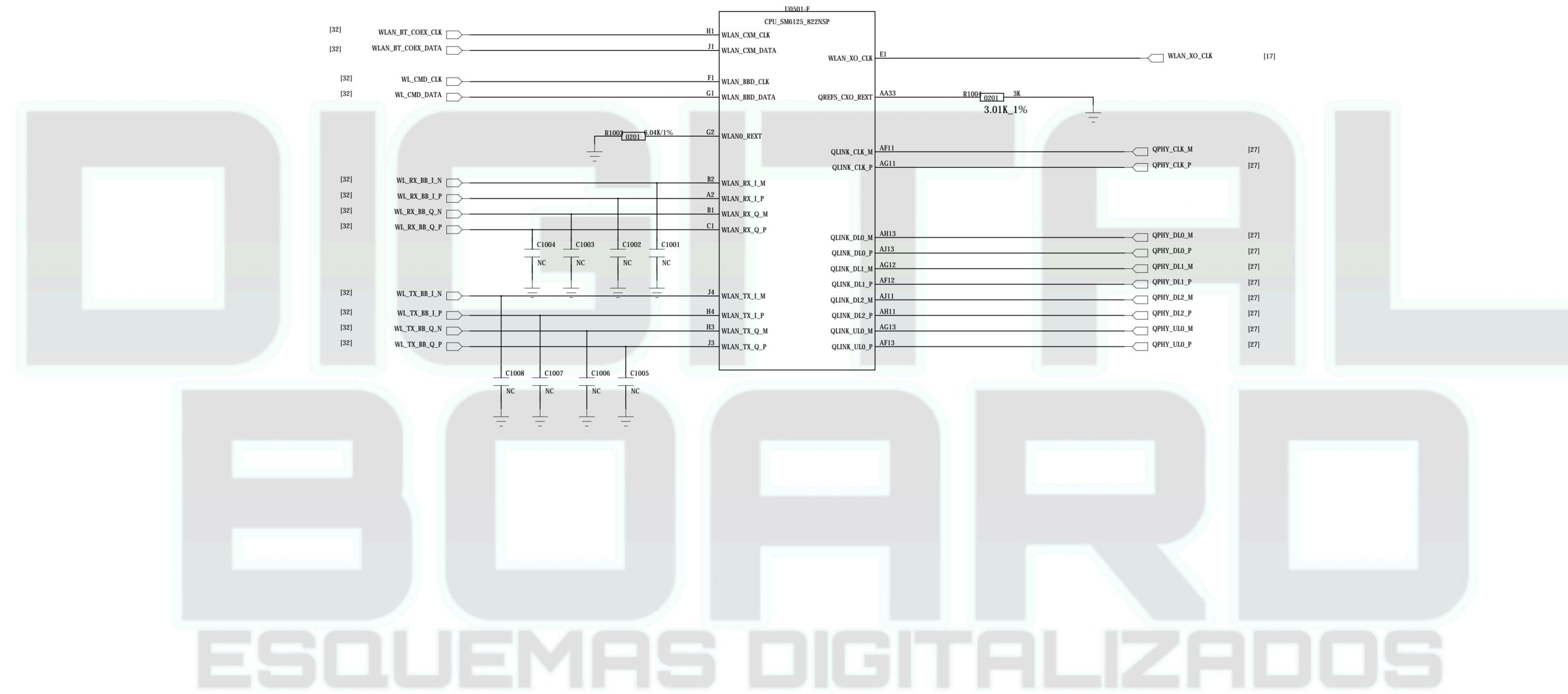
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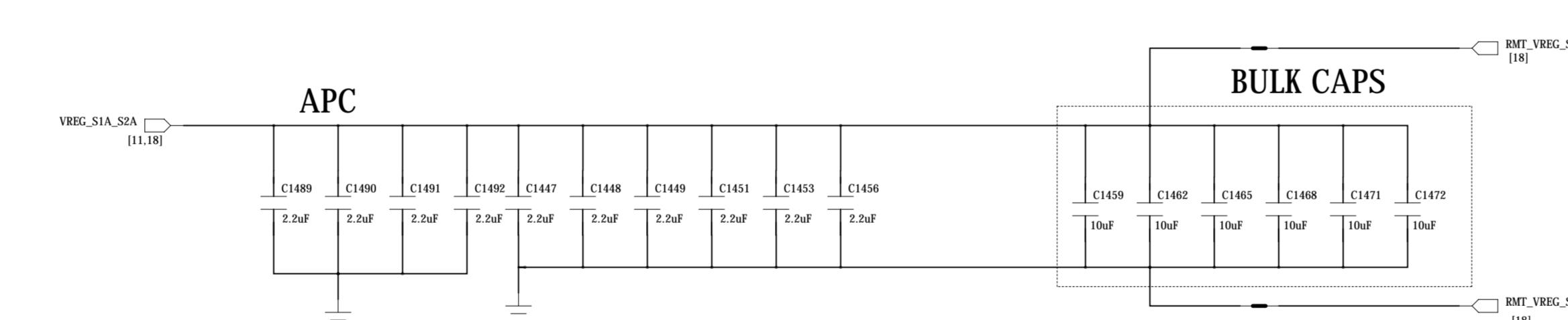
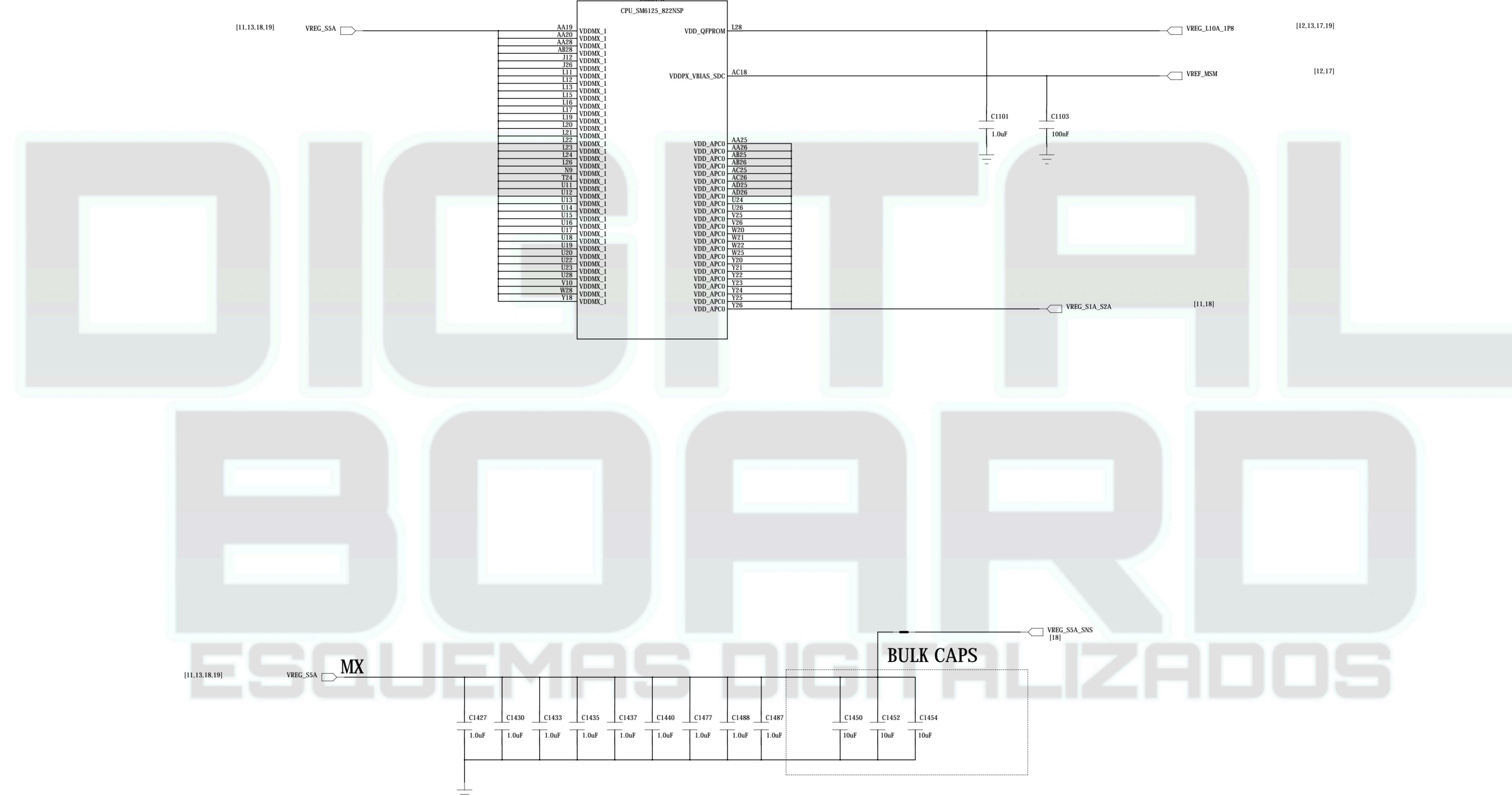




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21_LTE_B7

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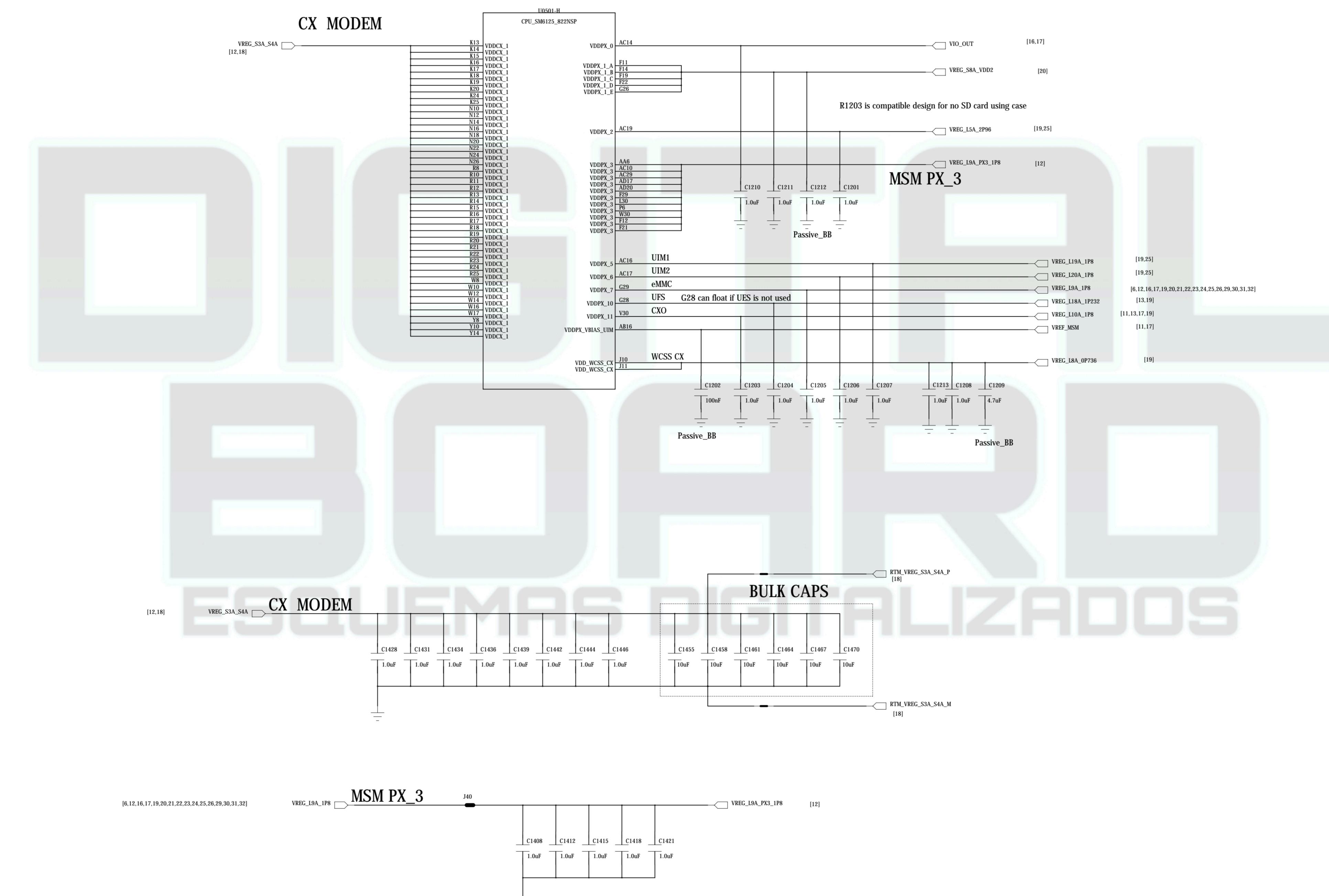


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DRAWING NO:

Drawing Numbers

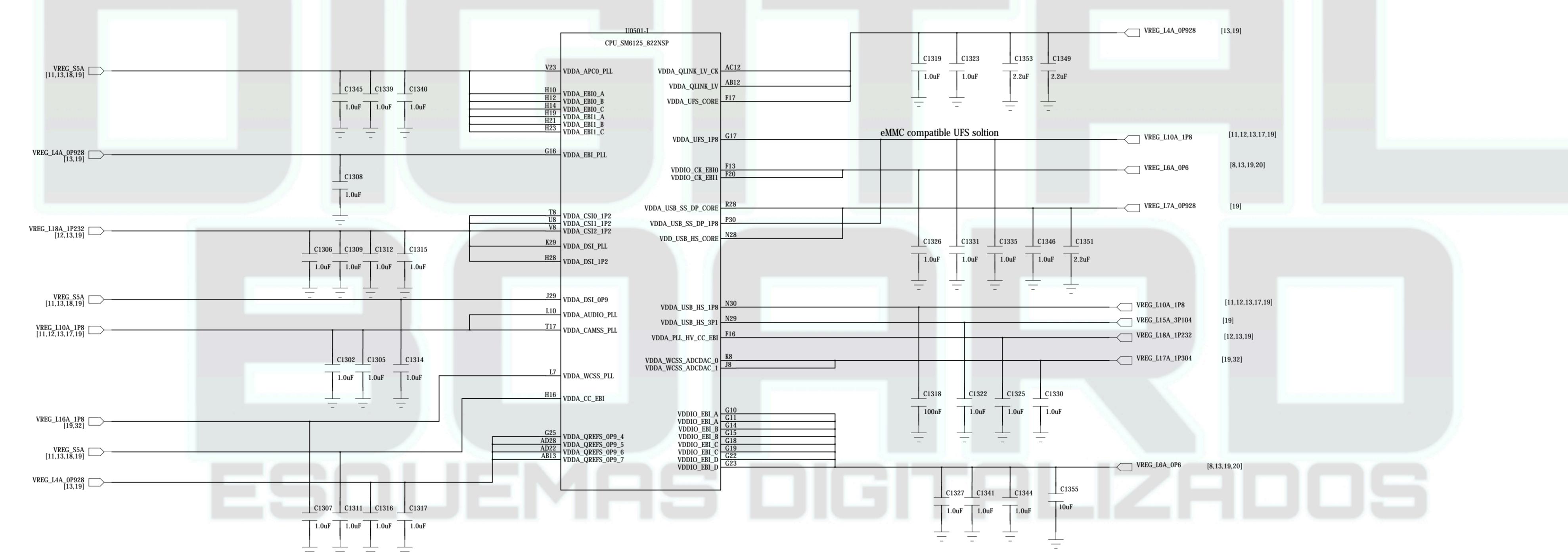
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<Company Name>

DRAWING NO:

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LTR	ECO NO:	APPROVED:	DATE:

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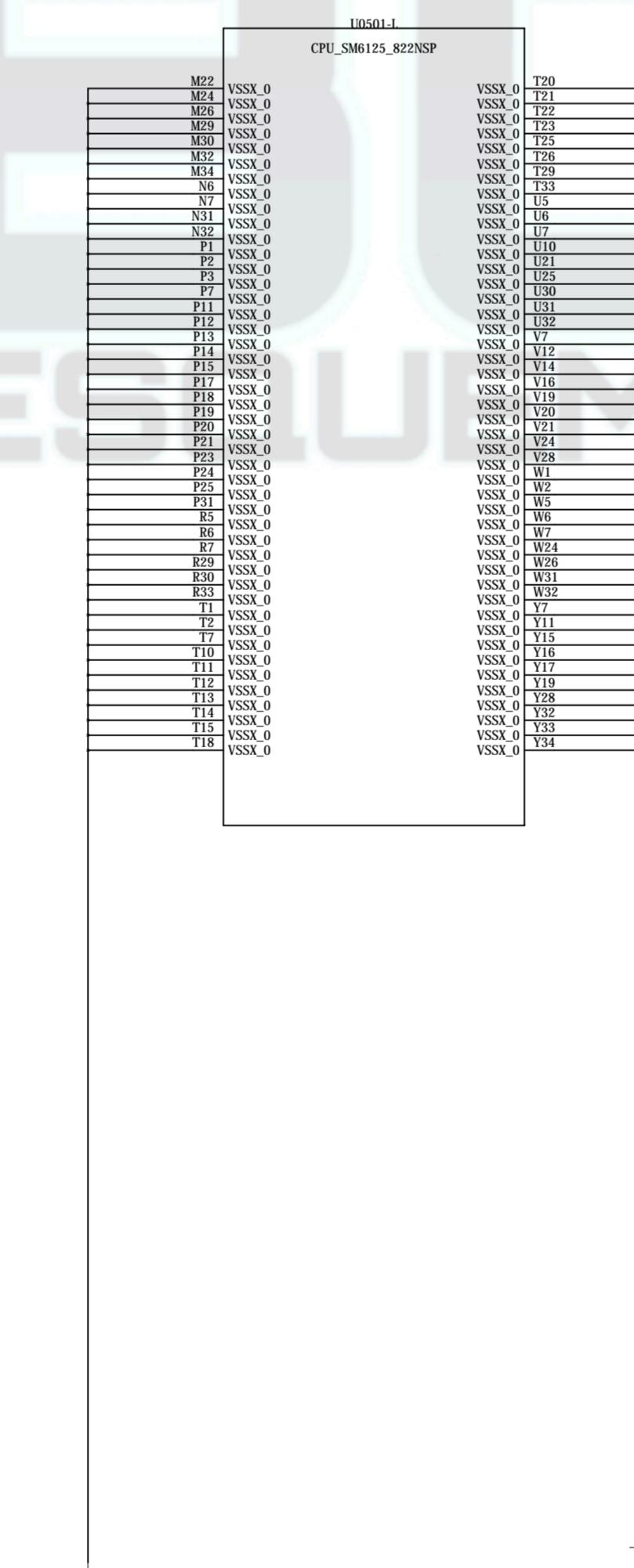
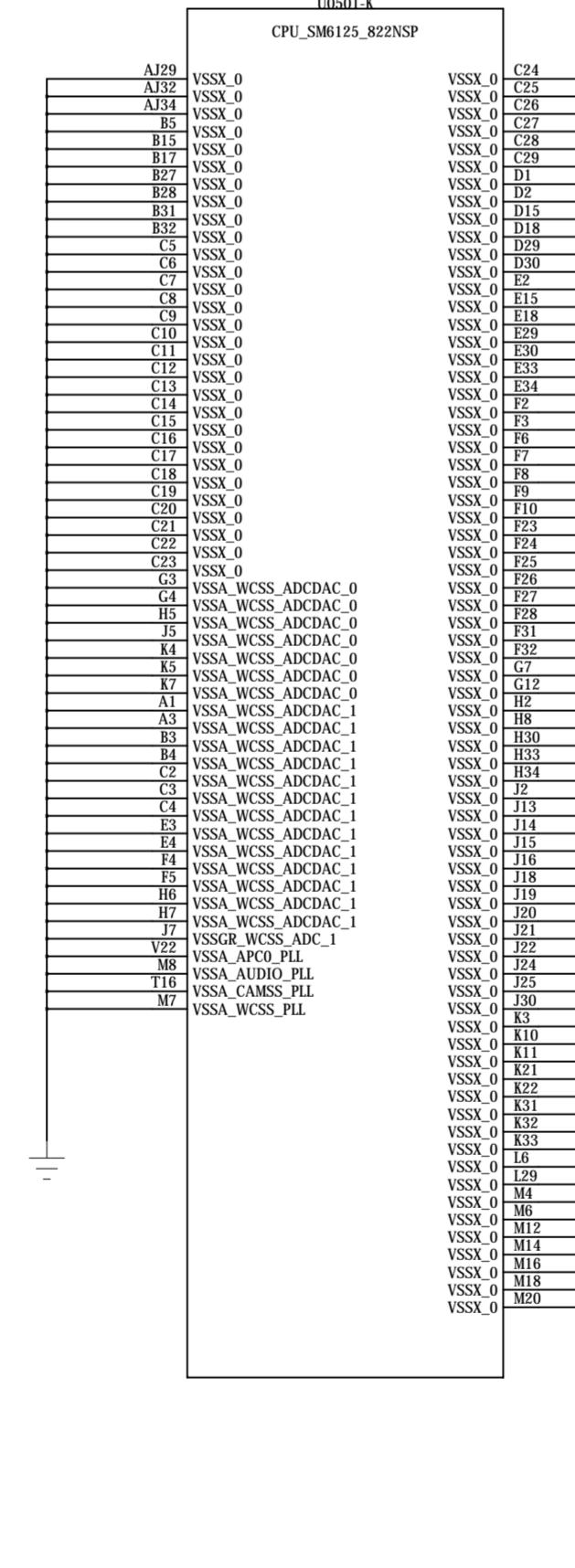
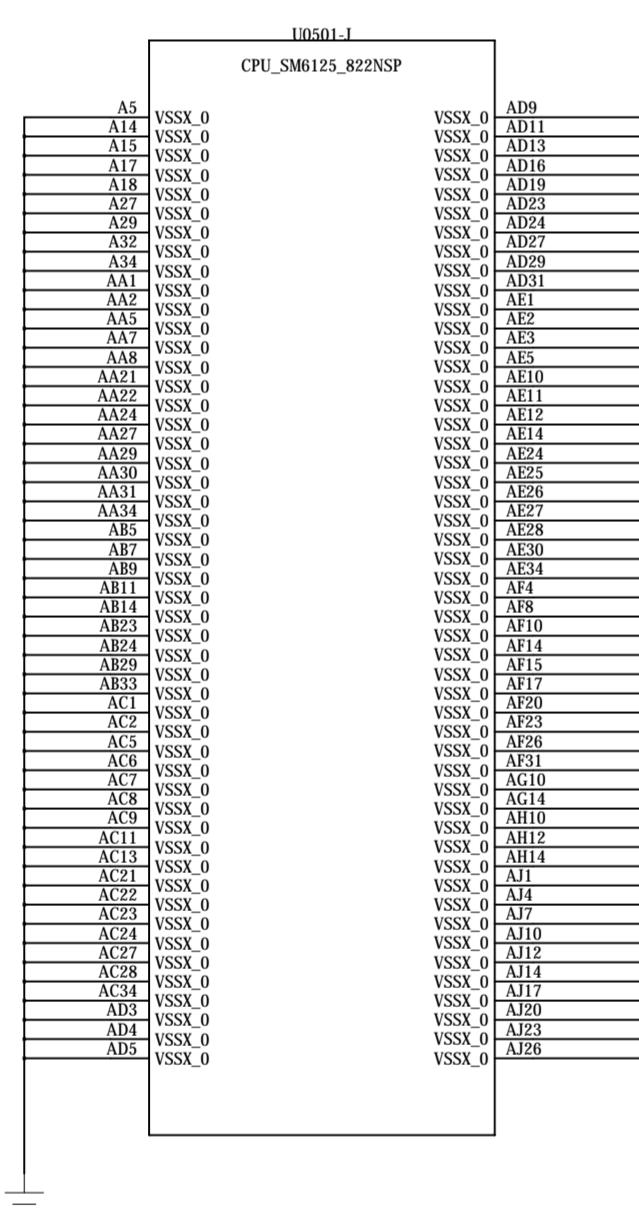
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B

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A



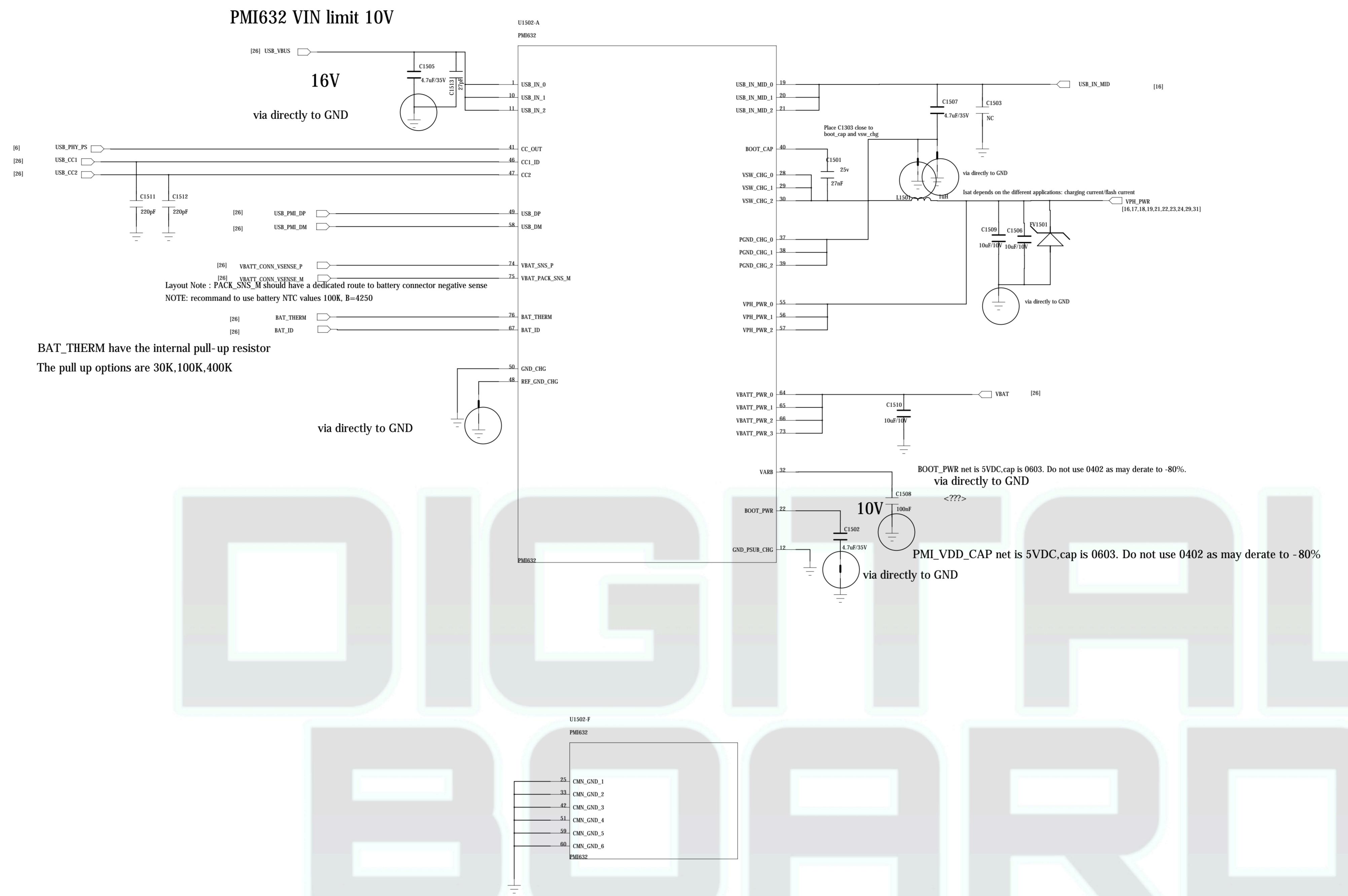
U0501-I

CPU_SM6125_822NSP

M22	VSSX_0	T20
M24	VSSX_0	T21
M26	VSSX_0	T22
M29	VSSX_0	T23
M30	VSSX_0	T25
M32	VSSX_0	T26
M34	VSSX_0	T29
N6	VSSX_0	T33
N7	VSSX_0	U5
N31	VSSX_0	U6
N32	VSSX_0	U7
P1	VSSX_0	U10
P2	VSSX_0	U21
P3	VSSX_0	U25
P7	VSSX_0	U30
P11	VSSX_0	U31
P12	VSSX_0	U32
P13	VSSX_0	V7
P14	VSSX_0	V12
P15	VSSX_0	V14
P17	VSSX_0	V16
P18	VSSX_0	V19
P19	VSSX_0	V20
P20	VSSX_0	V21
P21	VSSX_0	V24
P22	VSSX_0	V28

COMPANY:	<Company Name>		
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Date>	CODE:	SIZE:	DRAWING NO:
d Date>	<Code>	A0	<Drawing Number><Revision>
e>	SCALE: <Scale>		SHEET: #4 34
e Date>			

Use C1301 = 4.7uF for Single Charging Config
Use C1301 = 2.2uF for Parallel Charging Config



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S CONTENTS REVEALED IN ANY MANNER TO OTHERS WITHOUT THE
PRESS WRITTEN PERMISSION OF QUALCOMM.

PMI8952 Charger

		QUALCOMM Technologies, Inc. 5775 Morehouse Drive San Diego, CA 92121-1714 U.S.A.
Title Sheet		
Size	Name	Rev
	Date:	Sheet of

REVISION RECORD			
ITR	ECO NO.	APPROVED:	DATE

D

D

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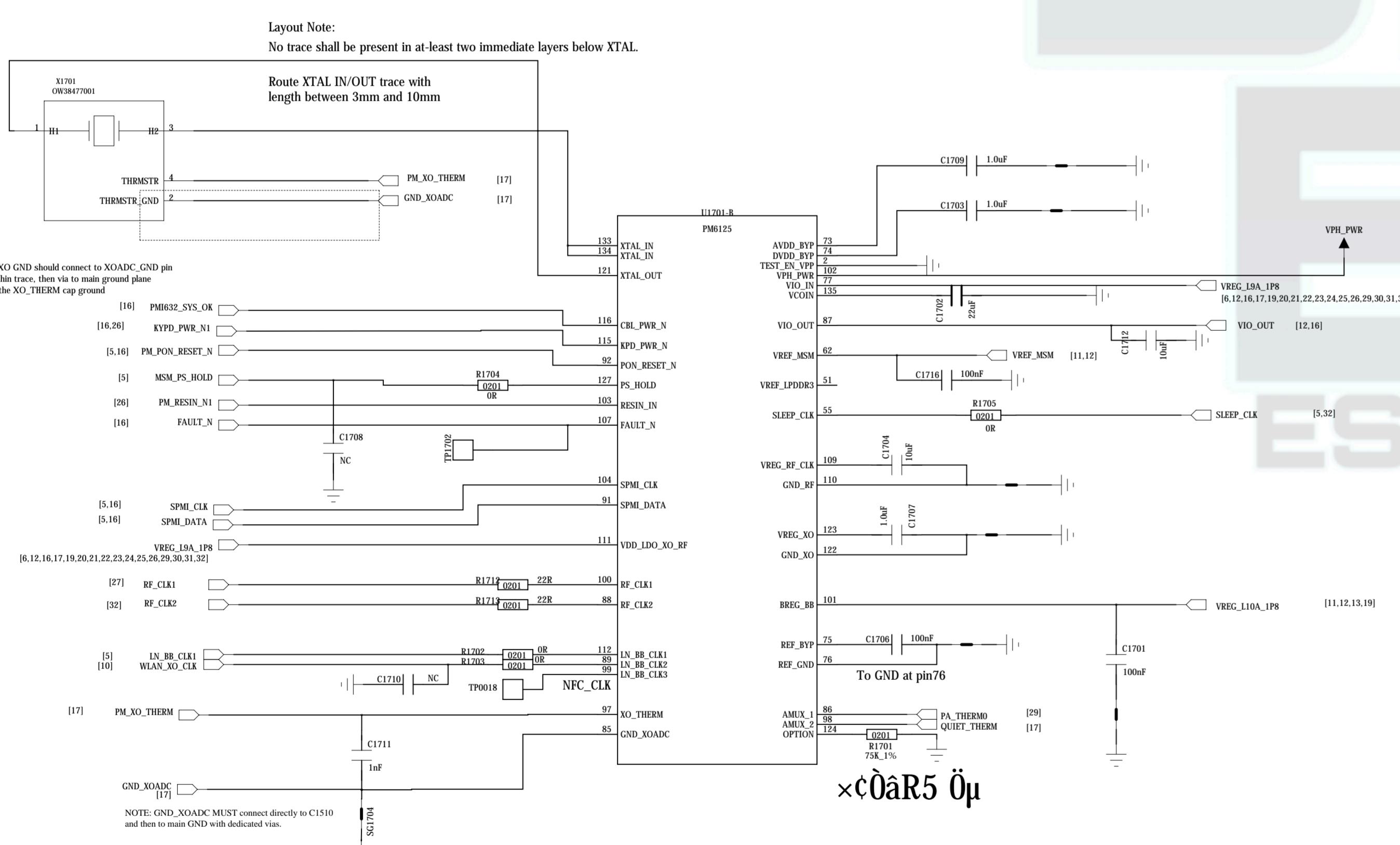
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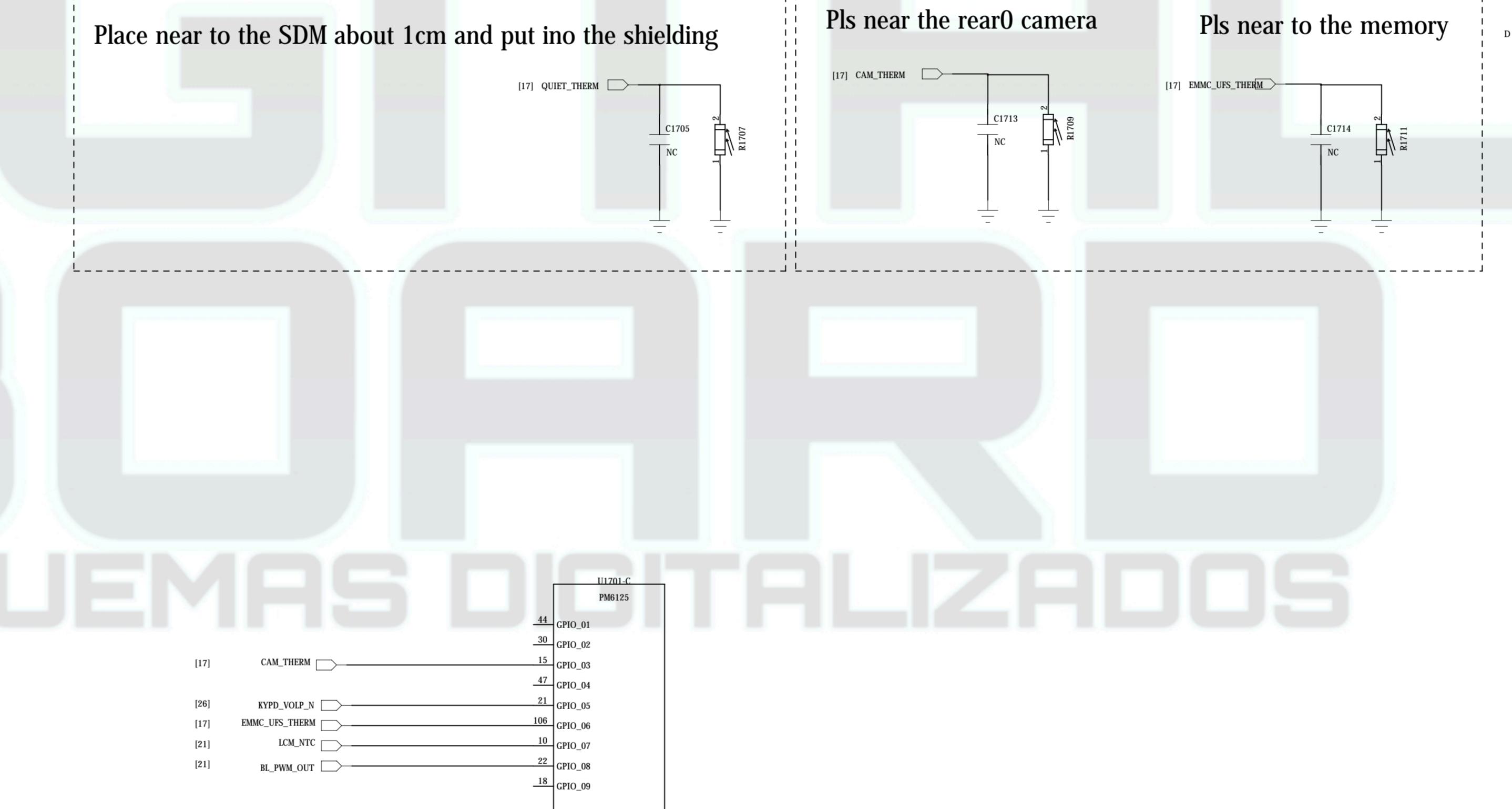
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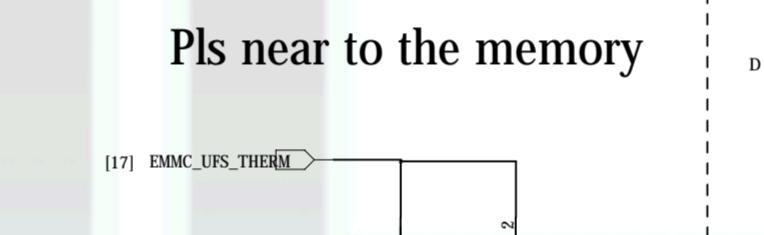
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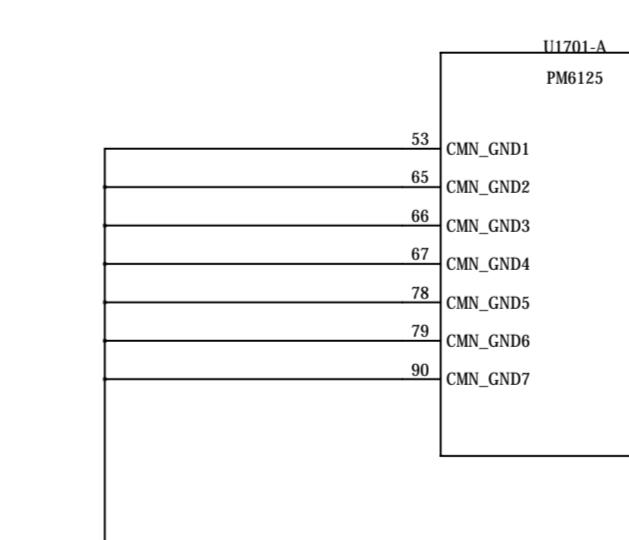
Place near to the SDM about 1cm and put into the shielding



Pls near to the rear0 camera

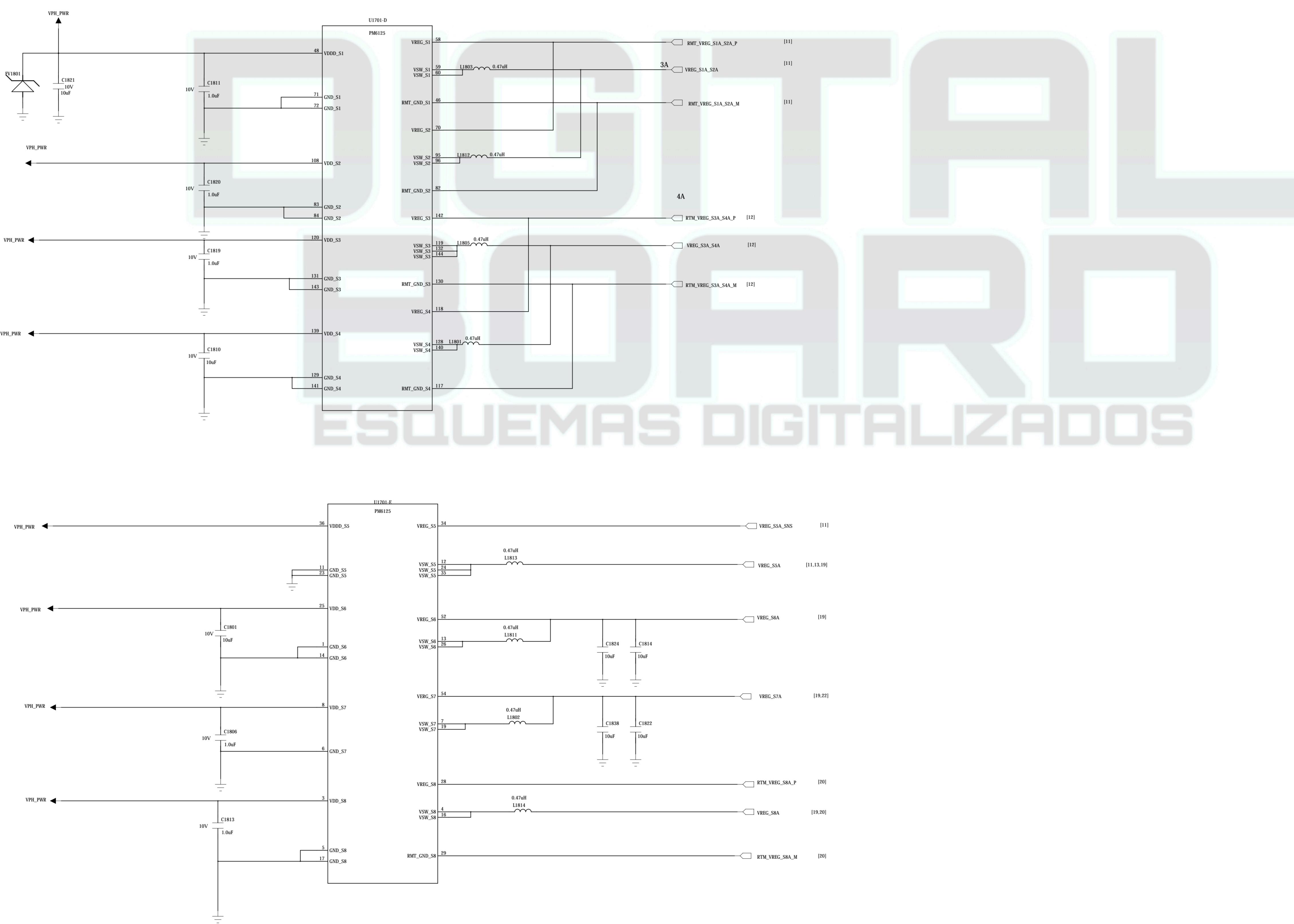


Pls near to the memory



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TITLE: <Title>
DRAWN: <Drawn By> DATE: <Drawn Date>
CHECKED: <Checked By> DATE: <Checked Date>
QUALITY CONTROL: <QC By> DATE: <QC Date>
RELEASED: <Released By> DATE: <Release Date>
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REVISION RECORD			
REV	ECO NO.	APPROVED	DATE



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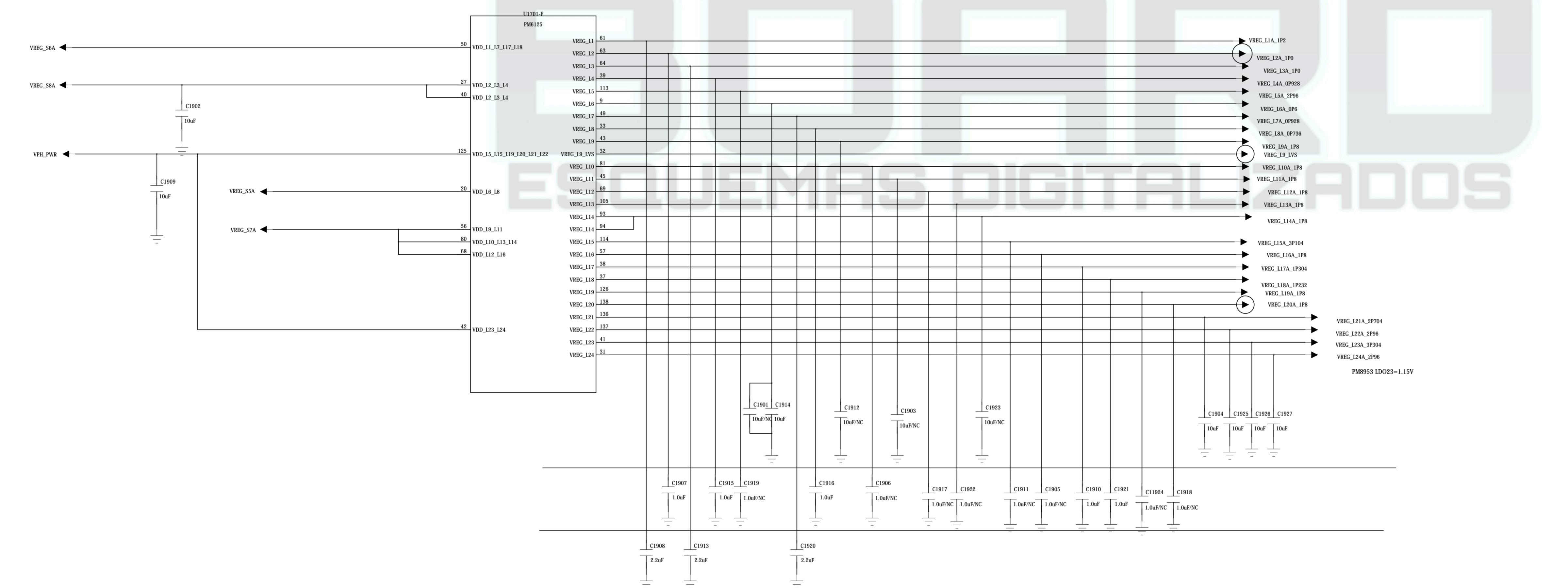
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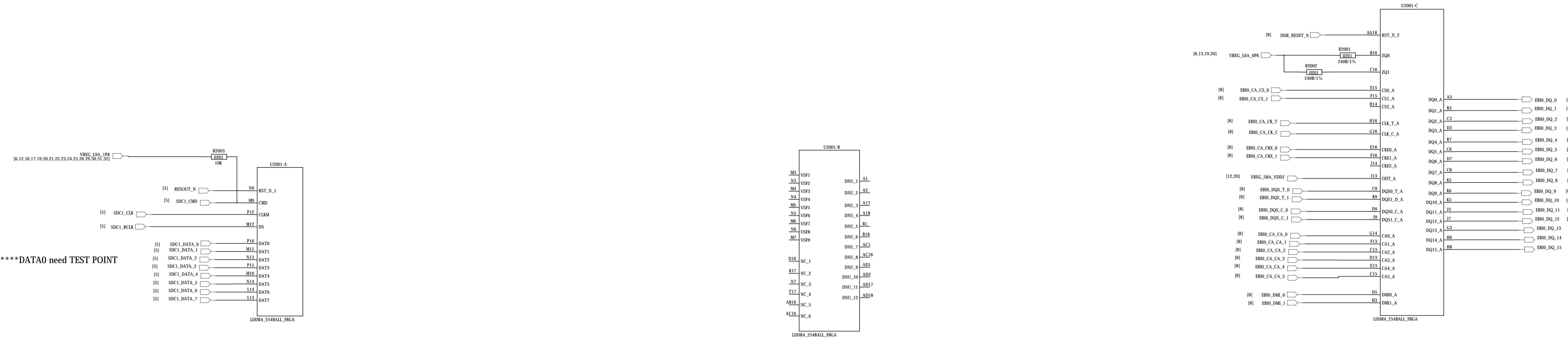


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TITLE:	<Title>		
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ate>	<Code>	A0	<Drawing Number><Revision>
se Date>	SCALE: <Scale>	SHEET: 10	34

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ITR	ECO NO.	APPROVED	DATE

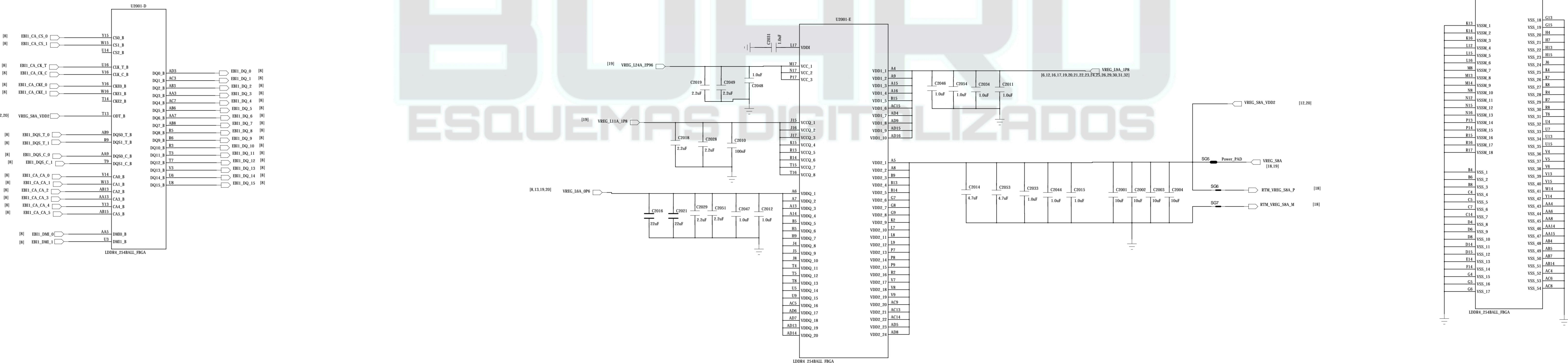
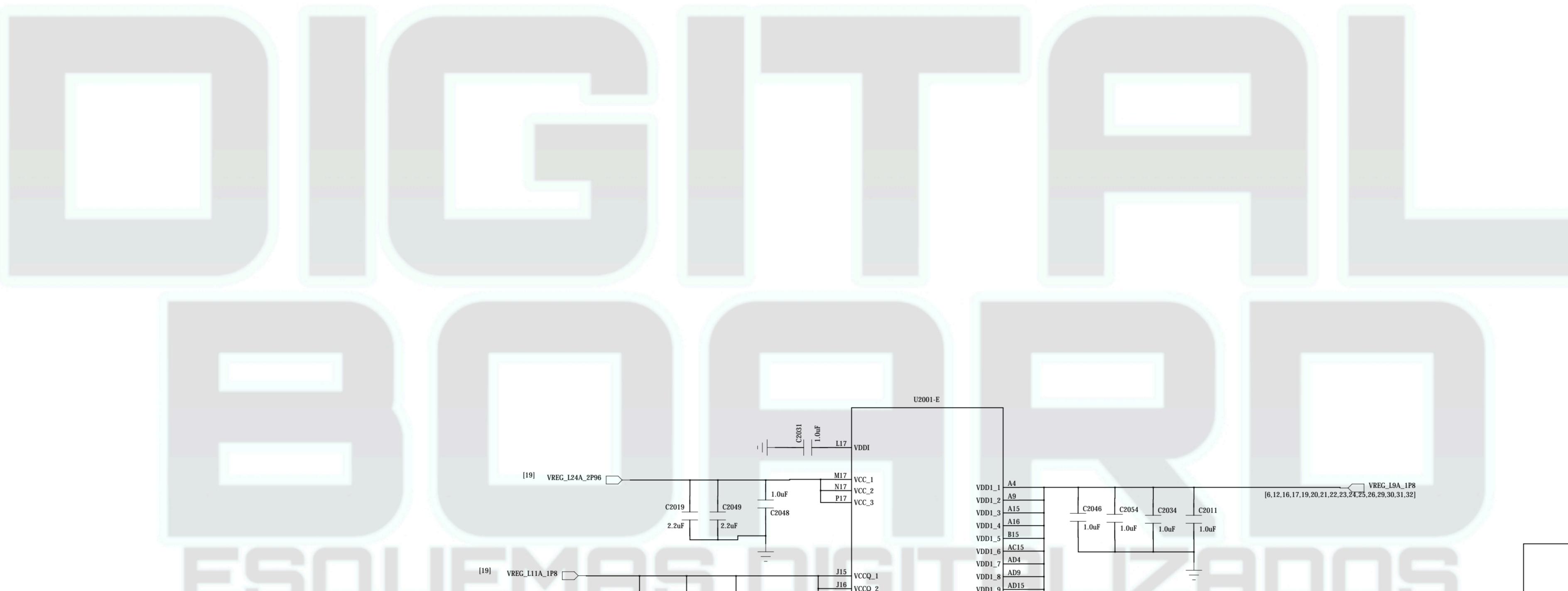
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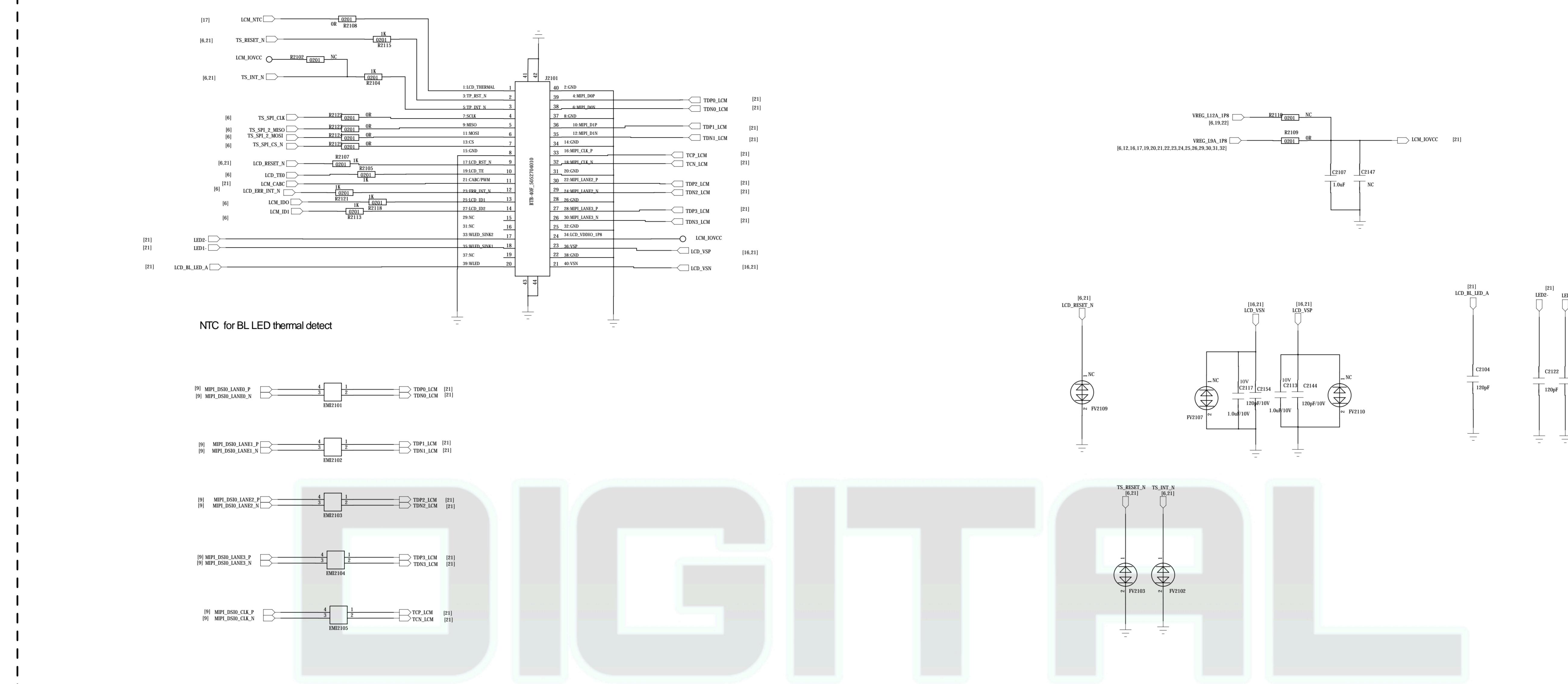
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B

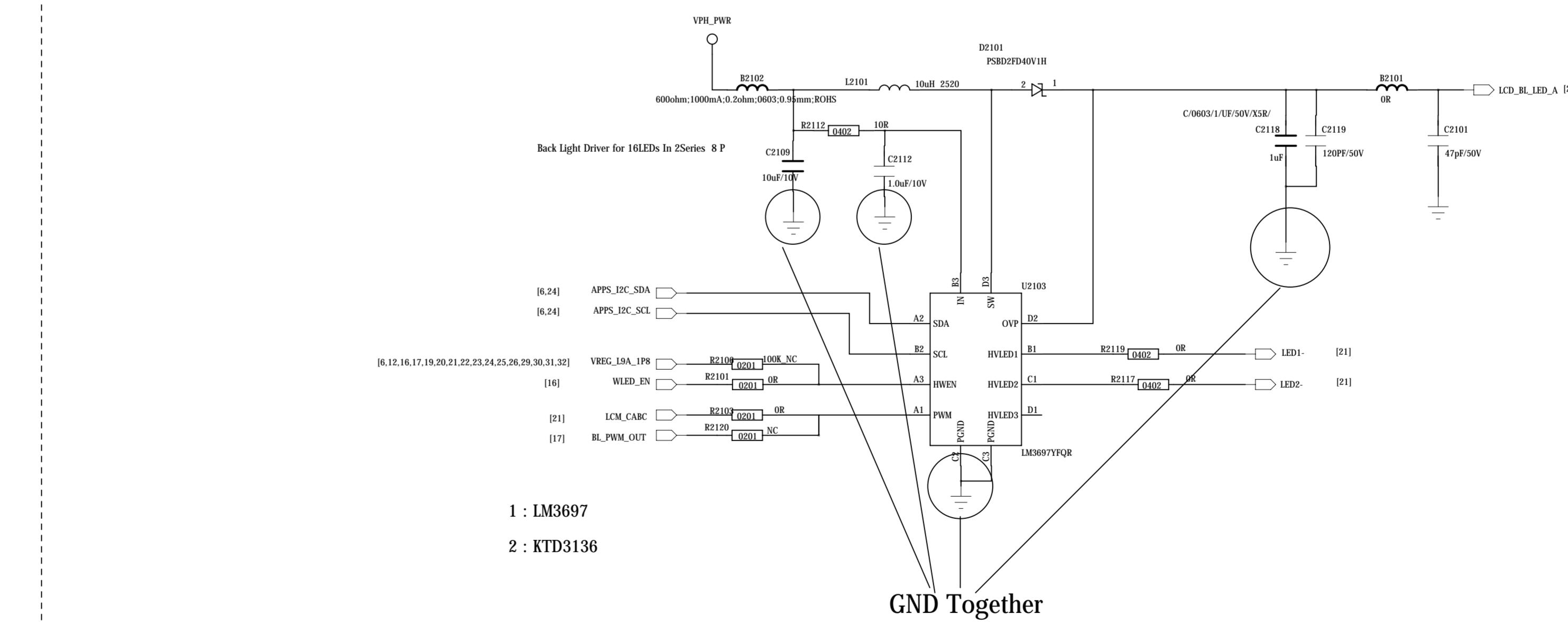
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RELEASED:	<Released By>	DATED: <Release Date>
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CODE:	SIZE:	DRAWING NO.:
<Code> A0 <Drawing Number><Revision>		

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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routing should directly from BUCK S



COMPANY:	<Company Name>		
TITLE:	<Title>		
te>	CODE:	SIZE:	DRAWING NO:
Date>	<Code>	A0	<Drawing Number><Revision>

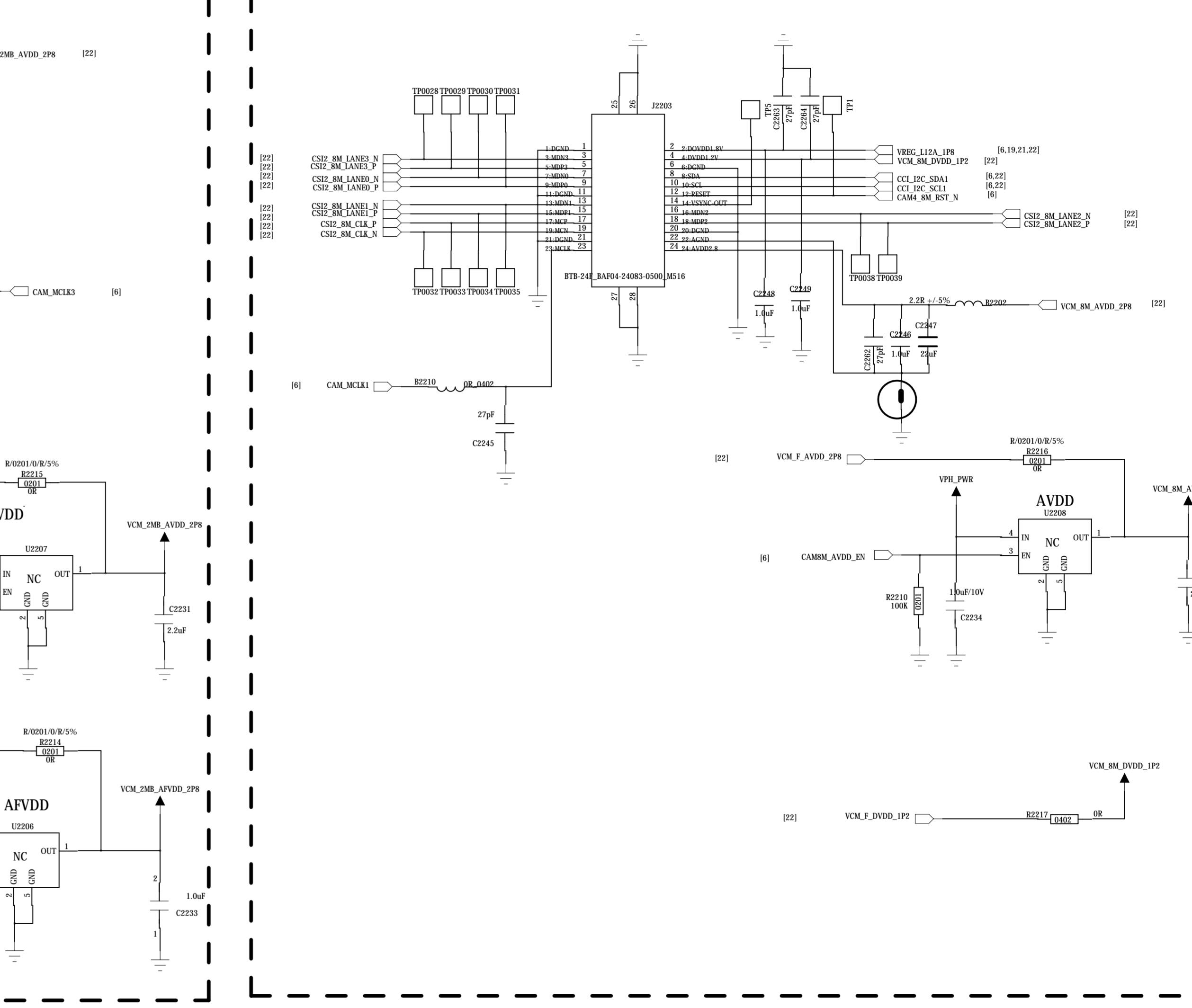
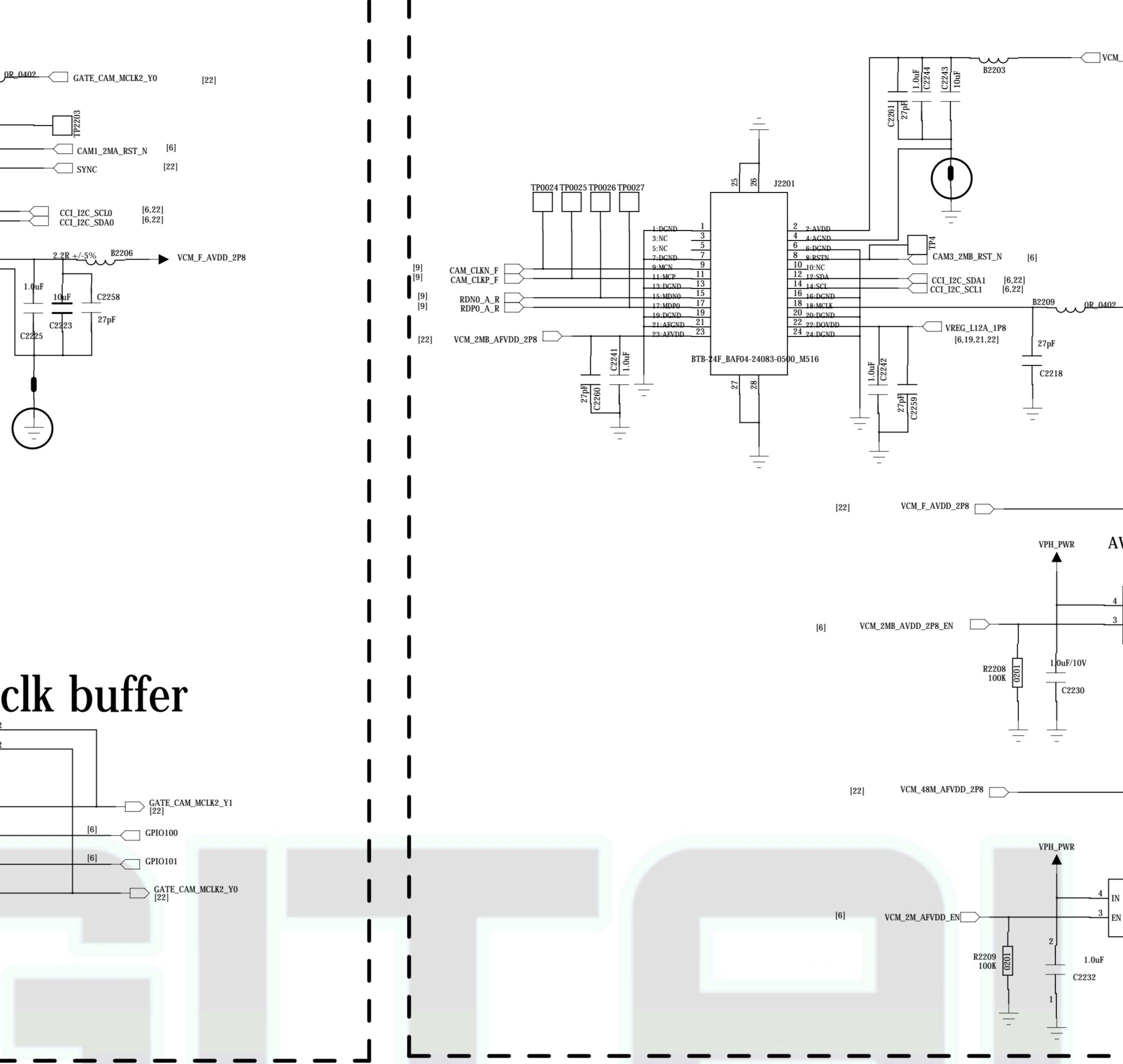
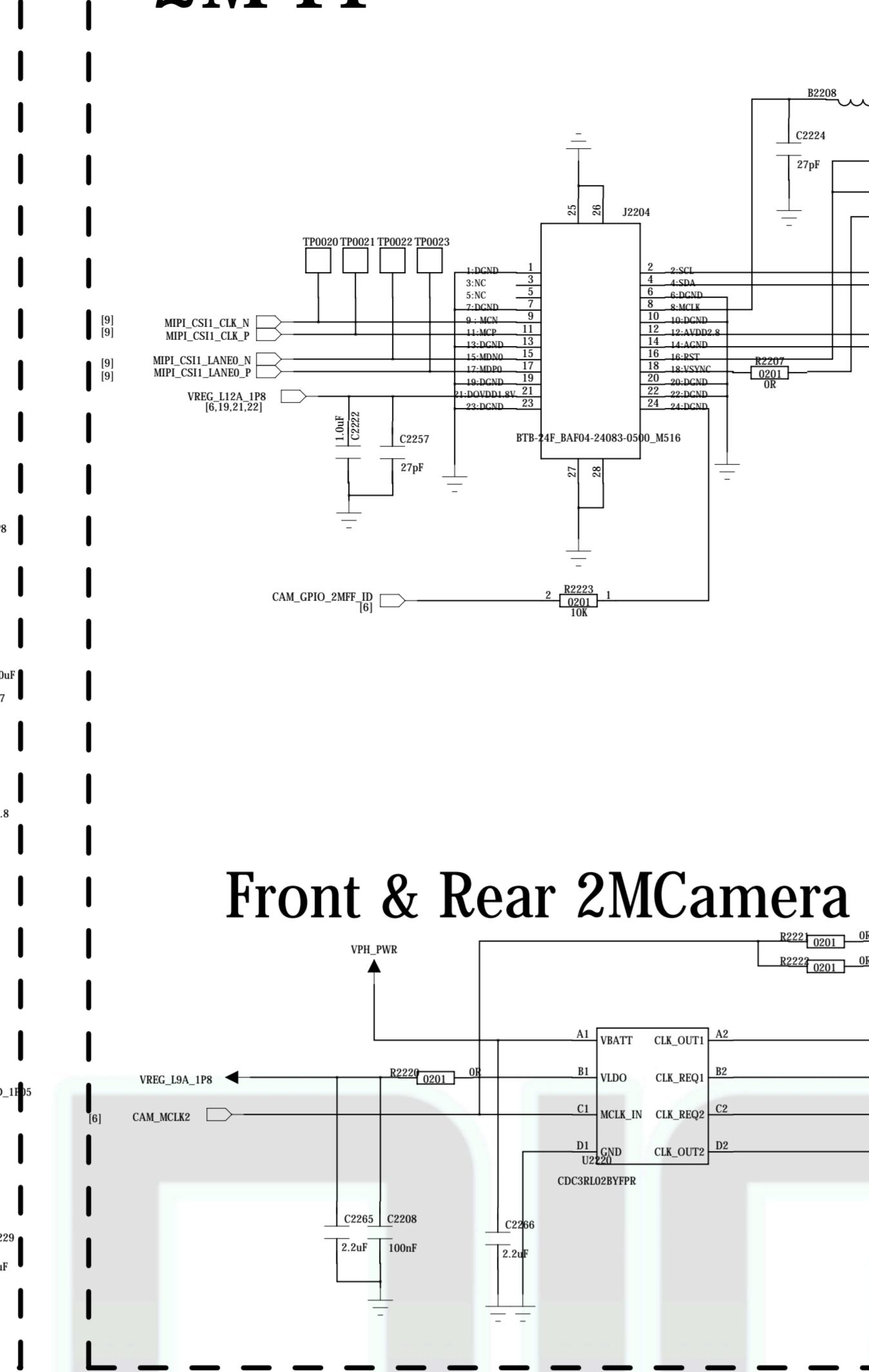
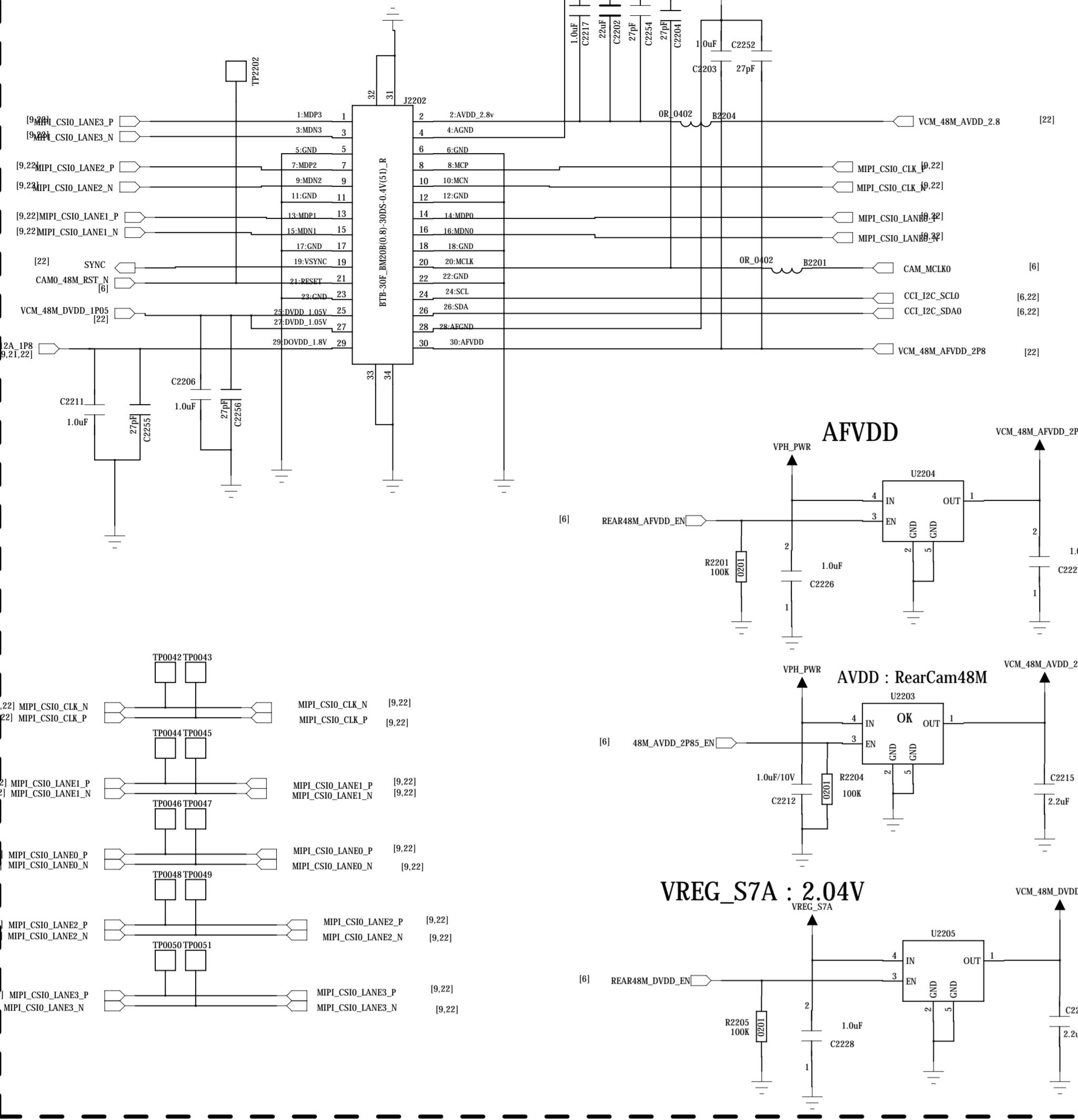
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REV	ECO NO.	APPROVED	DATE

Main Camera A

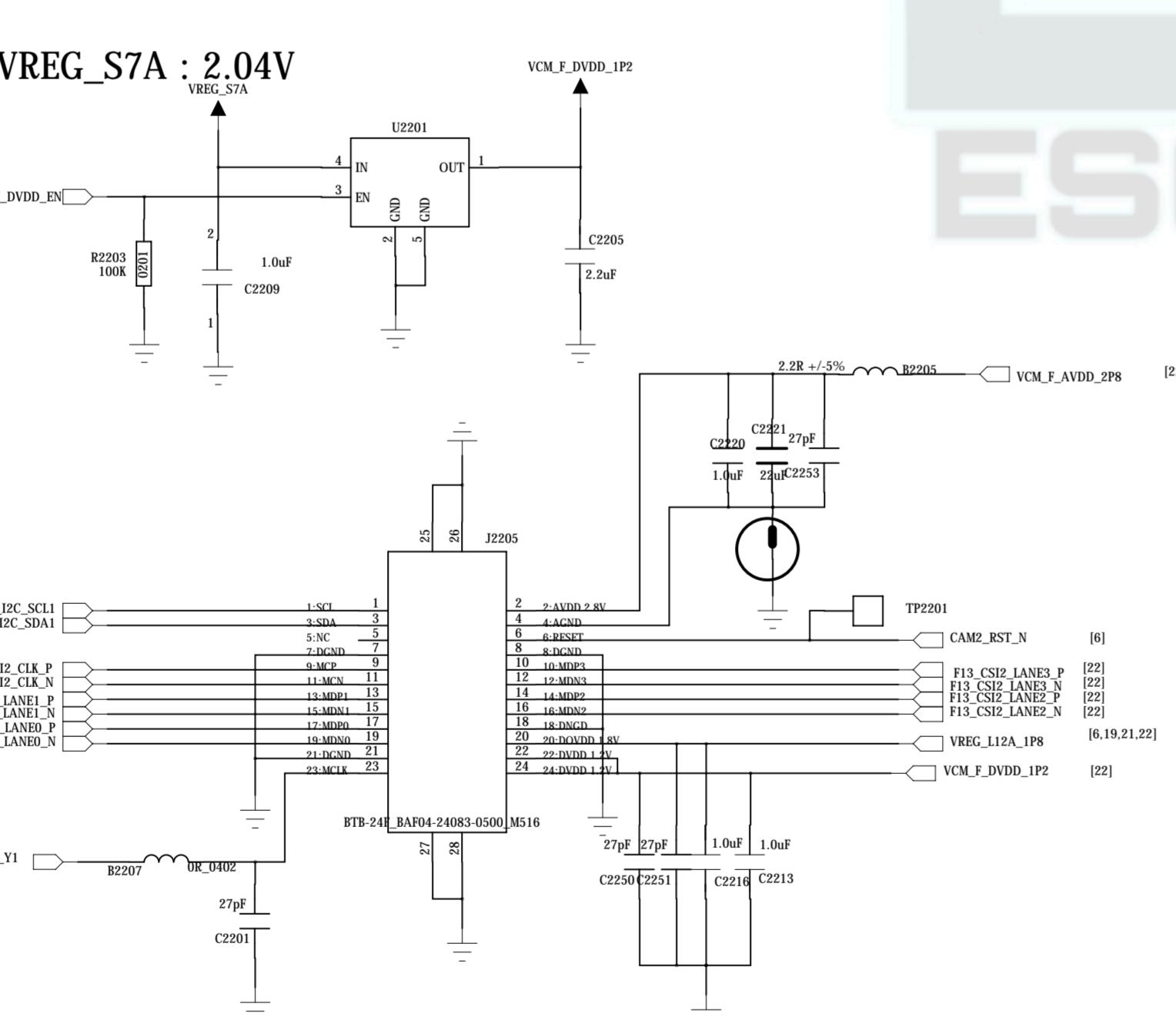
Main Camera B 2M 2M FF

Main Camera C 2M 2M AF

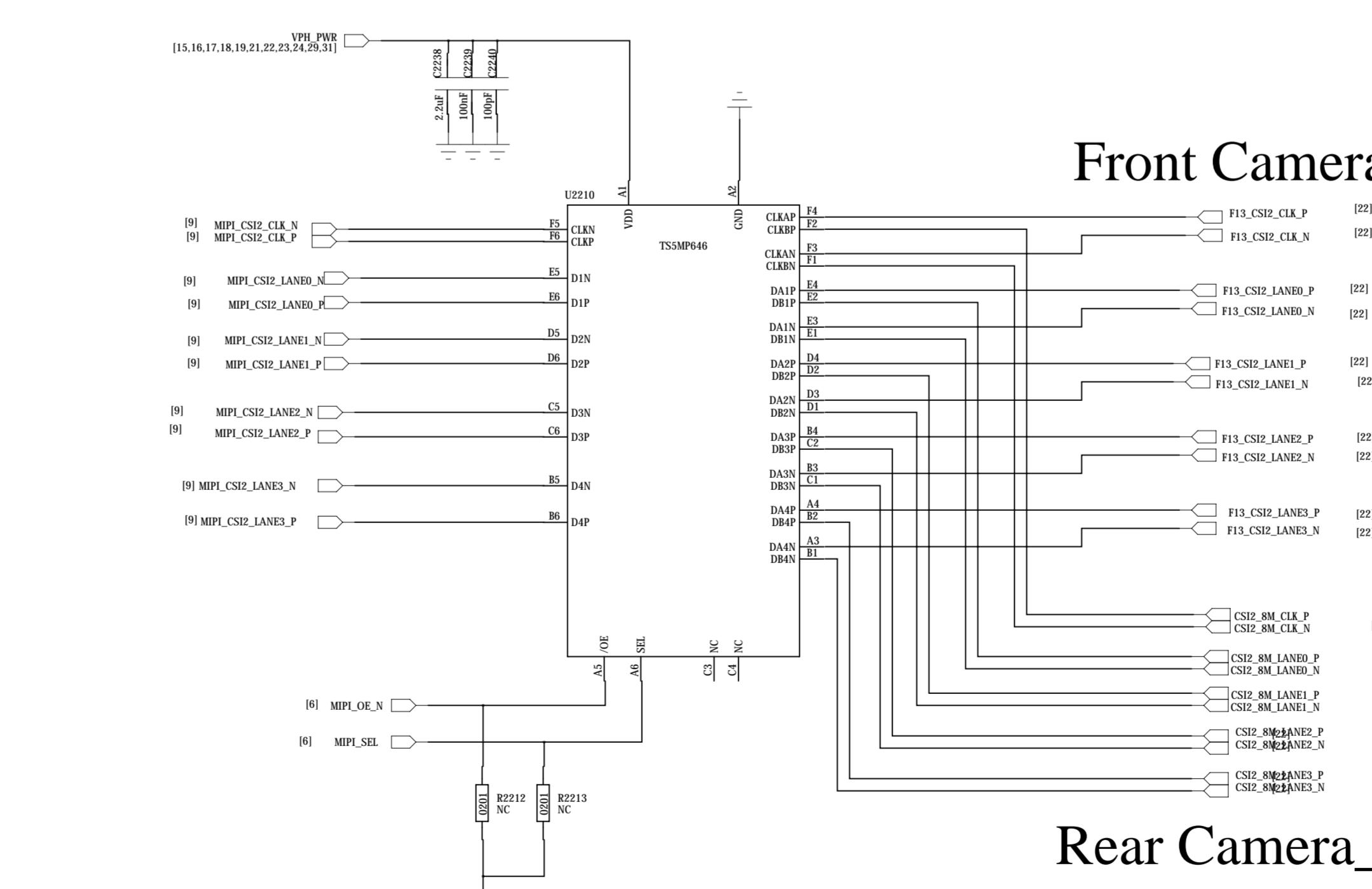
Main Camera D 8M



Front Camera



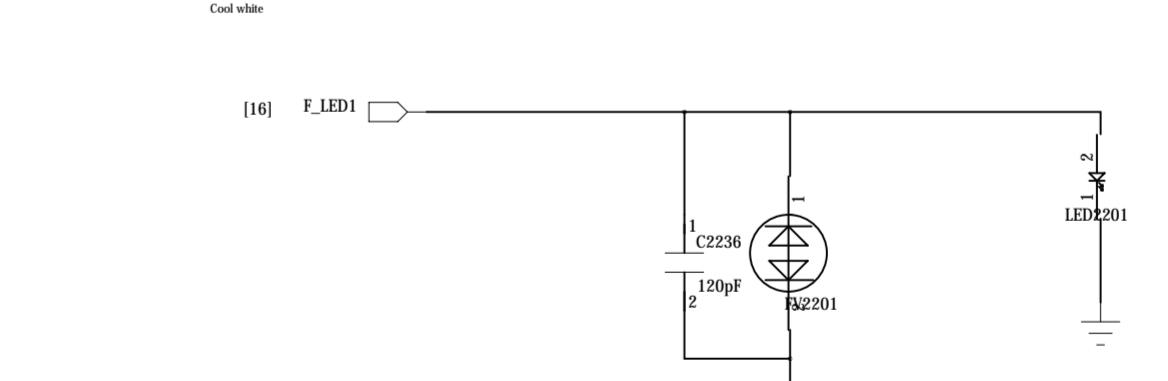
MIPI SW



Front Camera

Rear Camera_8M

Main FLASHLIGHT



Schematic design notice of "63_PERI_CAMERA_KEYPAD" page.

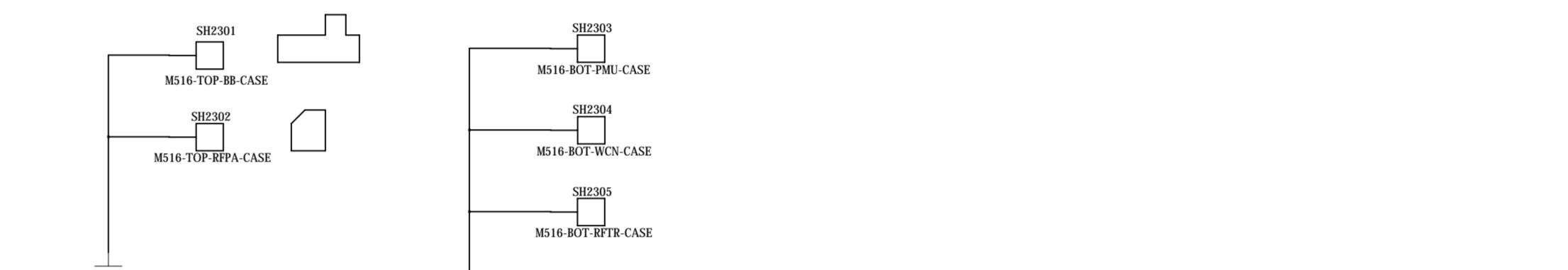
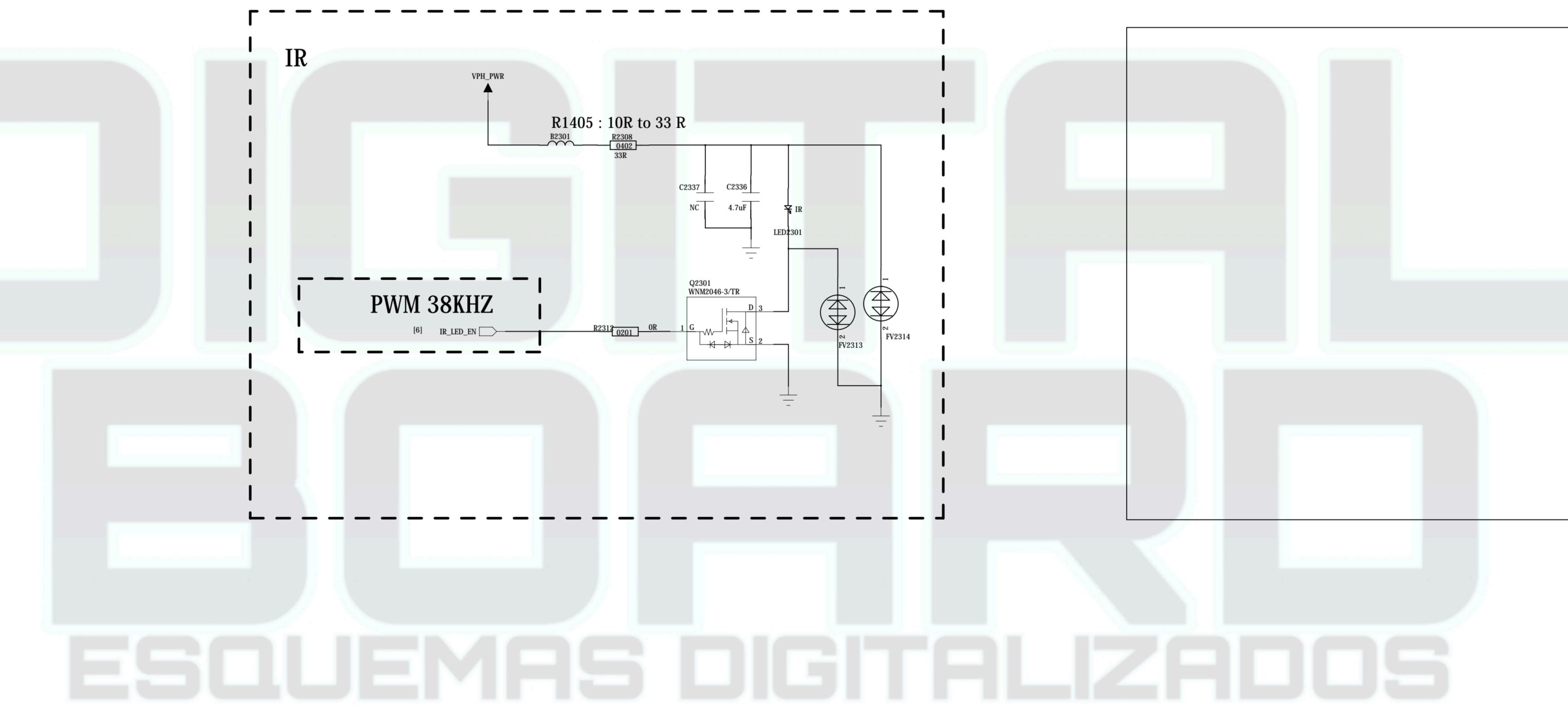
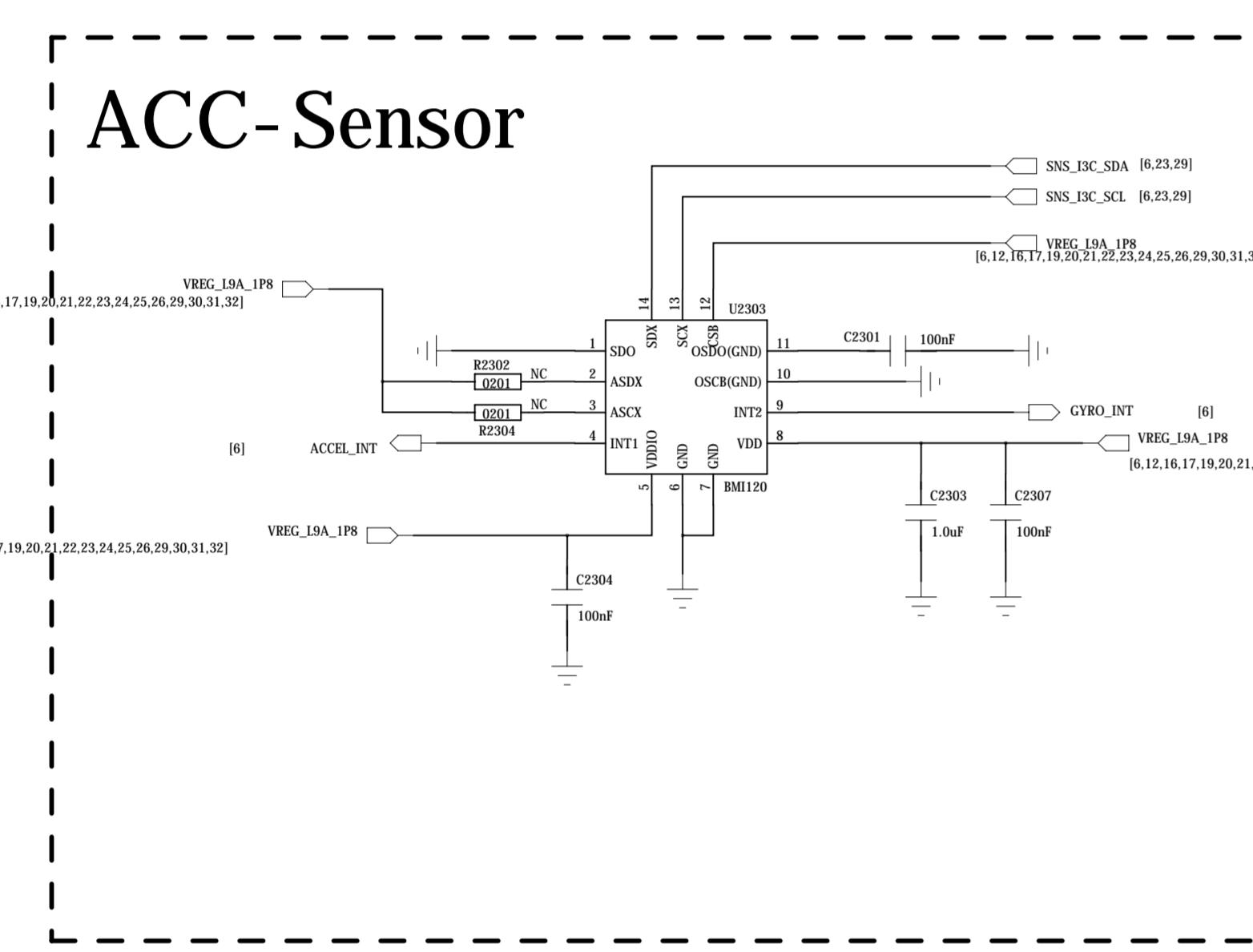
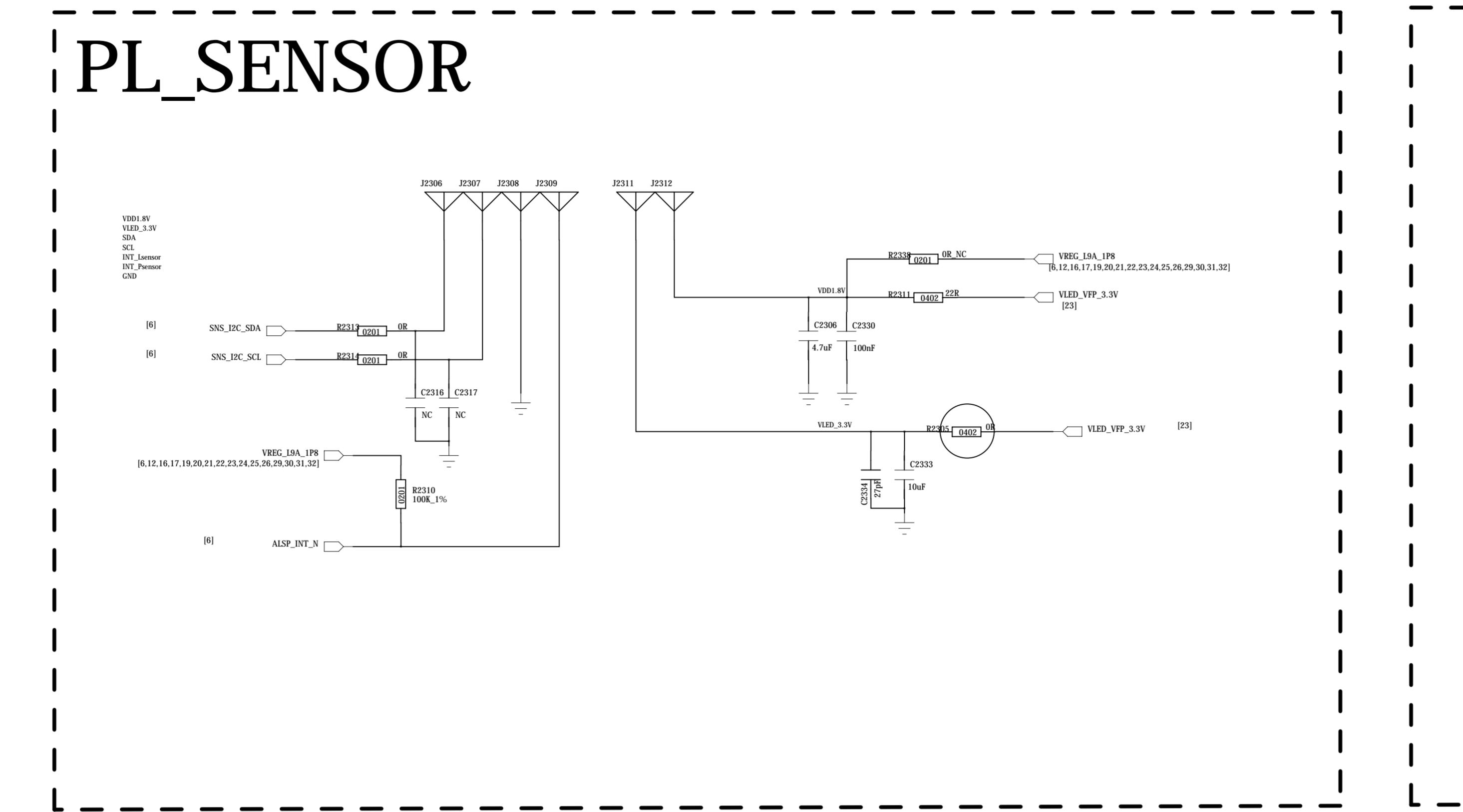
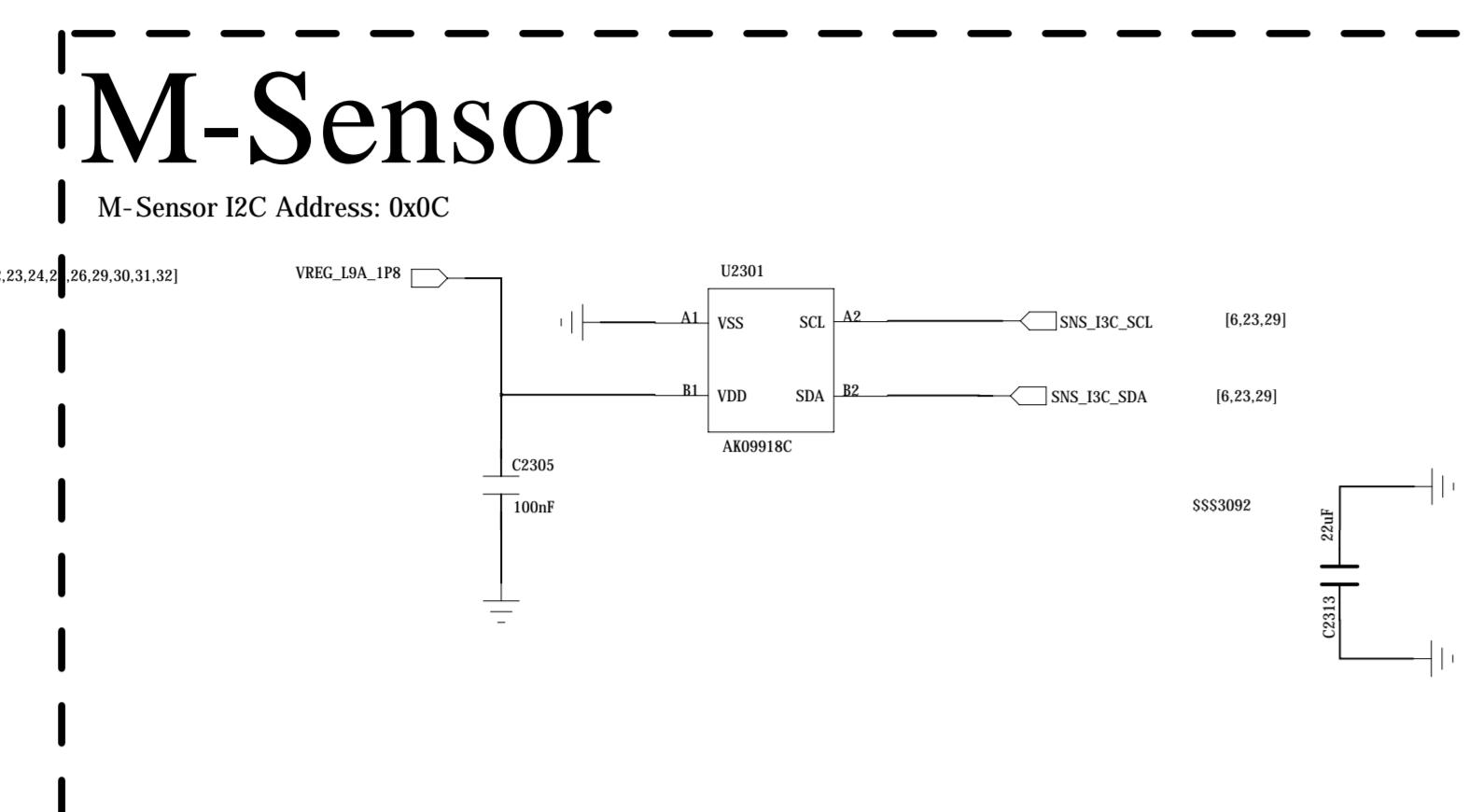
Note 62-1: The VCC of I2C_0 is pulled to "VCAM_IO_PMU".

Note 62-2: I2C control interface of front camera (with AF) must be assigned to I2C-2 bus when PIP/VIV feature is supported.

Note 62-3: Reserve a capacitor (27pF) on camera's MCLK and shunt it to GND to prevent GPS de-sense.

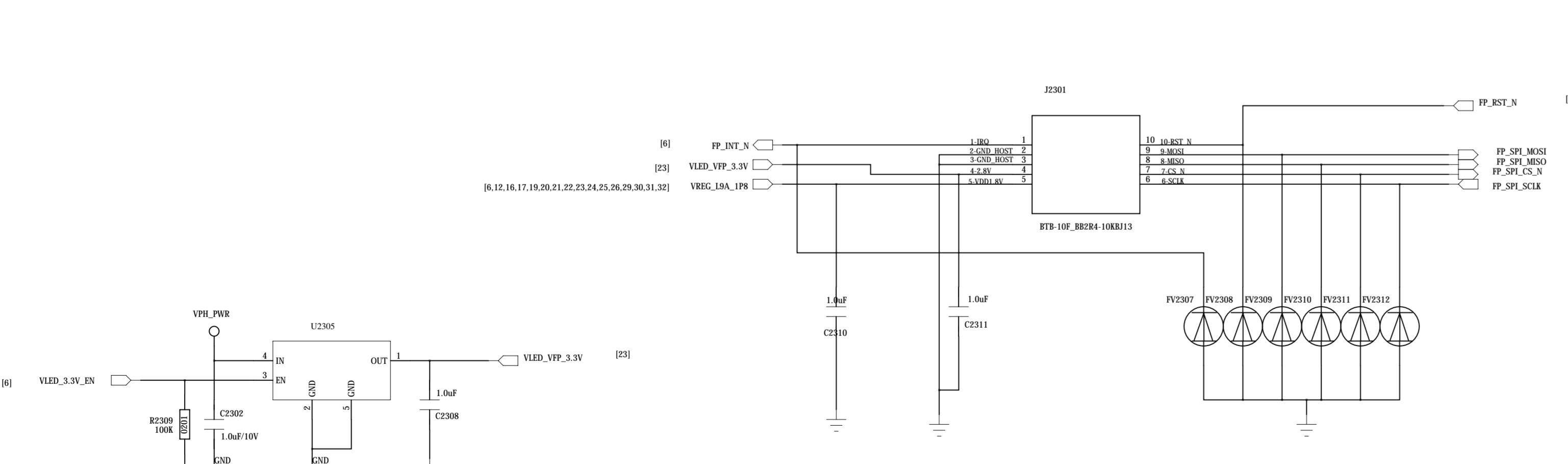
I2C ADDRESS 2M 0X7a(wite) 0x7b(read)

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CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>
CODE:	SIZE:	DRAWING NO.:	REV.
<Code>	A0	<Drawing Number>	<Revision>



FingerPrint-Sensor

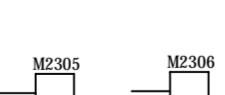
Default: I3C for Mag sensor + ACCEL/GYRO, I2C for the PL Sensor



LDO 3.3V or 3.0V for FP & VLEDA

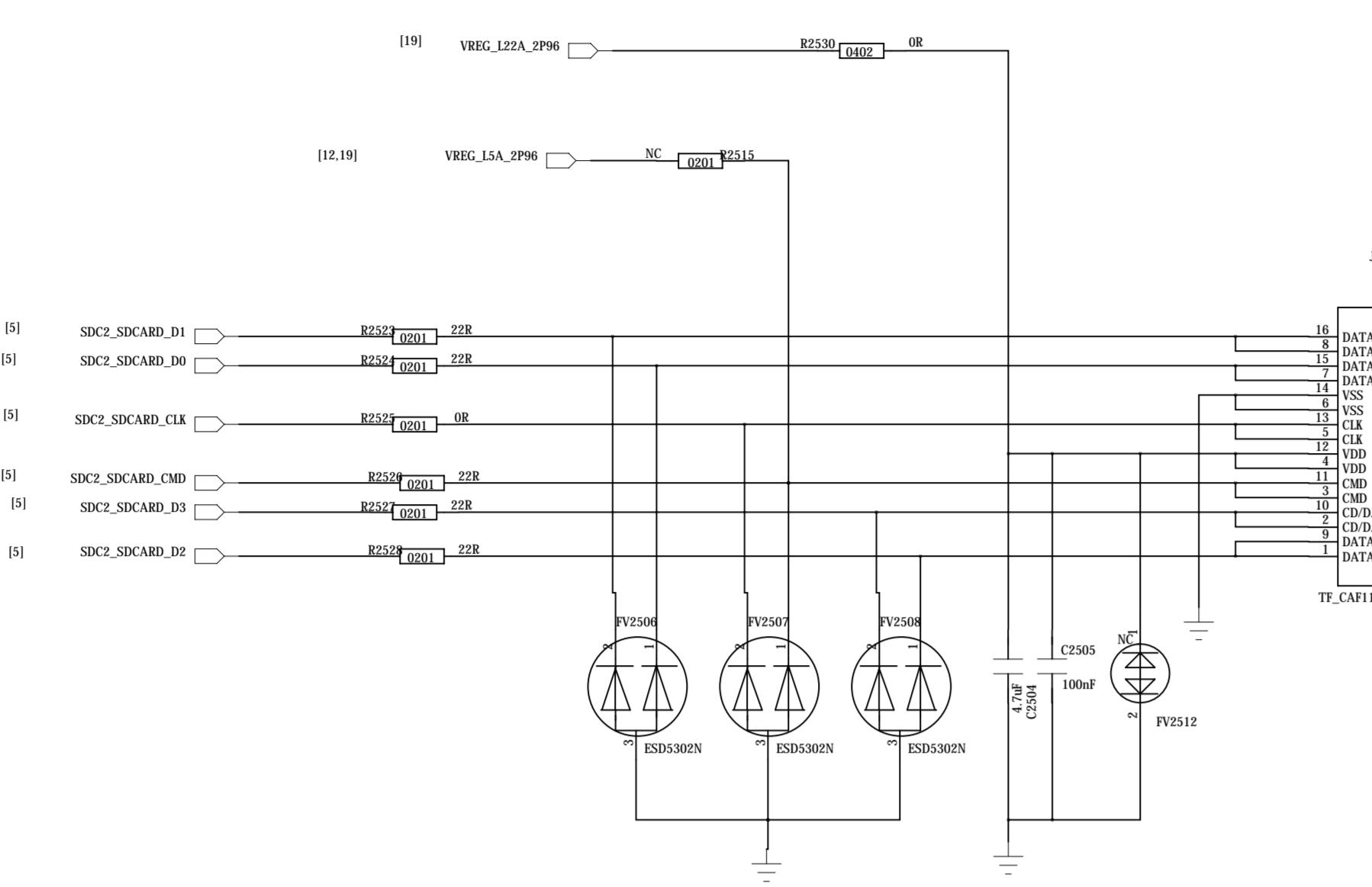
BAD MARK POINT

MARK POINT

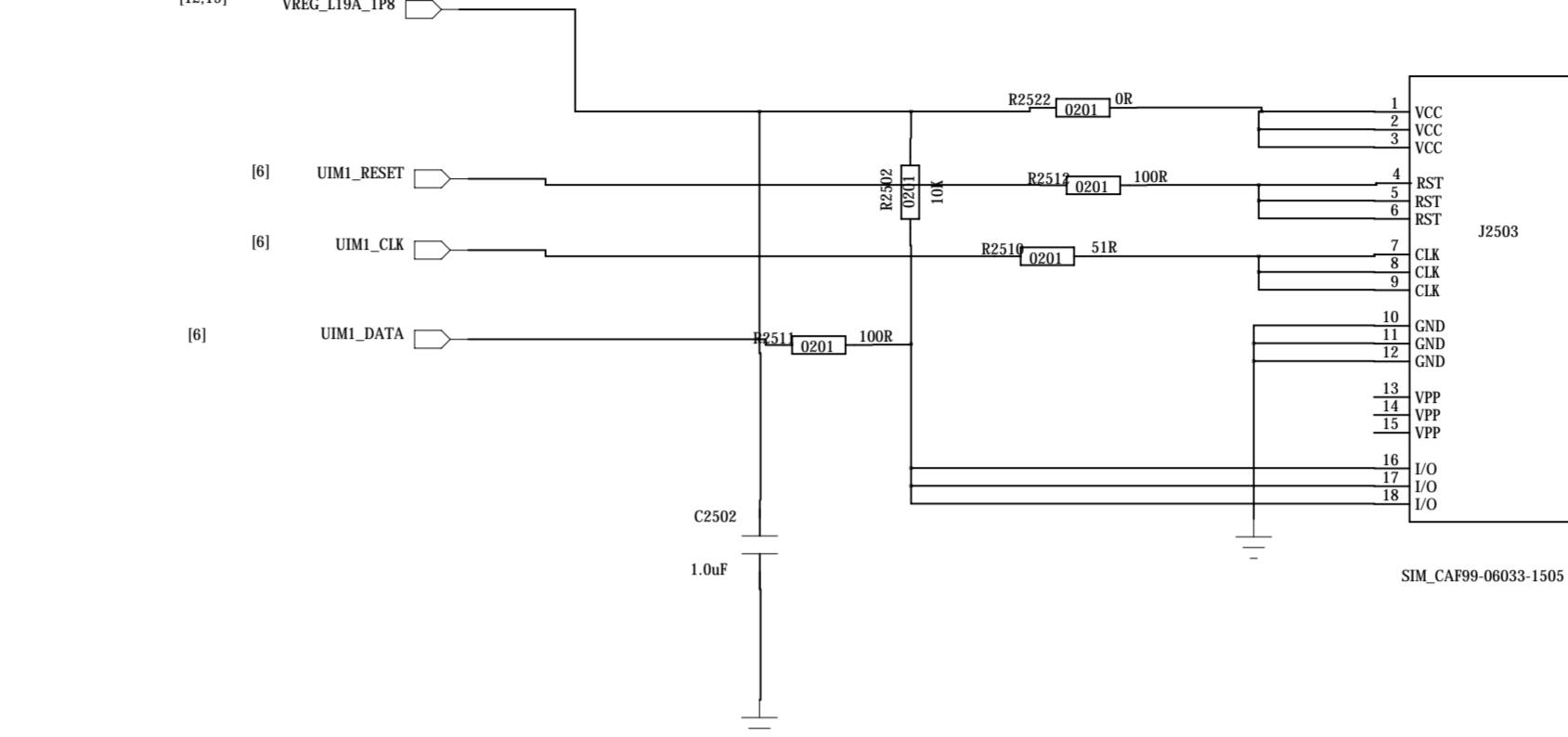


REVISION RECORD		
REV	ECO NO.	APPROVED:

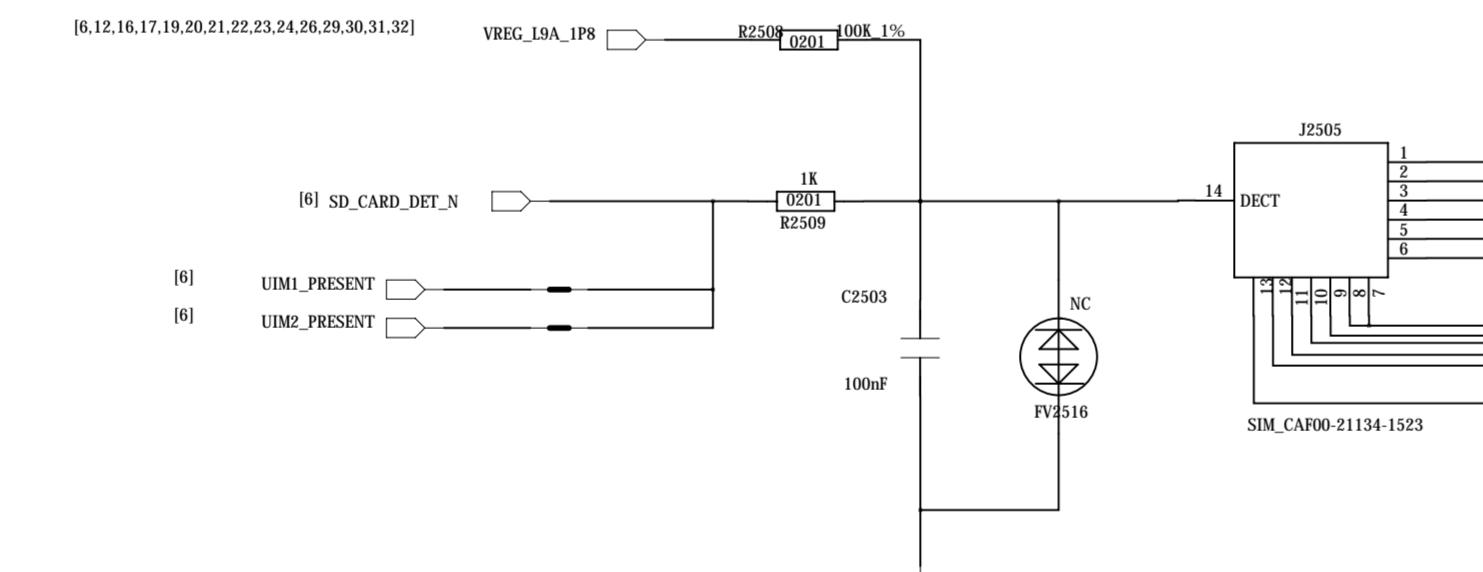
T-Card



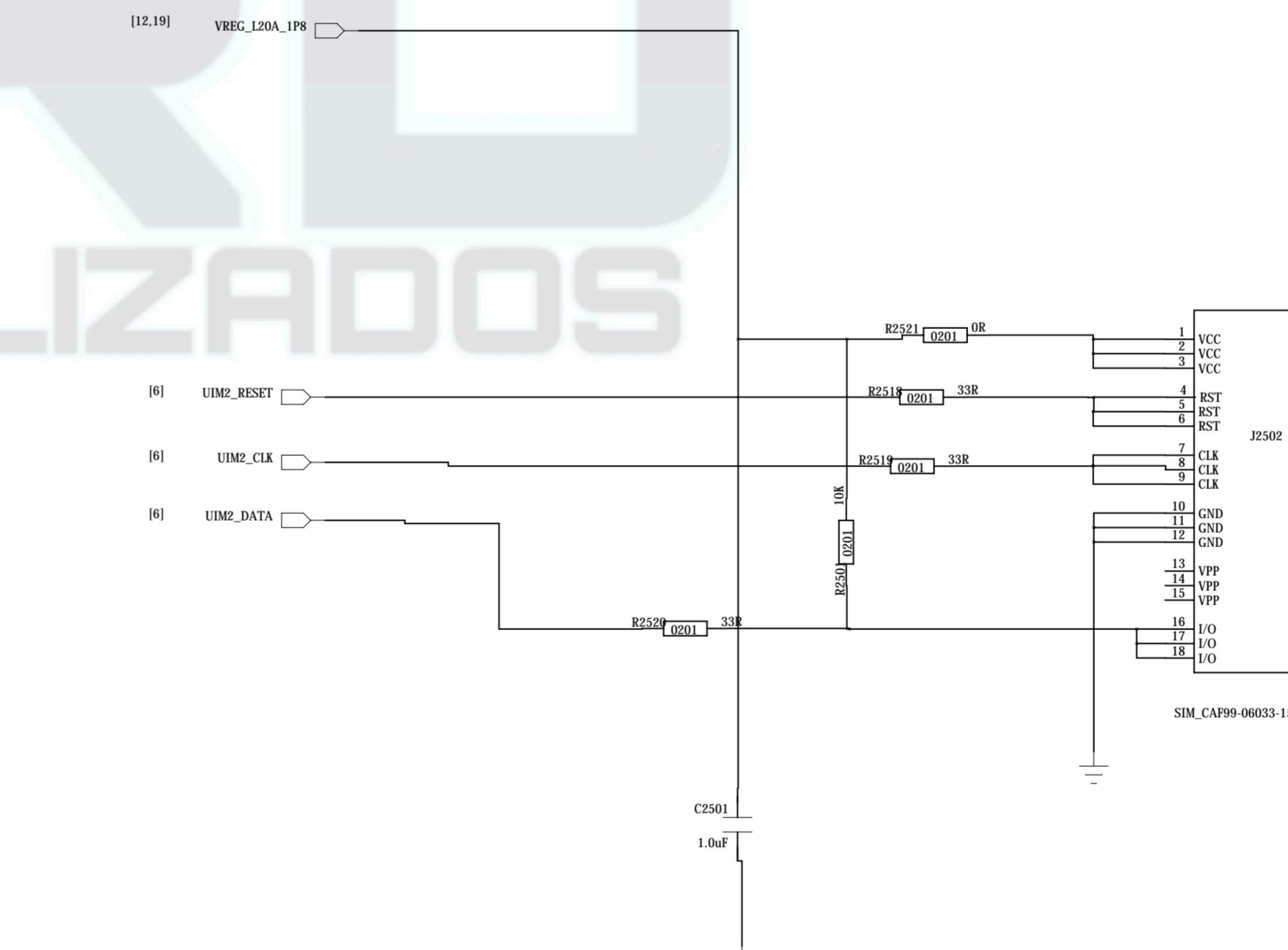
SIM1



INT



SIM2



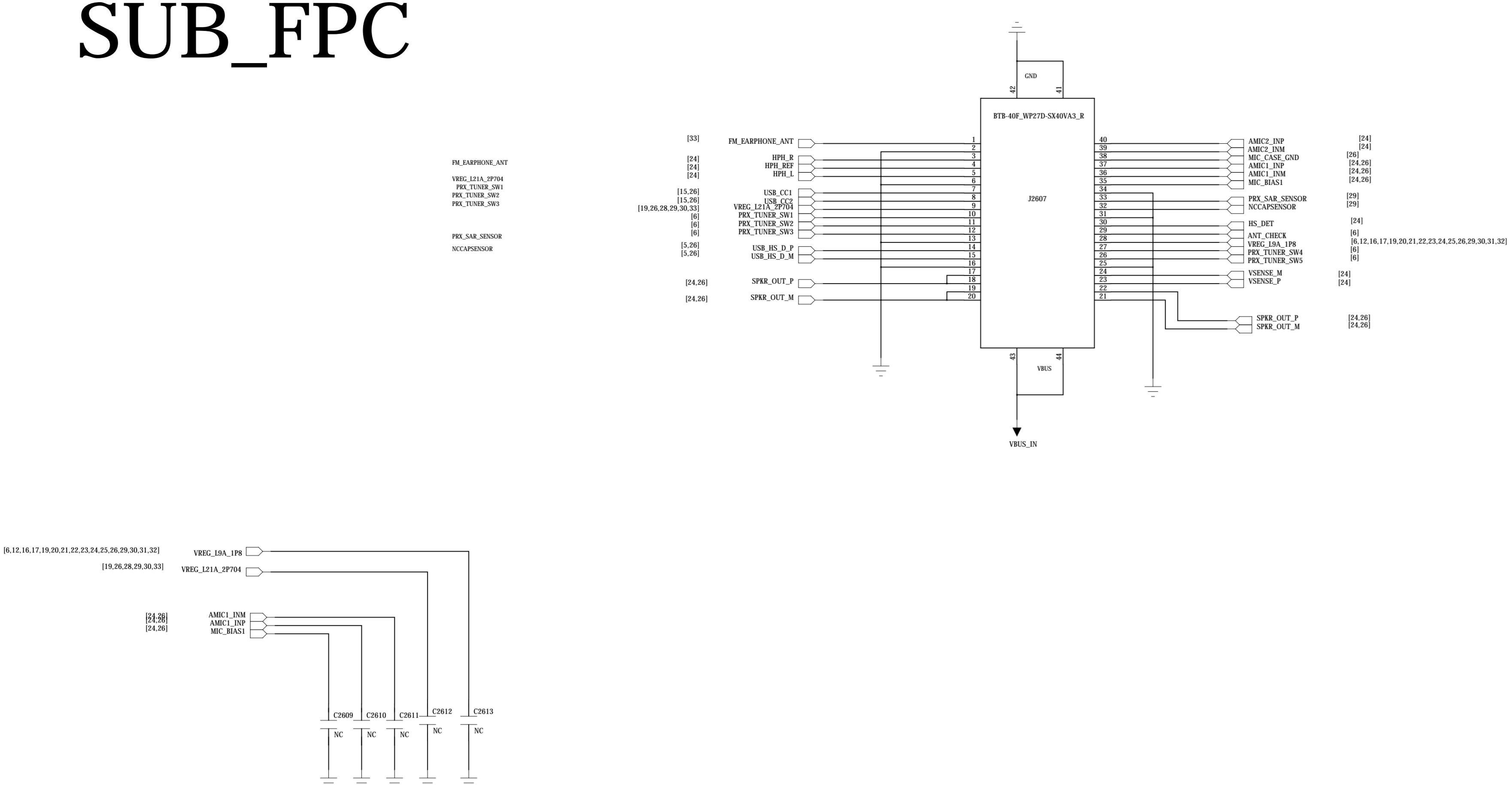
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CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

<Code> A0 <Drawing Number><Revision>

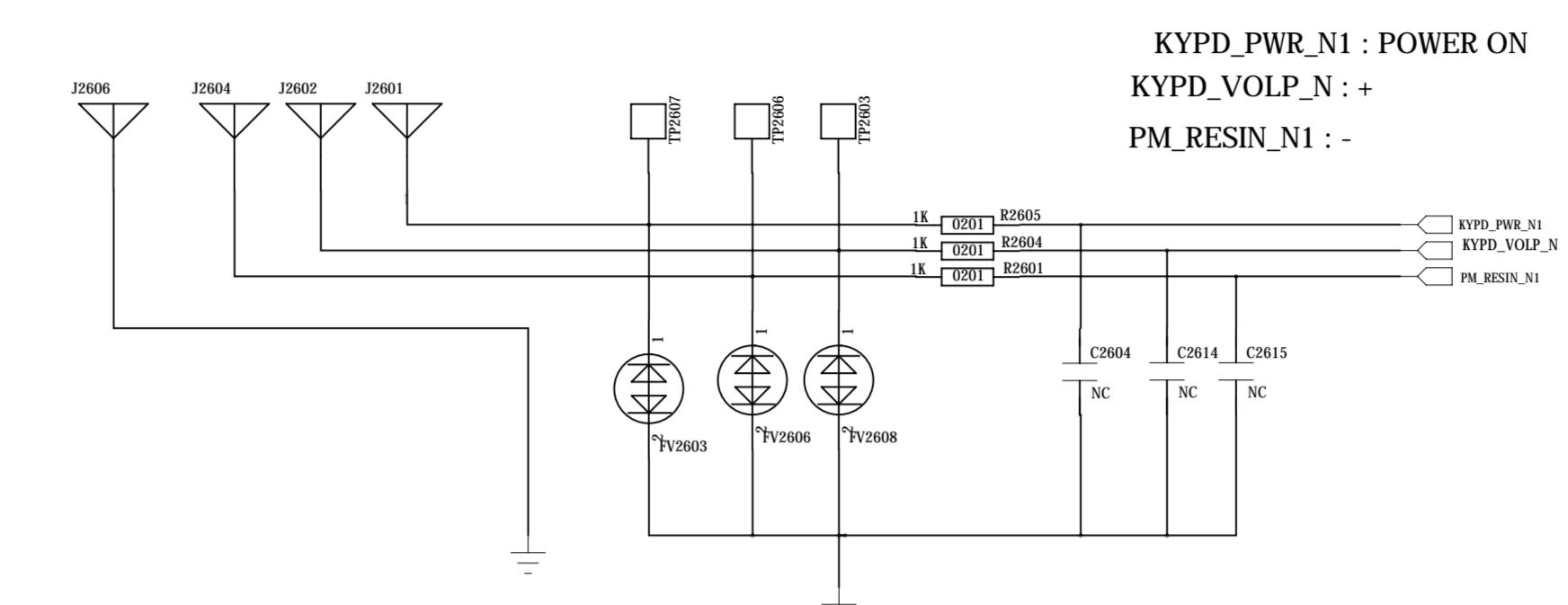
SCALE: <Scale>

REV: 26 34

SUB_FPC



Power Key

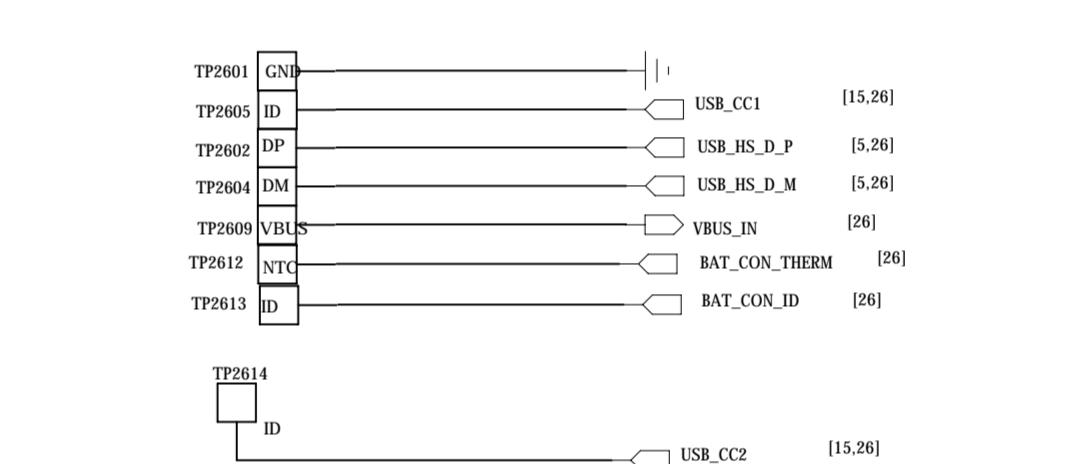


Schematic design notice of "65_PERI_Dual_SIM_ICUSB_KEYPAD" page.

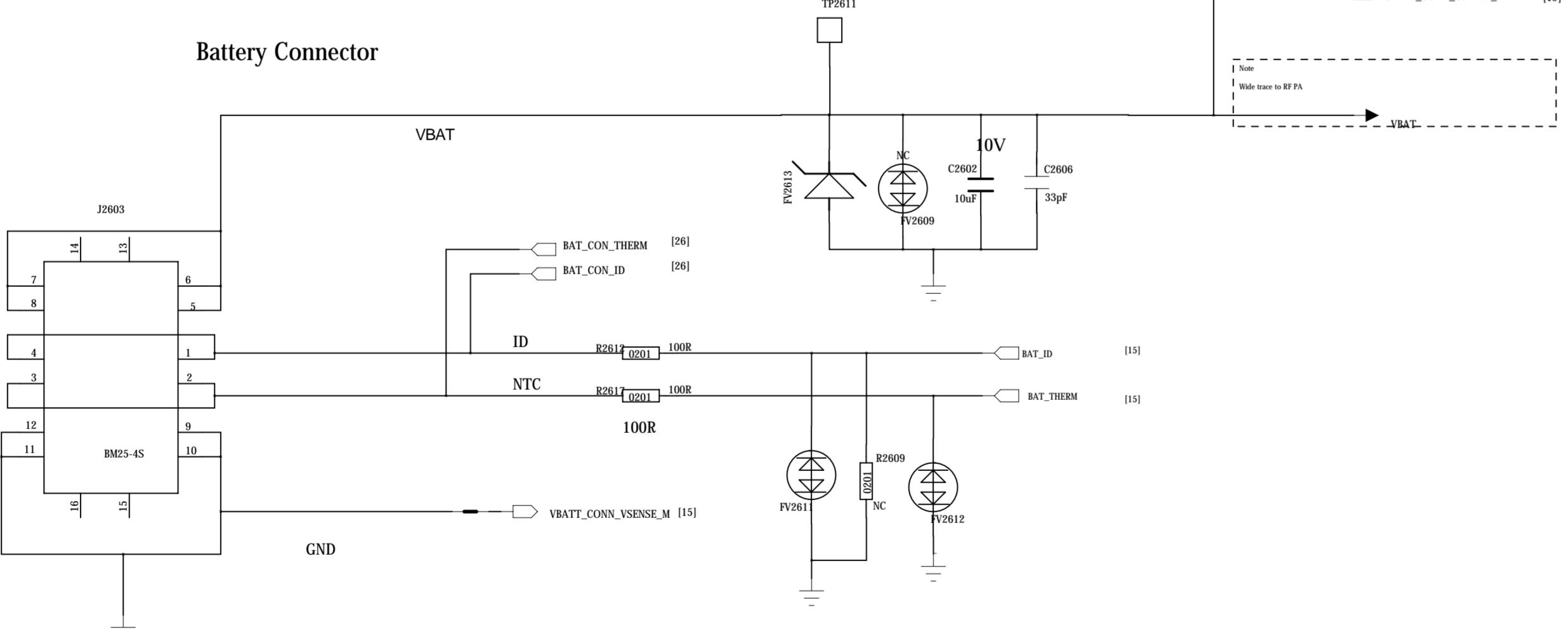
Note 65-1: DO NOT put pull-up resistor on PWRKEY

Note 65-2: Volume Up : HOME Key / GND
Volume Down : KPGOHS / KPGOLC KPGOLC / GND

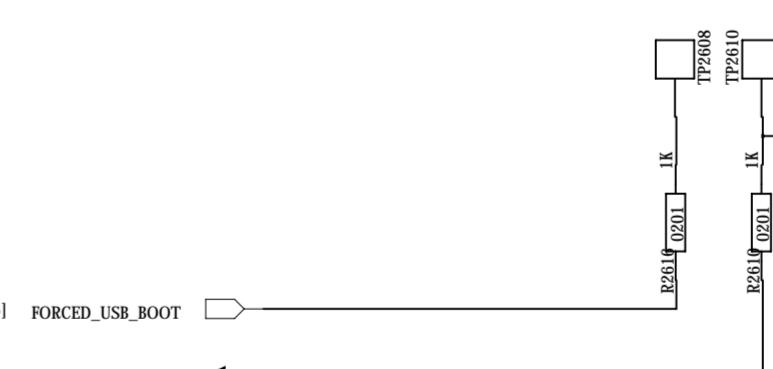
Test point



BAT

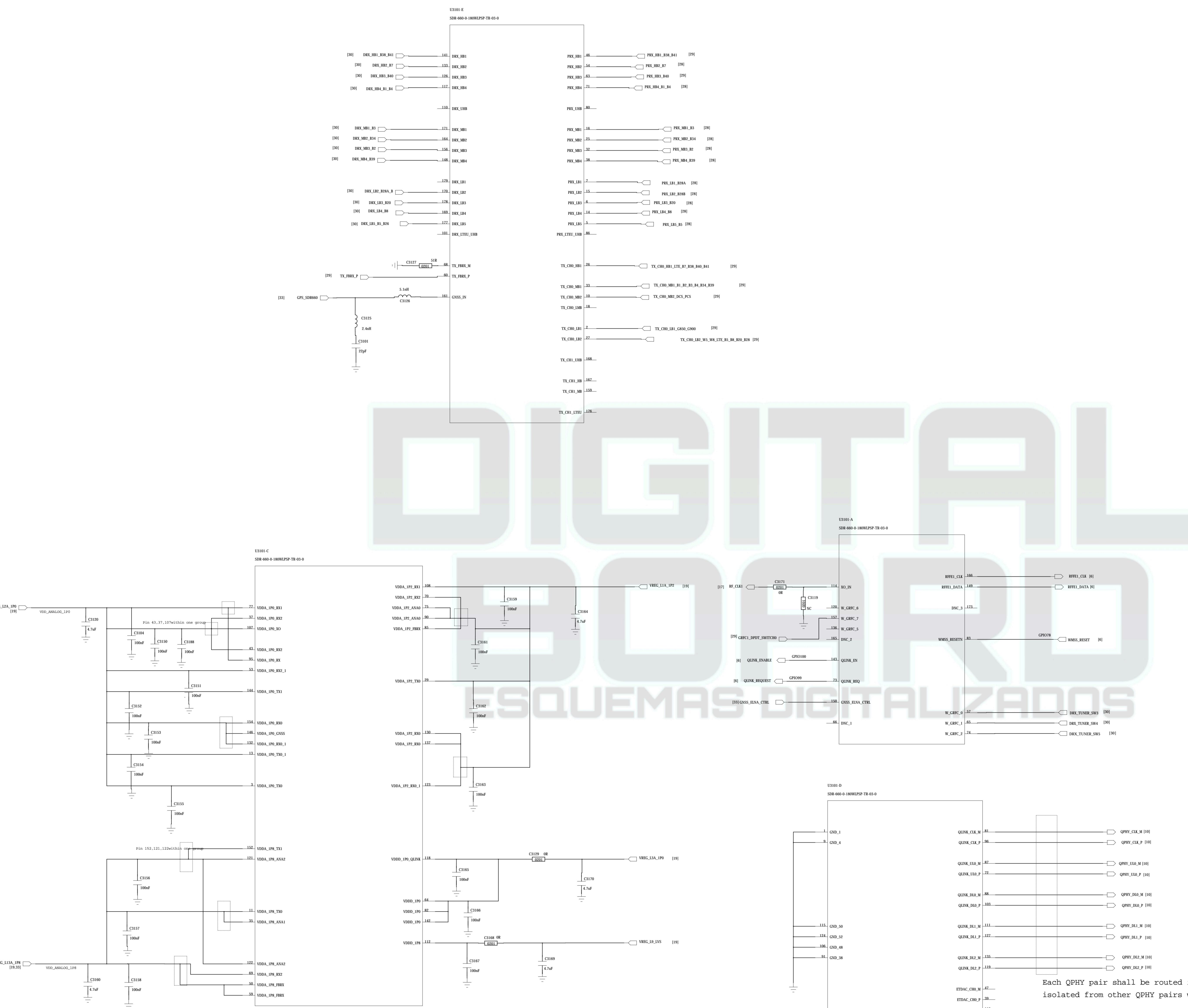


Force USB boot



REVISION RECORD			
ltr	ecno:	approved:	date:

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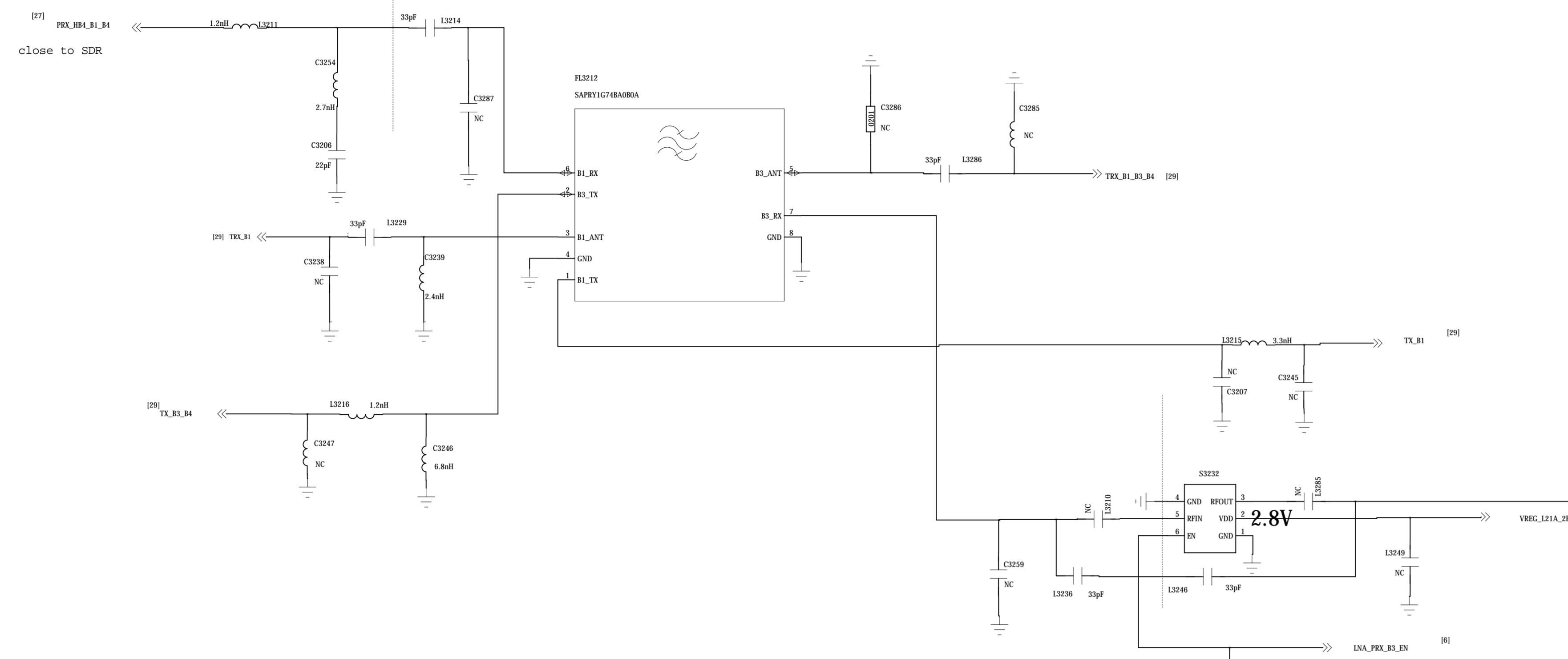


QPHY_ETDAC_TEST

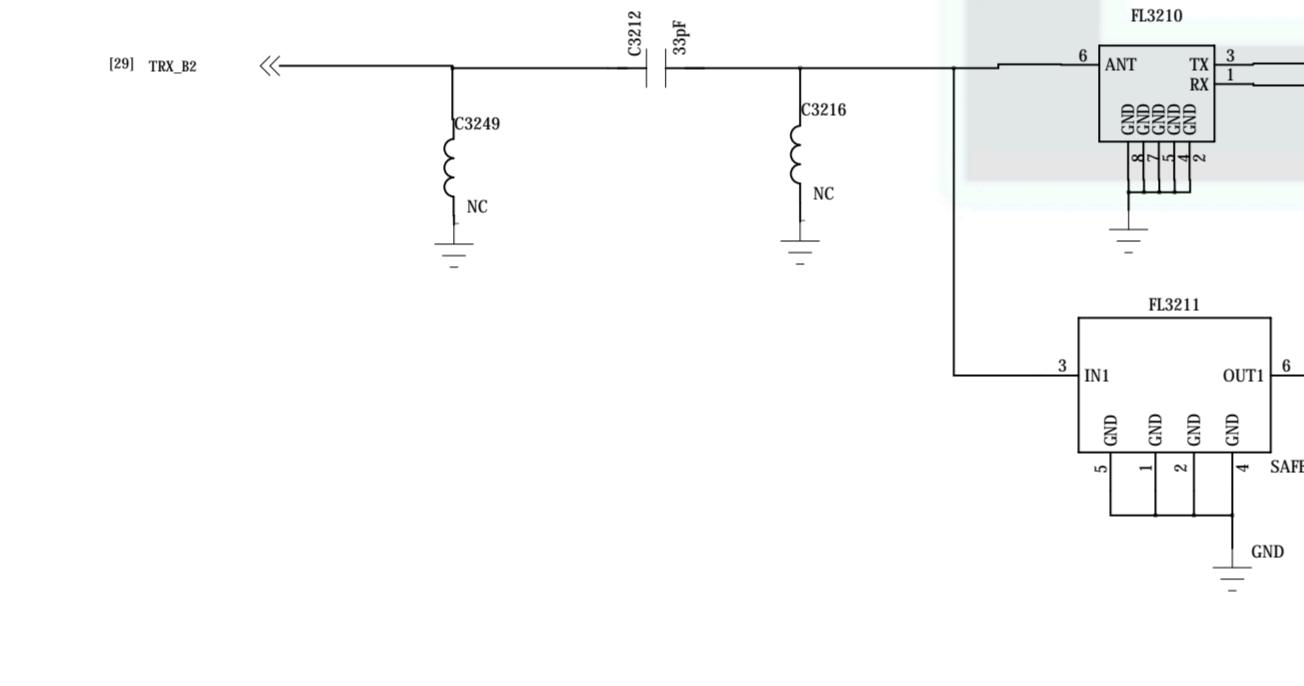
Each QPHY pair shall be routed in a channel together isolated from other QPHY pairs with surrounding gnd

DRAWN:	<Drawn By>	DATED:	<Drawn Date>
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CON:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>
CODE:	A0	DRAWING NO:	<Drawing Number>
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SHEET:	27	34	

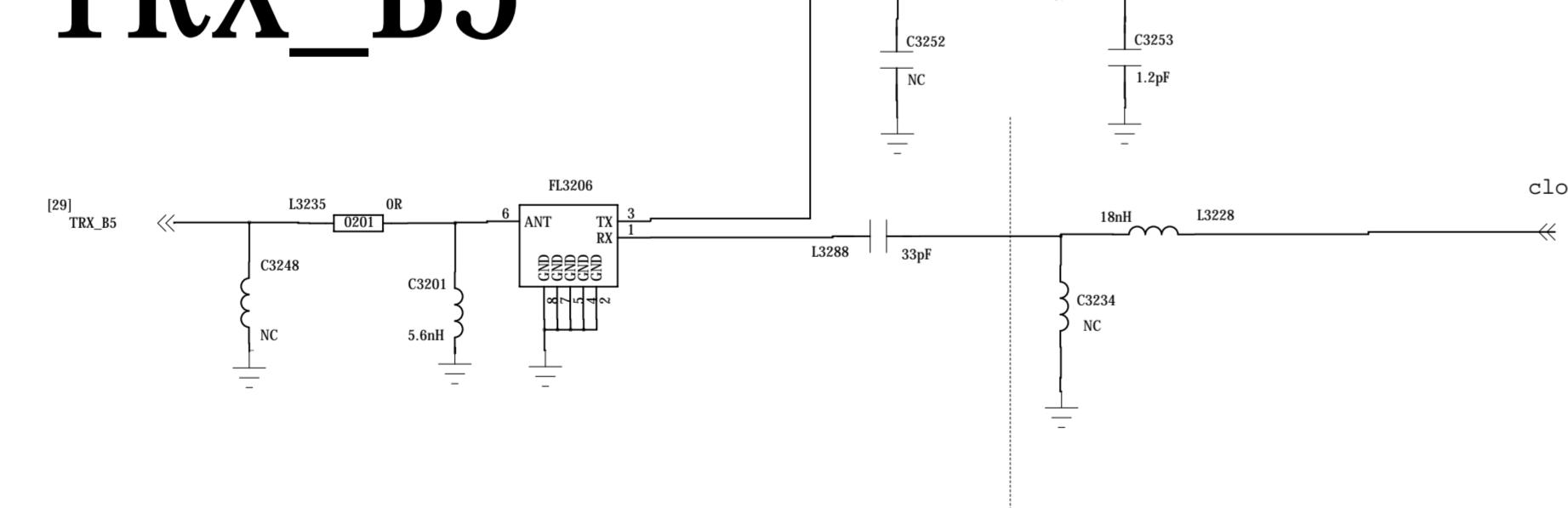
TRX_B1_B3_B4



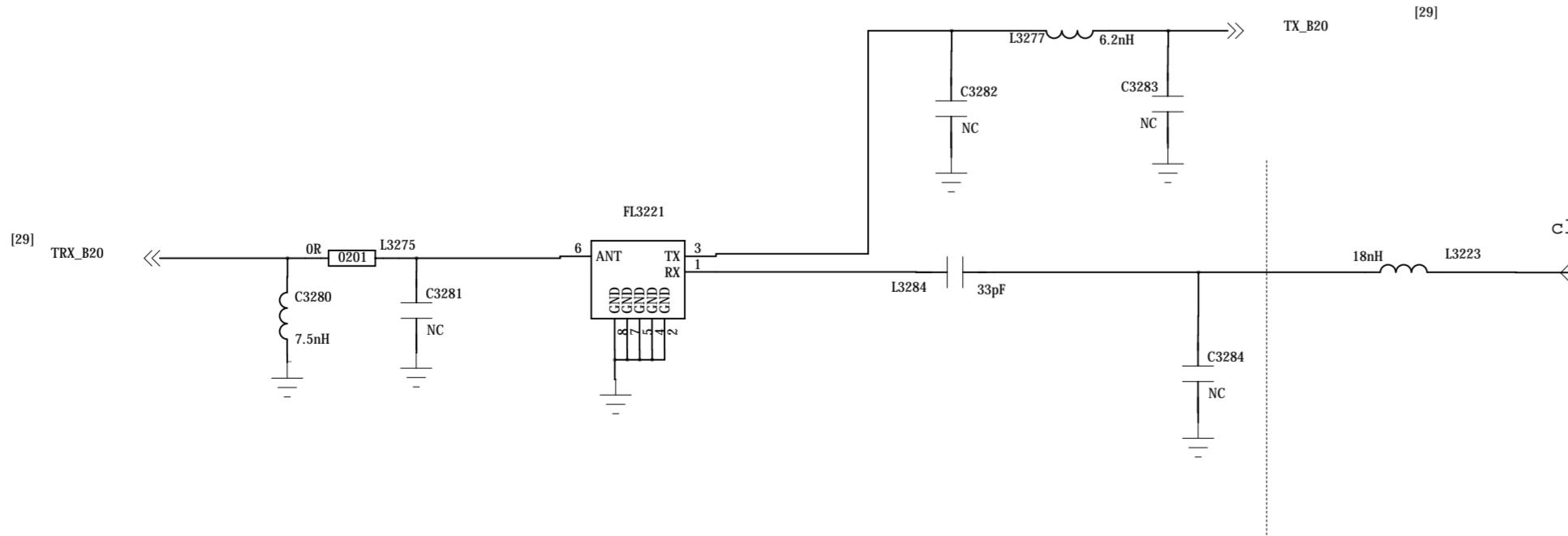
TRX_B2



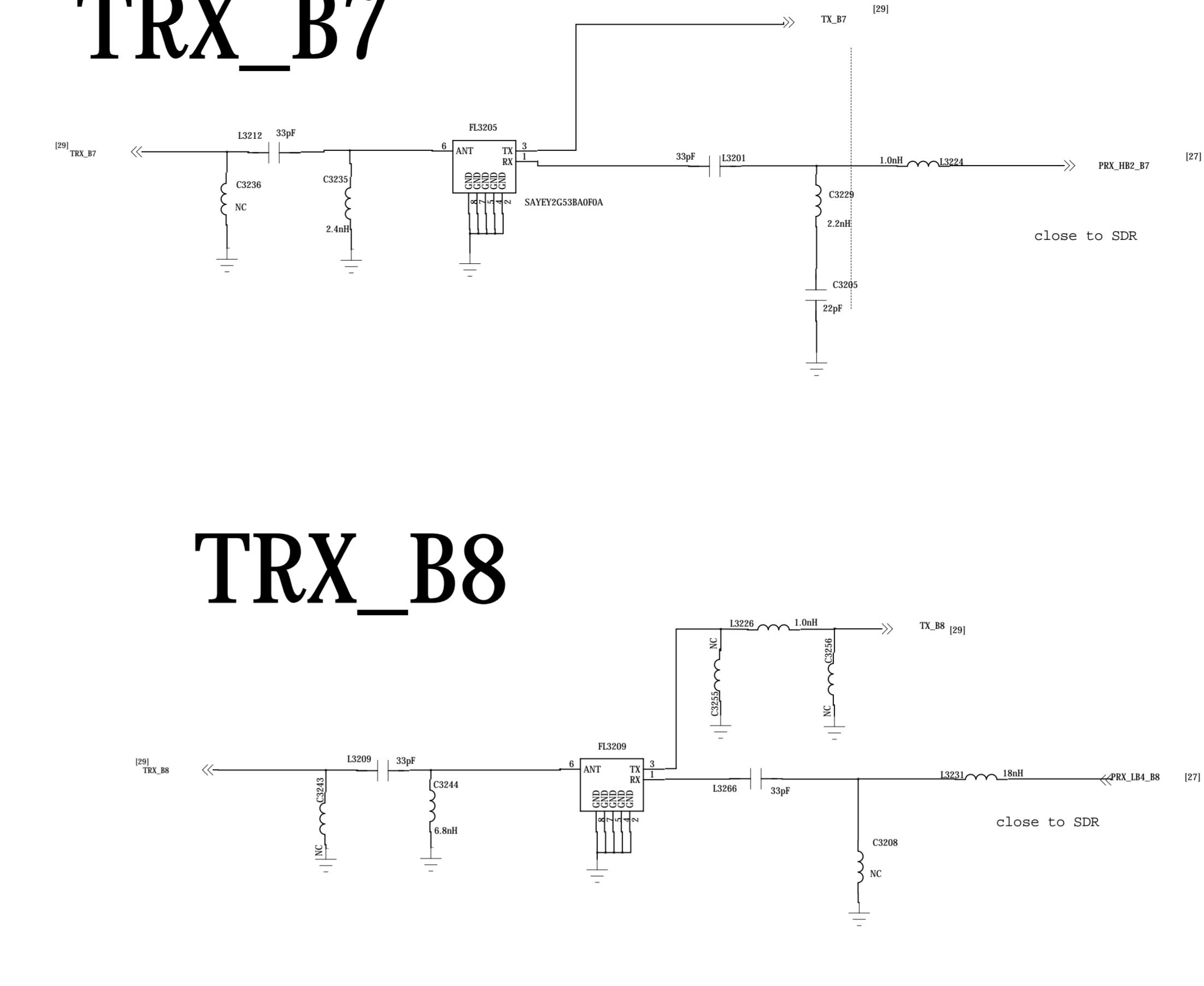
TRX_B5



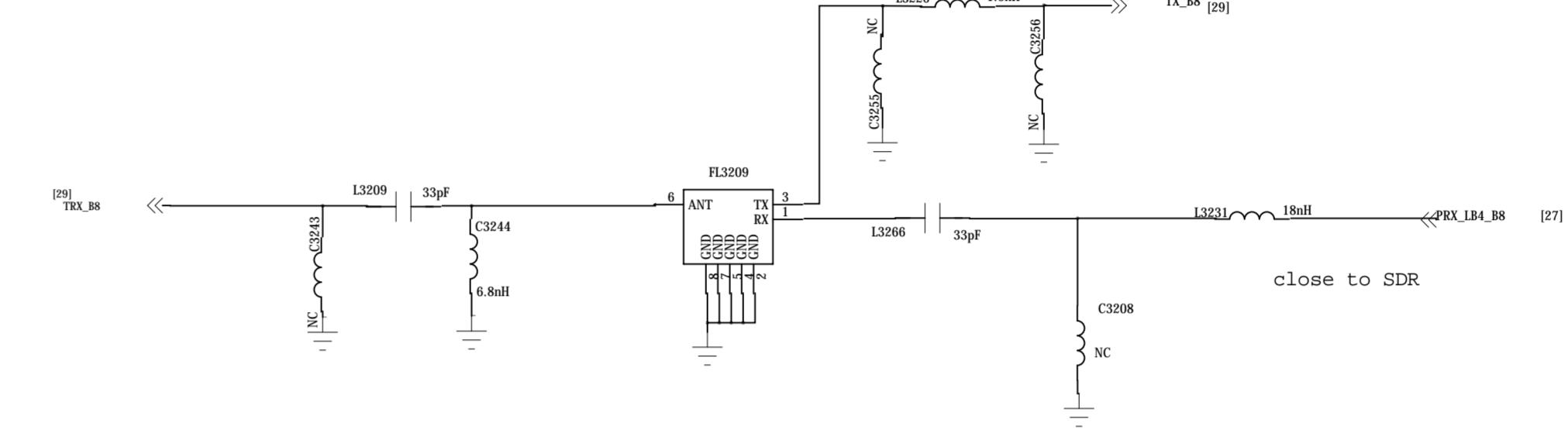
TRX_B20



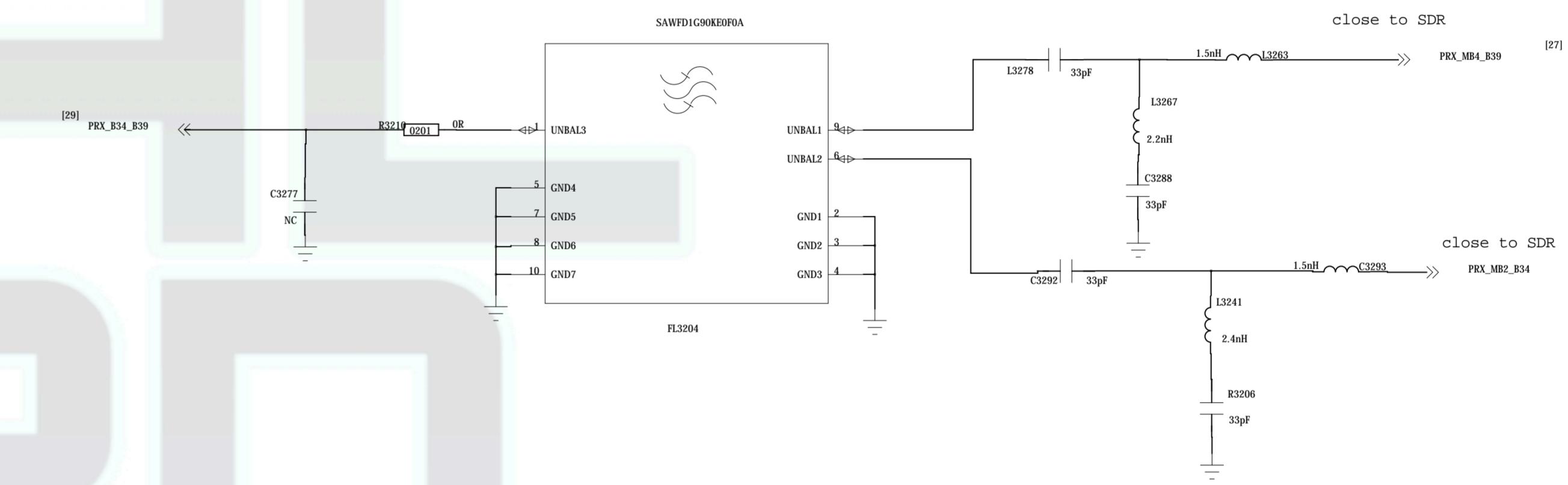
TRX_B7



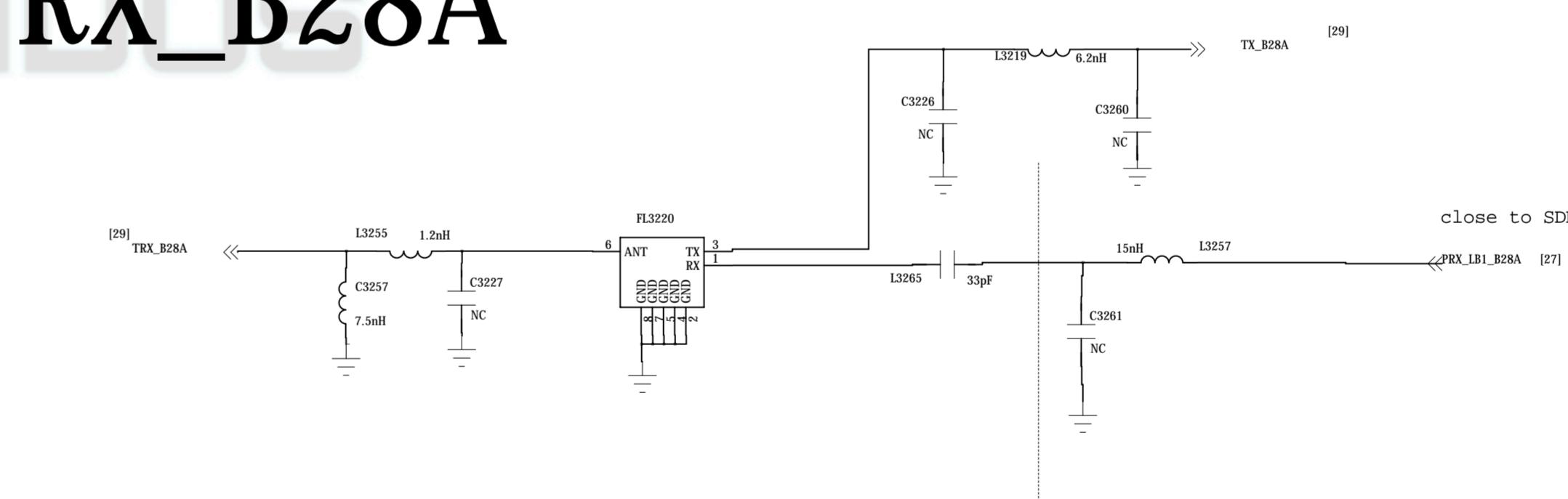
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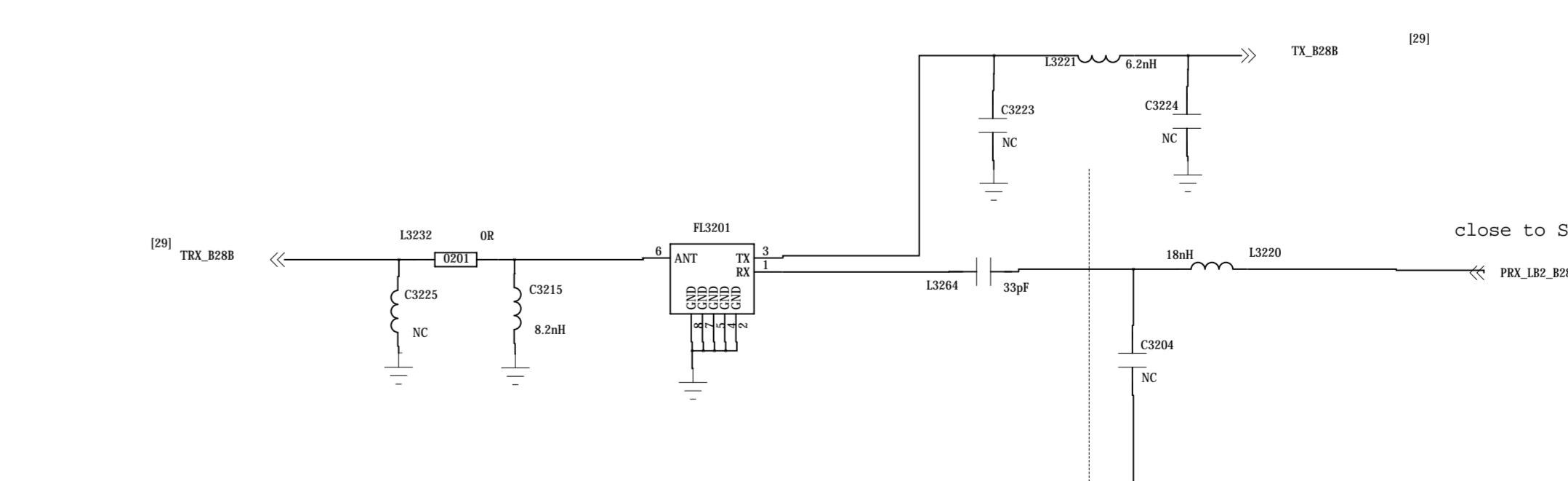
PRX_B34_B39



TRX_B28A

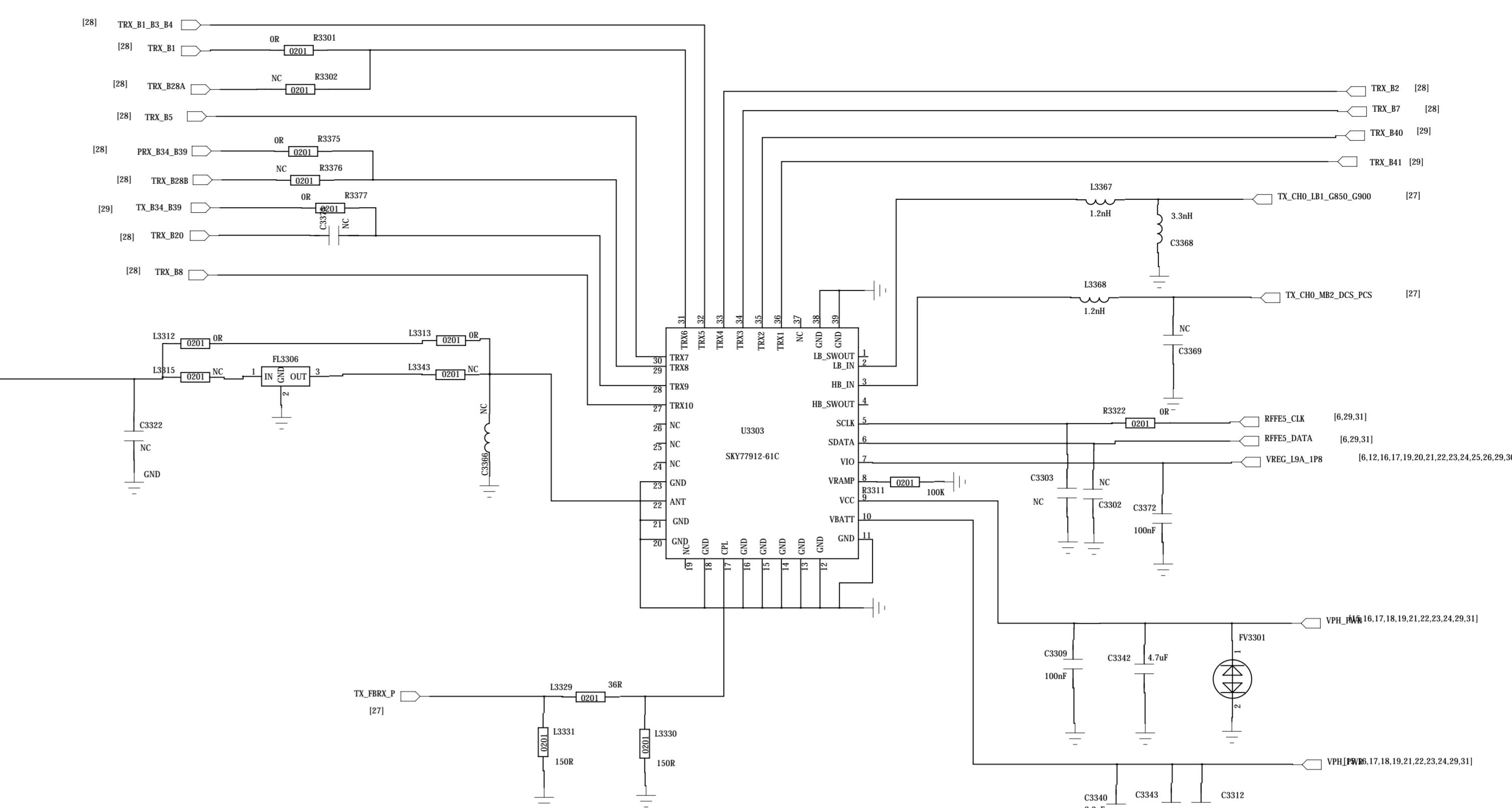


TRX_B28B



REVISION RECORD
LTR
ECO NO
APPROVED
DATE

TXM-RF5212A



MMMB_PA

PRX_B41

SAR_SENSOR

PRX_B40

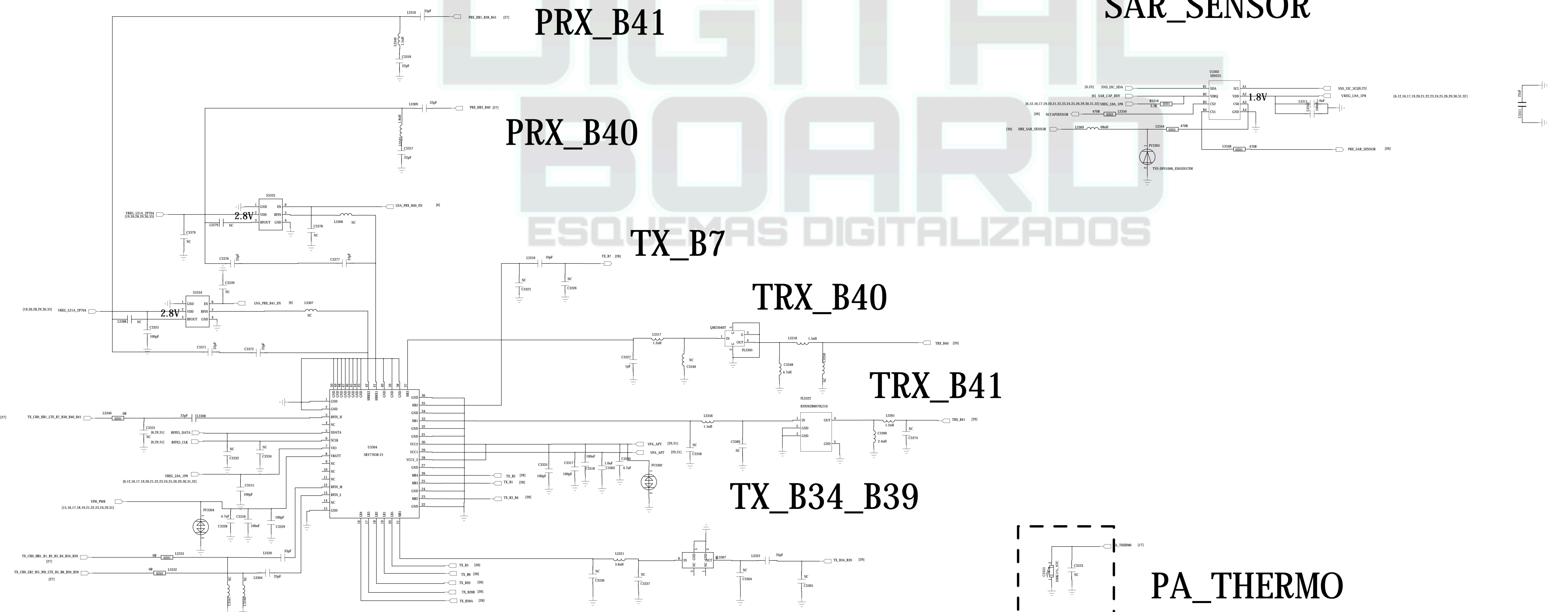
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TRX_B40

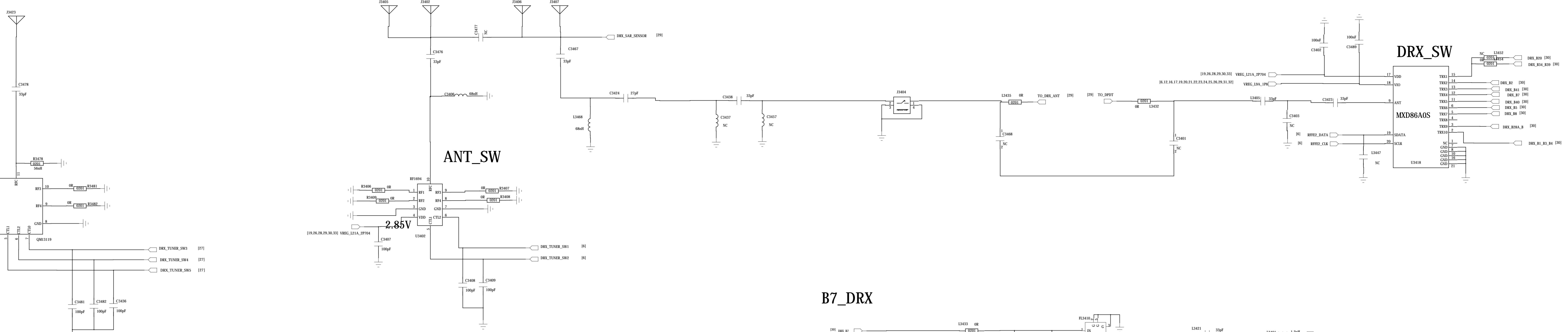
TRX_B41

TX_B34_B39

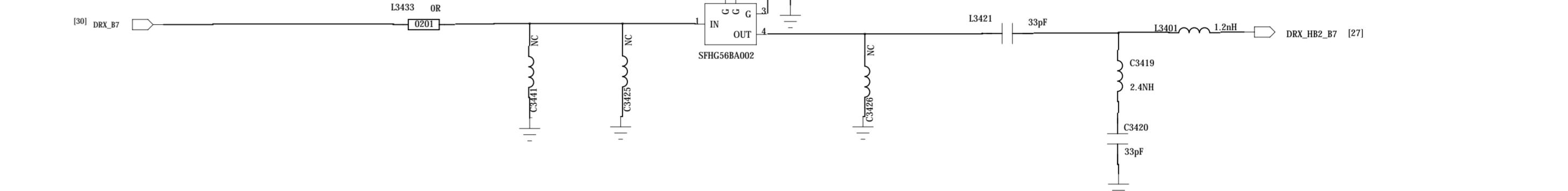
PA_THERMO



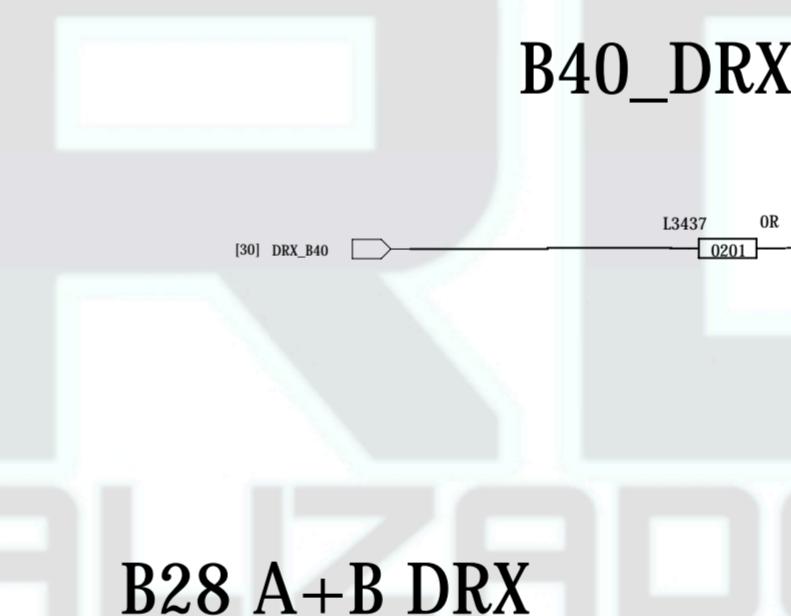
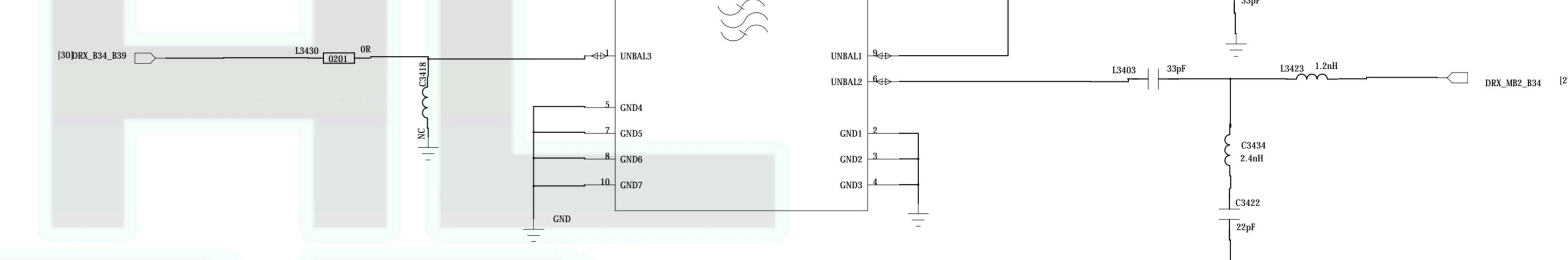
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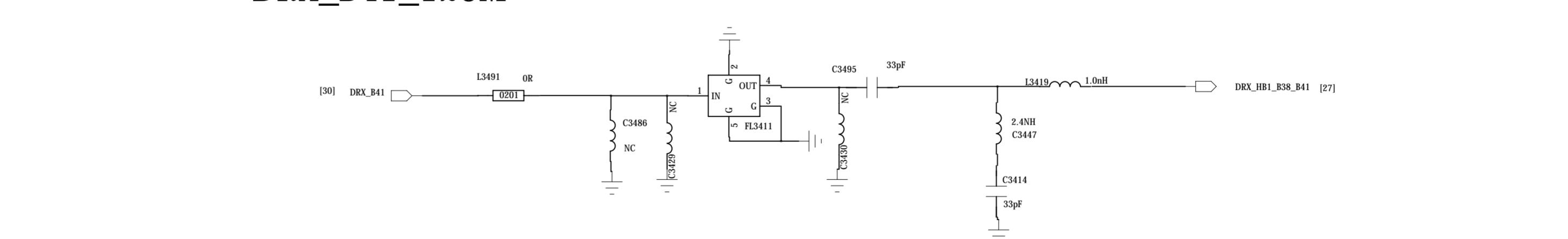
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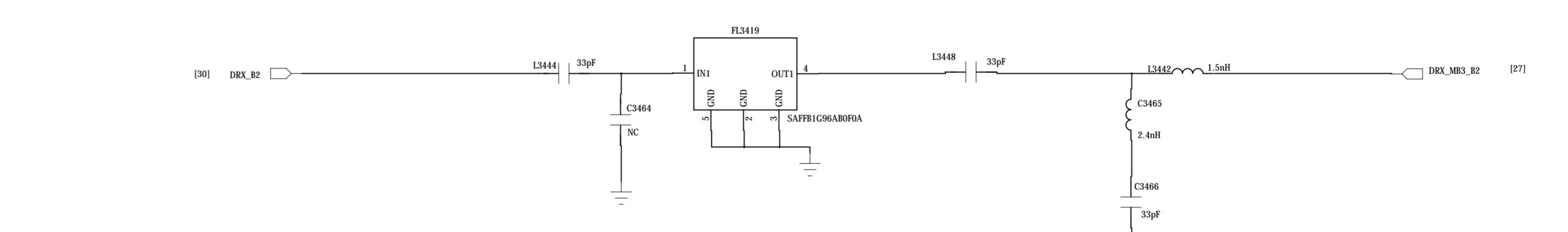
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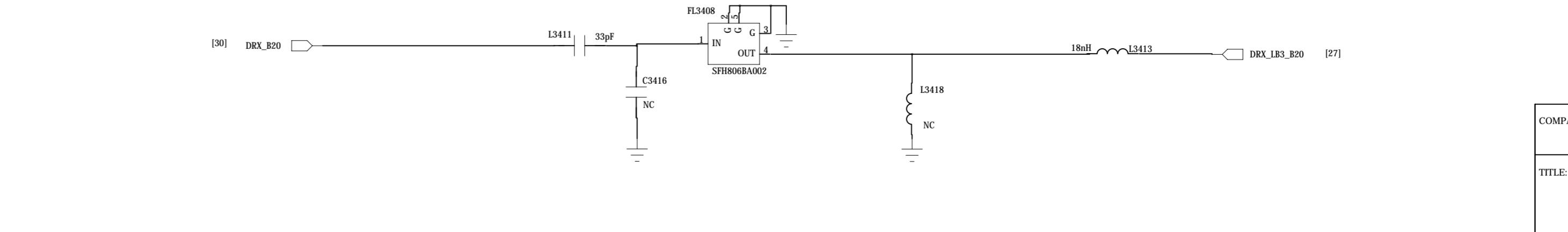
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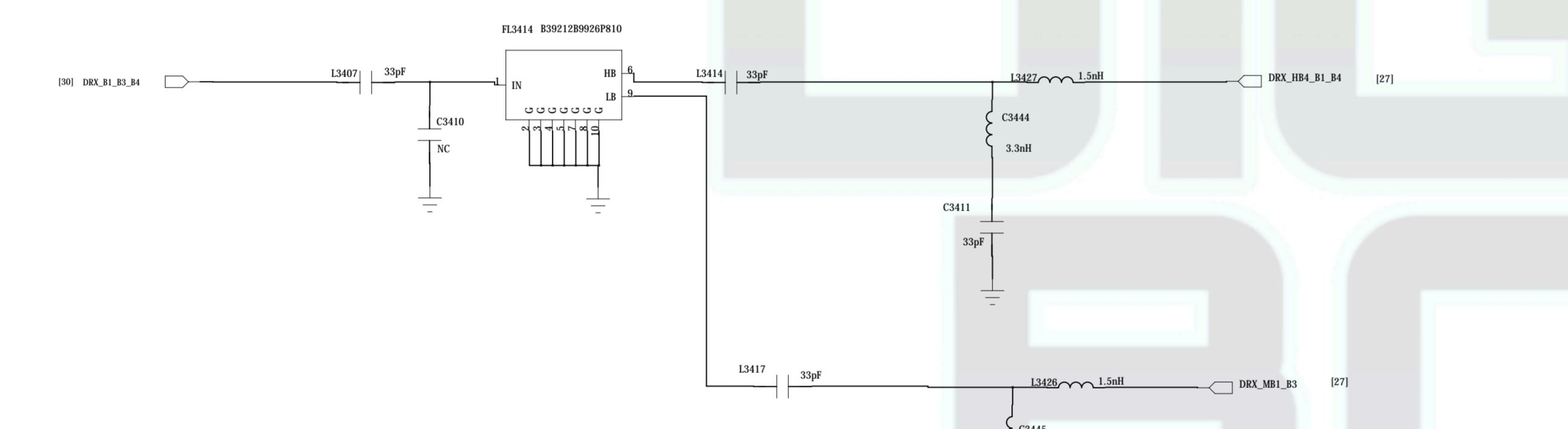
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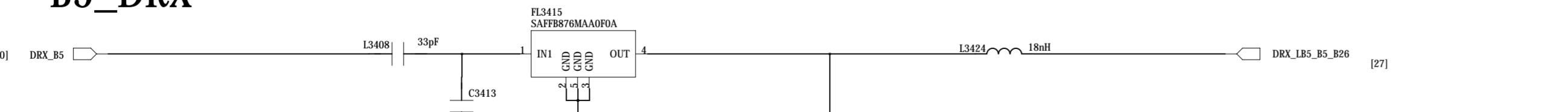
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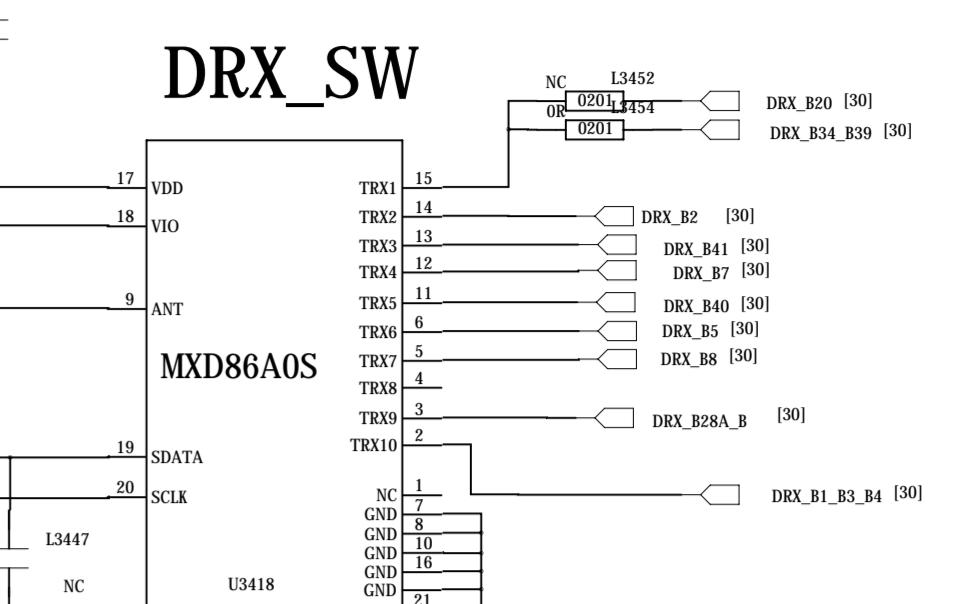
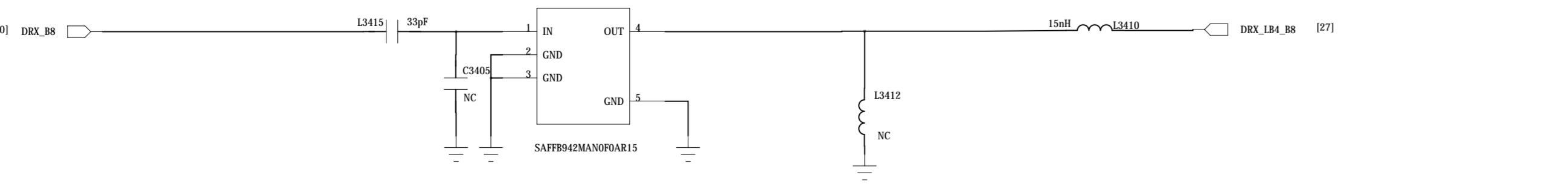
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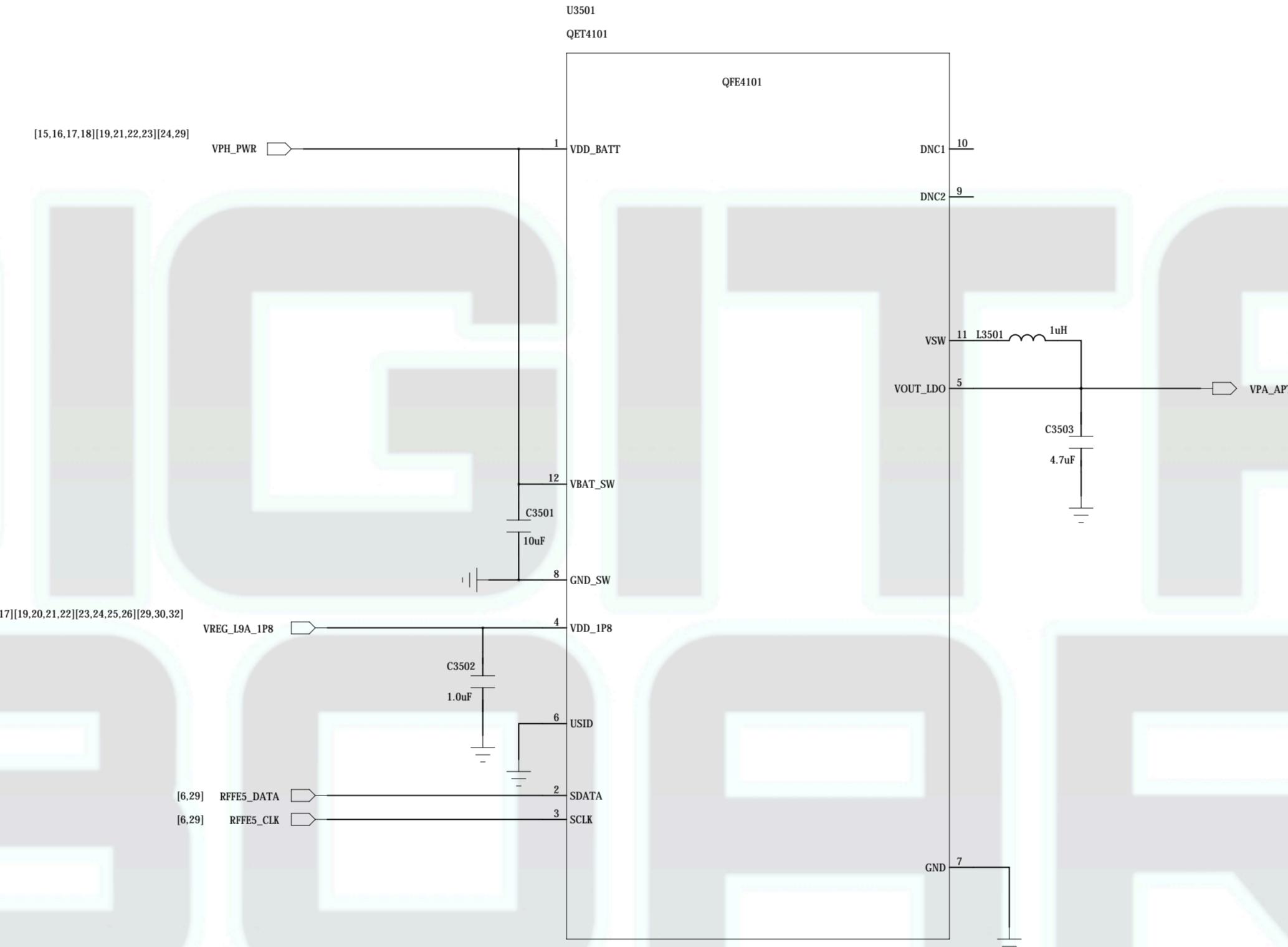
B5_DRX



B8 DRX

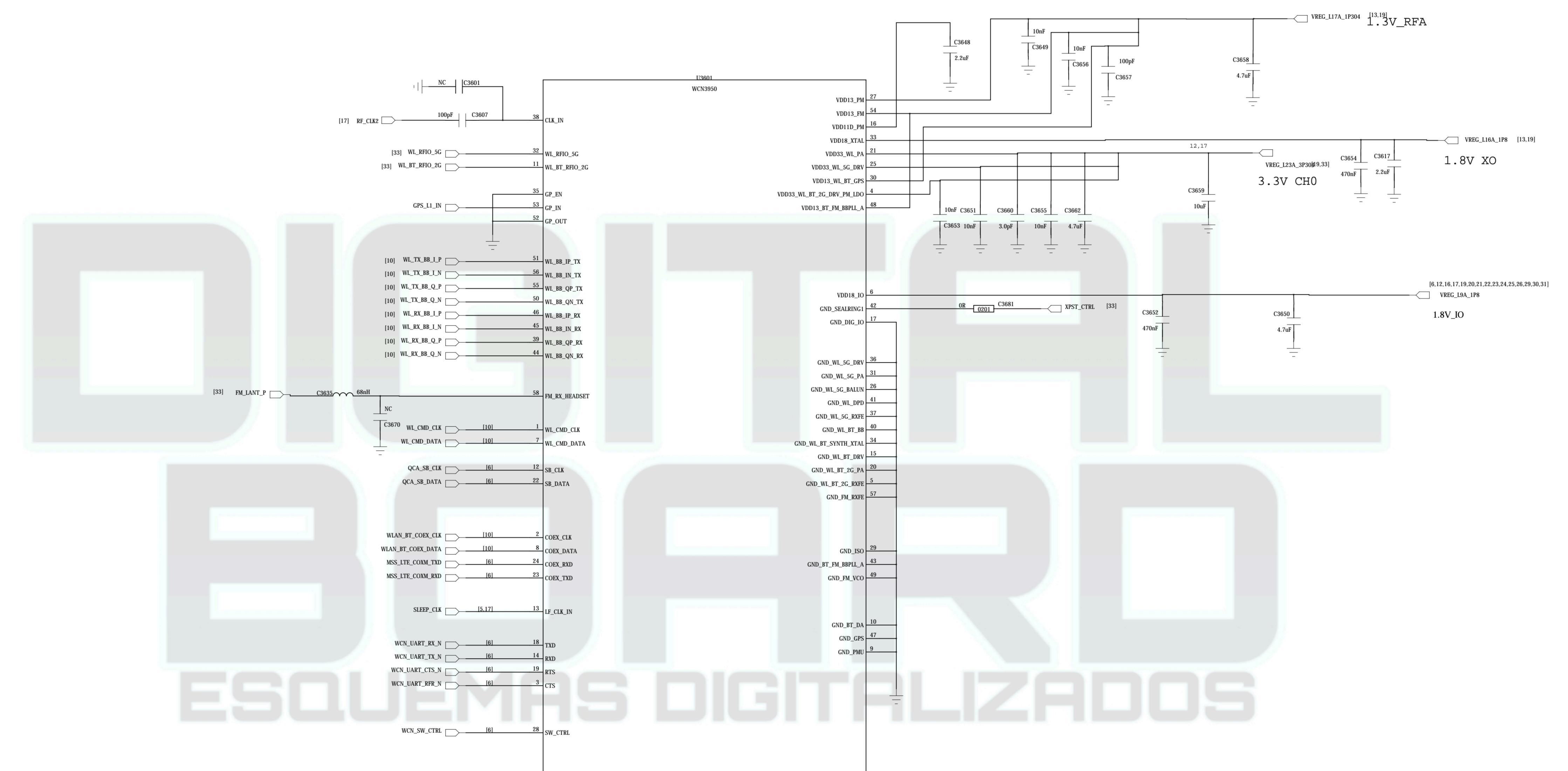


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SCALE:		<Scale>	SHEET: 31 of 34



The diagram shows the pinout for the WCN_UART interface. Pin 19 is labeled RTS and connected to the WCN_UART_CTS_N pin. Pin 3 is labeled CTS and connected to the WCN_UART_RFR_N pin. Pin 1 is labeled RxD and connected to the WCN_UART_TX_N pin. A ground connection is also shown.

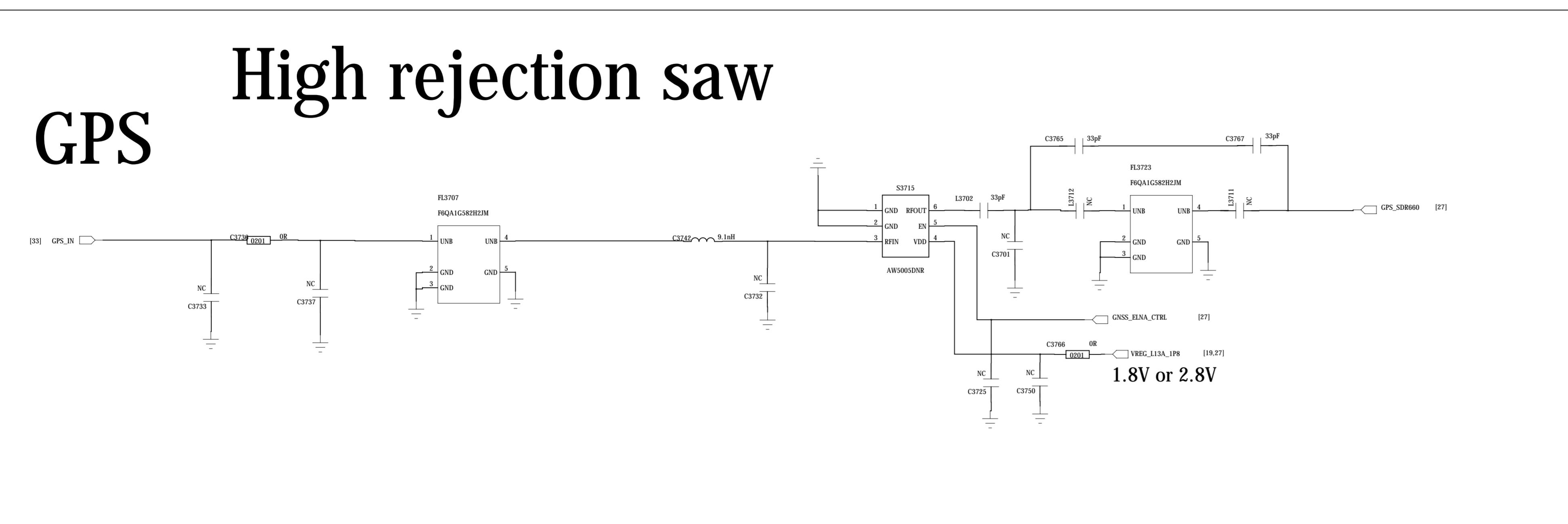
Pin	Function
19	RTS
3	CTS
1	RxD

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REVISION RECORD			
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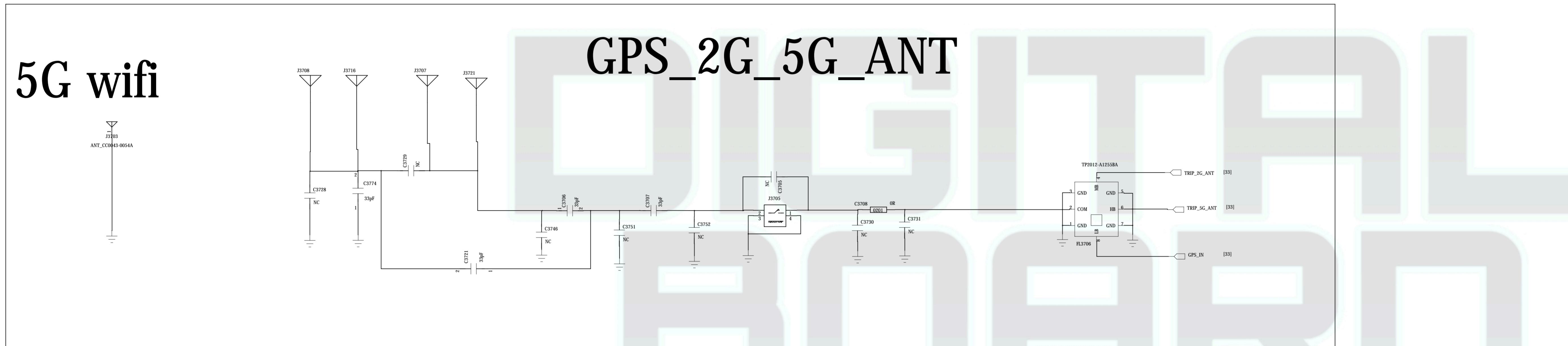
GPS

High rejection saw

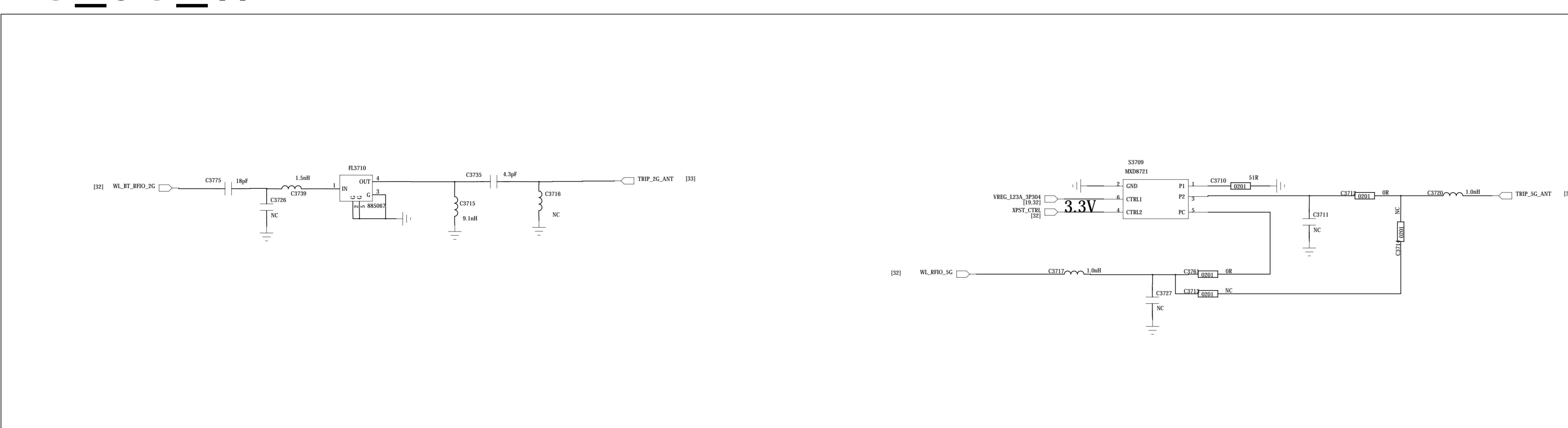


For 5G wifi

GPS_2G_5G_ANT



2G 5G WLAN



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