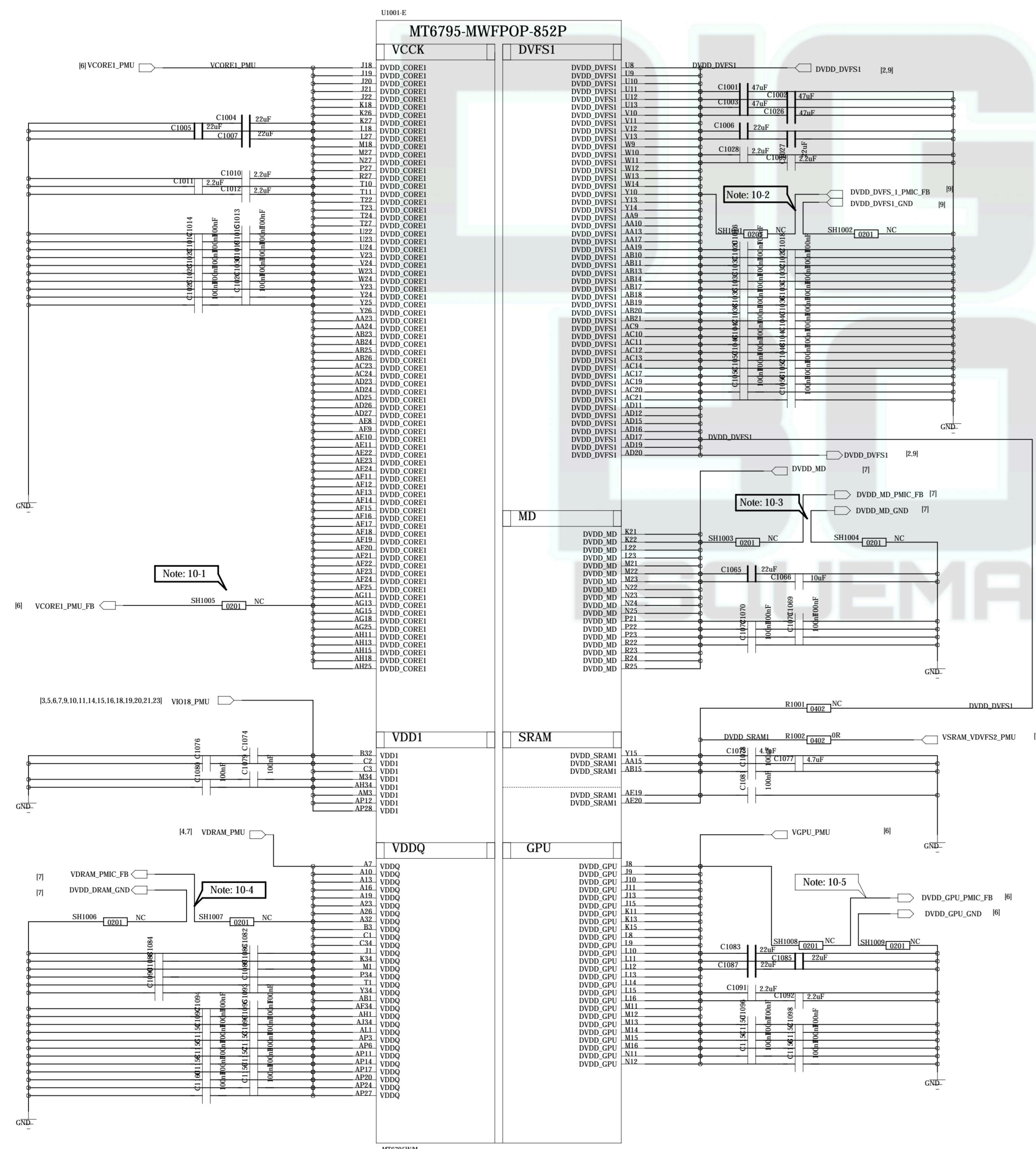


REVISION RECORD			
LTN	ECO NO.	APPROVED	DATE:



Schematic design notice of "10_BB_POWER_1" page.

Note 10-1: VCORE_1 remote sense must be close to MT6795's AG13 ball.
Remote sense trace with GNDshielding to PMIC (Differential)

Note 10-2: Differential pair of DVFS1 remote sense must be close to MT6795's Y10 ball.

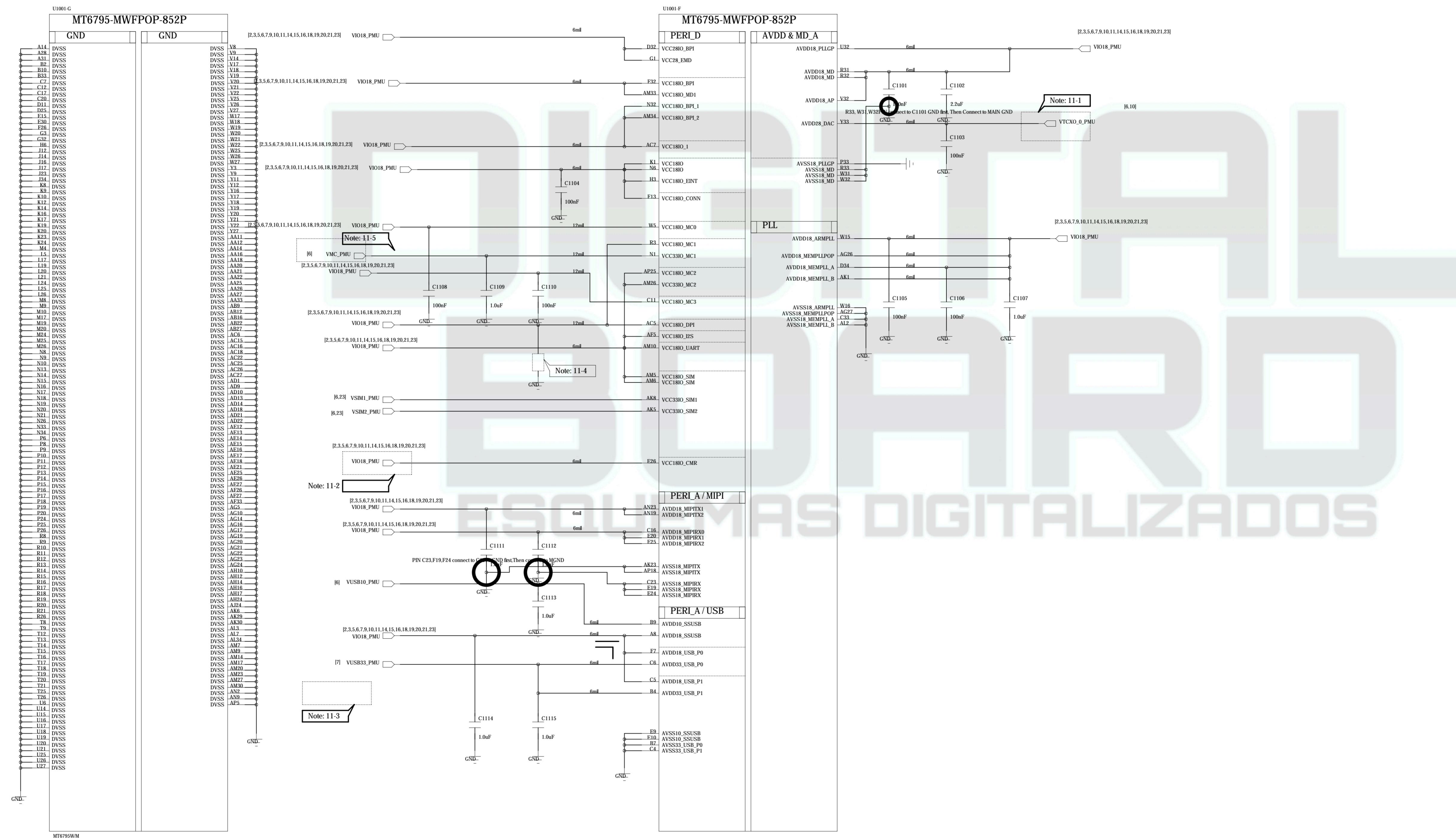
Note 10-3: Differential pair of DVDD_MD remote sense must be close to MT6795's K22 ball.

Note 10-4: VDRAM remote sense must be close to MT6795's A32 ball.

Note 10-5: Differential pair of GPU remote sense must be close to MT6795's J8 ball.

COMPANY:	<Company Name>		
TITLE:	<Title>		
DRAWN:	<Drawn By>	DATED:	<Drawn Date>
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>		
RELEASED:	<Released By>	DATED:	<Release Date>
SCALE:	<Scale>	SIZE:	<Drawing No>
REVIS:	<Revision>		

REVISION RECORD			
LTR	ECO NO.	APPROVED	DATE



Schematic design notice of "11_BB_POWER" page.

Note 11-1: AVDD28_DAC (Y33 ball) must be powered by "VTCXO_1_PMU".

Note 11-2: Connect VCC180_CMR (E26 ball) to "V18I_PMU" since C-2 I/O power is powered by VCC180_CMR.

Note 11-3: Connect AVDD33_USB_P1 (B4 ball) to "VSIMI_PMU" for IC-USB / Samrt card application. Connect AVDD33_USB_P1 (B4 ball) to "VUSB33_PMU" for USB application.

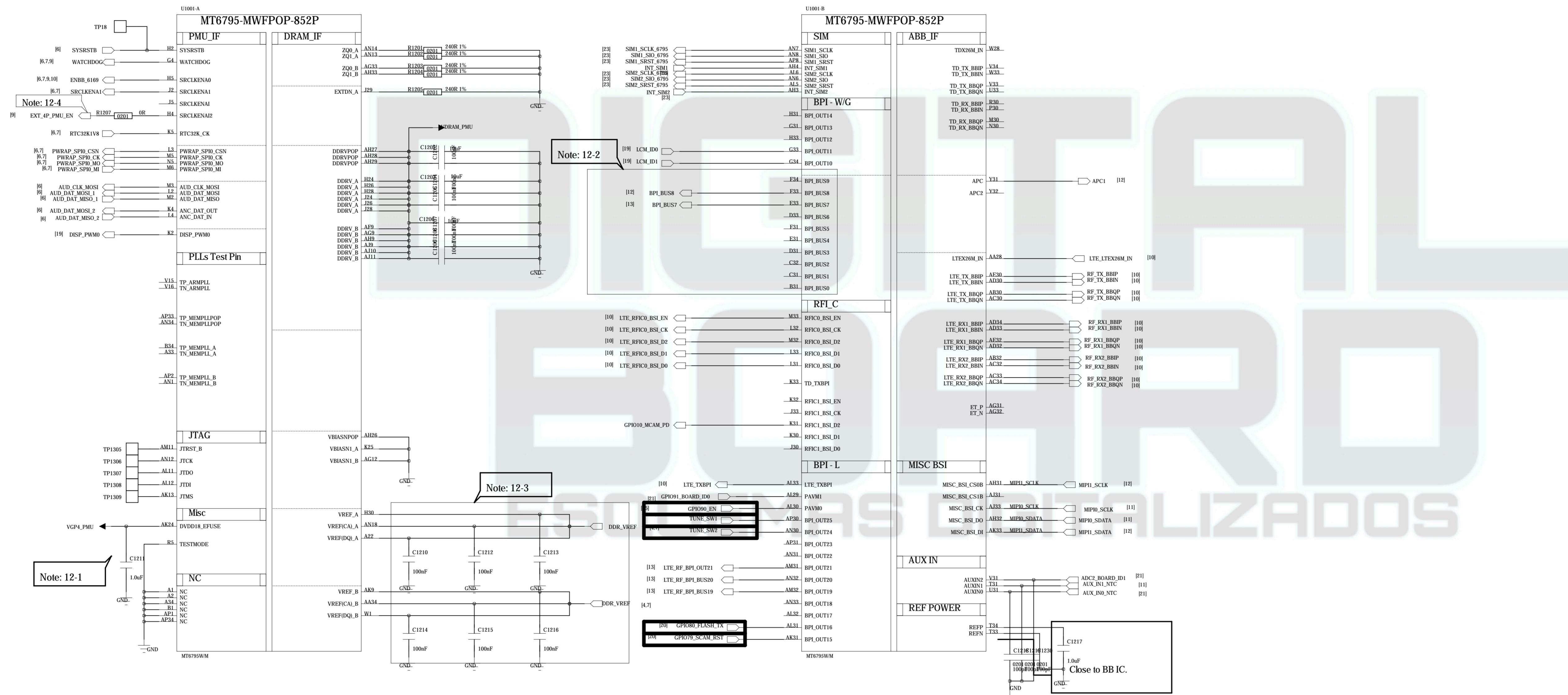
Note 11-4: Reserve 0.1uF decoupling cap in VCC180_DPI for MHL.

COMPANY: <Company Name>

TITLE: <Title>

BROKEN	DATES	CODE	SIZE	DRAWING NO.	REV.
<Drawn By>	<Drawn Date>	<Code>	A0	<Drawing Number><Revision>	
<Checked By>	<Checked Date>				
QUALITY CONTROL	<QC Date>				
<QC By>	<QC Date>				
RELEASED	<Released Date>				

REVISION RECORD			
ltr	ECO NO.	APPROVED	DATE



Schematic design notice of "11_BB_11" page.

Note 12-1: Apply 1.8V to DVDD18_EFUSE (AK24) for eFuse programming.

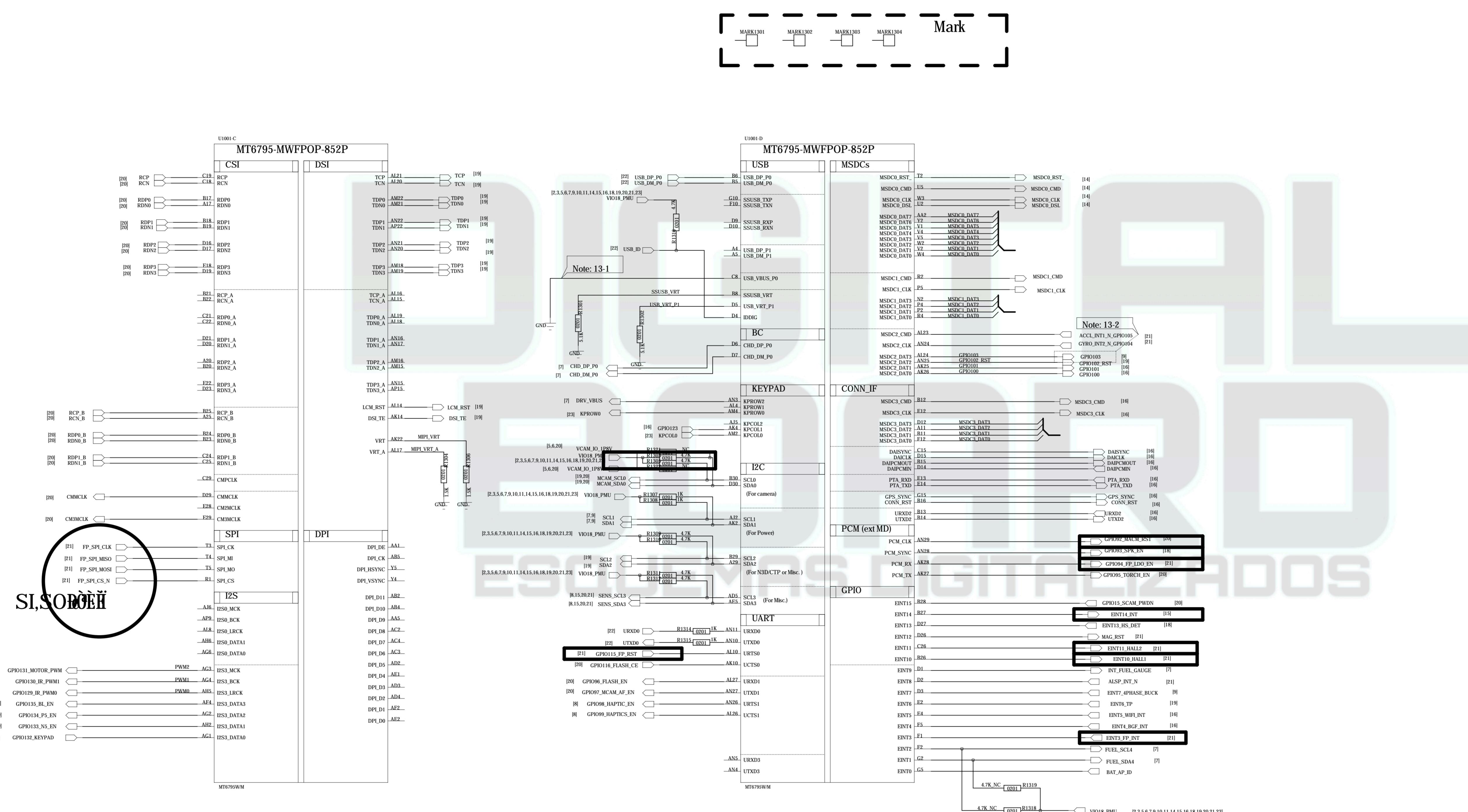
Note 12-2: The BPI_BUS0-BPI_BUS9 are capable of 2.8V I/O operation.

Note 12-3: The de-coupling cap. of DRAM VREF have to be placed as close to BB as possible.

Note 12-4: SRCLKNA12 features watch dog reset output to reset 4 phase buck. R1207 = OR when BOM option of U2401 is DA9210. R1207 = NC when BOM option of U2401 is 2nd source.

COMPANY:	<Company Name>		
TITLE:	<Title>		
DRAWN:	<Drawn By>	DATED:	<Drawn Date>
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>
CODE:	SIZE:	DRAWING NO.:	REV.:
<Code> A0		<Drawing Number><Revision>	
SCALE:	<Scale>	SHEET: 4/ 23	

REVISION RECORD			
ltr	ECN NO.	APPROVED	DATE



Schematic design notice of "12_BB_2" page.

Note 13-1: Connect USB_VBUS_P0 (C8 ball) pin to GND since USB OTG "B-Valid" detection has implemented by MT6332's ADC.

Note 13-2: GPIO103 is dedicated for DA9210 4-phase buck control.

COMPANY:	<Company Name>		
TITLE:	<Title>		
DRAWN:	<Drawn By>		
CHECKED:	<Checked By>		
QUALITY CONTROL:	<QC By>		
RELEASED:	<Released By>		
CODE:	SIZE:	DRAWING NO.:	REV:
<Code> A0		<Drawing Number><Revision>	

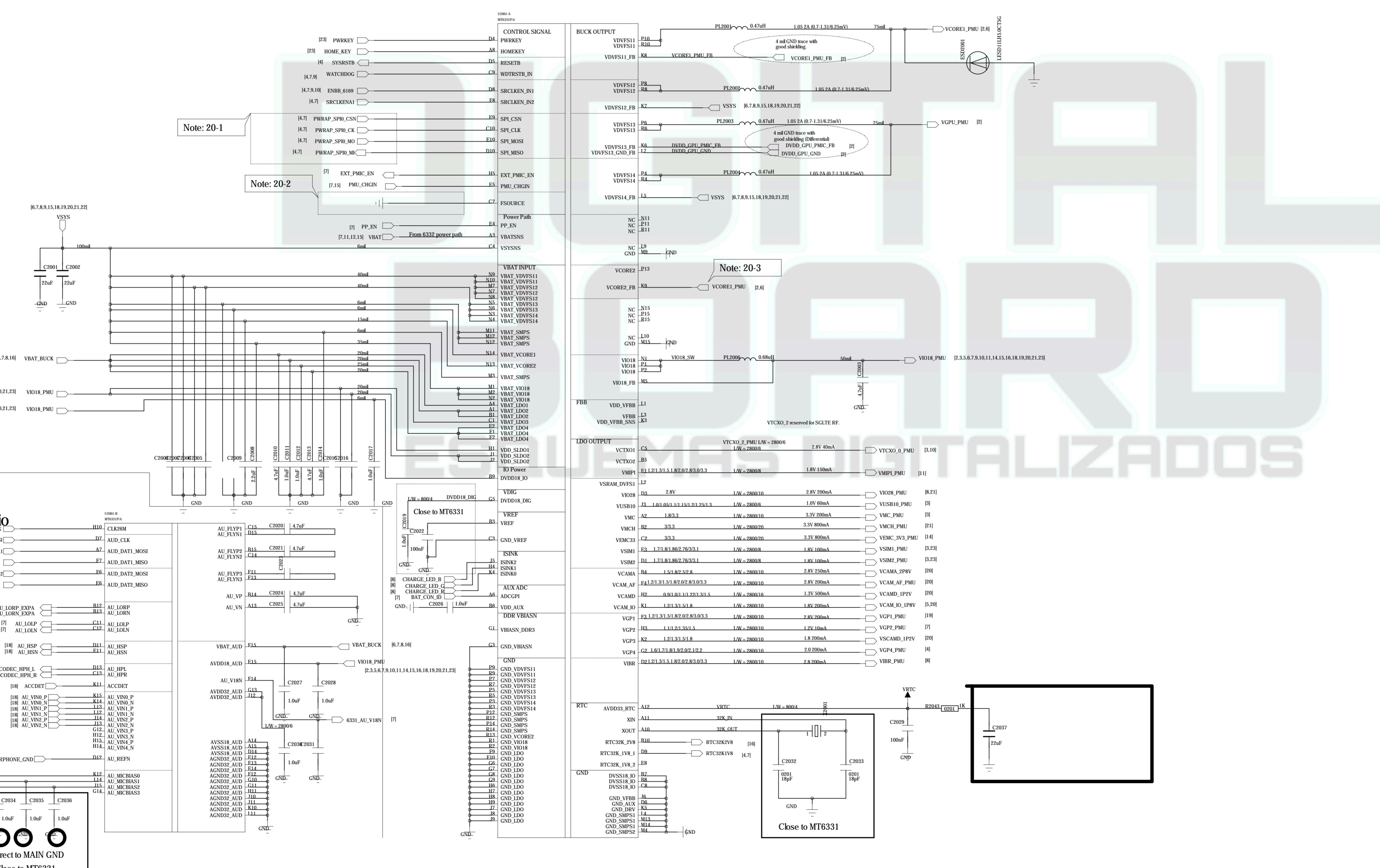
REVISION RECORD			
ltr	ECO NO	APPROVED	DATE

Schematic design notice of "20_POWER_MT6331" page.

Note 20-1: External pull resistor in PMU SPI interface is not allowed.

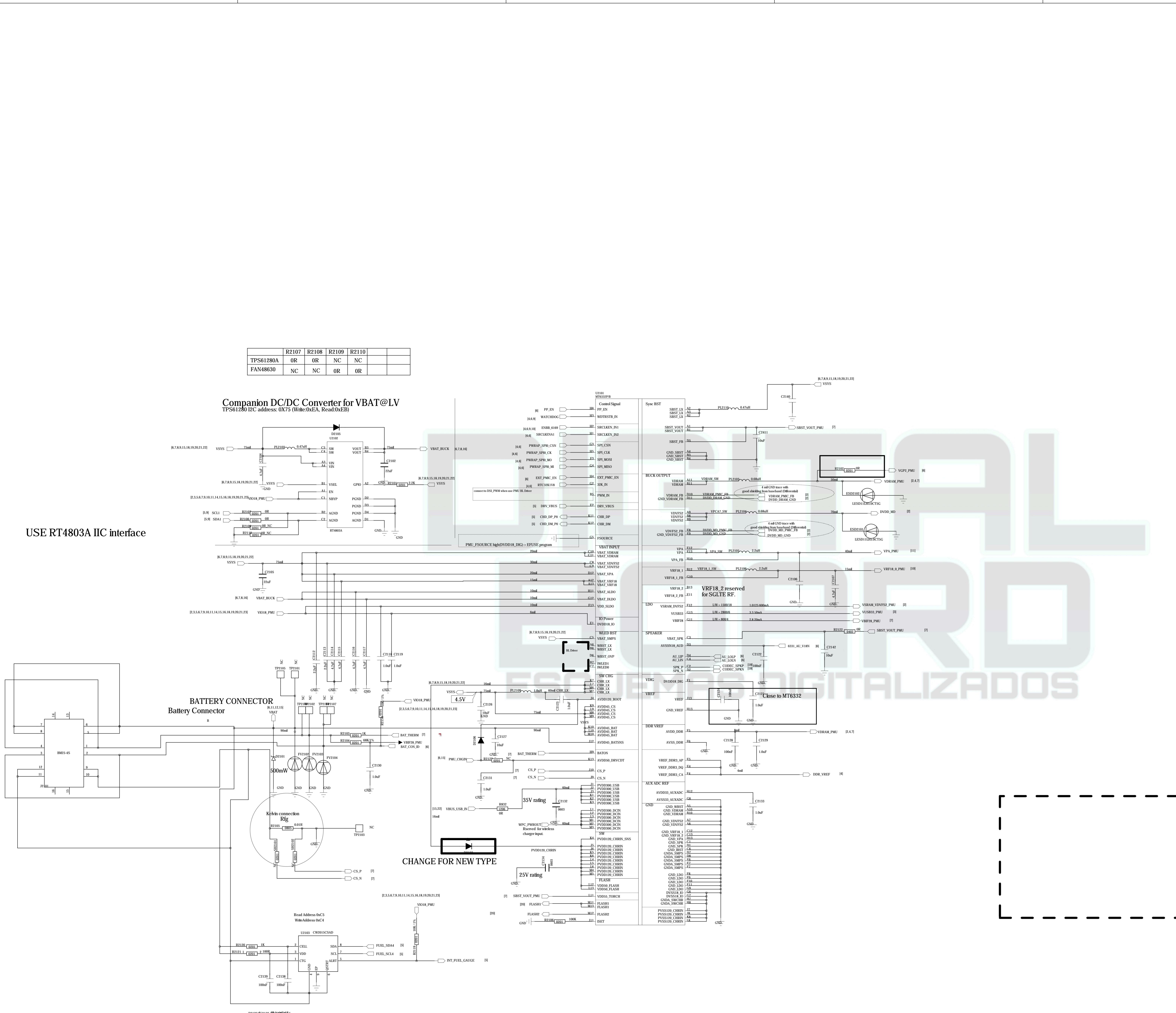
Note 20-2: PMU_FSOURCE high(DVDD18_DIG)->EFUSE program.

Note 20-3: Connect MT6331's K9 pin to "VCORE1_PMU" when VCORE2 is not used.



COMPANY	<Company Name>	
NAME	<Title>	
DRAWN	<Drawn By>	DATED <Drawn Date>
CHECKED	<Checked By>	DATED <Checked Date>
QUALITY CONTROL	<QC By>	DATED <QC Date>
RELEASED	<Released By>	DATED <Release Date>
CODE	SIZE	DRAWING NO
<Code> A0		<Drawing Number><Revision>
SCALE	<Scale>	Sheet 6/ 23

REVISION RECORD			
LTR	ECO NO.	APPROVED	DATE

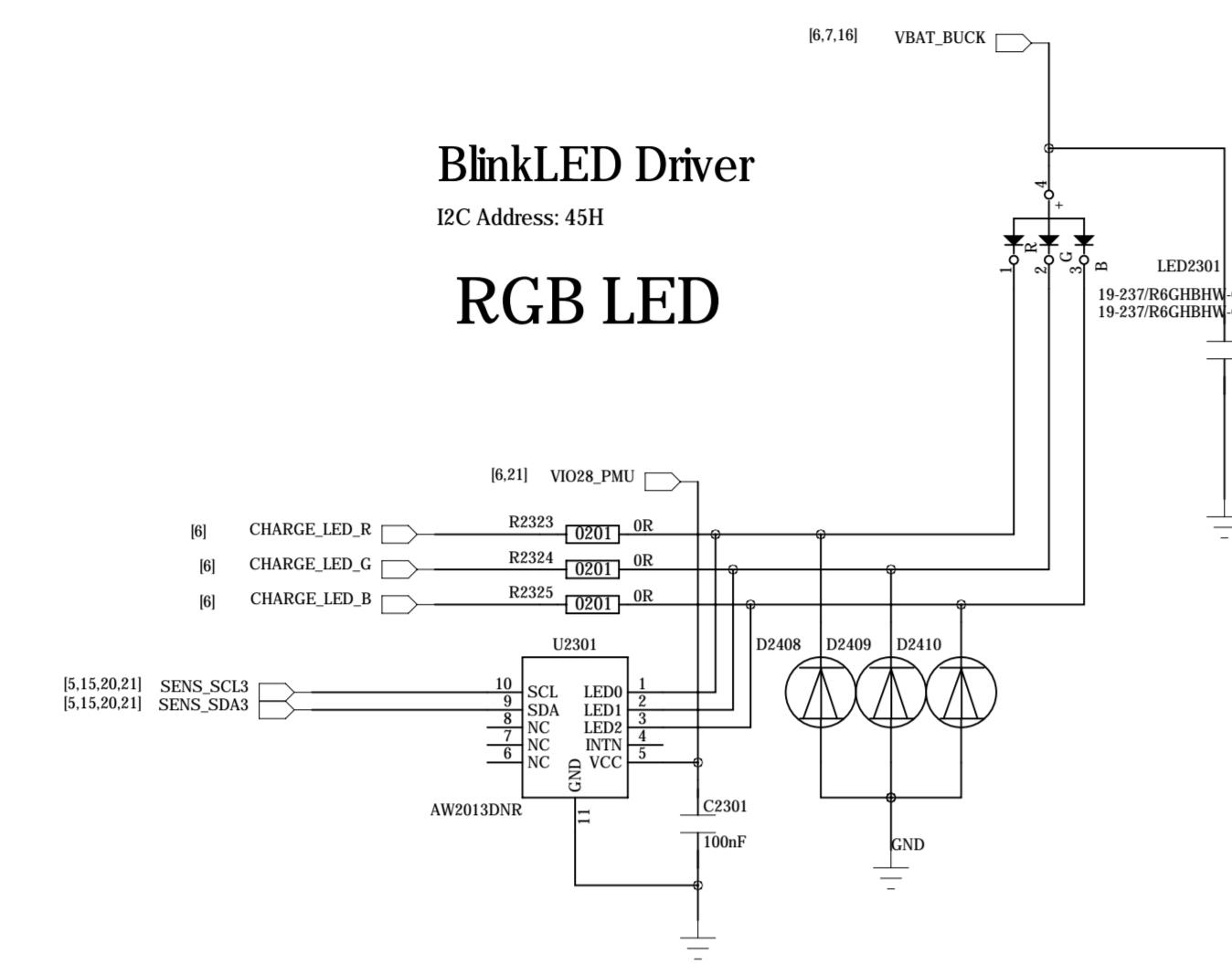


COMPANY:	<Company Name>		
TITLE:	<Title>		
DRAWN:	<Drawn By>	DATED:	<Drawn Date>
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>
CODE:	SIZE:	DRAWING NO.	REV.
<Code> A0		<Drawing Number><Revision>	
SCALE:	<Scale>	SCALE:	<Scale>
SHEET	6	SHEET	23

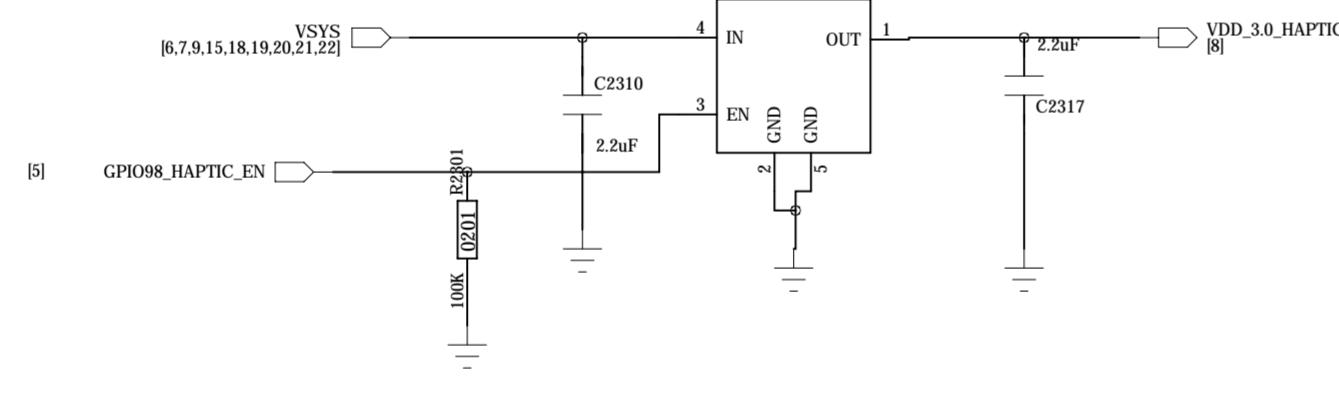
REVISION RECORD			
ltr	ECO NO	APPROVED	DATE

D

D



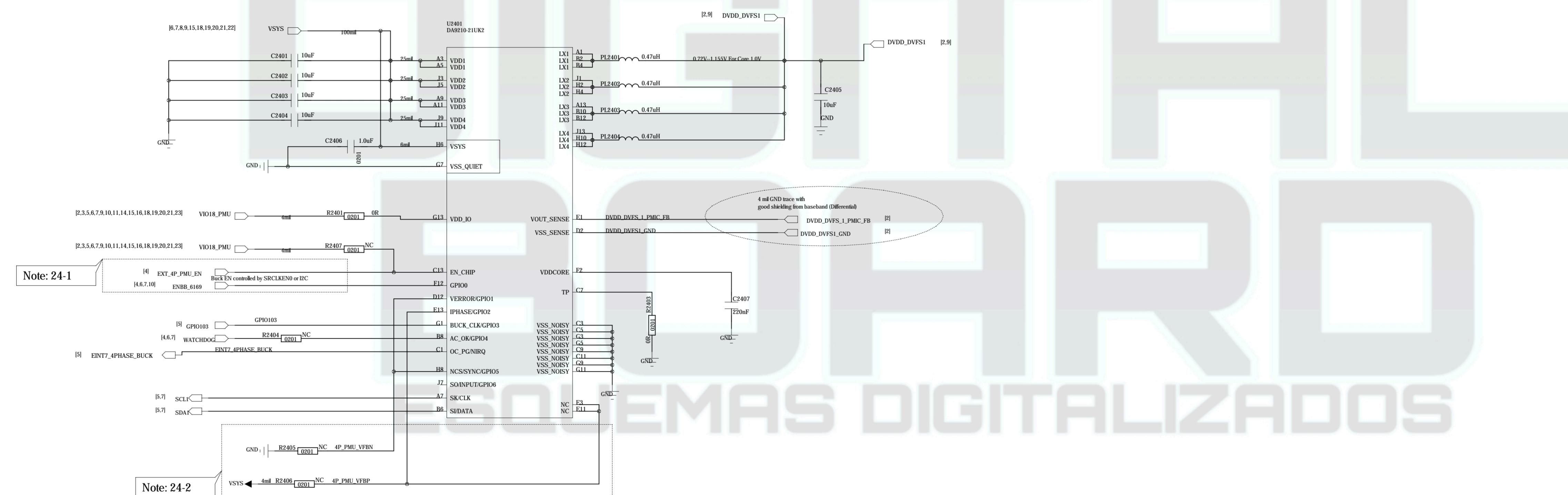
MOTOR DRIVER CIRCUIT



REVISION RECORD			
TR	ECO NO	APPROVED	DATE

4-Phase Buck

4-Phase Buck I2C address: 0x68 (Write:0xD0, Read:0xD1)



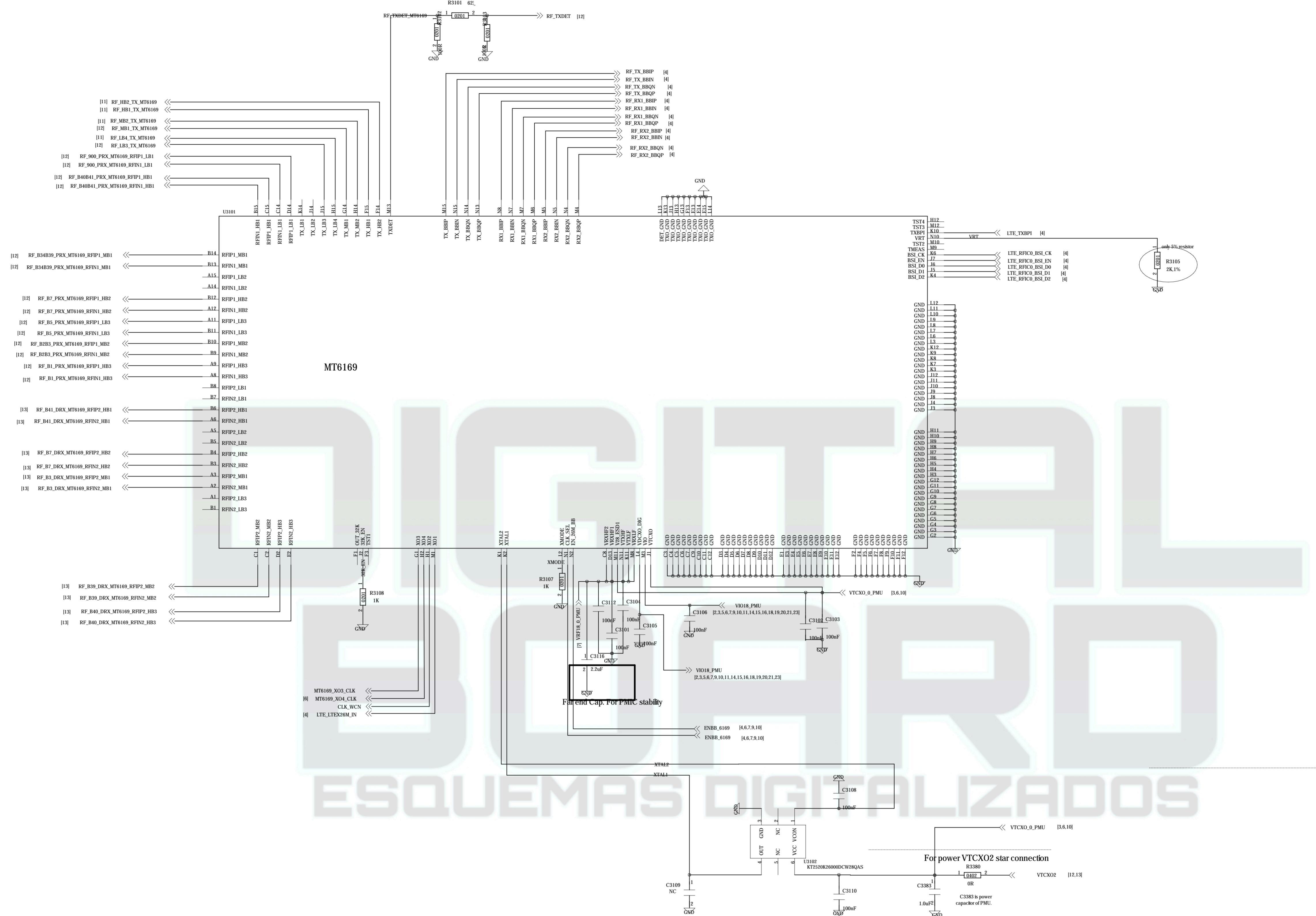
Schematic design notice of "24_POWER_EXT_4PHASE_BUCK" page.

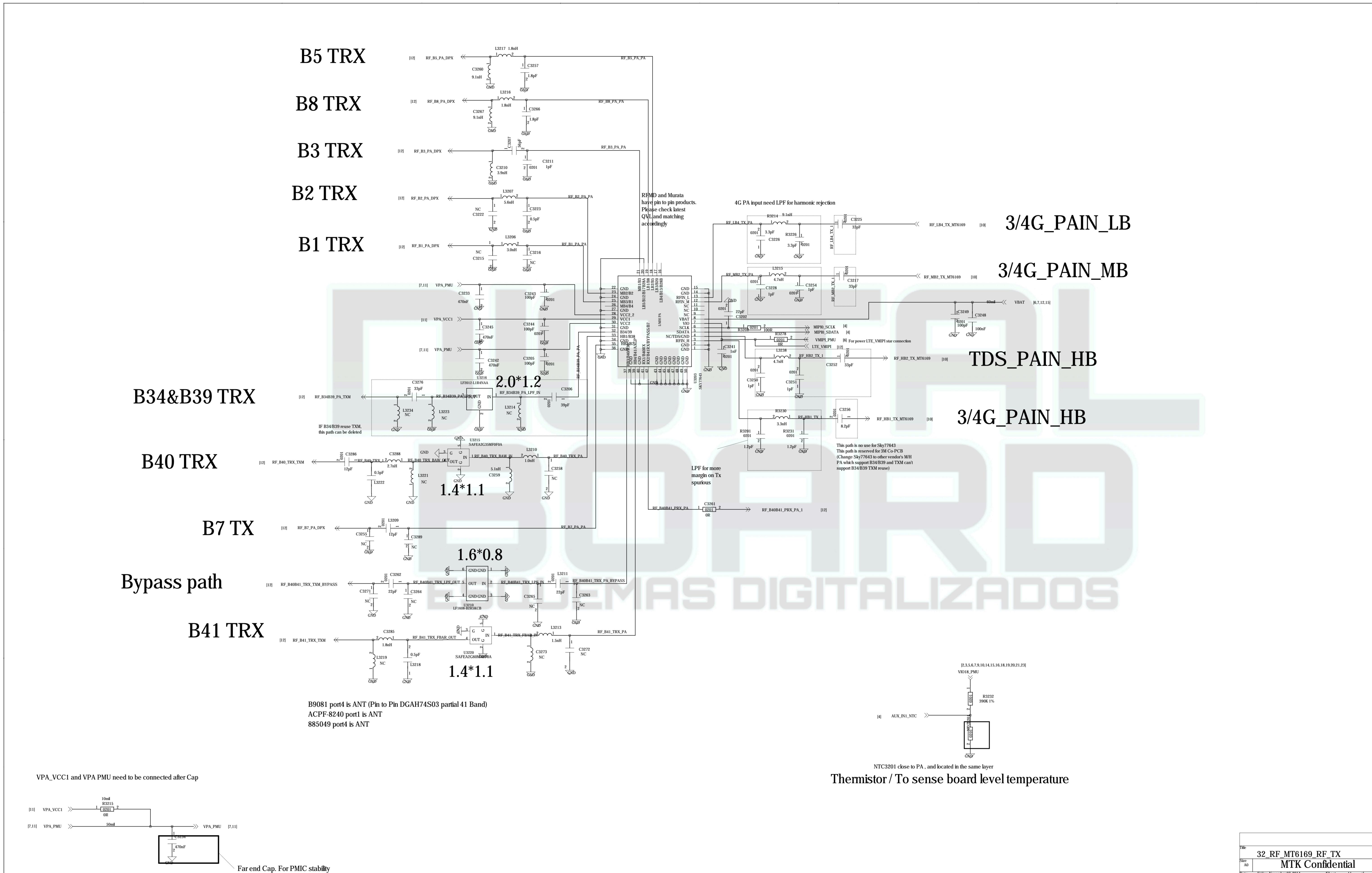
Note 24-1: DA9210's GPIO0 = SRCLKEN0 => Buck EN controlled by SRCLKEN0 or I2C.

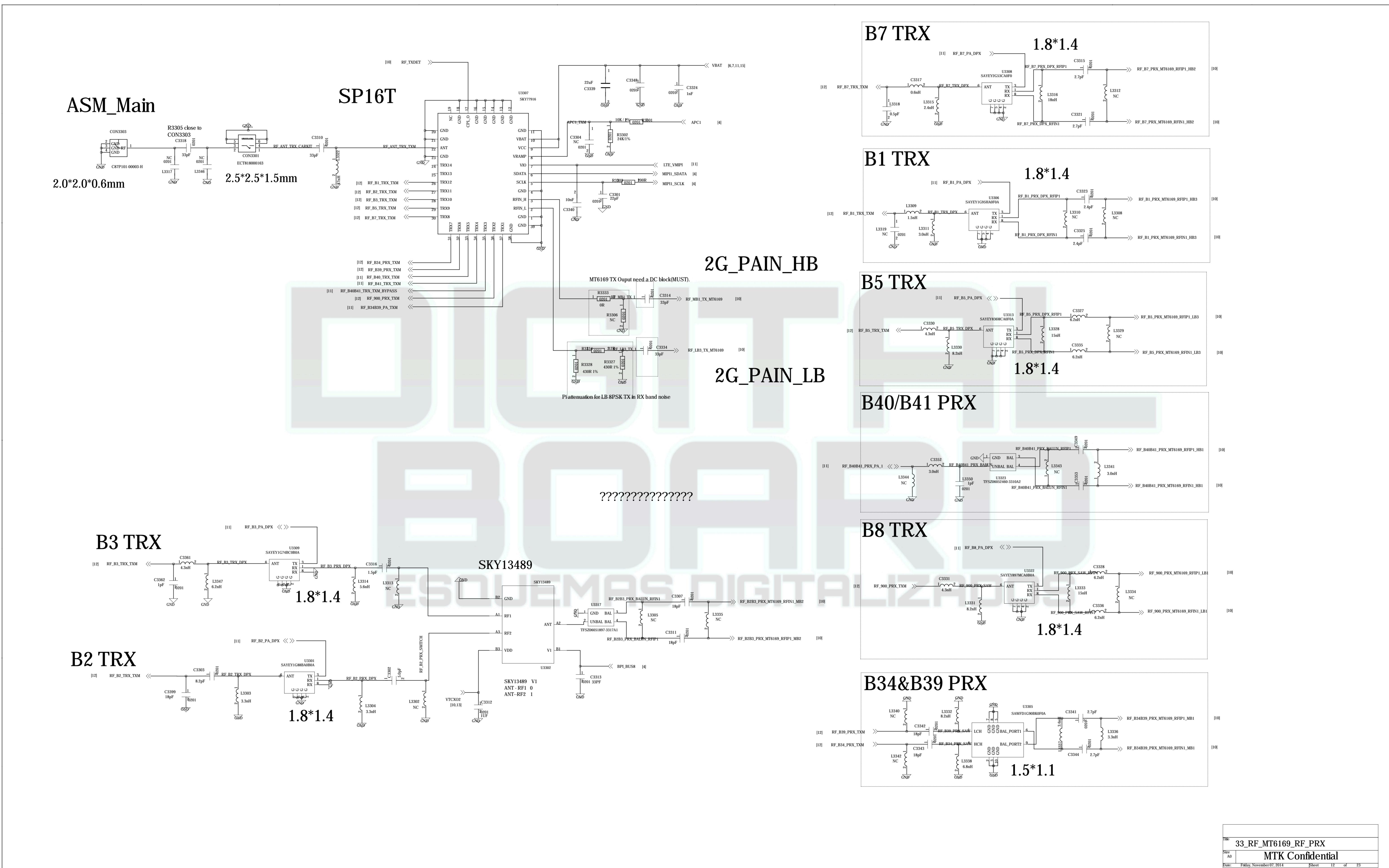
Note 24-2: BOM option to select which 4-phase buck be applied? DA9210 or second source.

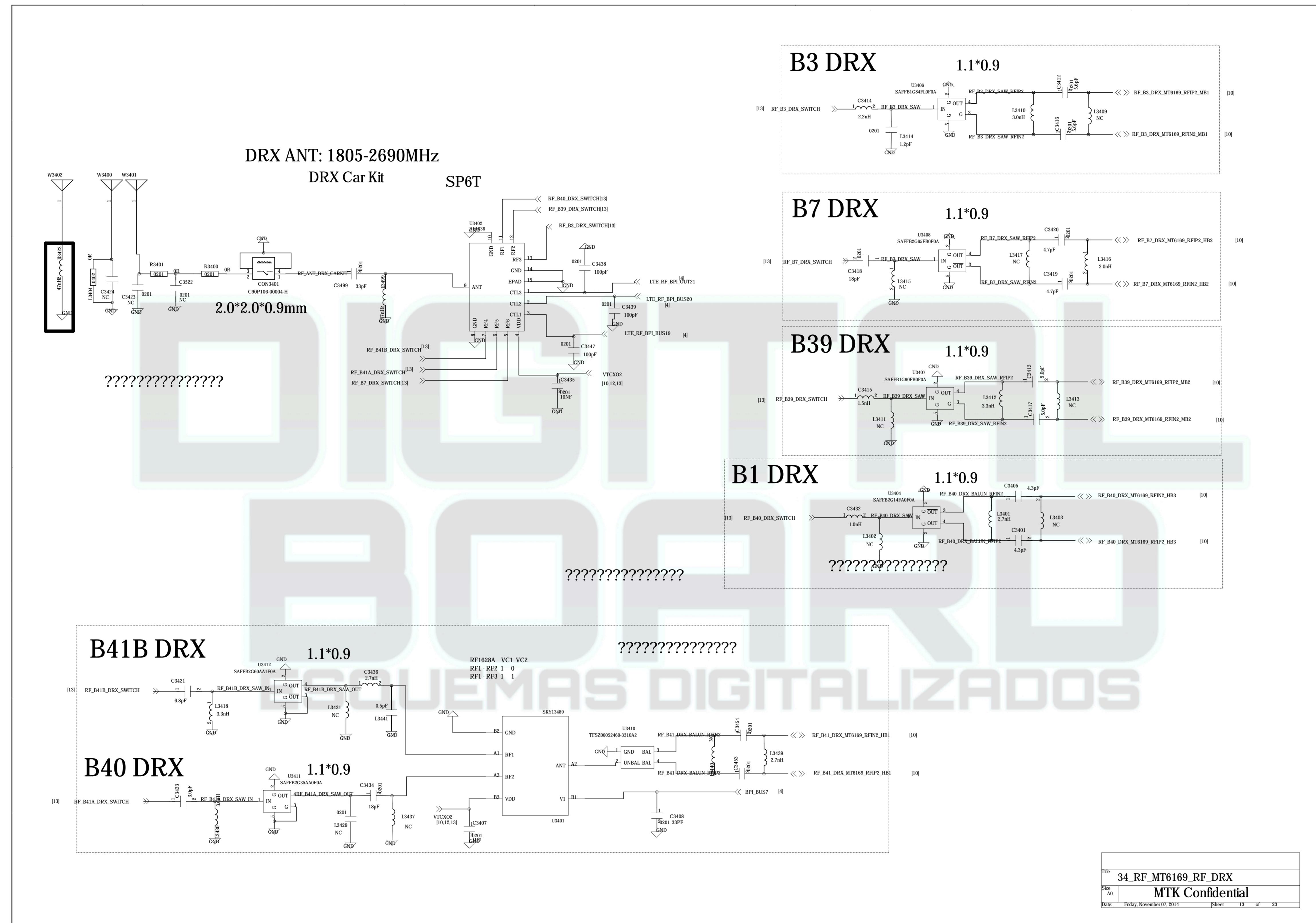
	R2402	R2404	R2405	R2406
DA9210	N/M	N/M	N/M	N/M
2nd source	10K	0R	0R	0R

COMPANY:	<Company Name>		
TITLE:	<Title>		
DRAWN:	<Drawn By>	DATED:	<Drawn Date>
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROLLED:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>
CODE:	SIZE:	DRAWING NO:	REV:
<Code> A0		<Drawing Number><Revision>	
SCALE:	<Scale>	SHEET # 23	

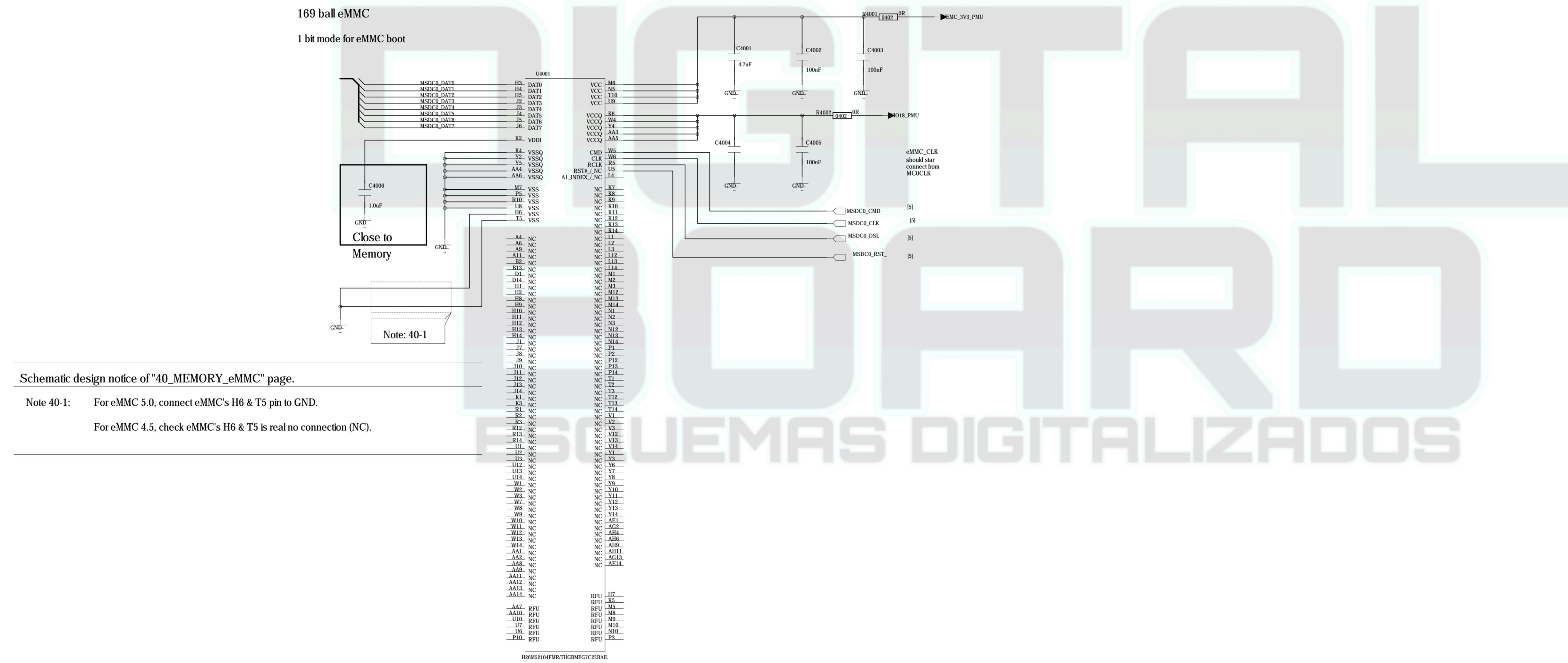






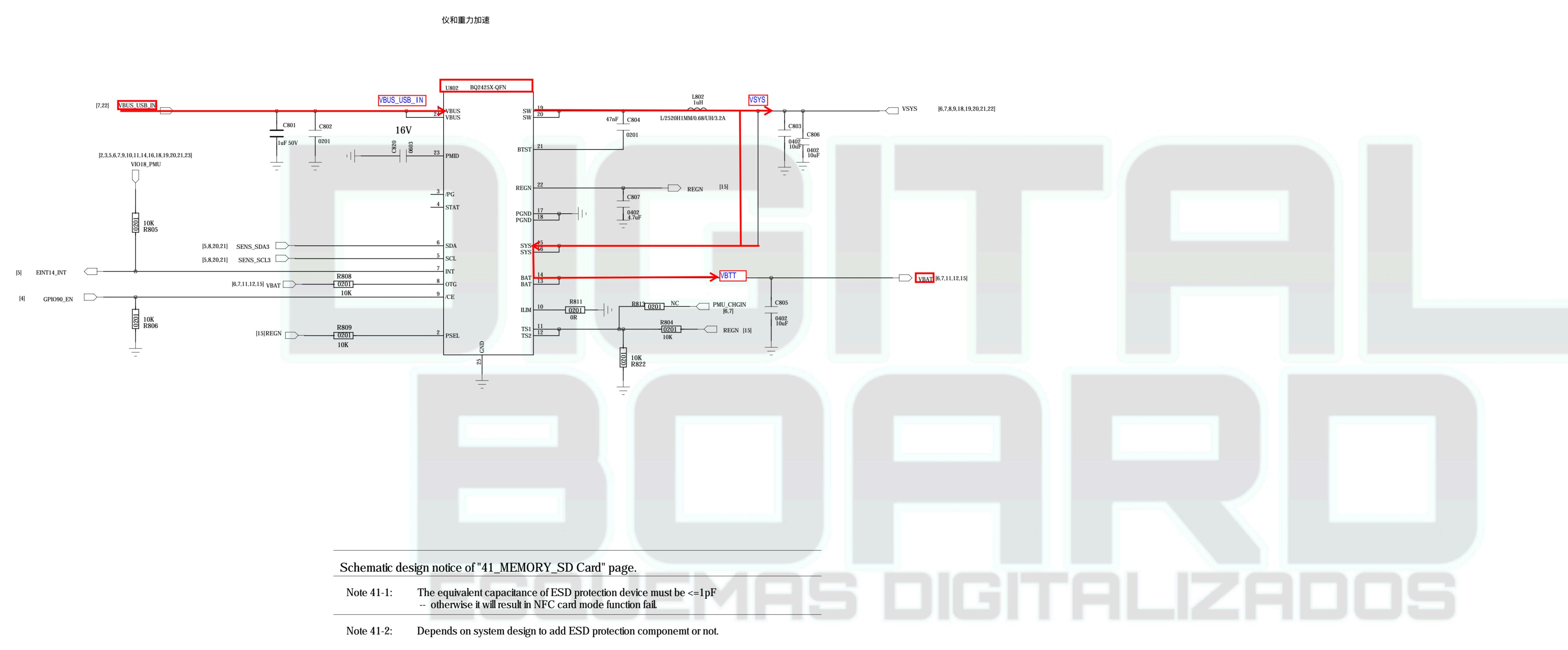


REVISION RECORD			
ltr	ECO NO.	APPROVED	DATE

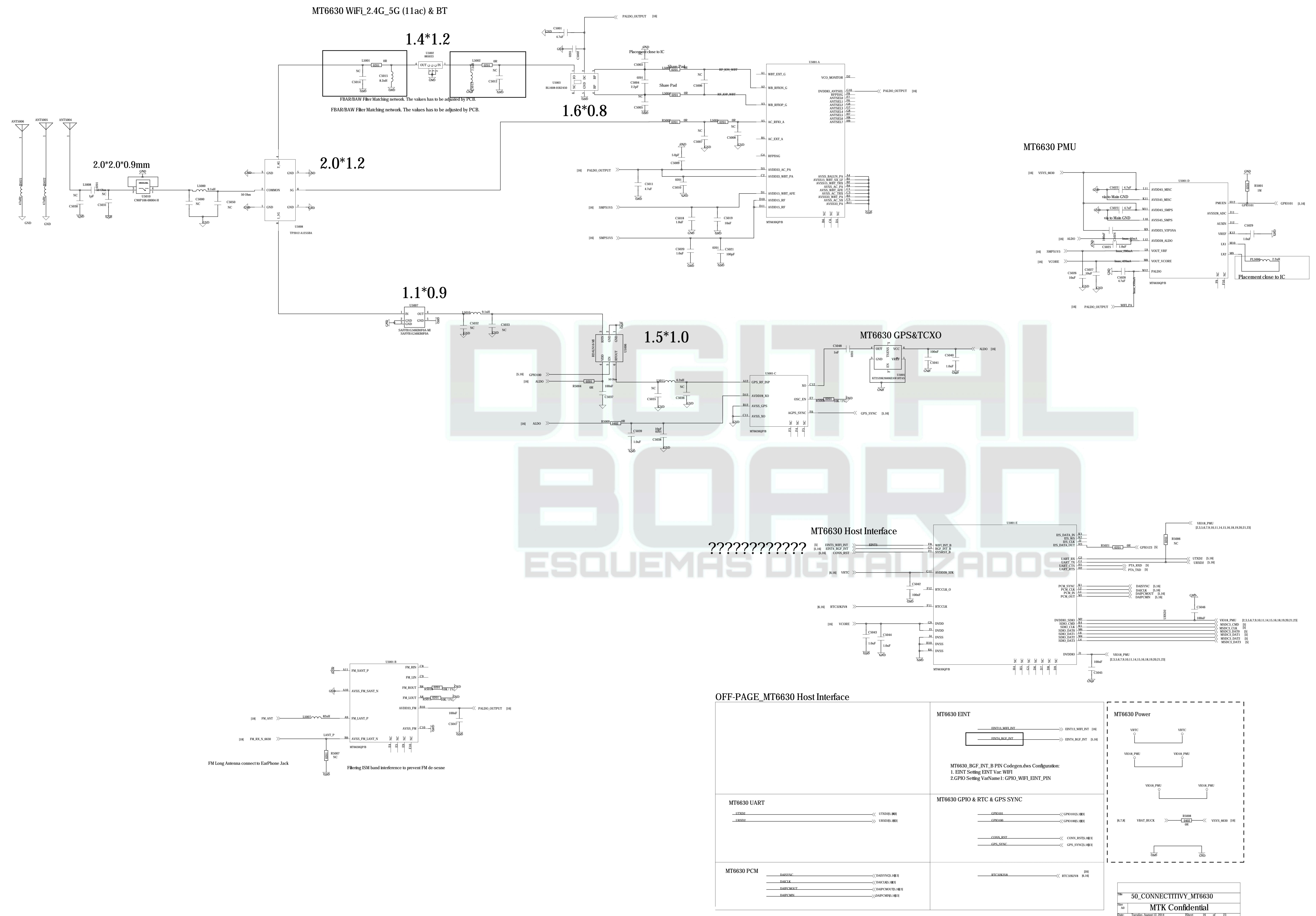


COMPANY		<Company Name>	
TITLE			
<Title>			
DRAWN	<Drawn By>	DATED	<Drawn Date>
CHECKED	<Checked By>	DATED	<Checked Date>
QUALITY CONTROL	<QC By>	DATED	<QC Date>
RELEASED	<Released By>	DATED	<Release Date>
CODE	A0	DRAWING NO.	REV.
<Code>		<Drawing Number><Revision>	
SCALE	<Scale>	SCALE	<Scale>
SHEET 14 / 23			

REVISION RECORD			
ltr	ECN NO.	APPROVED	DATE



COMPANY: <Company Name>	
TITLE: <Title>	
DRAWN: <Drawn By>	DATED: <Drawn Date>
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>
CODE: <Code>	SIZE: <Scale>
DRAWING NO: <Drawing Number><Revision>	
REV: <Rev>	SCALE: <Scale>
SHEET: 46 / 23	



**DIGITAL-
BOARD**
ESQUEMAS DIGITALIZADOS

REVISION RECORD			
LTR	ECO NO.	APPROVED	DATE

D

D

C

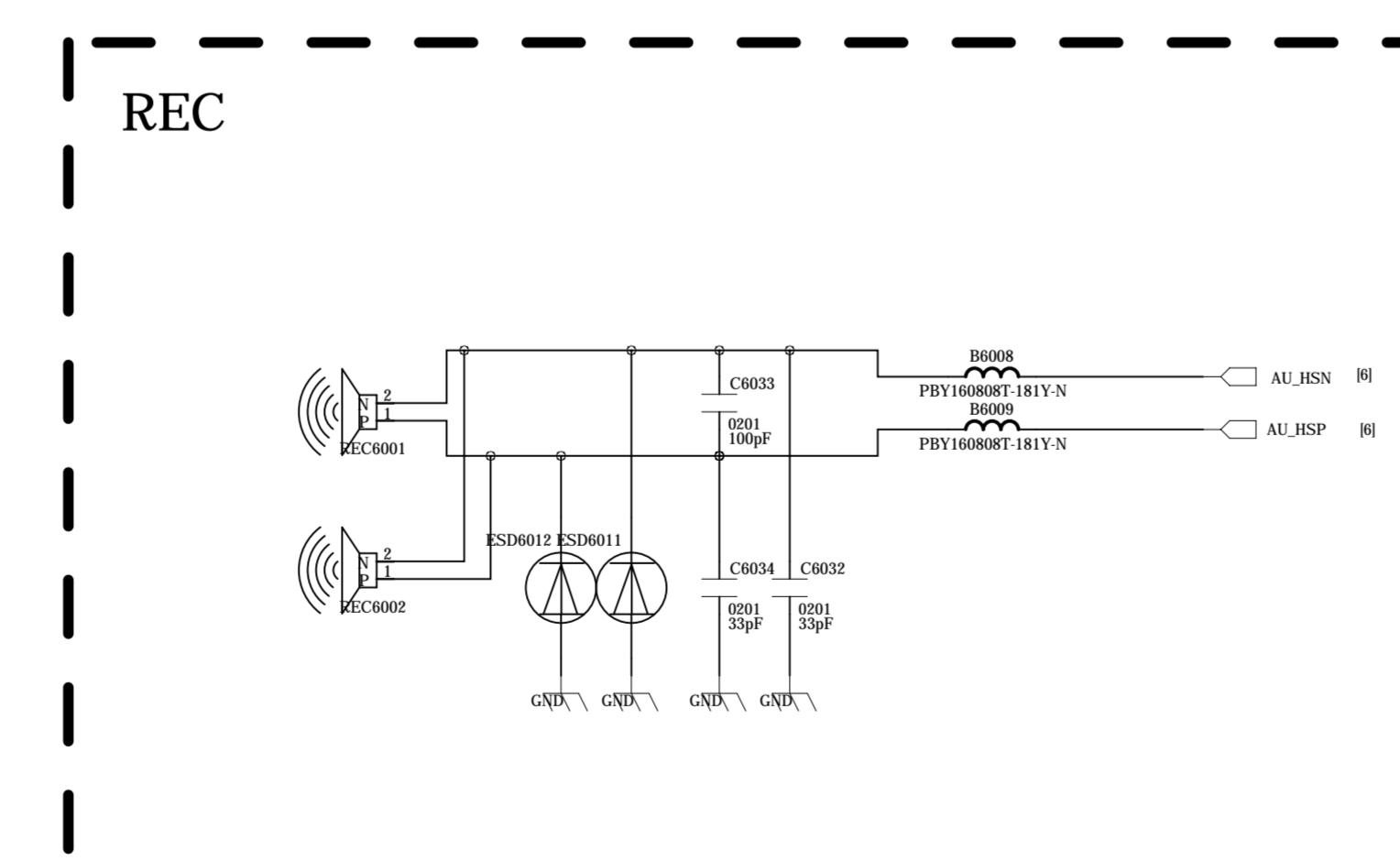
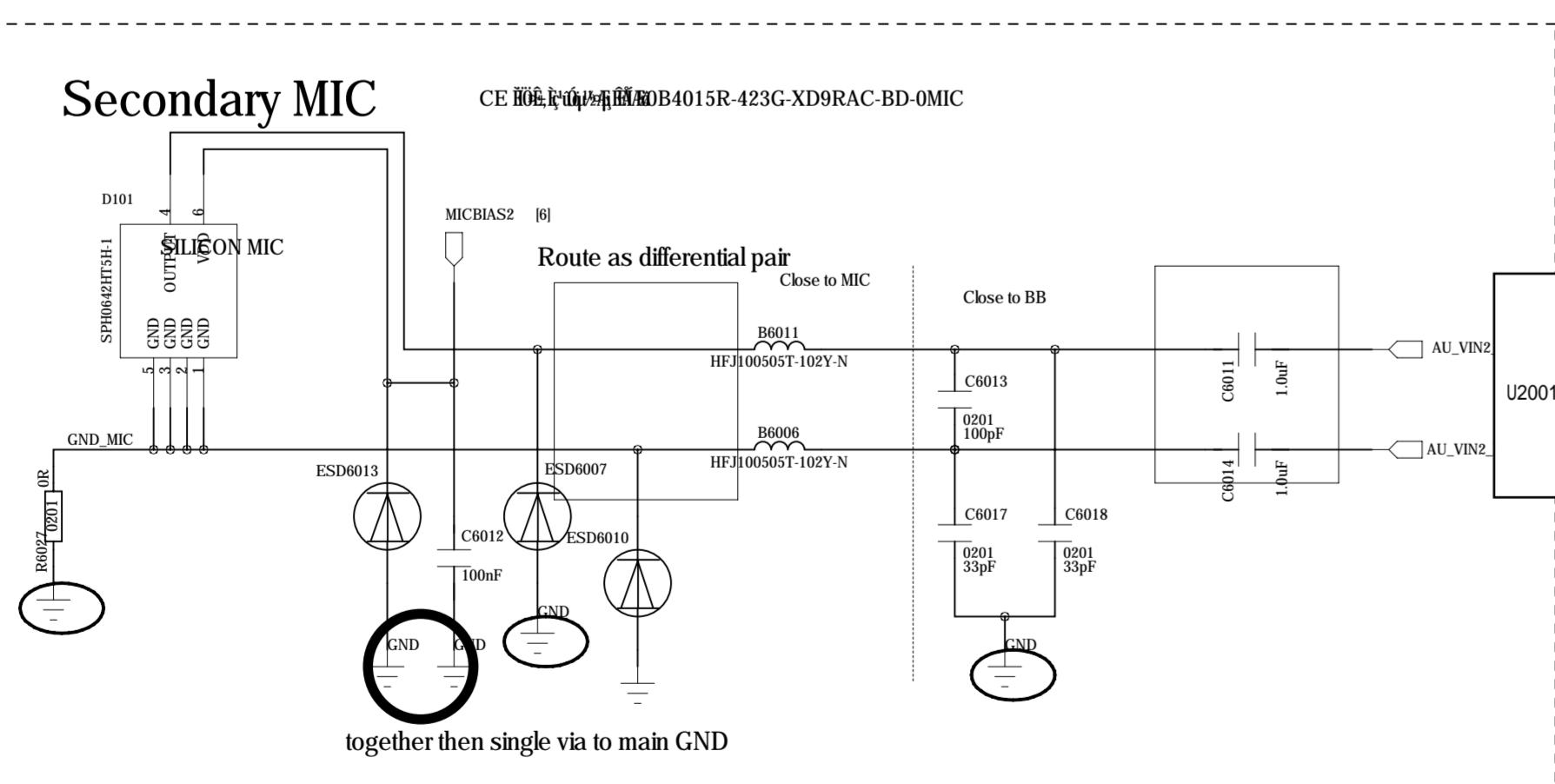
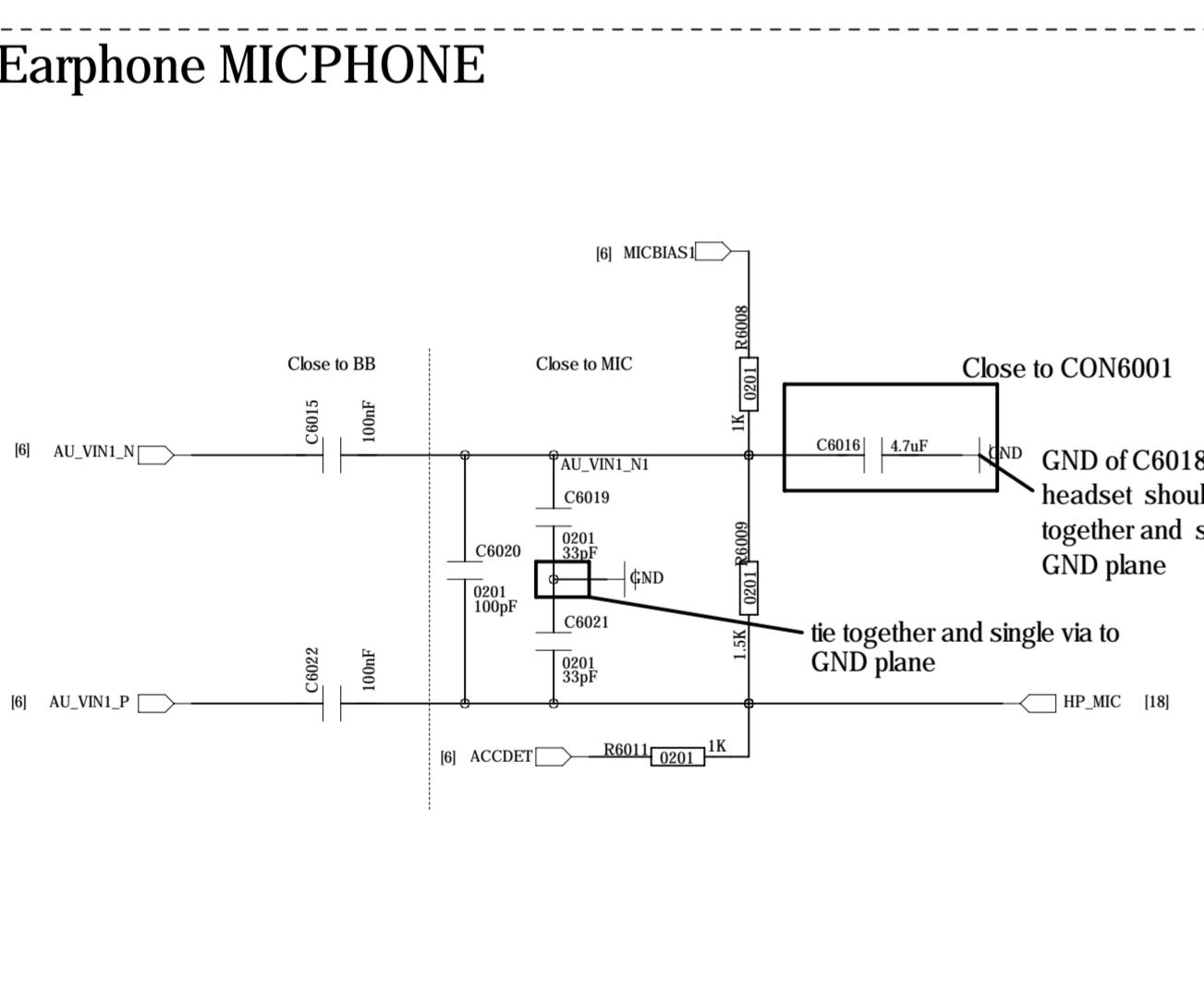
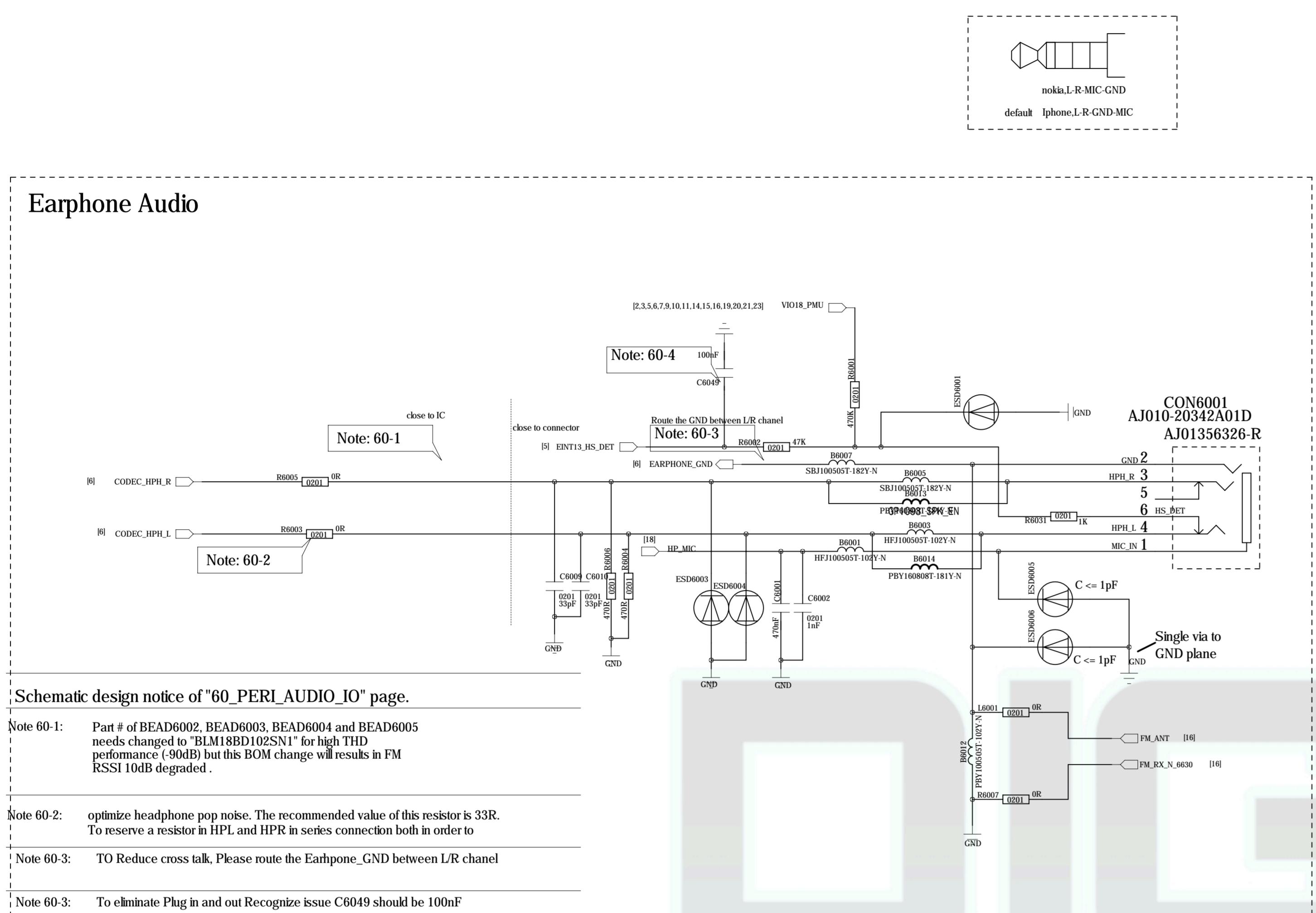
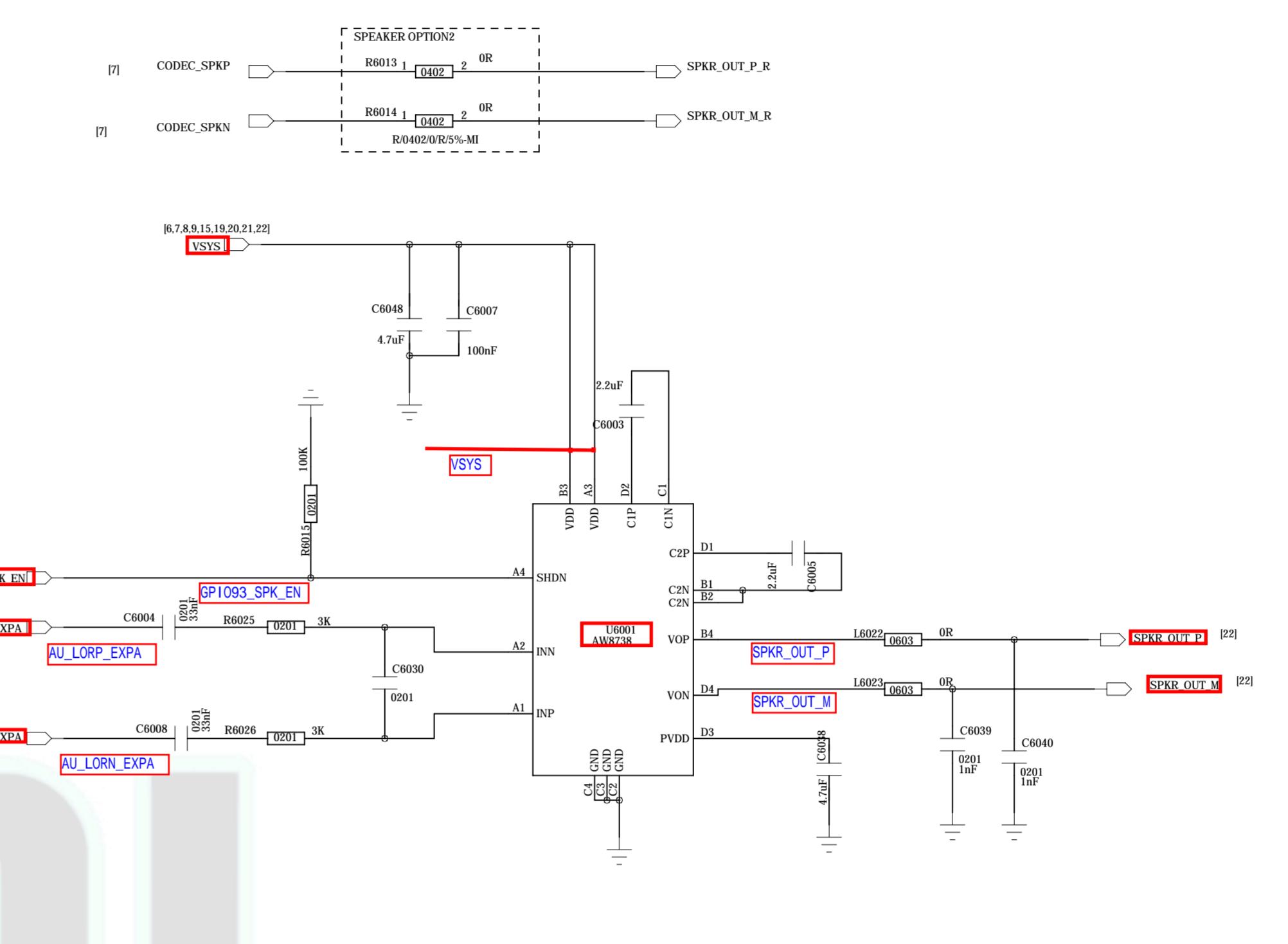
C

B

B

A


A

**SPEAKER**

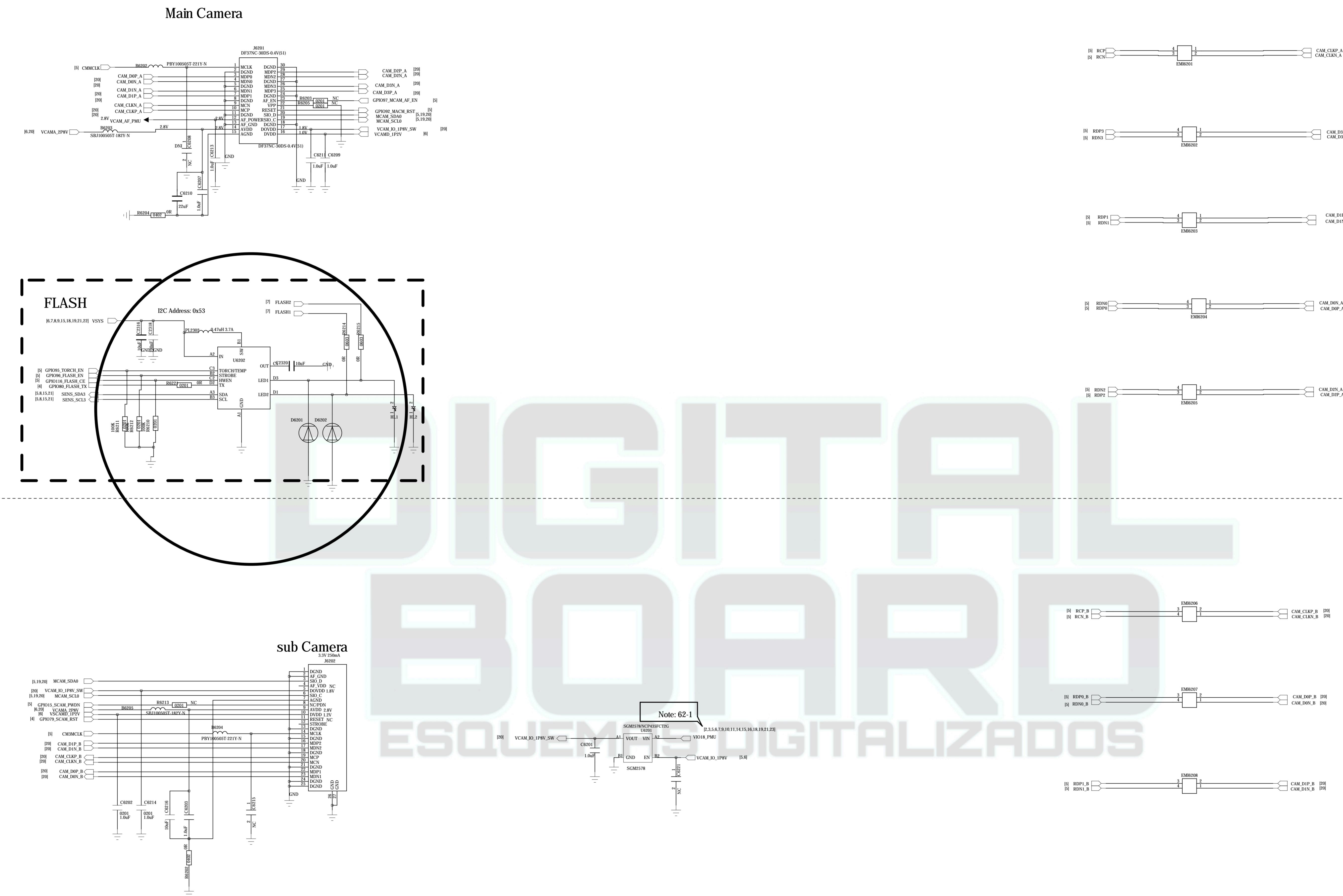
C8368when use AW145 please use 1uF CAP;when use AW8155 or AW8155A please use 0 ohm res

COMPANY:	<Company Name>		
TITLE:	<Title>		
DRAWN:	<Drawn By>	DATED:	<Drawn Date>
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>
SCALE:	<Scale>	REV:	
CODE:	A0	DRAWING NO.:	
HEET:	48	23	

REVISION RECORD			
LTN	ECN NO.	APPROVED	DATE



REVISION RECORD			
LTN	ECN NO.	APPROVED	DATE



Schematic design notice of "63_PERI_CAMERA_KEYPAD" page.

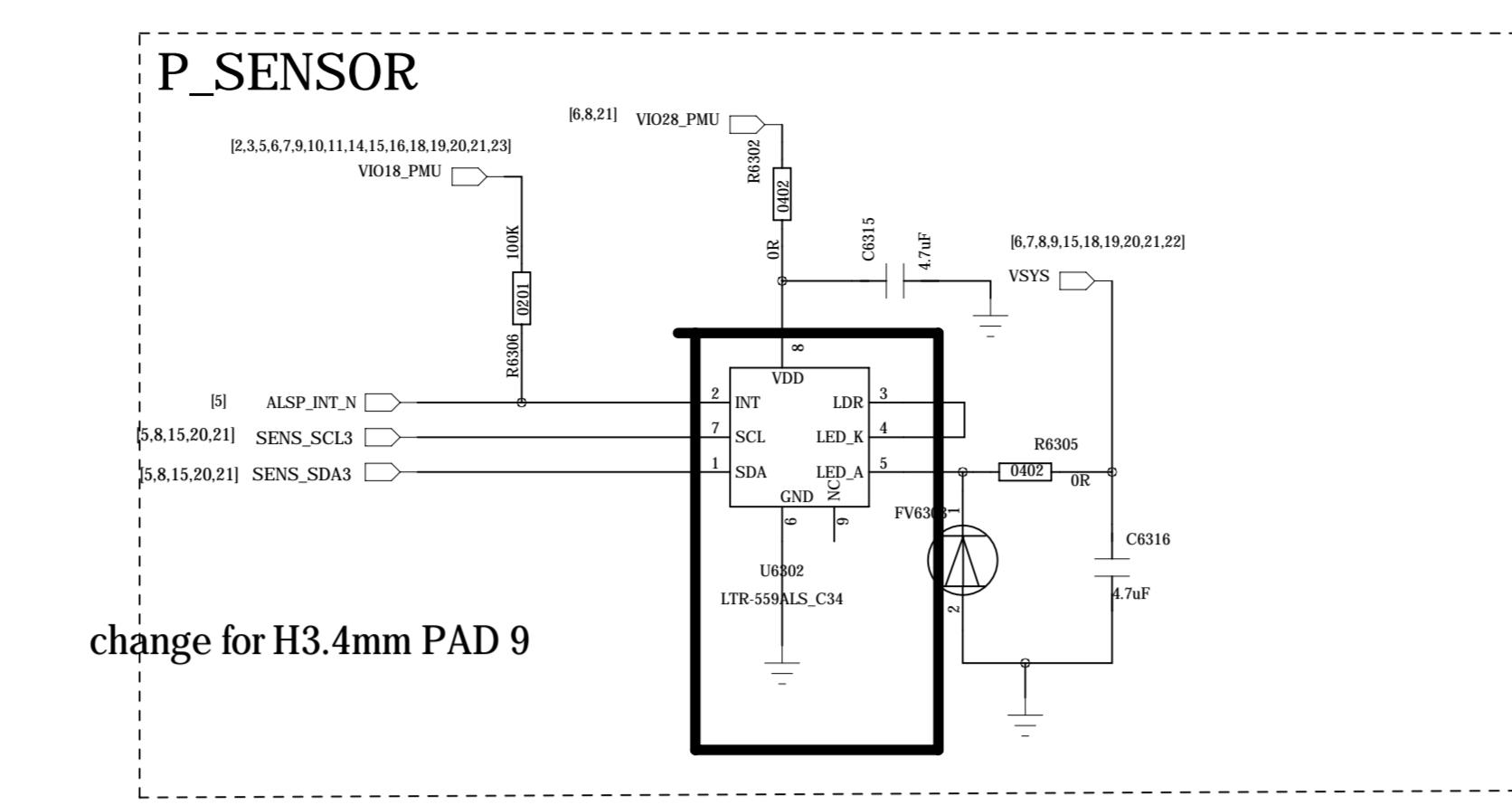
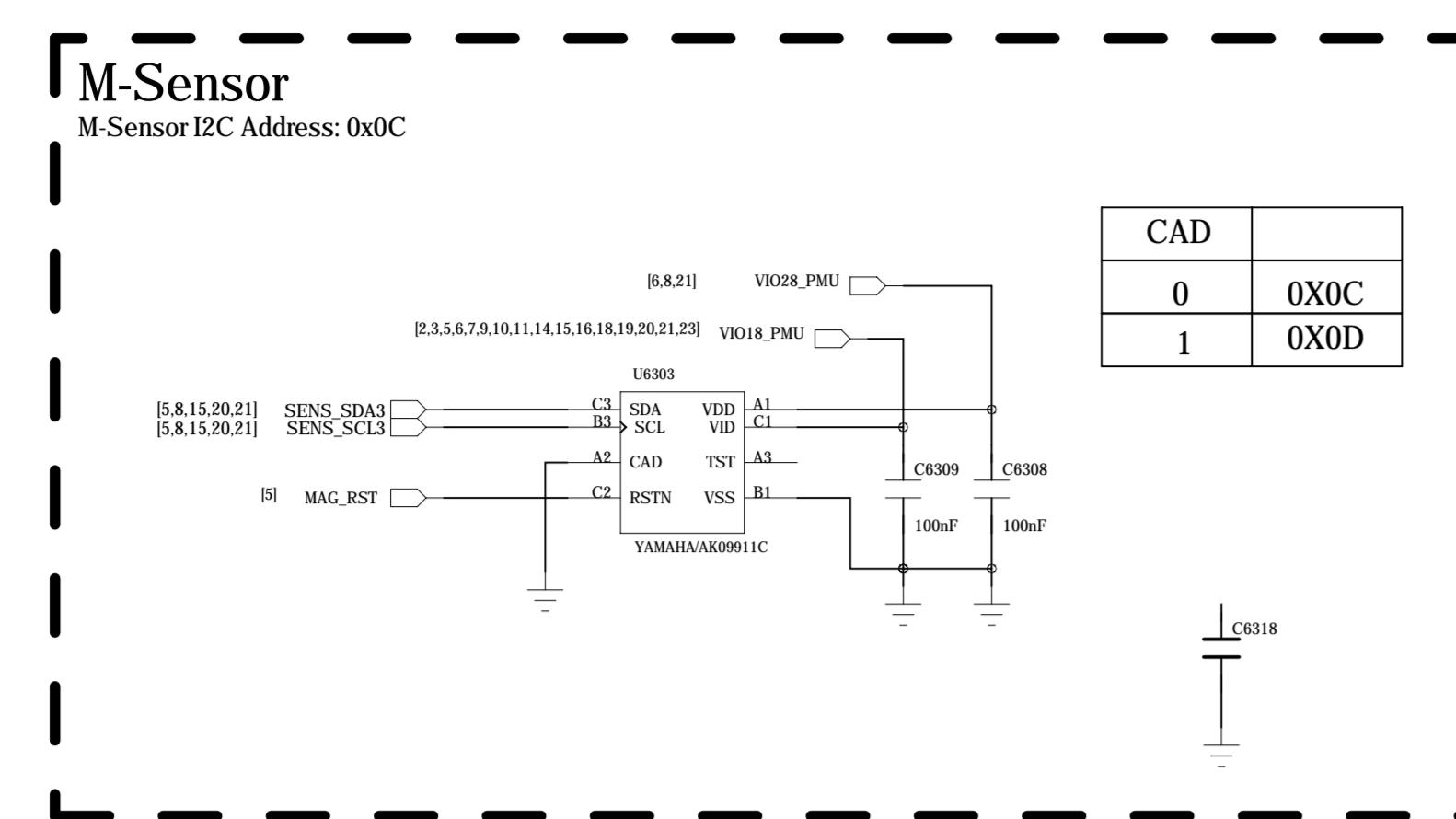
Note 62-1: The VCC of I2C_0 is pulled to "VCAM_IO_PMU".

Note 62-2: I2C control interface of front camera (with AF) must be assigned to I2C-2 bus when PIP/VIV feature be supported.

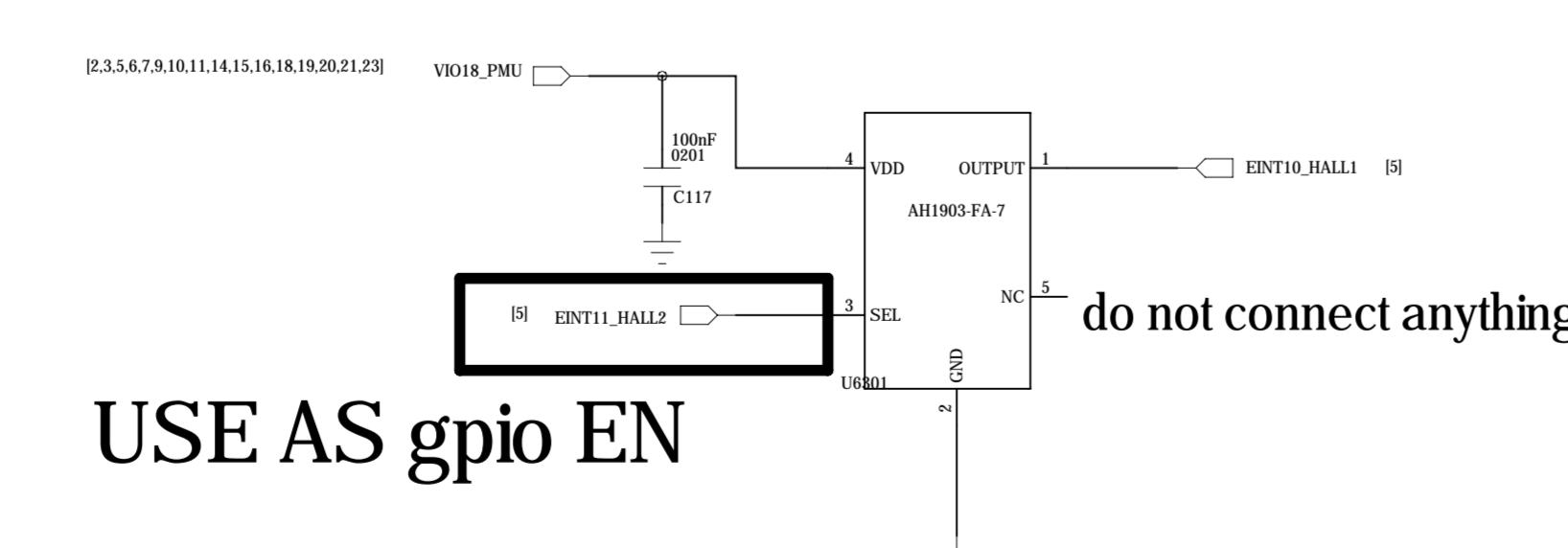
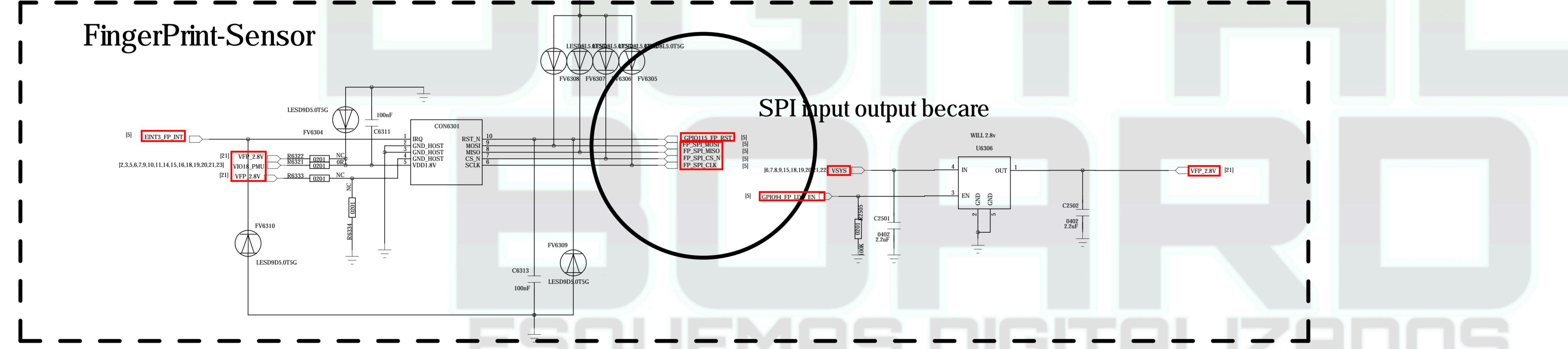
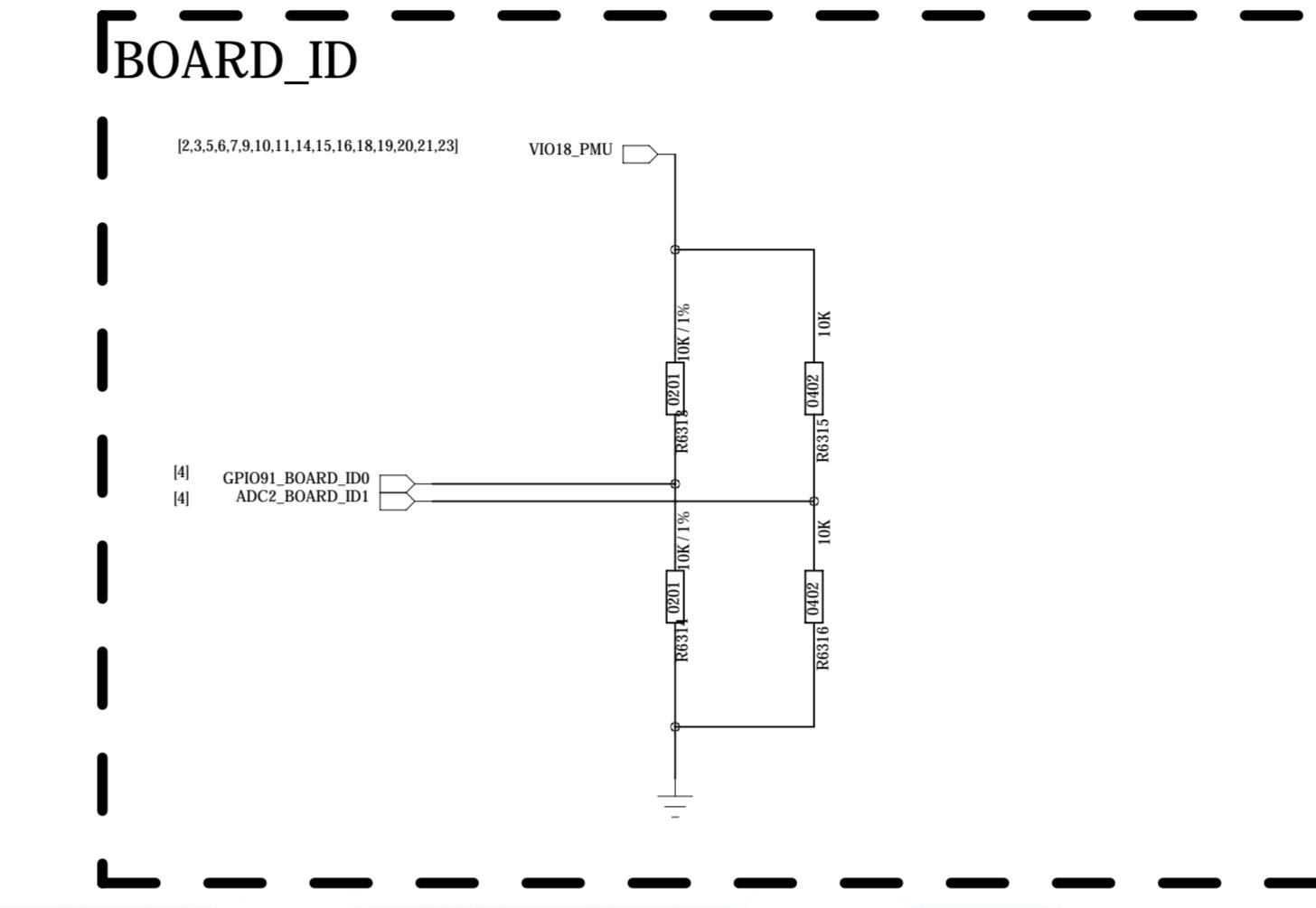
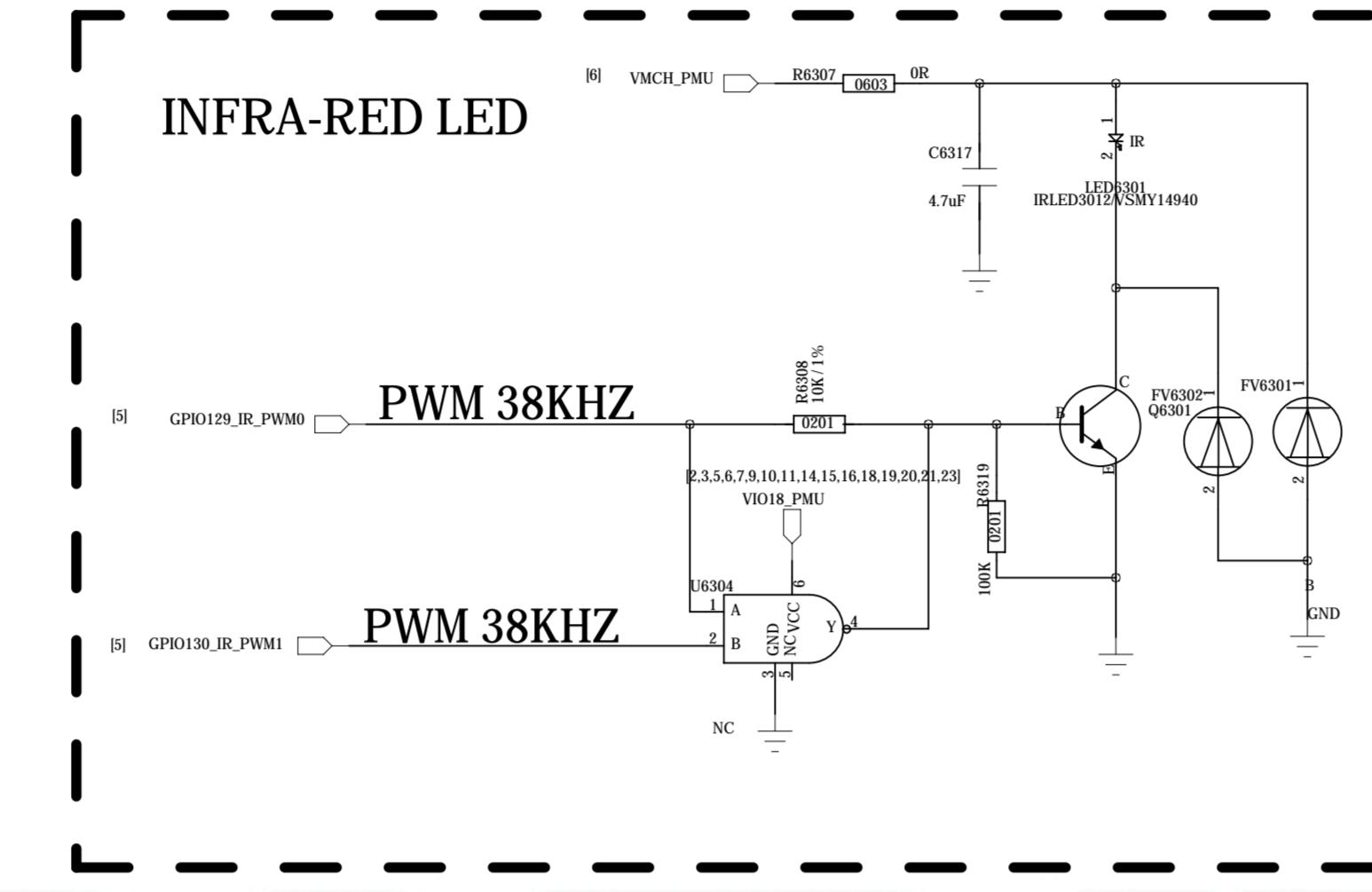
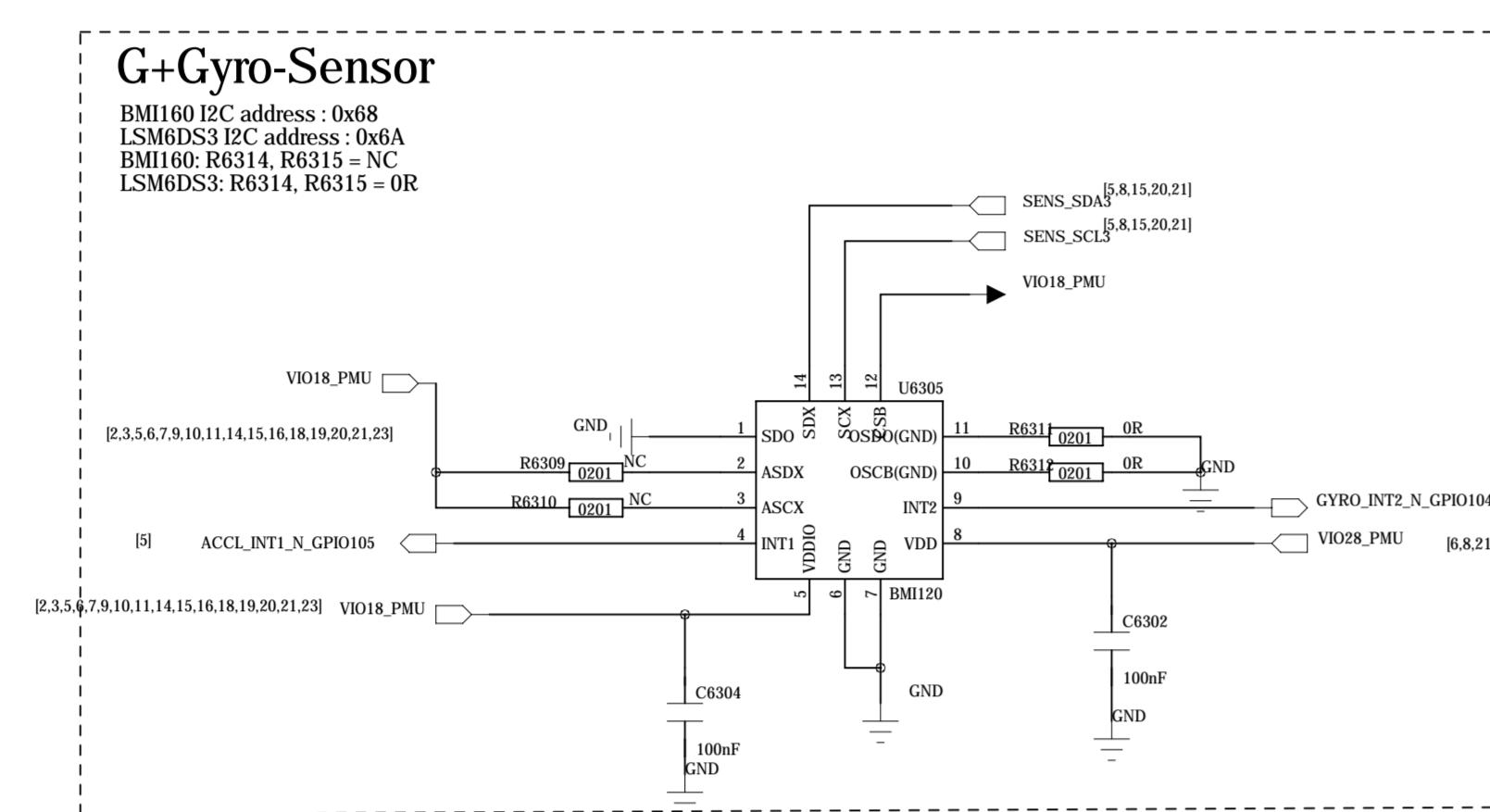
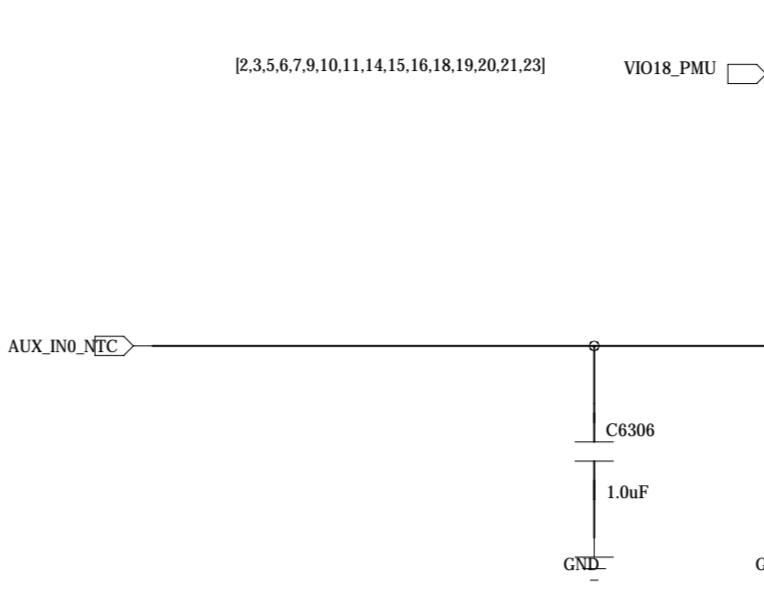
Note 62-3: Reserve a capacitor (27pF) on camera's MCLK and shunt it to GND to prevent GPS de-sense.

COMPANY: <Company Name>	
TITLE: <Title>	
DRAWN: <Drawn By>	DATED: <Drawn Date>
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	SCALE: <Scale>
CODE: <Code>	SIZE: <Size>
REV: <Revision>	DRAWING NO: <Drawing Number>

REVISION RECORD			
LTR	ECO NO	APPROVED	DATE

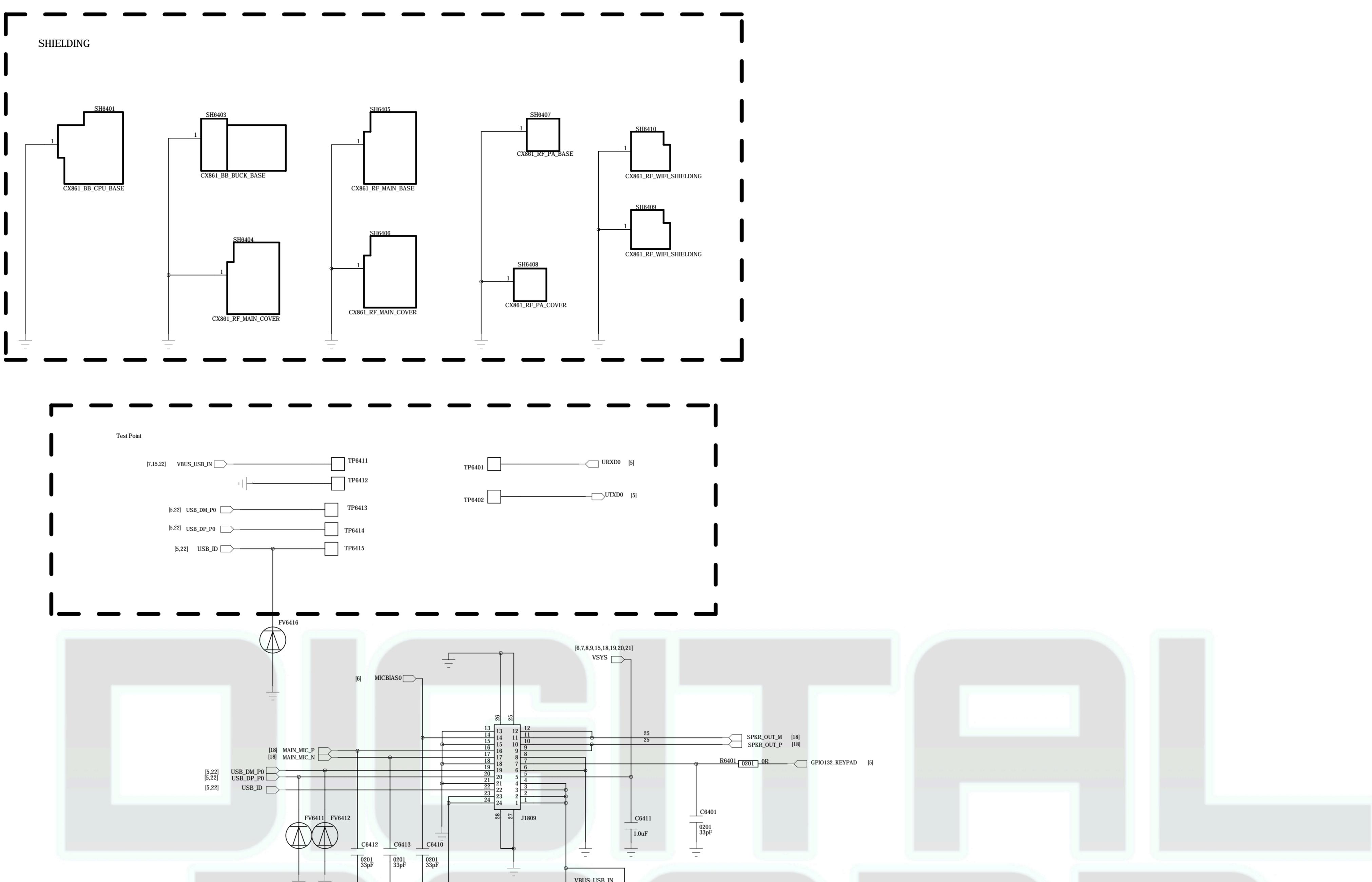


Thermister / To sense board level temperature



COMPANY:	<Company Name>		
TITLE:	<Title>		
DRAWN:	<Drawn By> DRAWN DATE		
CHECKED:	<Checked By> CHECKED DATE		
QUALITY CONTROLLED:	<QC By> QC DATE		
RELEASED:	<Released By> RELEASED DATE		
SCALE:	<Scale>	SIZE:	<Drawing Number><Revision>
REV:			

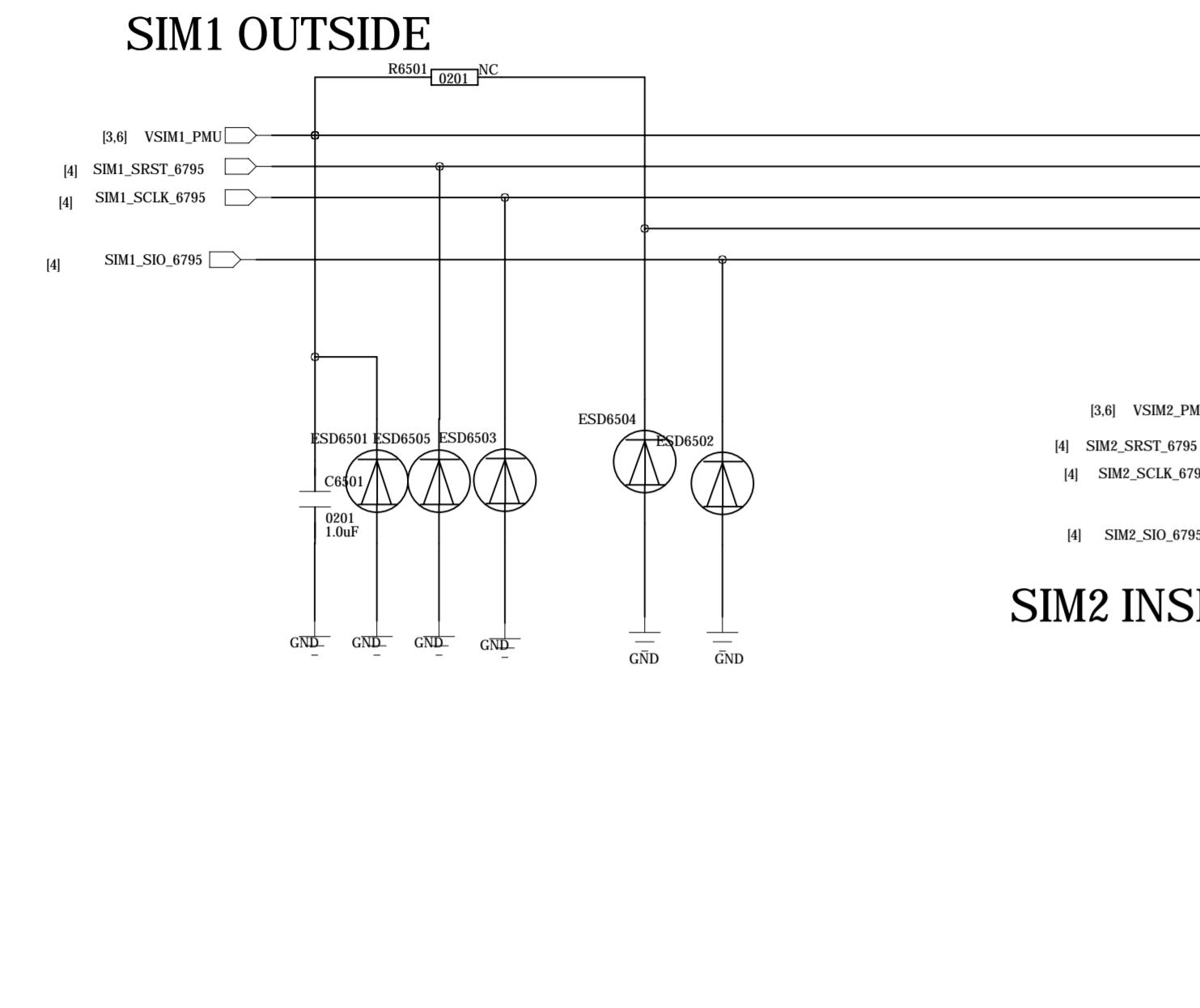
REVISION RECORD			
ltr	ECO NO	APPROVED	DATE:



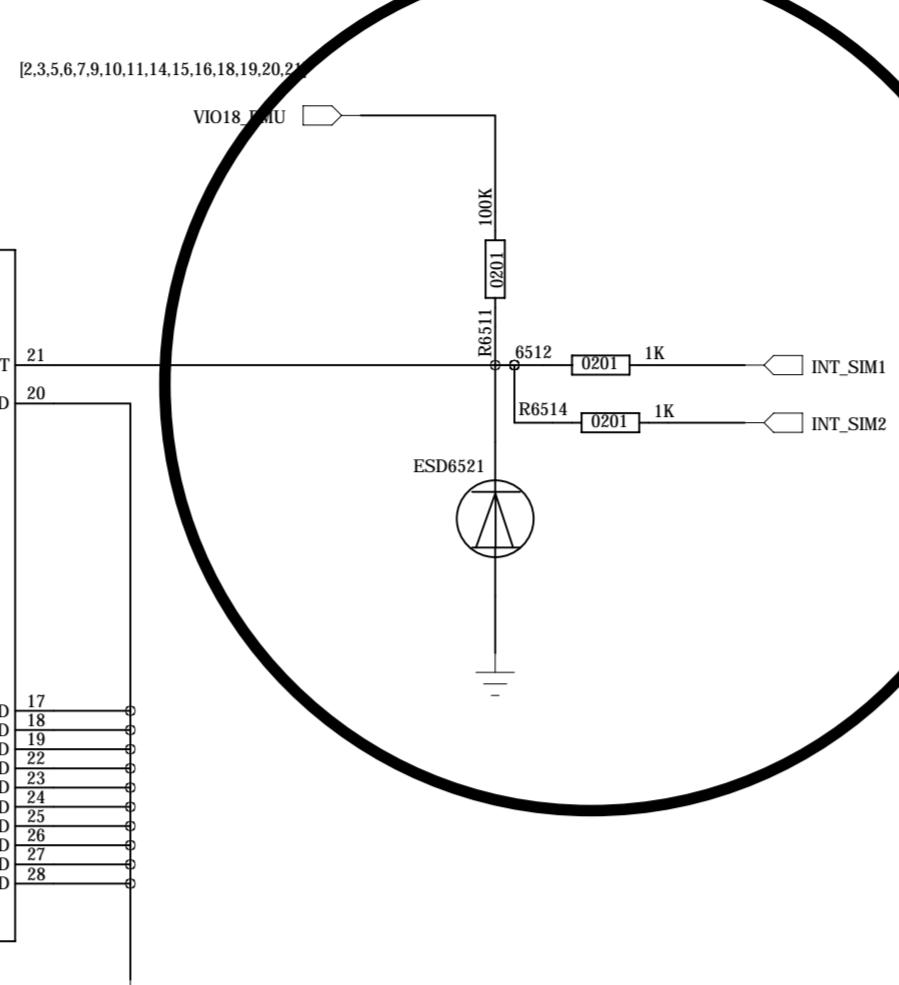
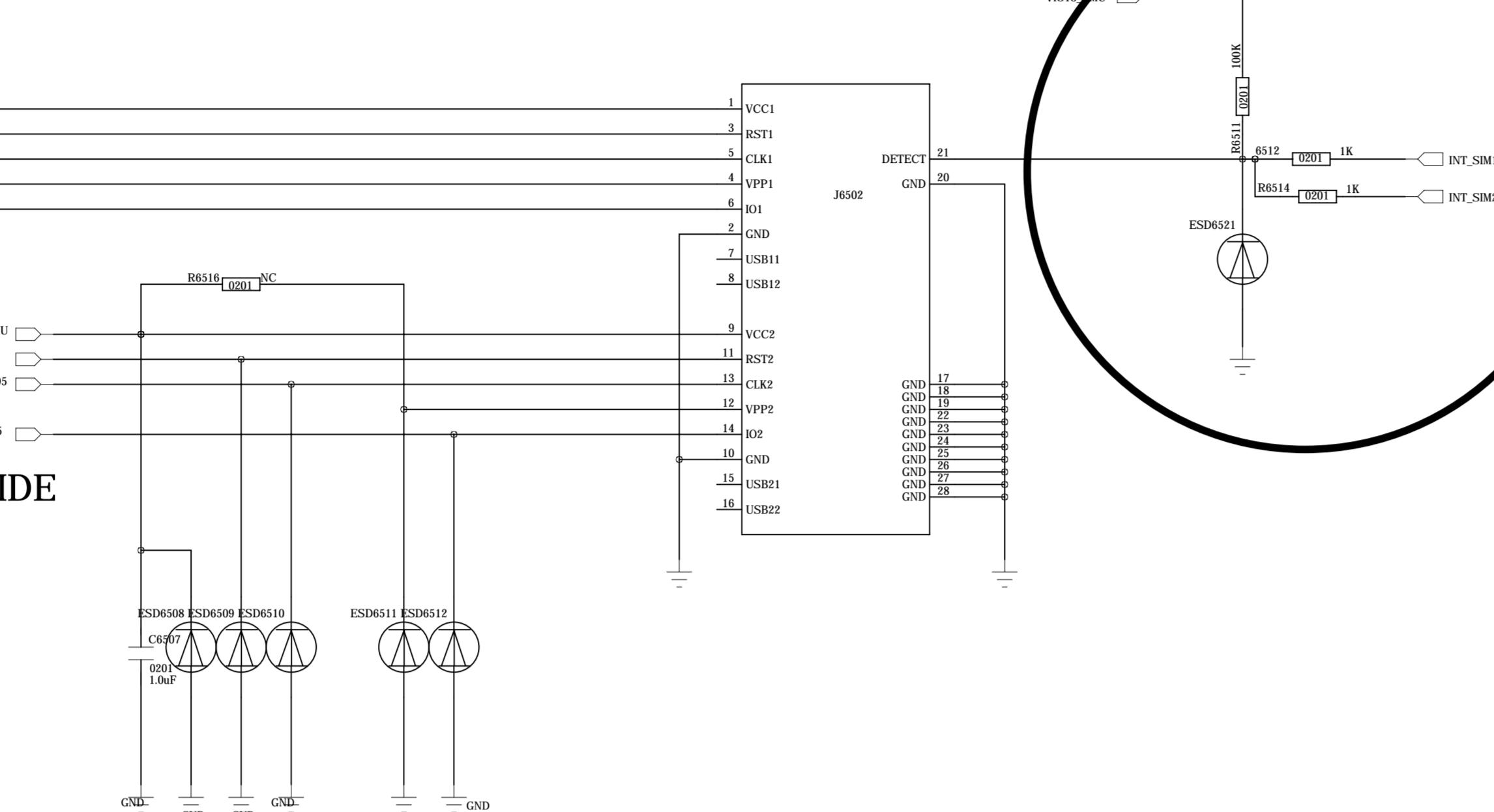
REVISION RECORD			
ltr	ECN NO.	APPROVED	DATE

D

D



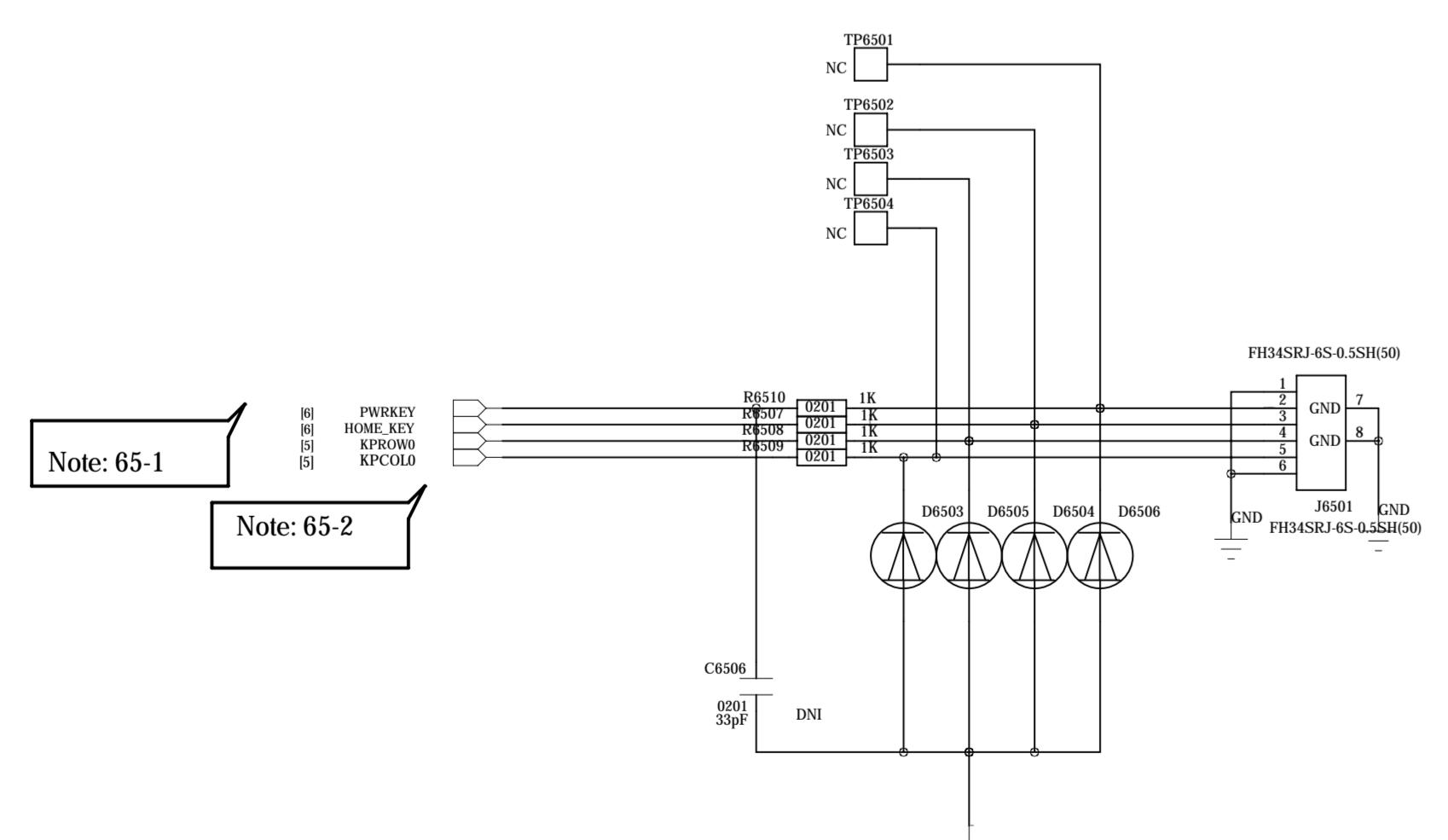
SIM2 INSIDE



DIGITAL BOARD

ESQUEMAS DIGITALIZADOS

Power Key



Schematic design notice of "65_PERI_Dual_SIM_ICUSB_KEYPAD" page.

Note 65-1: DO NOT put pull-up resistor on PWRKEY

Note 65-2: Volume Up : HOME Key/GND
Volume Down : (KPROW0/KPCOL0) or (KPCOL0/GND)

COMPANY: <Company Name>
TITLE: <Title>
DRAWN: <Drawn By> DATE: <Drawn Date>
CHECKED: <Checked By> DATE: <Checked Date>
QUALITY CONTROL: <QC By> DATE: <QC Date>
RELEASED: <Released By> DATE: <Released Date>
CODE: <Code> SIZE: <Size> DRAWING NO.: <Drawing Number> REV: <Revision>