

SDM439 GPIO Configuration For QRD439+PMI632

GPIO_0	NFC_SPI_ESE_MOSI	GPIO_41		GPIO_82	FM_DATA
GPIO_1	NFC_SPI_ESE_MISO	GPIO_42	ACCL_INT1	GPIO_83	BT_CTL
GPIO_2	NFC_SPI_CS_N_0	GPIO_43	ALSP_INT_N	GPIO_84	BT_DATA
GPIO_3	NFC_SPI_ESE_CLK	GPIO_44	MAG_DRDY_INT	GPIO_85	FP_SPI_MOSI
GPIO_4	QUP_UART_TX_2	GPIO_45	GYRO_INT	GPIO_86	FP_SPI_MISO
GPIO_5	QUP_UART_RX_2	GPIO_46		GPIO_87	FP_SPI_CS
GPIO_6	WSA_SMB_I2C_SDA	GPIO_47	SENSOR_SPI_CS2_N	GPIO_88	FP_SPI_CLK
GPIO_7	WSA_SMB_I2C_SCL	GPIO_48	FP_INT_N1	GPIO_89	
GPIO_8	TP_SPI_MOSI	GPIO_49	UIM_BATT_ALARM	GPIO_90	
GPIO_9	TP_SPI_MISO	GPIO_50		GPIO_91	KEY_VOL_UP_N
GPIO_10	TP_SPI_CS_SDA	GPIO_51	UIM1_DATA	GPIO_92	
GPIO_11	TP_SPI_CLK_SCL	GPIO_52	UIM1_CLK	GPIO_93	NFC_ESE_PWR_REQ
GPIO_12	WSA_INTR	GPIO_53	UIM1_RESET	GPIO_94	WSA_IO_DATA
GPIO_13	LCD_ID	GPIO_54	UIM1_PRESENT	GPIO_95	WSA_IO_CLK
GPIO_14	SENSOR_I2C_SDA	GPIO_55	UIM2_DATA	GPIO_96	WSA_EN
GPIO_15	SENSOR_I2C_SCL	GPIO_56	UIM2_CLK	GPIO_97	HomeKey_FP_INT
GPIO_16	NFC_DISABLE	GPIO_57	UIM2_RESET	GPIO_98	
GPIO_17	NFC_IRQ	GPIO_58	UIM2_PRESENT	GPIO_99	
GPIO_18	NFC_I2C_SDA	GPIO_59		GPIO_100	RFFE1_CLK
GPIO_19	NFC_I2C_SCL	GPIO_60	LCD0_RESET_N	GPIO_101	RFFE1_DATA
GPIO_20	SENSOR_SPI_MOSI	GPIO_61	SMB_INT	GPIO_102	RFFE2_CLK
GPIO_21	SENSOR_SPI_MISO	GPIO_62		GPIO_103	RFFE2_DATA
GPIO_22	SENSOR_SPI_CS0_N	GPIO_63		GPIO_104	
GPIO_23	SENSOR_SPI_CLK	GPIO_64	TP_RST_N	GPIO_105	
GPIO_24	LCD_TE0	GPIO_65	TP_INT_N	GPIO_106	
GPIO_25	WSA_MCLK	GPIO_66		GPIO_107	GRFC3_SEL
GPIO_26	CAM_MCLK0	GPIO_67	SDCARD_DET_N	GPIO_108	
GPIO_27	CAM_MCLK1	GPIO_68		GPIO_109	GRFC5_SEL
GPIO_28		GPIO_69	CDC_PDM_CLK	GPIO_110	GRFC6_SEL
GPIO_29	CAM_I2C_SDAO	GPIO_70	CDC_PDM_SYNC	GPIO_111	GRFC7_SEL
GPIO_30	CAM_I2C_SCL0	GPIO_71	CDC_PDM_TX	GPIO_112	GRFC8_SEL
GPIO_31	CAM_I2C_SDA1	GPIO_72	CDC_PDM_RX0	GPIO_113	GRFC9_SEL
GPIO_32	CAM_I2C_SCL1	GPIO_73	CDC_PDM_RX1	GPIO_114	GRFC10_SEL
GPIO_33		GPIO_74	CDC_PDM_RX2	GPIO_115	
GPIO_34	FLASH_STROBE_NOW	GPIO_75	BT_SSBI	GPIO_116	
GPIO_35	CAM_AVDD_LDO_EN	GPIO_76	WL_CMD_DATA_2	GPIO_117	
GPIO_36	MCAM_RST_N	GPIO_77	WL_CMD_DATA_1	GPIO_118	EXT_GPS_LNA_EN
GPIO_37	FORCE_USB_BOOT	GPIO_78	WL_CMD_DATA_0	GPIO_119	CHO_GSM_TX_PHASE_D0
GPIO_38	SCAM_RST_N	GPIO_79	WL_CMD_SET	GPIO_120	RFFE5_CLK
GPIO_39		GPIO_80	WL_CMD_CLK	GPIO_121	RFFE5_DATA
GPIO_40		GPIO_81	FM_SSBI	GPIO_122	

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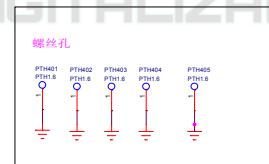
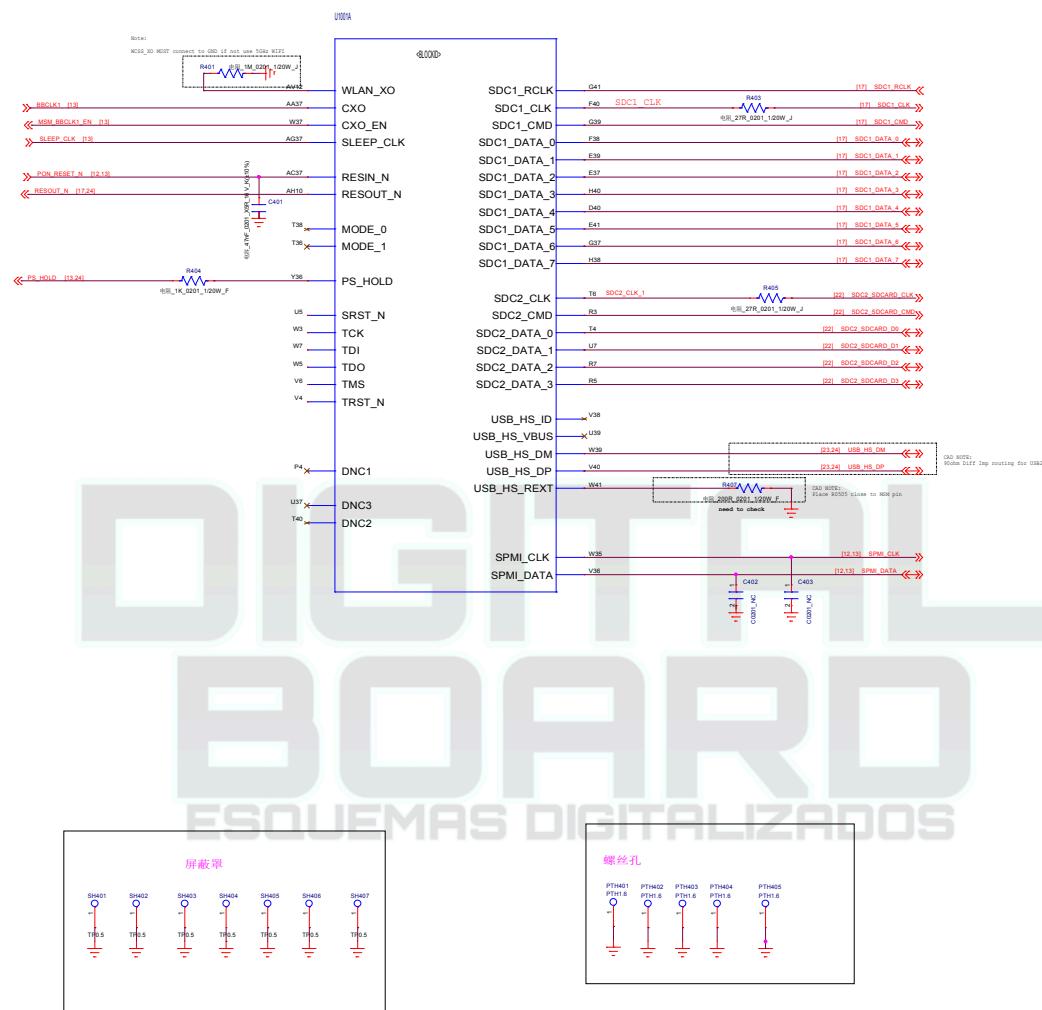


GPIO_123			
GPIO_124	FP_RESET		
GPIO_125			
GPIO_126			
GPIO_127			
GPIO_128			
GPIO_129			
GPIO_130	NFC_DWL_REQ		
GPIO_131			
GPIO_132	RCM_MARKER1		
GPIO_133	RCM_MARKER2		

PMI632 GPIO/MPP Configuration			
GPIO_1	CONNECTOR_THERMAL	GPIO_5	FLASH_STROBE_NOW
GPIO_2	SMB_PARALLEL_CHG_EN	GPIO_6	NEBULA_PWM
GPIO_3	SKIN_THEREMAL	GPIO_7	SMB_VCHG_P
GPIO_4	SMB_THEREMAL	GPIO_8	SMB_VCHG_M

PM439 GPIO/MPP Configuration			
GPIO_1		MPP_1	VDD_PX_BIAS_MPP_1
GPIO_2	NFC_CLK_REQ		
GPIO_3	UIM_BATT_ALARM	MPP_3	VREF_DAC_MPP_3
GPIO_4	WLED_EN	MPP_4	QUIET_THERM_TP_LED_K
GPIO_5			
GPIO_6			
GPIO_7			
GPIO_8	LCM_BL_PWM		

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SDM439 EBI

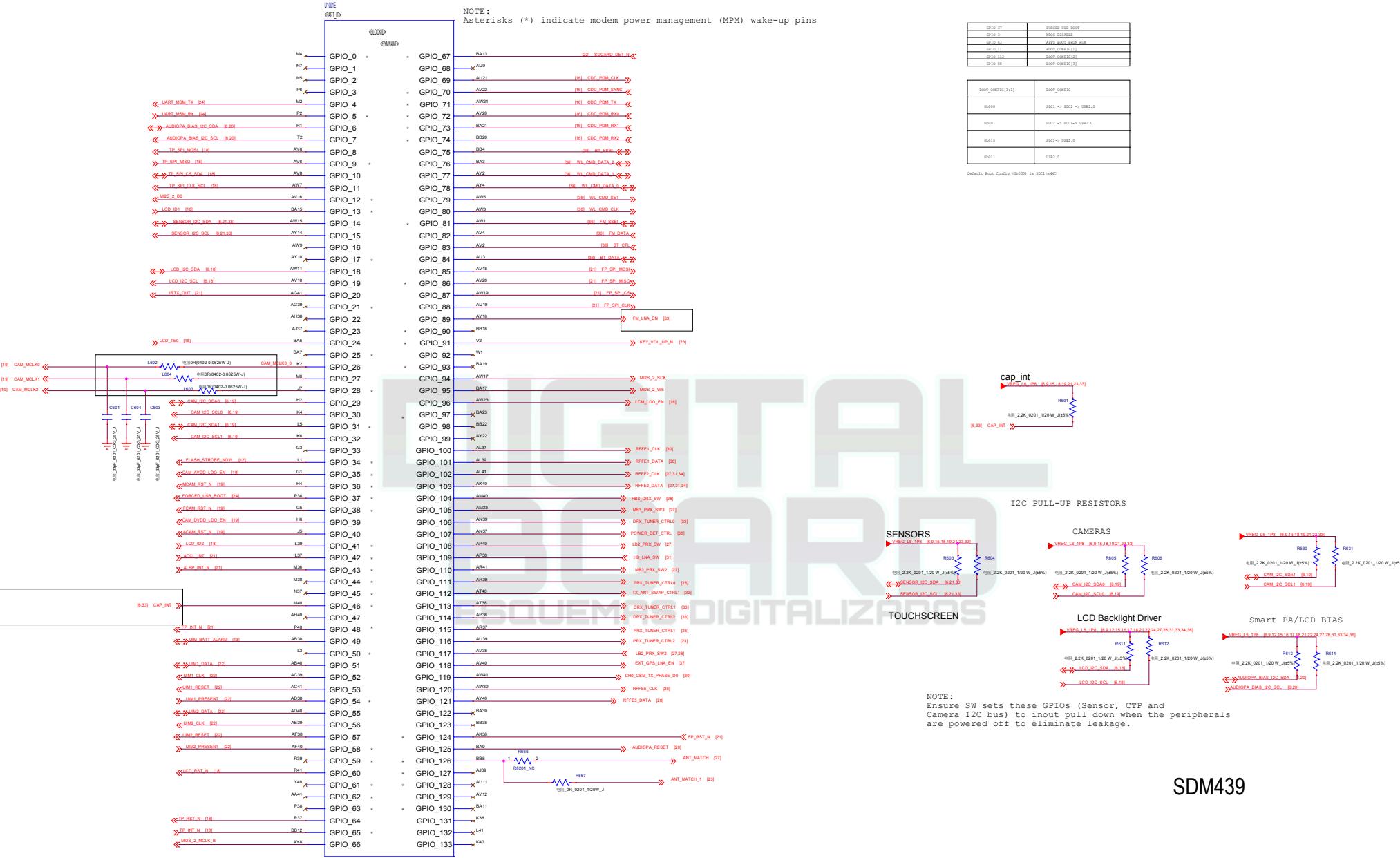
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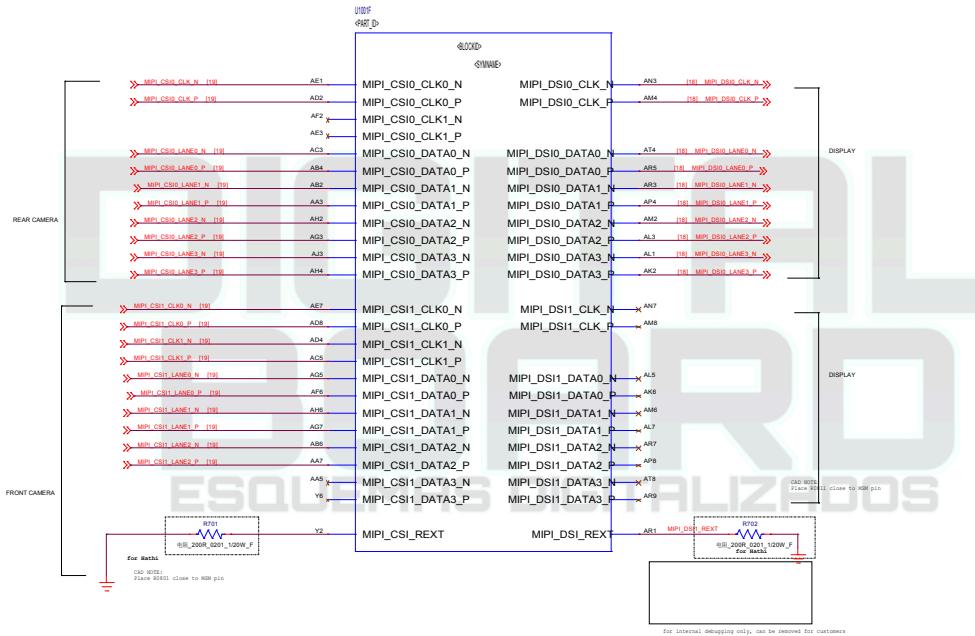
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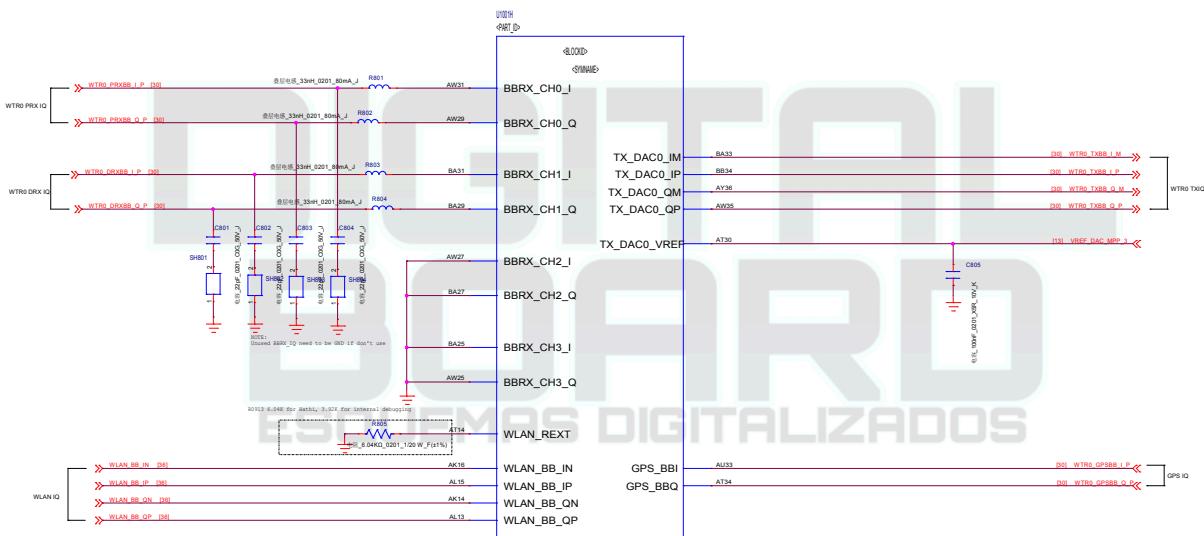
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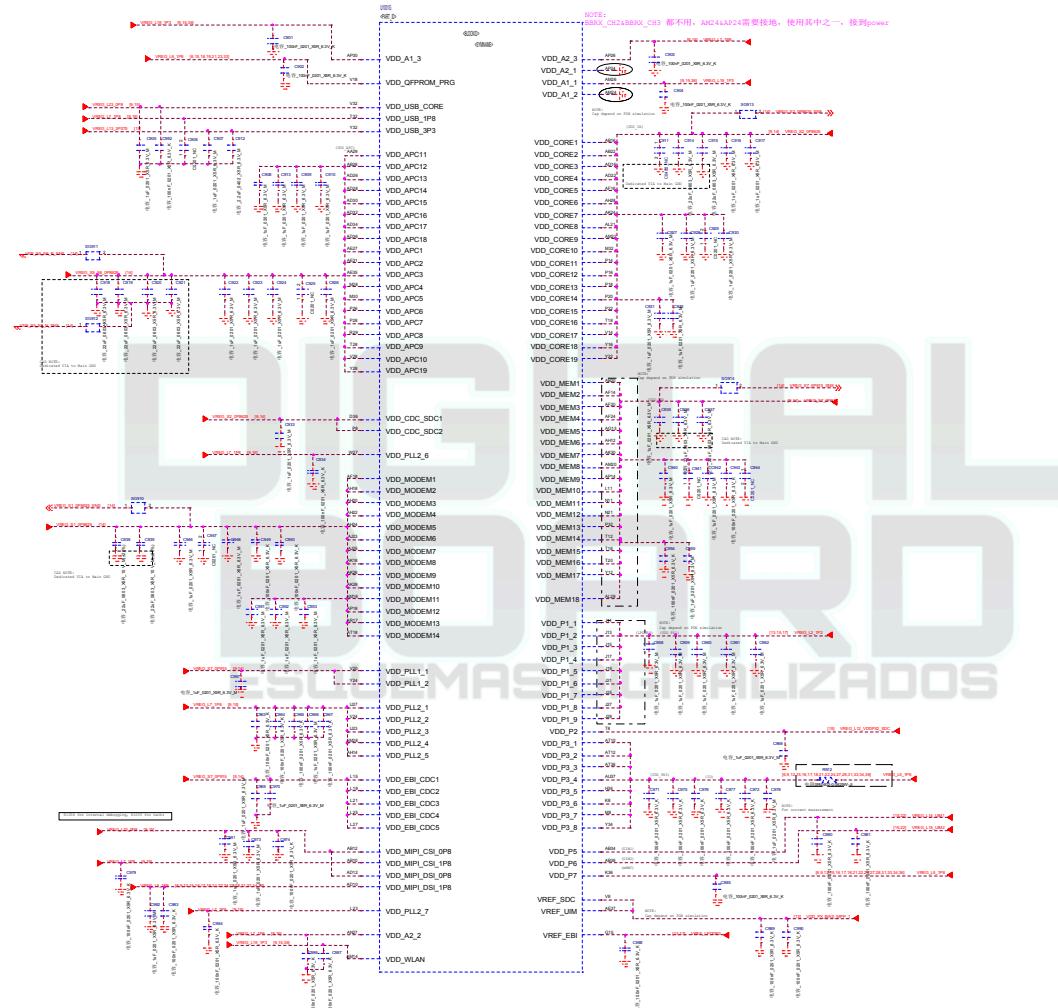
Title: Schematic_CCA_MARL_QDQSB_4-2_EVFLS
Date: LDDA-PF223
Page: Thursday, June 27, 2019
Sheet: 3 of 33



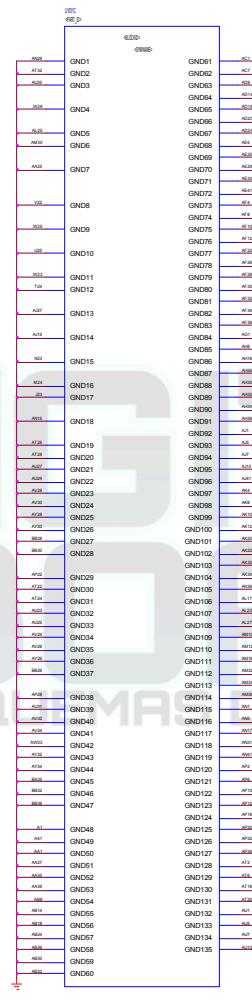
SDM439

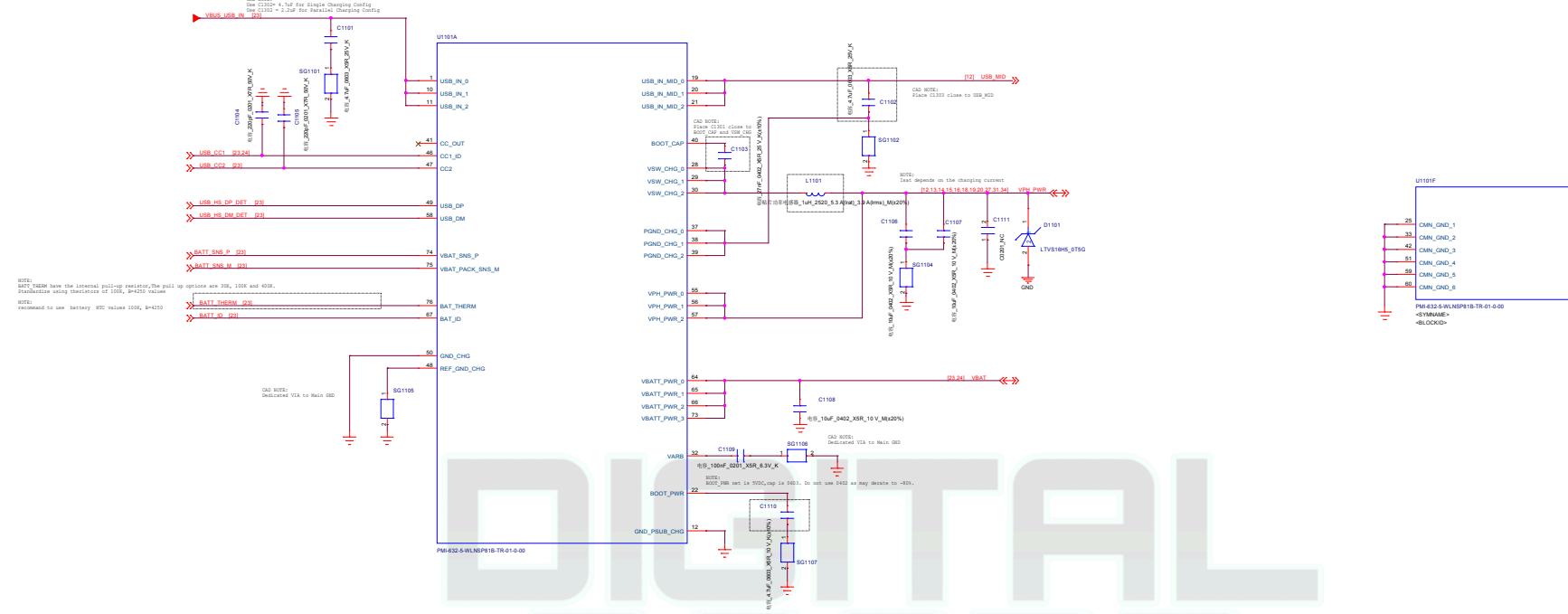


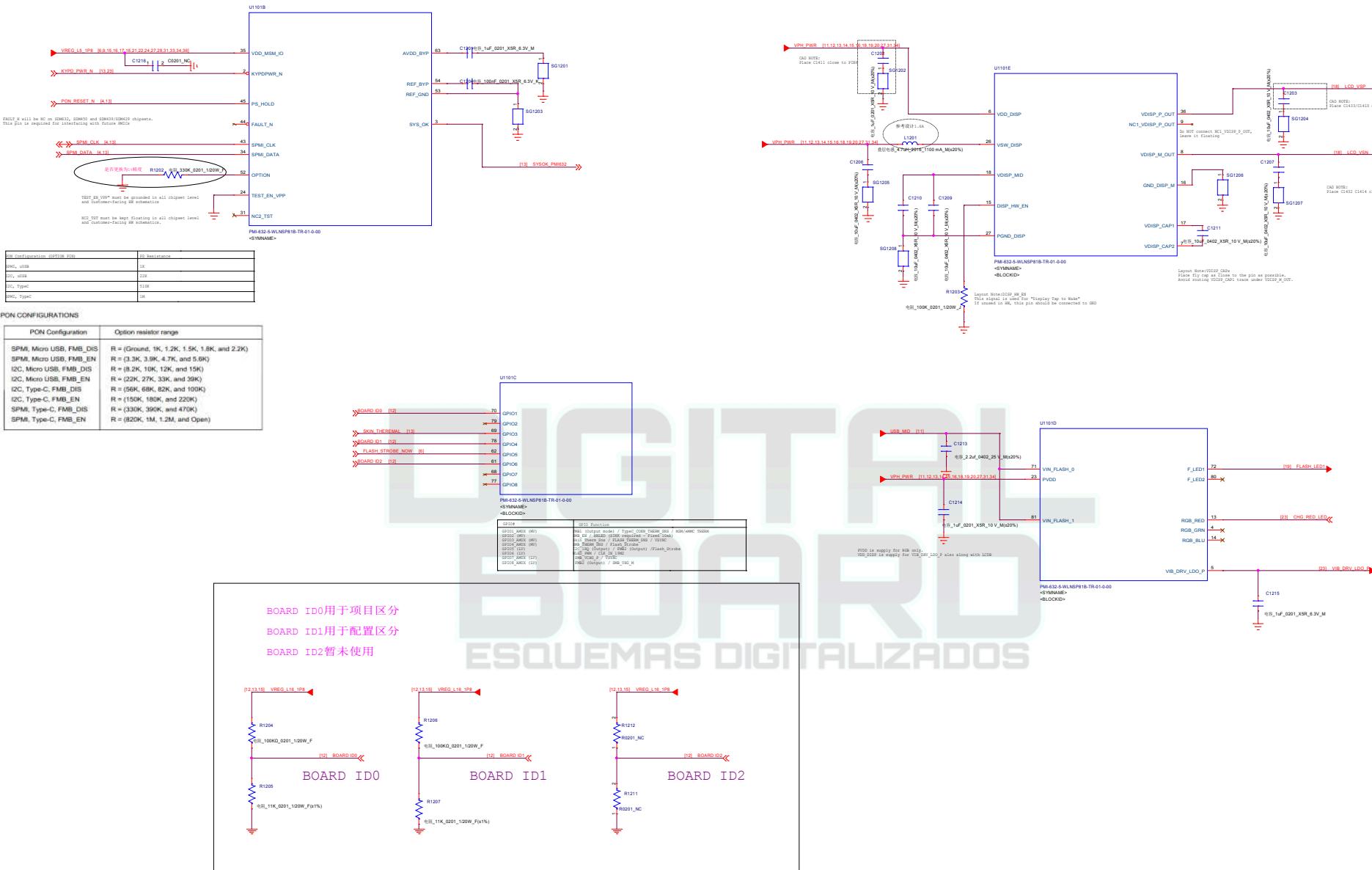


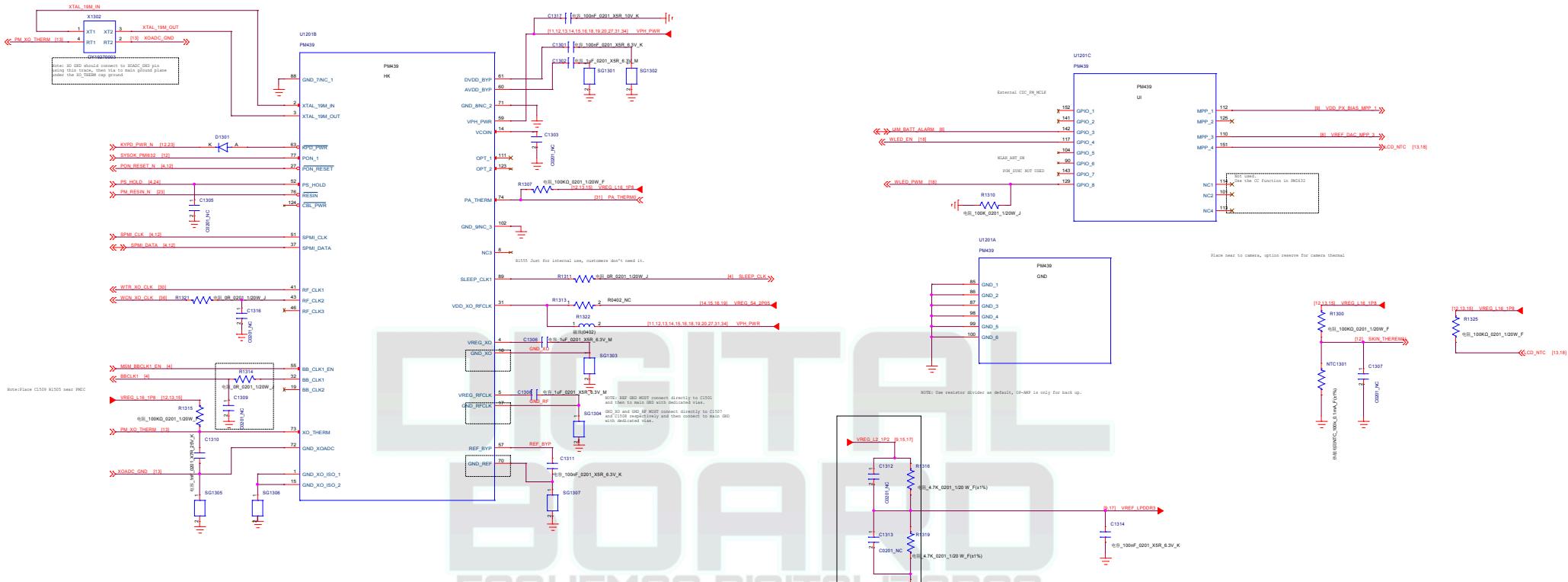


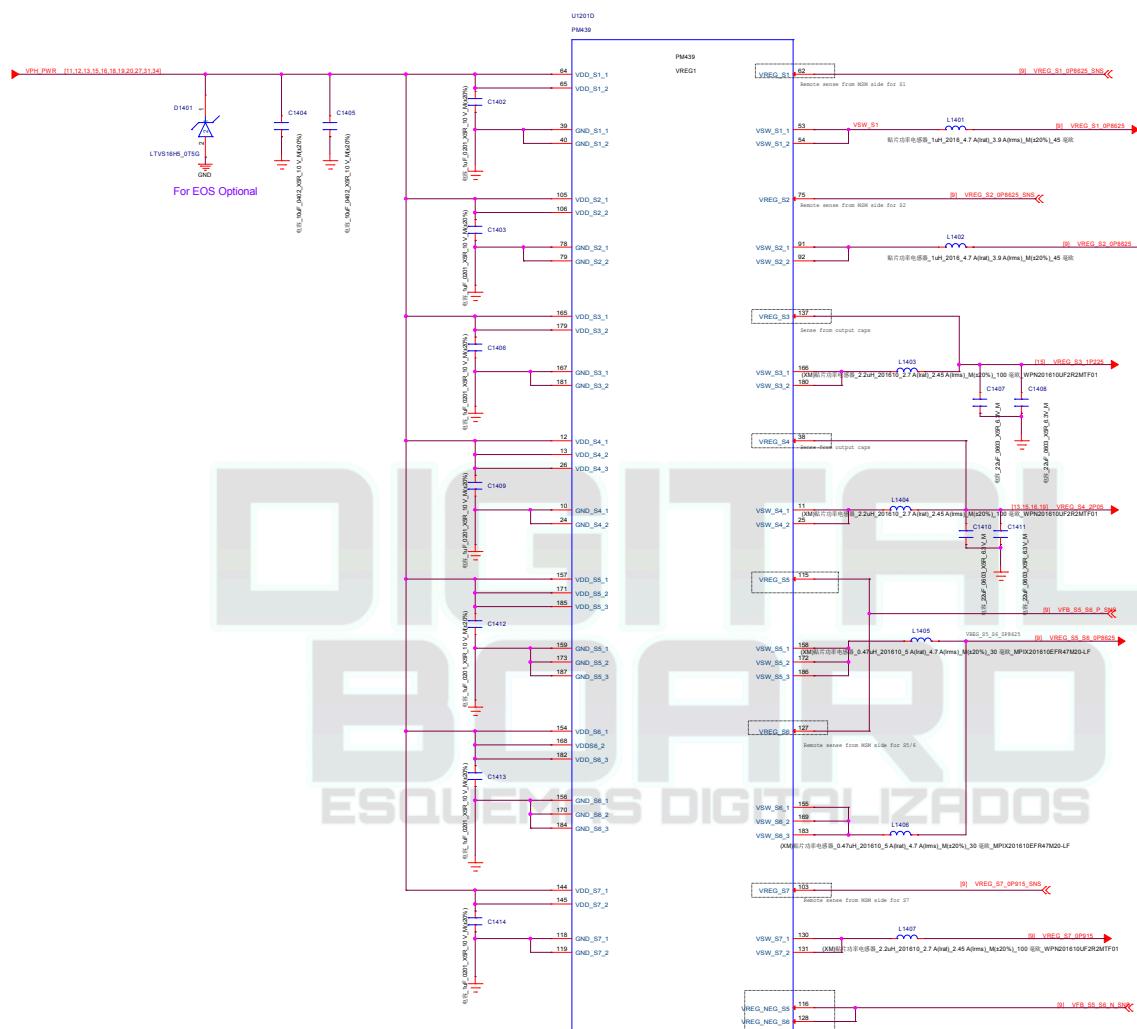
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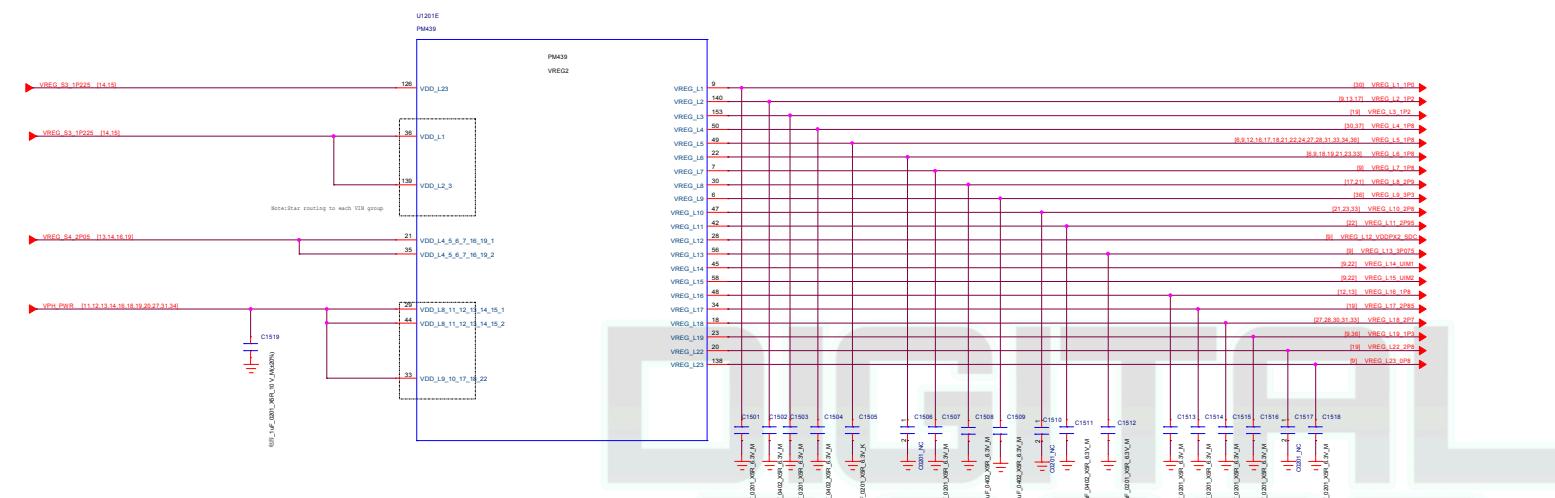


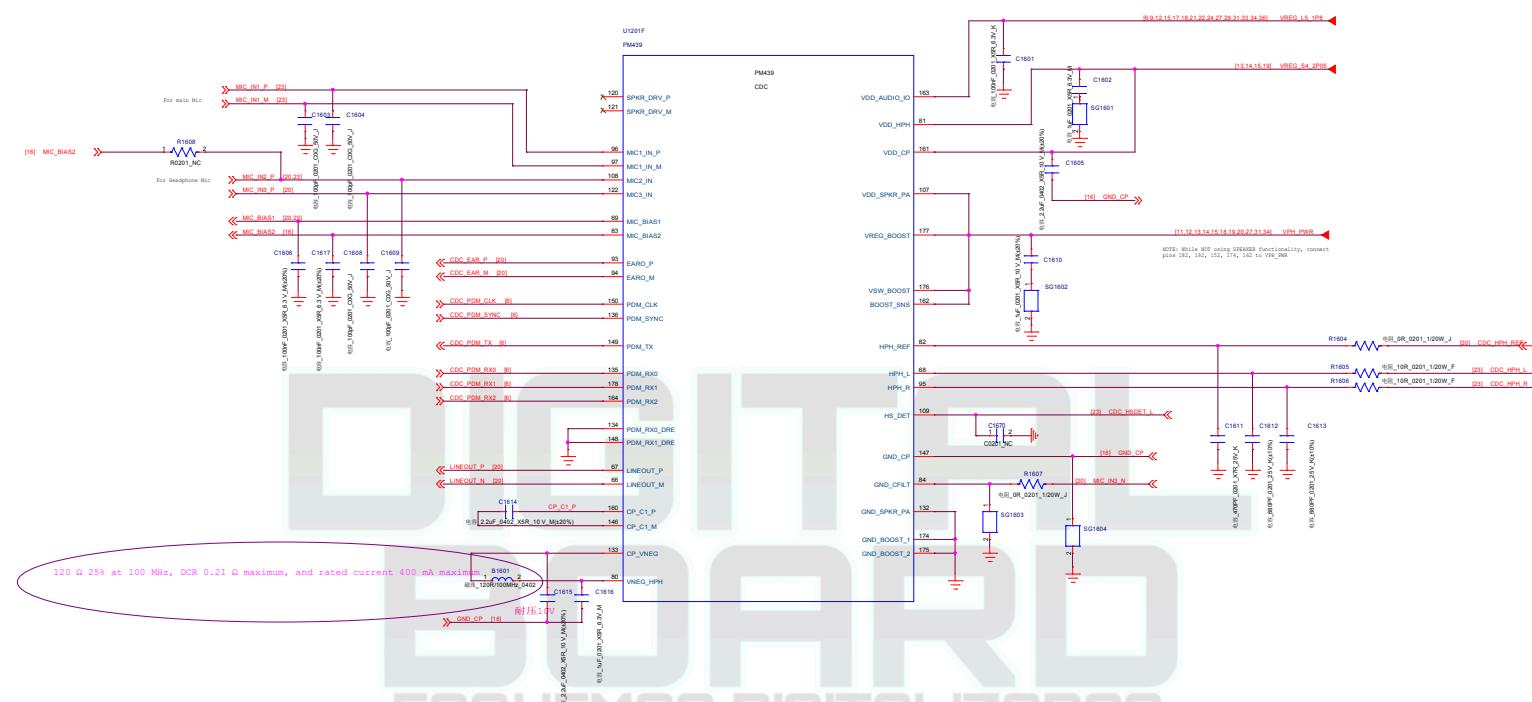


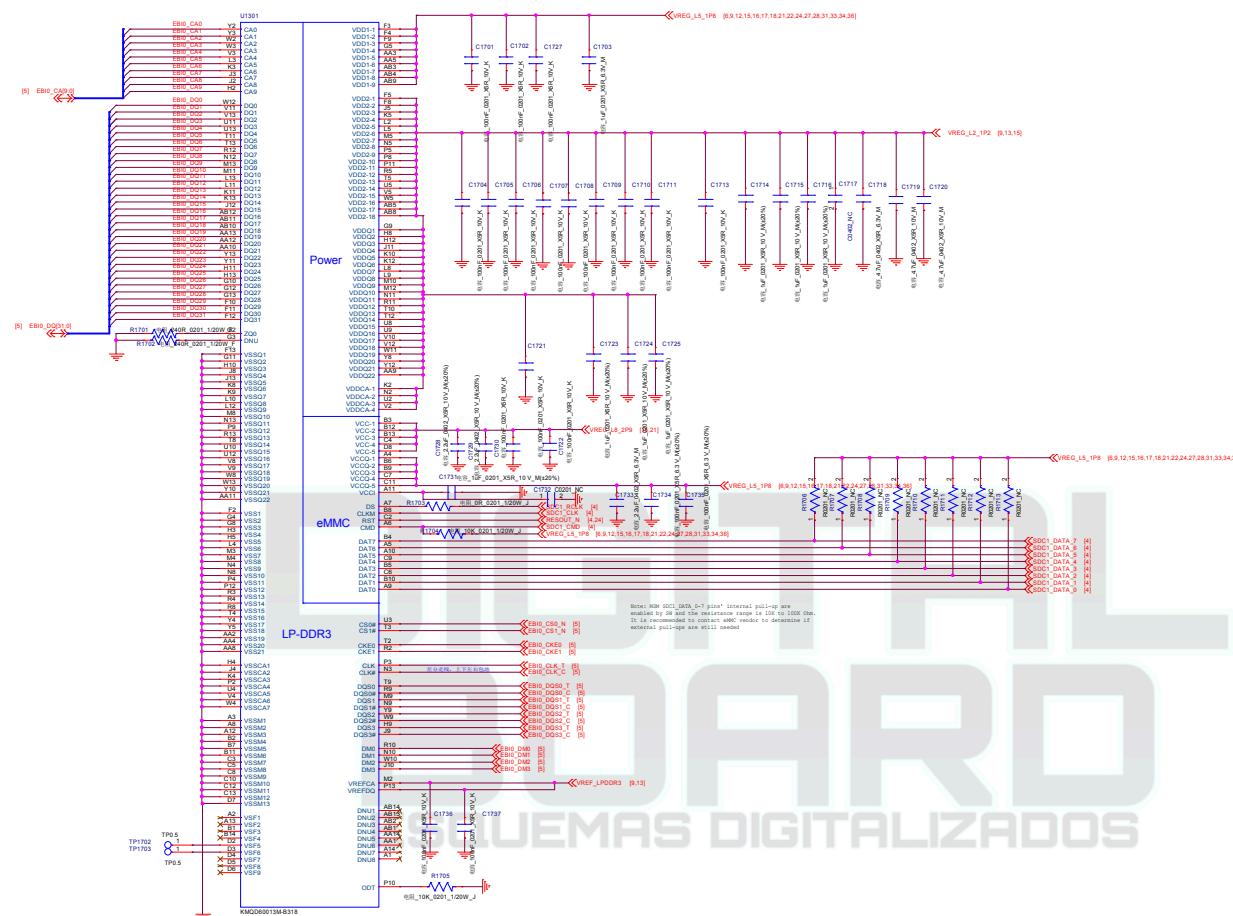


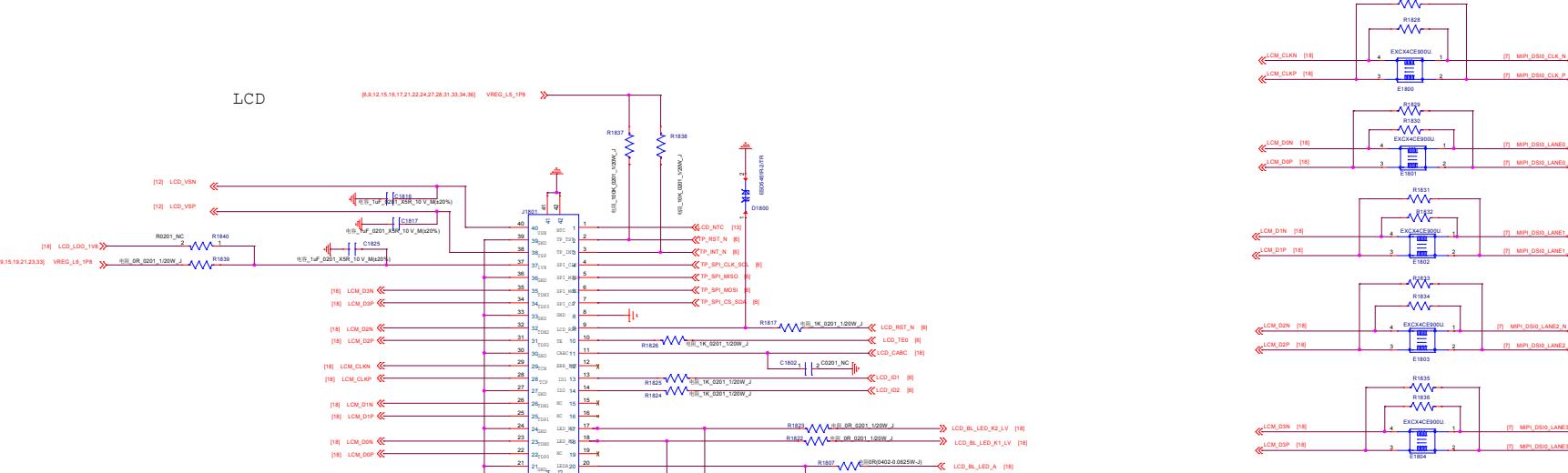






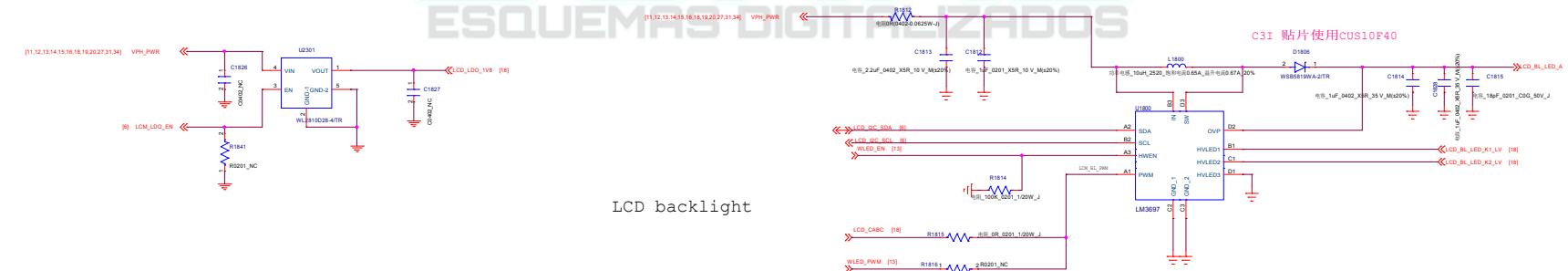


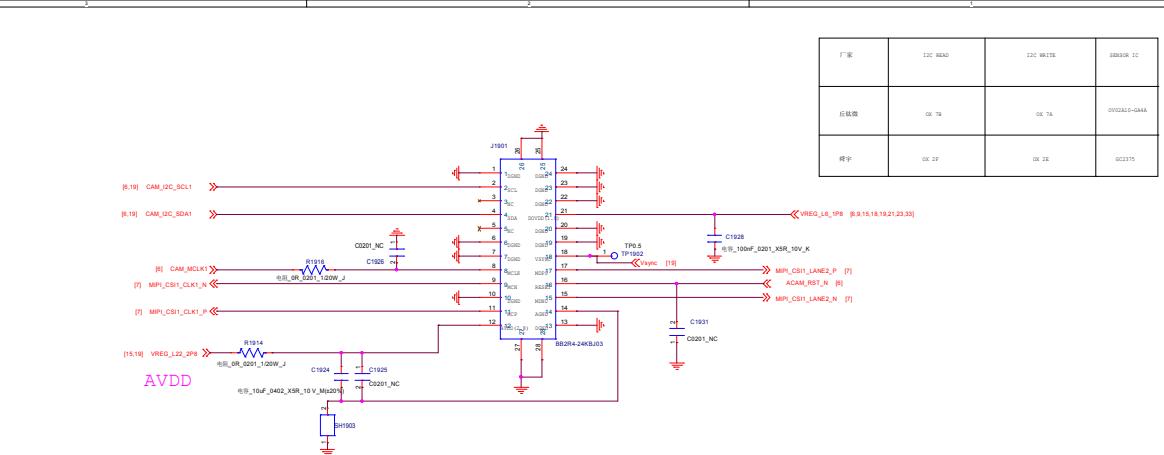
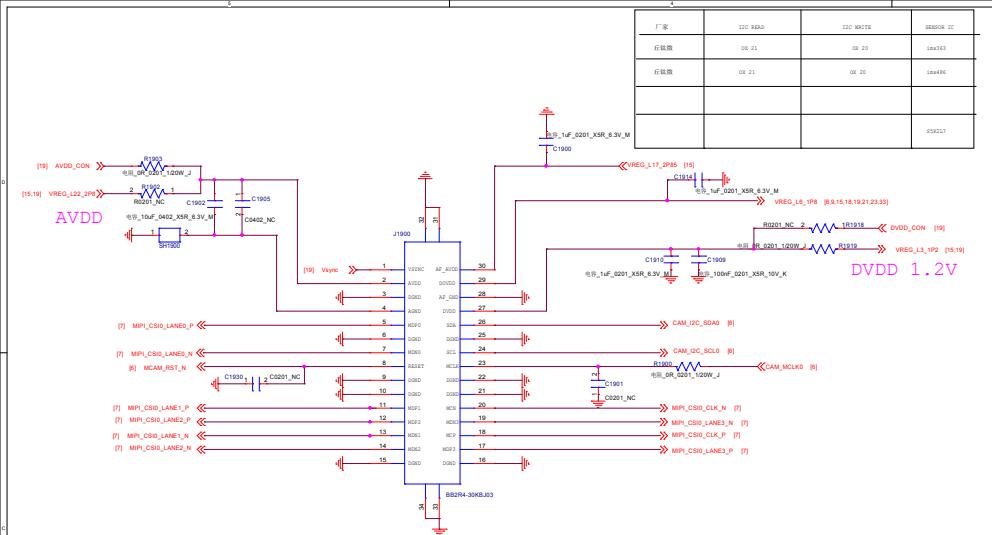




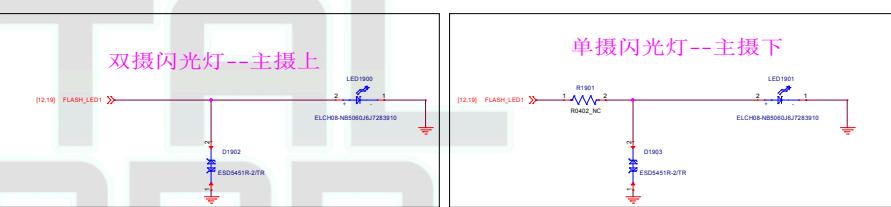
厂家	LOC ADDRESS	SERIAL NO
RTD	CH 34---01110110	RTD1136
TU	CH 34---01110110	LM6697

LCD-BACKLIGHT

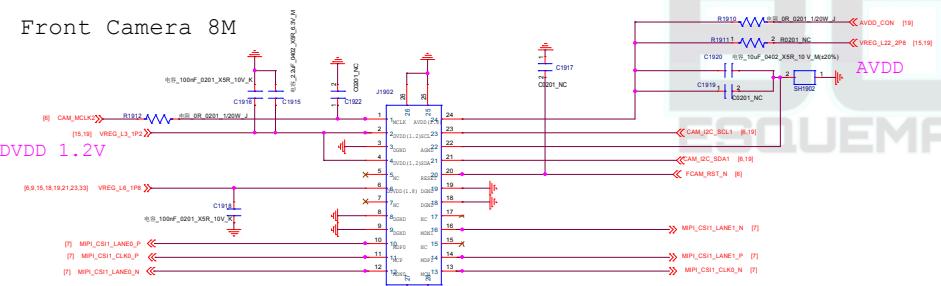




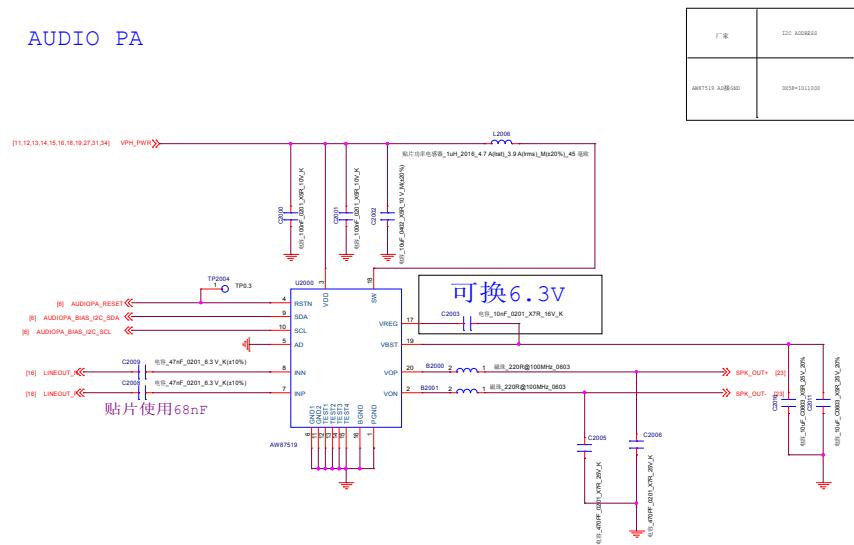
Rear Camera Flash FH15B



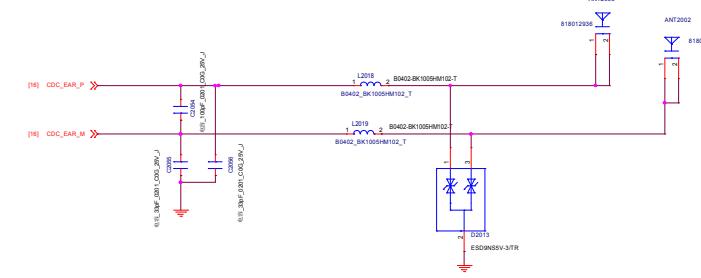
Front Camera 8M



AUDIO PA

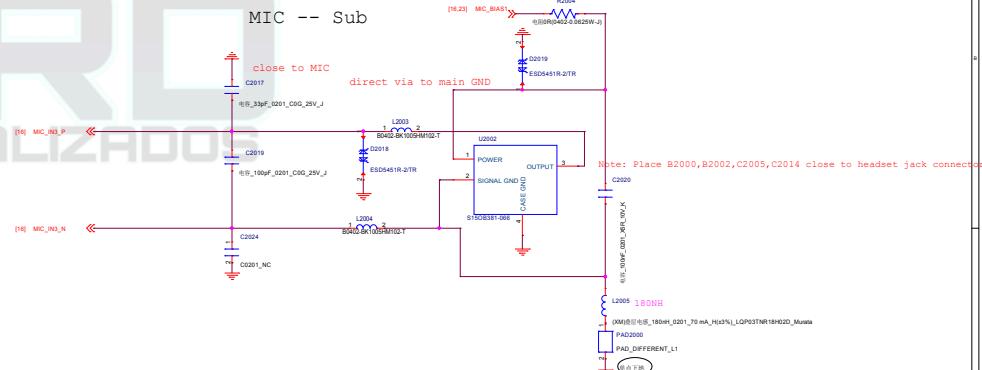
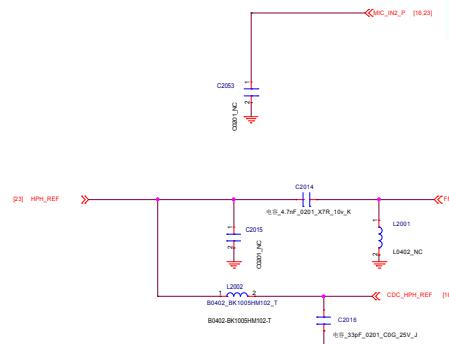


RECEIVER



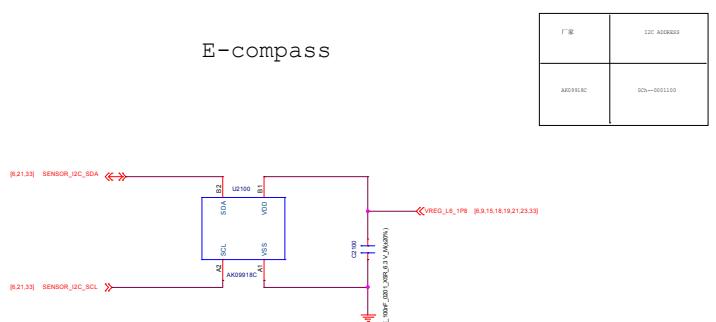
American Standard

EARPHONE

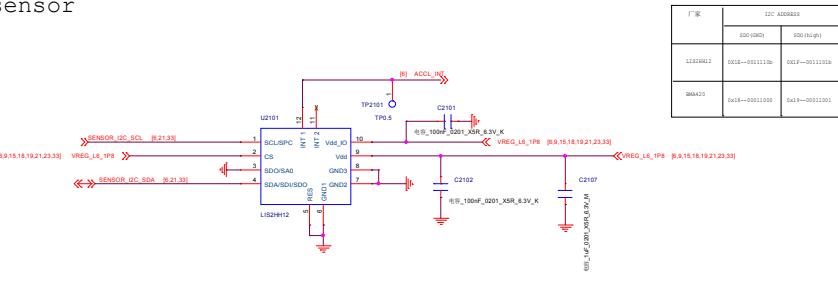


Note: Place B2000,B2002,C2005,C2014 close to headset jack connector

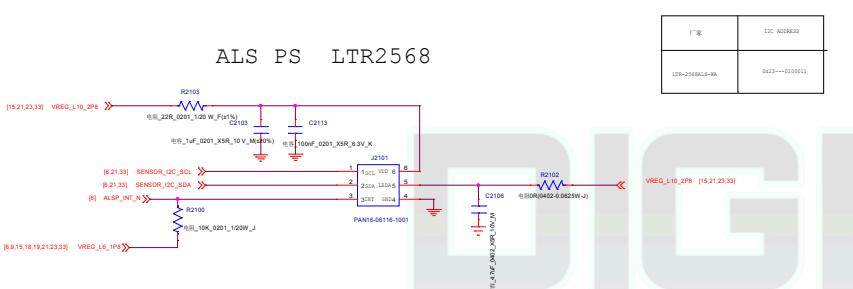
E-compass



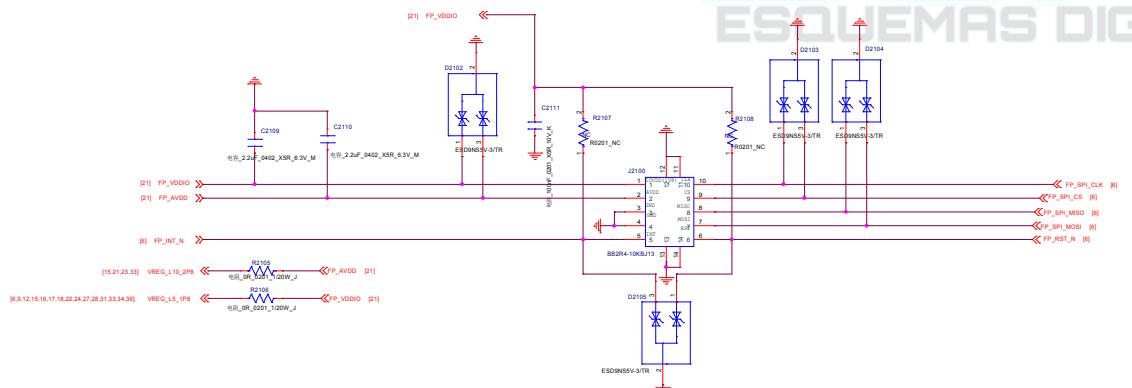
A sensor



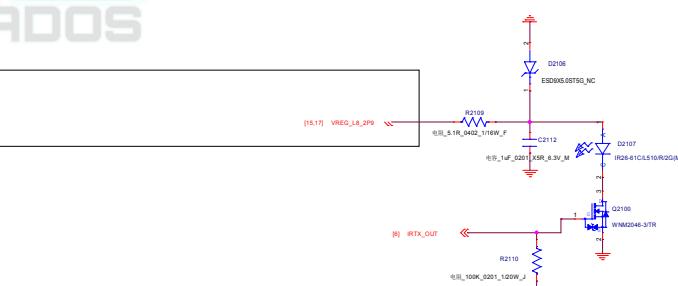
ALS PS LTR2568

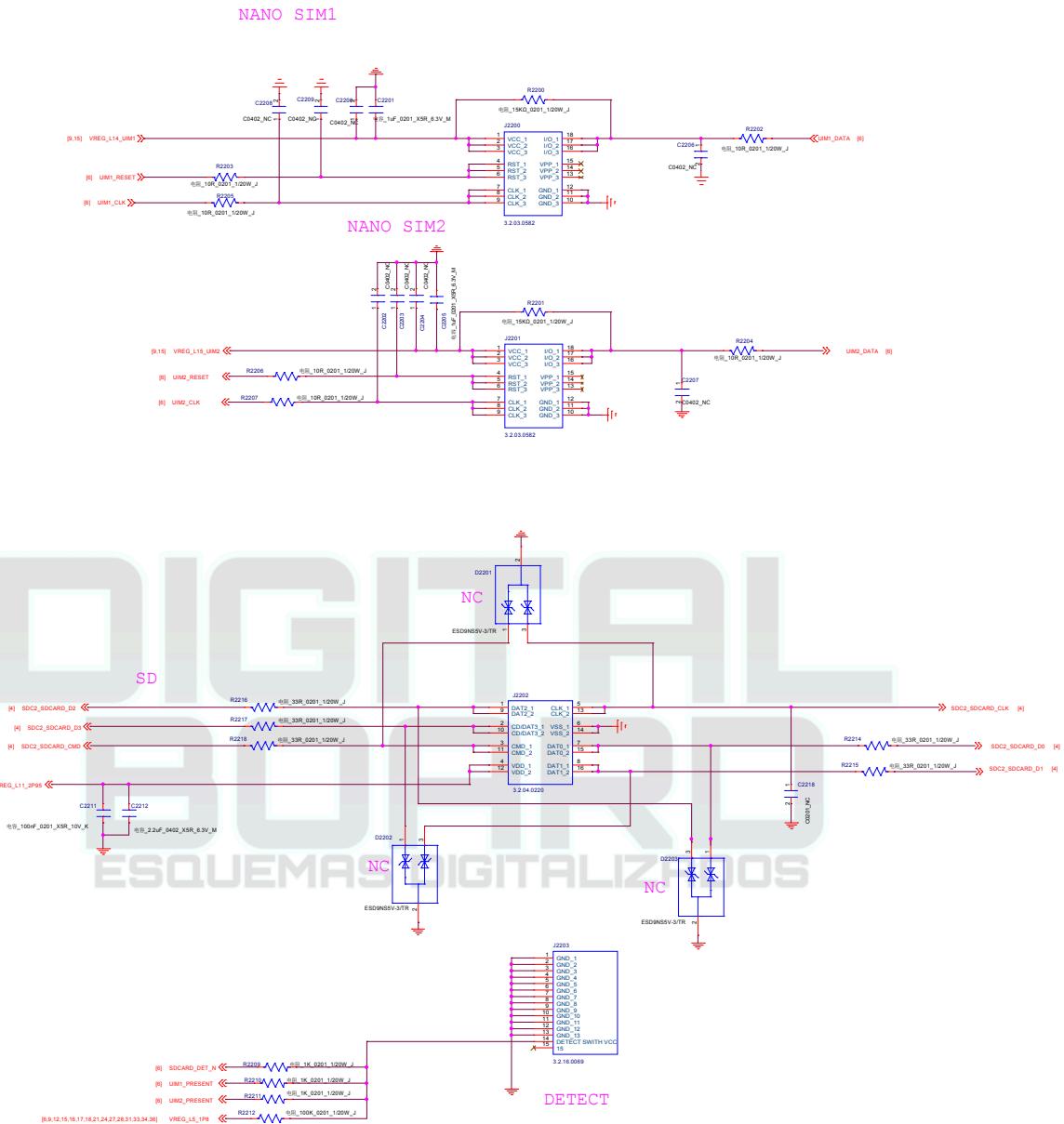


Finger Print

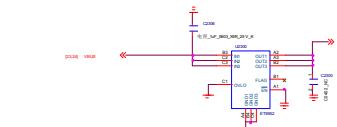


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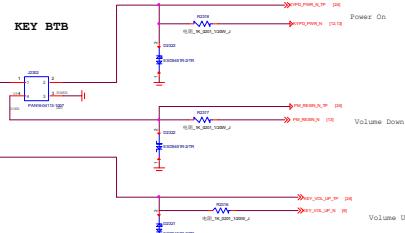




OVP 6.8V ET9552L
OVP 10.5V ET9552

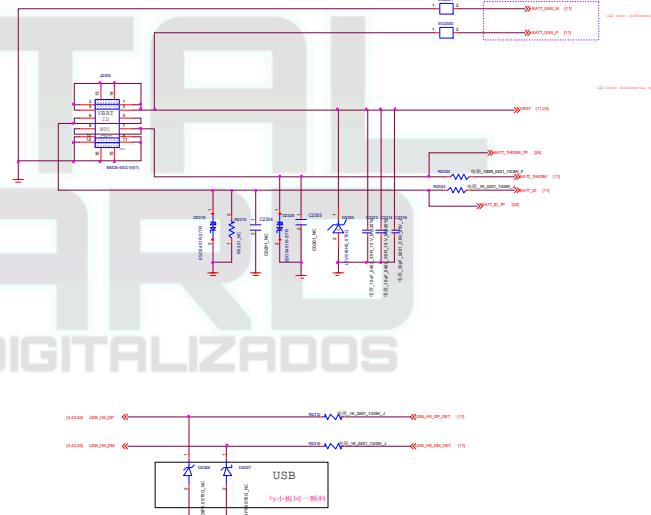


$V_{OV10} = V_{OV10_0} \cdot (1 + R1/R2)$
Note: $V_{OV10_0} = 1.2V$ (TYP.)

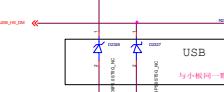
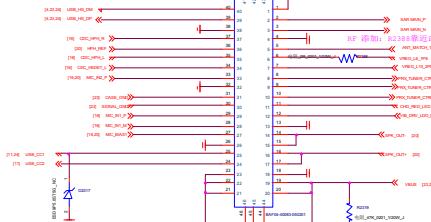


Signal	Description
KYD VOL UP_N	Volume Up
KYD VOL DN_N	Volume Down
KYD PWR_N	POWER ON
KEY_A1	XIAOMI AI

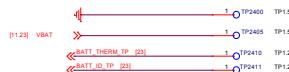
Battery Conector



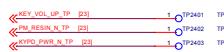
MAIN PFC BTB



VBAT TEST POINT



KEY TEST point



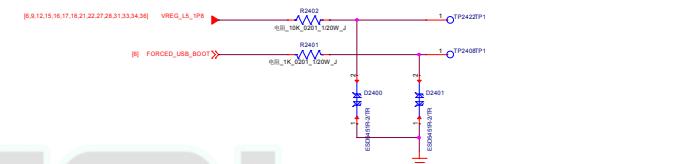
UART TEST point



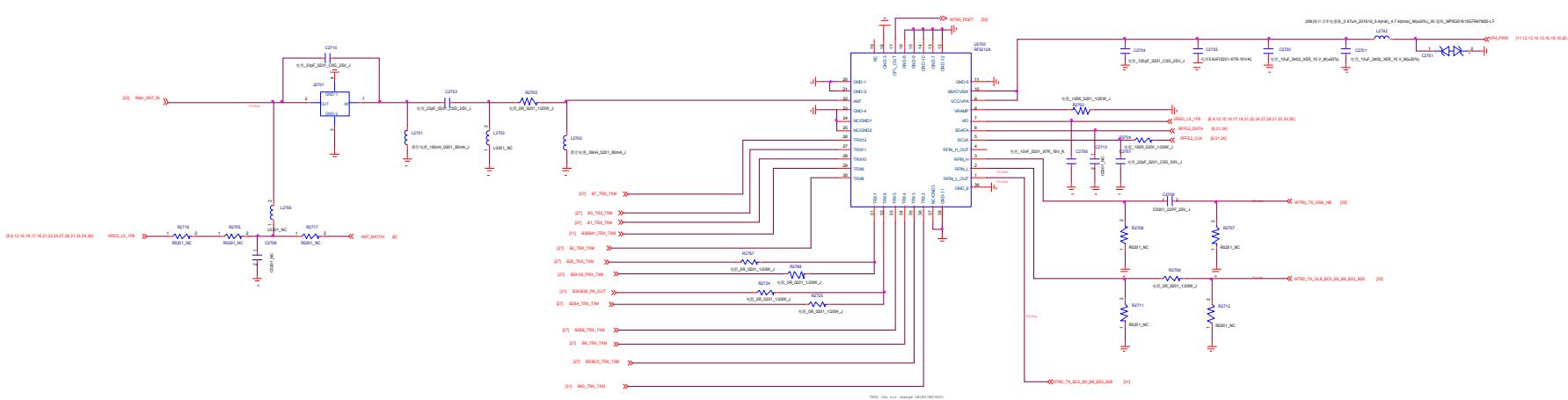
USB TEST POINT



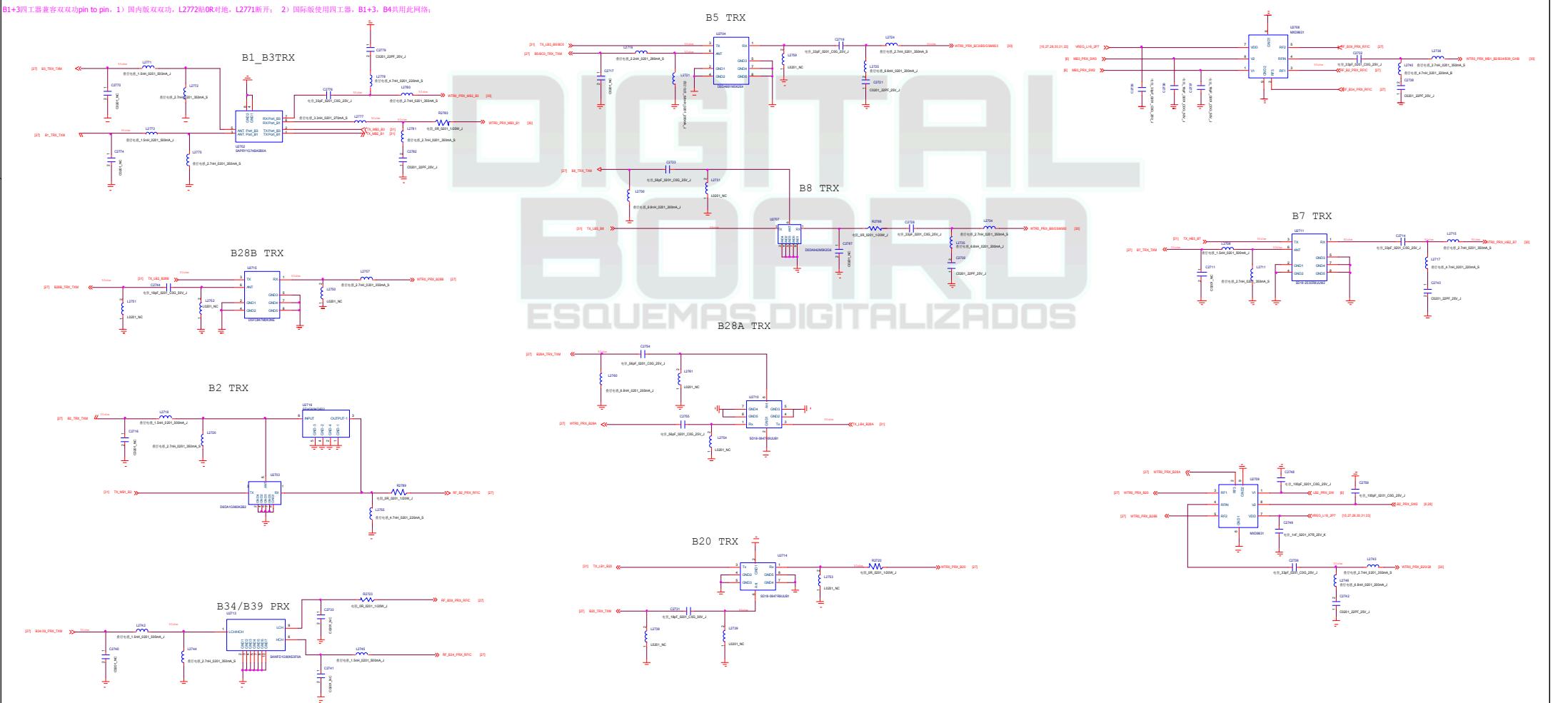
Force USB boot

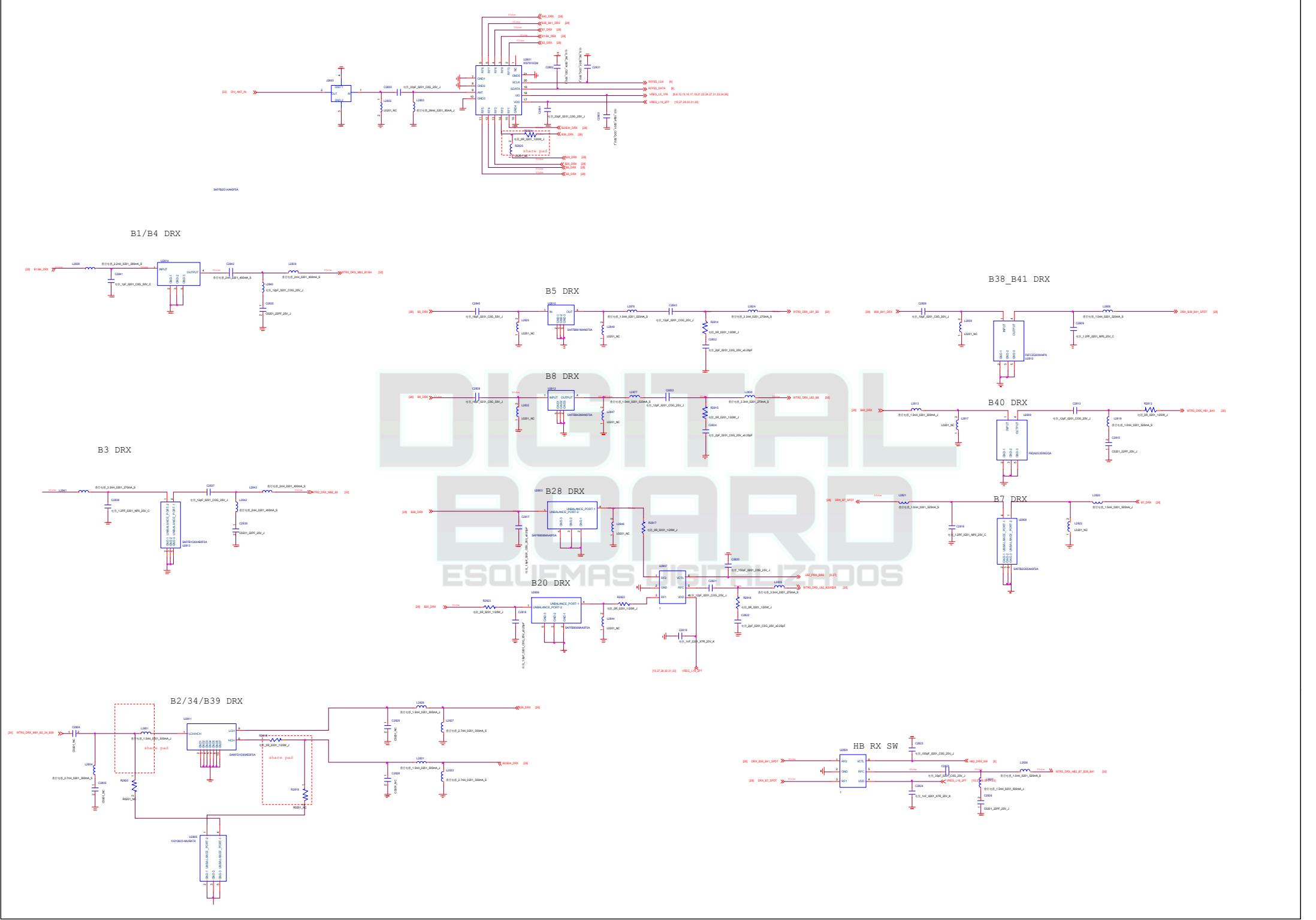


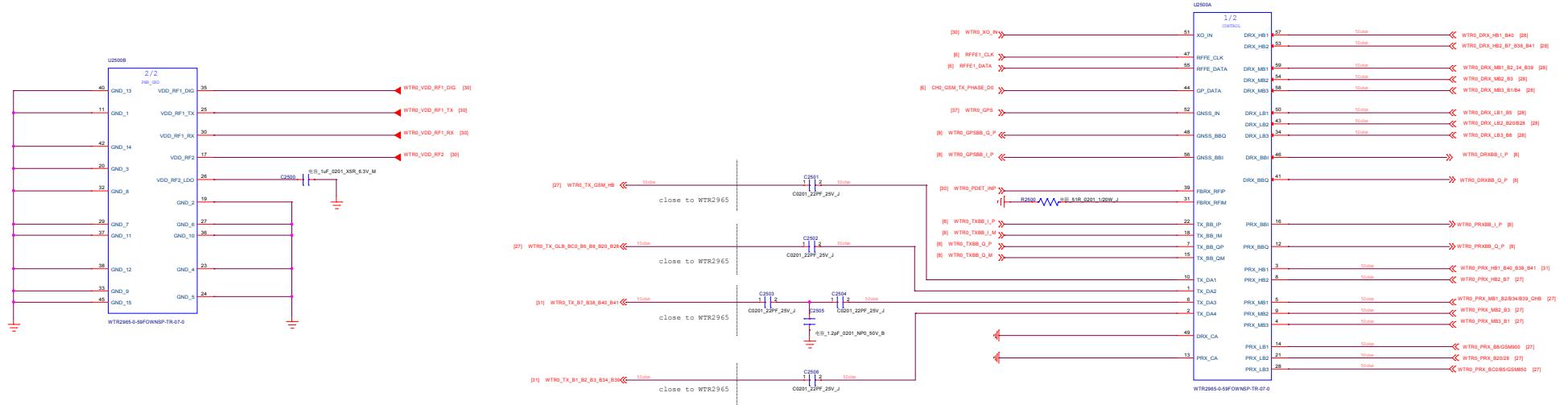
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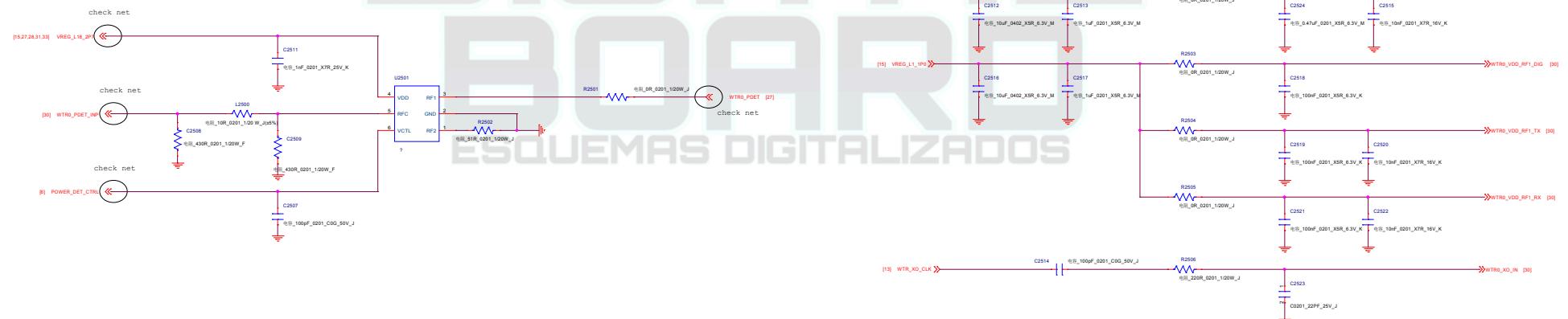
B1+3四工器兼容双双功pin to pin, 1) 国内版双双功, L2772贴OR对地, L2771断开; 2) 国际版使用四工器, B1+3, B4共用此网络;

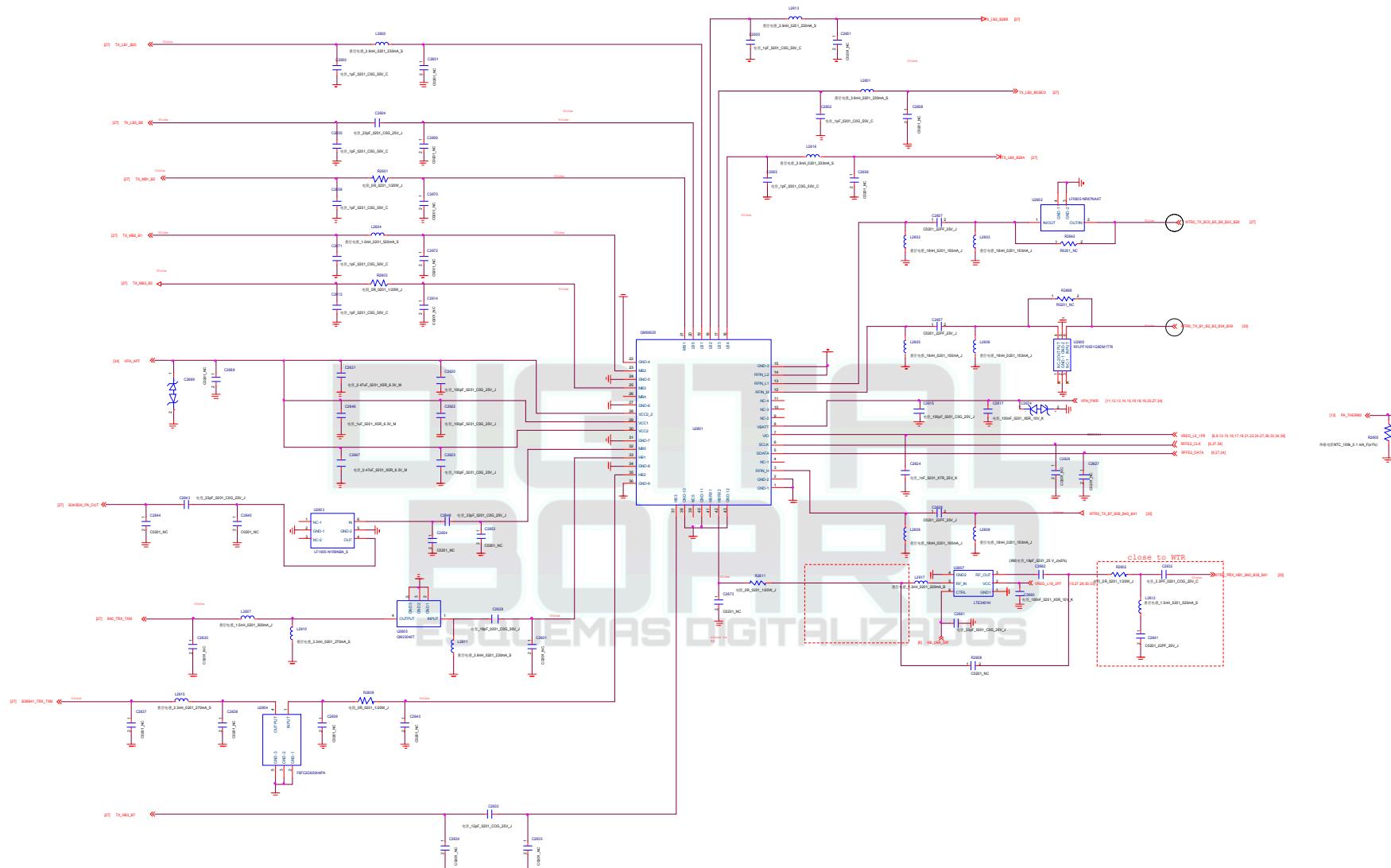




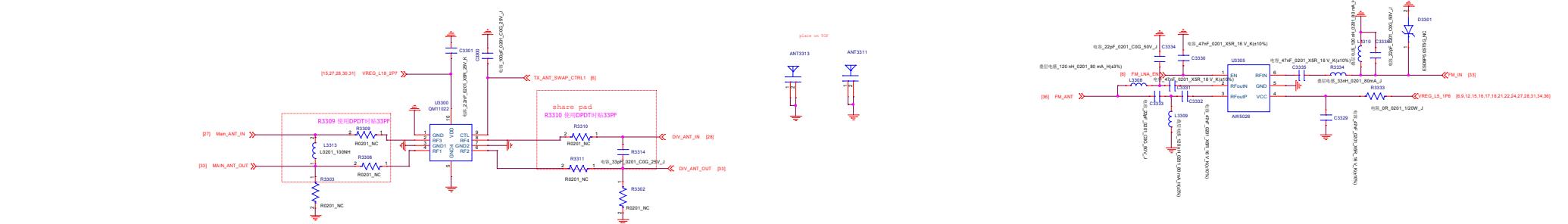


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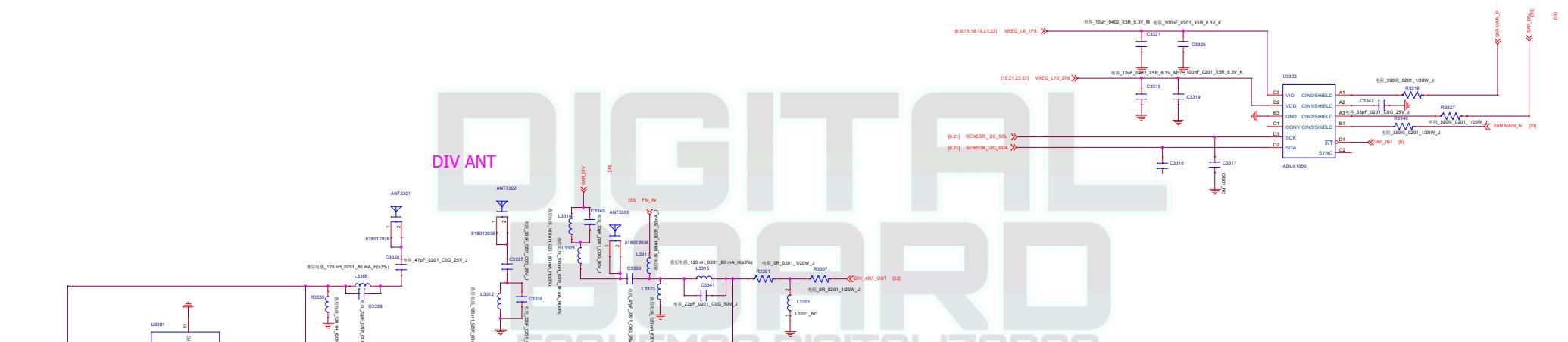




DPDT SWITCH



SAR Sensor

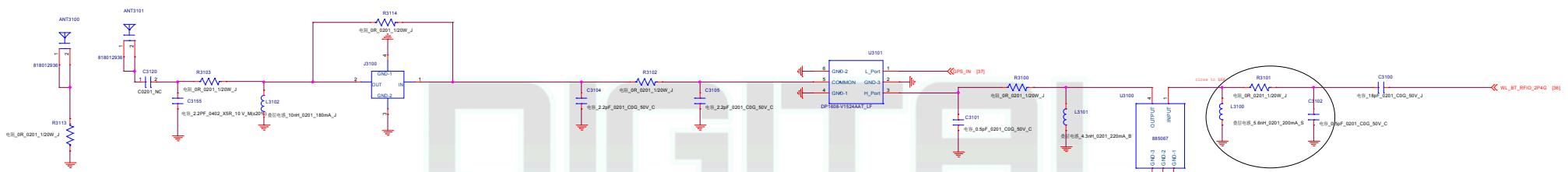


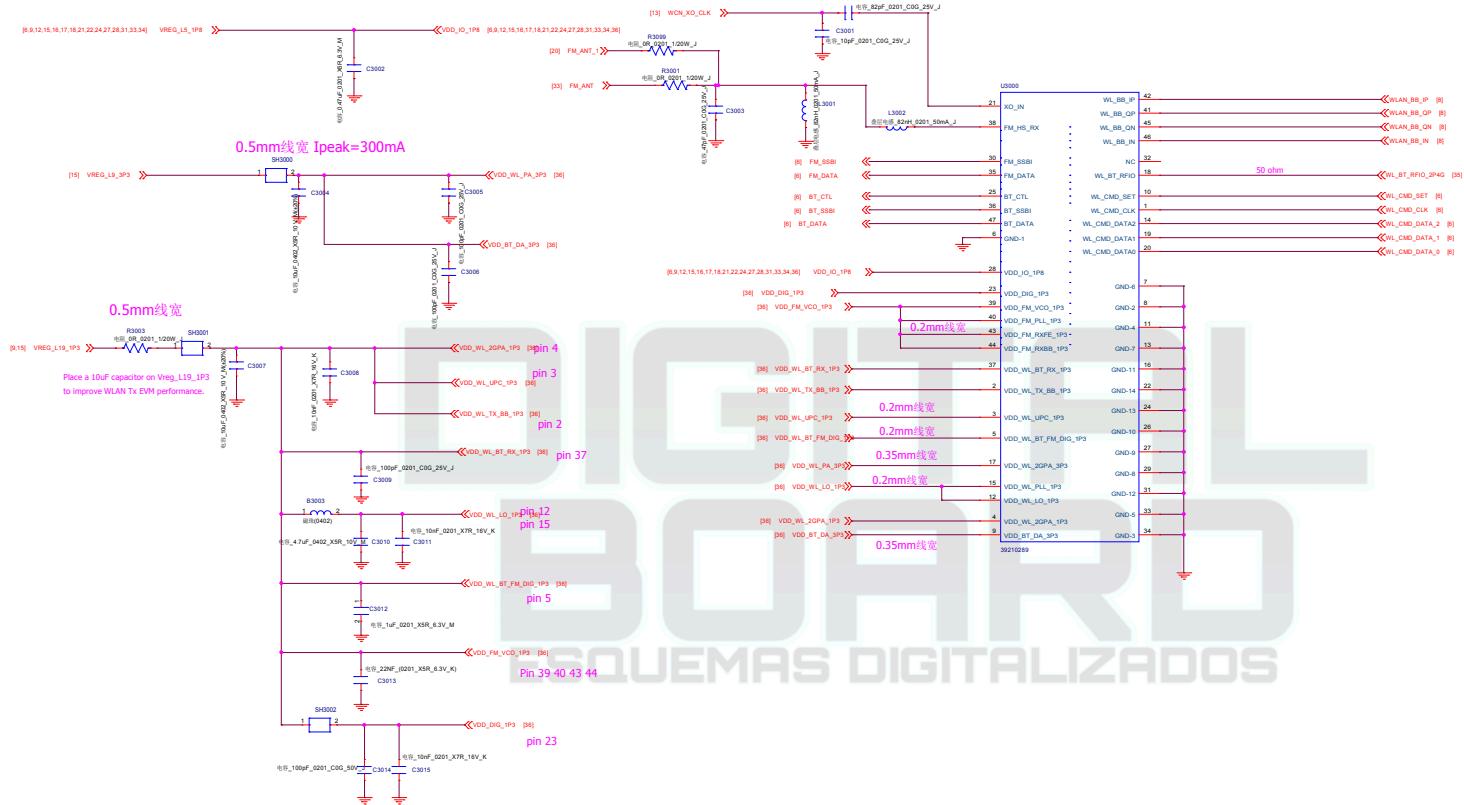


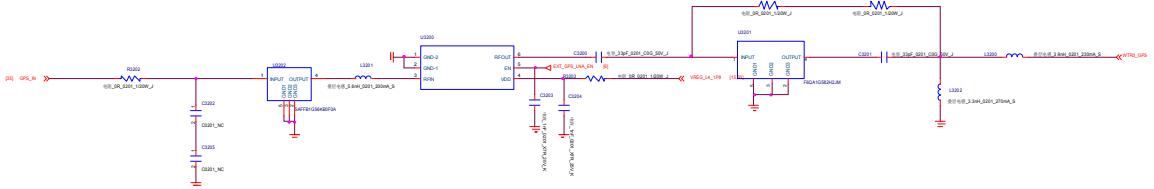
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Main ANT







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