

J1	#HUB75-PIN	#FPGA-PIN	#FPGA-PORT
1	D	Y2	PB9B
2	C	U1	PB6A
3	B	T1	PB4B
4	A	R1	PB4A
5	GND		
6	CLK	C18	PR2A
7	GND		
8	LAT	R17	PR35D
9	GND		
10	OE	M1	PL44B
11	E	W1	PB9A
12	R1	T18	PR44D
13	G1	R18	PR44B
14	B1	R17	PR35D
15	GND		
16	R2	N2	PL44A
17	G2	N3	PL41A
18	B2	T2	PB13A
19	GND		
20	R3	P17	PR32D
21	G3	M17	PR32B
22	B3	T17	PR47D
23	GND		
24	R4	M13	PL41B
25	G4	T3	PB18A
26	B4	R3	PB15B
27	S-CS		
28	S-CLK		
29	S-MOSI		
30	S-ISO		

J2			
1	D	Y2	PB9B
2	C	U1	PB6A
3	B	T1	PB4B
4	A	R1	PB4A
5	GND		
6	CLK	C18	PR2A
7	GND		
8	LAT	R17	PR35D
9	GND		
10	OE	M1	PL44B
11	E	W1	PB9A
12	R1	U18	PR47A
13	G1	U1	PR47B
14	B1	P18	PR38D
15	GND		
16	R2	N4	PL38C
17	G2	M4	PL38A
18	B2	L4	PL35C

19	GND		
20	R3	N17	PR35A
21	G3	N18	PR32C
22	B3	M18	PR29D
23	GND		
24	R4	L5	PL35D
25	G4	P16	PR35B
26	B4	J16	PR11D
27	S-CS		
28	S-CLK		
29	S-MOSI		
30	S-ISO		

J3			
1	D	Y2	PB9B
2	C	U1	PB6A
3	B	T1	PB4B
4	A	R1	PB4A
5	GND		
6	CLK	C18	PR2A
7	GND		
8	LAT	R17	PR35D
9	GND		
10	OE	M1	PL44B
11	E	W1	PB9A
12	R1	L20	PR28A
13	G1	L18	PR29C
14	B1	K20	PR23D
15	GND		
16	R2	J18	PR20C
17	G2	J17	PR11C
18	B2	H18	PR11A
19	GND		
20	R3	K19?	PR23B
21	G3	J20	PR23C
22	B3	J19?	PR23A
23	GND		
24	R4	H17	PR11B
25	G4	G18	PR8B
26	B4	H16	PR8D
27	S-CS		
28	S-CLK		
29	S-MOSI		
30	S-ISO		

J4			
1	D	Y2	PB9B
2	C	U1	PB6A
3	B	T1	PB4B
4	A	R1	PB4A

5	GND		
6	CLK	C18	PR2A
7	GND		
8	LAT	R17	PR35D
9	GND		
10	OE	M1	PL44B
11	E	W1	PB9A
12	R1	J18?	PR20B
13	G1	G20	PR17D
14	B1	G19?	PR20A
15	GND		
16	R2	F18	PR5D
17	G2	G16	PR8C
18	B2	E18	PR5C
19	GND		
20	R3	F20	PR17C
21	G3	E20	PR17A
22	B3	?	
23	GND		
24	R4	F17	PR8A
25	G4	F16	PR2D
26	B4	E16	PR2C
27	S-CS		
28	S-CLK		
29	S-MOSI		
30	S-ISO		