J1	#HUB75-PIN	#FPGA-PIN	#FPGA-PORT
1	D	Y2	PB9B
2	С	U1	PB6A
3	В	T1	PB4B
4	A	R1	PB4A
5	GND	KI	IDIN
6	CLK	C18	DDOA
		C18	PR2A
7	GND		
8	LAT	R17	PR35D
9	GND		
10	OE	M1	PL44B
11	Е	W1	PB9A
12	R1	T18	PR44D
13	G1	R18	PR44B
14	B1	R17	PR35D
15	GND		111002
16	R2	N2	PL44A
17	G2		PL41A
		N3	
18	B2	T2	PB13A
19	GND		
20	R3	P17	PR32D
21	G3	M17	PR32B
22	В3	T17	PR47D
23	GND		
24	R4	M13	PL41B
25	G4	T3	PB18A
26	B4	R3	PB15B
27	S-CS	No	TDTOD
28	S-CLK		
29	S-MOSI		
30	S-ISO		
J2			
1	D	Y2	PB9B
2	C	U1	PB6A
3	В	T1	PB4B
4	A	R1	PB4A
5	GND		
6	CLK	C18	PR2A
7	GND	010	1 11211
8	LAT	R17	PR35D
9		K17	LIVOOD
	GND	1/1	DI 44D
10	OE	M1	PL44B
11	E	W1	PB9A
12	R1	U18	PR47A
13	G1	U1	PR47B
14	B1	P18	PR38D
15	GND		
16	R2	N4	PL38C
17	G2	M4	PL38A
18	B2	L4	PL35C
10	<i>-</i>		1 1000

19 20 21 22 23 24 25 26 27 28 29 30	GND R3 G3 B3 GND R4 G4 B4 S-CS S-CLK S-MOSI S-ISO	N17 N18 M18 L5 P16 J16	PR35A PR32C PR29D PL35D PR35B PR11D
J3 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30	D C B A GND CLK GND CLK GND LAT GND OE E R1 G1 B1 GND R2 G2 B2 GND R3 G3 B3 GND R4 G4 B4 S-CS S-CLK S-MOSI S-ISO	Y2 U1 T1 R1 C18 R17 M1 W1 L20 L18 K20 J18 J17 H18 K19? J20 J19? H17 G18 H16	PB9B PB6A PB4B PB4A  PR2A  PR35D  PL44B PB9A PR29C PR23D  PR20C PR11C PR11A  PR23B PR23C PR23A  PR11B PR8B PR8D
J4 1 2 3 4	D C B A	Y2 U1 T1 R1	PB9B PB6A PB4B PB4A

5	GND		
6	CLK	C18	PR2A
7	GND		
8	LAT	R17	PR35D
9	GND		
10	OE	M1	PL44B
11	E	W1	PB9A
12	R1	J18?	PR20B
13	G1	G20	PR17D
14	B1	G19?	PR20A
15	GND		
16	R2	F18	PR5D
17	G2	G16	PR8C
18	B2	E18	PR5C
19	GND		
20	R3	F20	PR17C
21	G3	E20	PR17A
22	В3	?	
23	GND		
24	R4	F17	PR8A
25	G4	F16	PR2D
26	B4	E16	PR2C
27	S-CS		
28	S-CLK		
29	S-MOSI		
30	S-ISO		