Laboratory 6

Introduction to the Use of PSPICE

Objectives

- Learn what PSPICE is and its basic functions.
- Learn how to use PSPICE to construct a simple circuit.
- Learn how to use PSPICE to simulate and analyze the circuit.

Equipment and components

- A computer
- PSPICE software

Preliminary

PSPICE is a PC version of SPICE which runs on personal computers and offers many improvements over SPICE. SPICE is an acronym for Simulation Program with Integrated Circuit Emphasis. PSPICE is widely used in industries and universities. Though developed for IC design, it is a useful tool for both students and circuit designers to simulate and analyze non-IC circuits.

The version of PSPICE for use in the School of Engineering lab (SRE 303) is a PSPICE Student V.9.1.

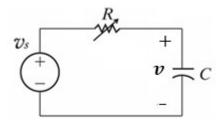
You can download the updated free trial version 17.4 from https://www.orcad.com/resources/orcad-downloads or the updated student 17.4 from https://www.orcad.com/orcad-academic-program.

Or you can download the PSpice from https://ucmerced.app.box.com/v/orcadpspicefree

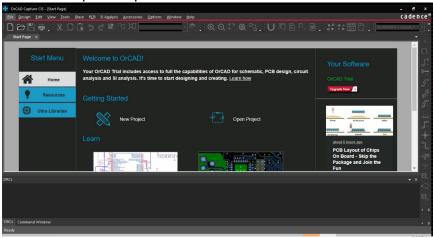
You can also download the newest version of MacSpice from https://www.macspice.com/Download.html for mac computers. You can refer to the link http://www.macspice.com/ for how to use MacSpice.

Procedure Version 17.4

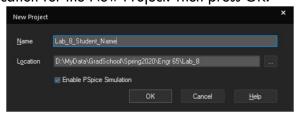
 The following circuit will be built and simulated in PSPICE. You can observe steady-state responses of a RC circuit. The input (source) is a sinusoidal input voltage source and the output(response) is the voltage across the capacitor.



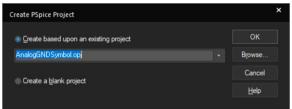
2. Go to the All Program. Under the "OrCad Trial 17.4", click the "Capture CIS 17.4" and the window "OrCAD Capture" opens.



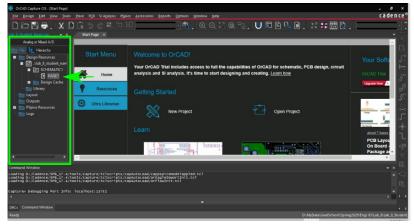
- 3. Click the "Create Document" icon on the tool bar and create a name for your design on the desktop of your computer.
 - a. Make sure to check "Enable PSpice Simulation" dialog box as shown below.
 - b. Select a location for the New Project. Then press OK.



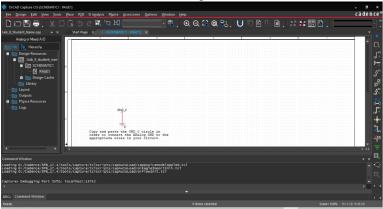
c. Select "Create based upon existing project" and choose "AnalogGNDSymbol.opj". Then press OK.



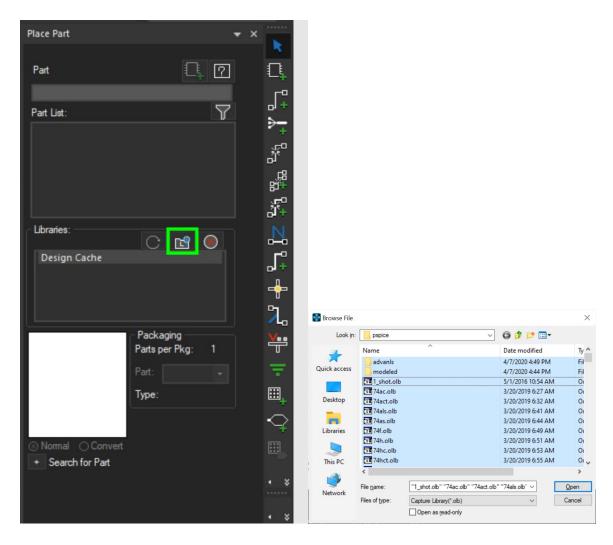
d. Next, locate the left menu called "Analog or Mixed A/D". Expand "Design Resources" > ".\lab_6_student_name" > "Schematic1" and double click on "PAGE1".



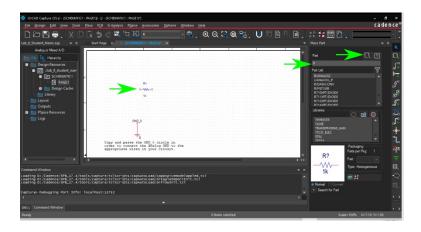
e. You screen should look like the image below.



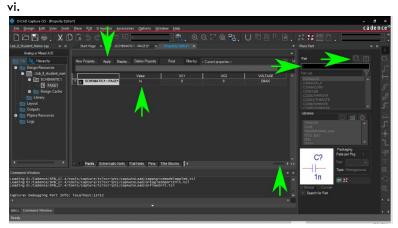
- 4. On the right of the window, locate the tool bar menu.
 - a. Find following symbol that is highlighted in green (see image to the right). This is the add "part" tool. When clicked, a new menu will appear that allows you to search for circuit elements. You can use the shortcut key by pressing "P".
 - b. Find the symbol highlighted in red (see image to the right). This is the add "wire" tool. This tool allows you to connect the elements of your circuit. You can also use the shortcut key "W".
 - c. Now click on the add "part" symbol or press "P" to bring up the "Place Part" menu.
- 5. Below is the "Place Parts" Menu. If you do not have any libraries in the "Libraries", Click "Add Library" and under "Pspice" directory, select all and then click "Open". Now you are ready to build a circuit.



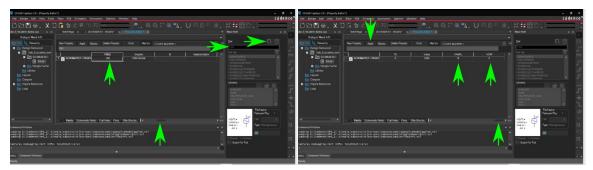
- 6. Build the following the circuit by selecting different parts from the window.
 - a. In the search field, type "R/Analog".
 - i. Then click on "R/Analog" in the Parts List.
 - ii. Next click the "Place Part" symbol that is right above the search field or use the shortcut key "Enter". This changed the mouse pointer to the part you choose.
 - iii. On the schematic page, left click where you would like to place the part.
 - iv. Press "Esc" to stop adding parts. Note: If you add to many parts, after pressing the "Esc" key, you can left click on the part then push "backspace" to delete the extra part.
 - v. You can change the value of the part by double clicking on the part to enter the Property Editor. For a resistor, you must scroll all the way to the right of the description. Ensure the resistor's value is $1 \, \mathrm{K} \, \Omega$.
 - vi. You can rotate the part by right clicking on the element and selecting rotate.



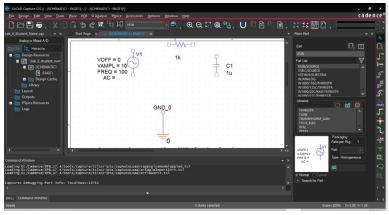
- b. Repeat the same procedure but search for "C".
 - i. Type "C" in the "Part" search field.
 - ii. Press "Enter' to place a capacitor on the schematic.
 - iii. Press "Esc" to stop adding new parts.
 - iv. Double click with the left mouse button to bring up the Property Editor Menu.
 - v. Scroll to the right and find the "value" field and set the capacitance of the capacitor as 1 μF by typing in "1u". Then click on apply. Close the Property Editor.



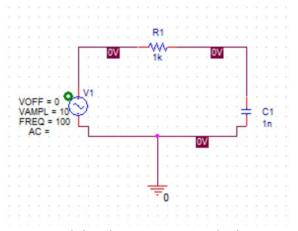
- c. Repeat the same procedure but search for "Vsin"
 - i. Select "Vsin" from the parts menu and press "Enter" to and the part.
 - ii. Press "Esc" to stop placing new parts.
 - iii. Double click with the left mouse button on the Source to open the Property Editor.
 - iv. Scroll to the right to find the value fields and set them to "FREQ=100", "VAMPL=10" and "VOFF=0". Note: These fields are not all next to each other. You will have to scroll all the way to the right to find the last two value fields. See images below.



d. If your schematic is missing the "Ground" icon, repeat the same procedure to search for "0" and select the item from the parts list. At this point your schematic show look like the image below.

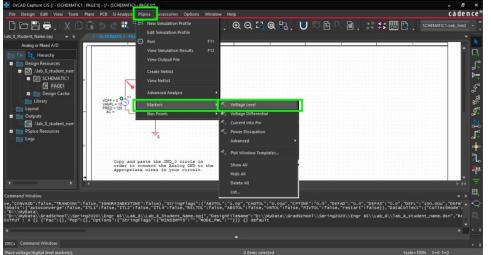


- e. Locate the add wire symbol on the right menu bar or press "W". This enables you to add a connecting wire to all you join all your elements together.
 - i. Note: Most problems stem from the ground not being connected to the circuit wire. It is easier to delete the "GND_0" text, wire from the ground and the "o" symbol. Then connect a wire from the square in the ground to the circuit until you see it connect properly. See the pink dot.

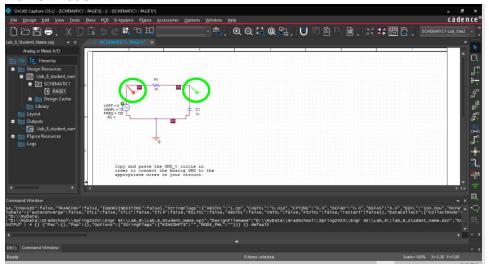


7. Please note v_s is a sinusoidal voltage source with the expression of $v_s = V_{sm} \sin \omega t = 10 sin 200 \pi t$, v is the output across the capacitor and $v = V_m \sin(\omega t)$

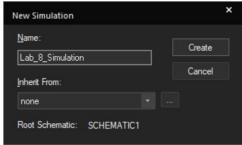
- φ). Note: V_m and V_{sm} are the peak value of the output voltage and voltage source, respectively, and φ is called the phase angle. Here, φ also represents the phase angle difference between two voltages: v_s and v.
- 8. To add a Voltage Measurement, click on the "PSpice" in the top menu.
 - a. Next, find "Markers" then select the first option "Voltage Level"



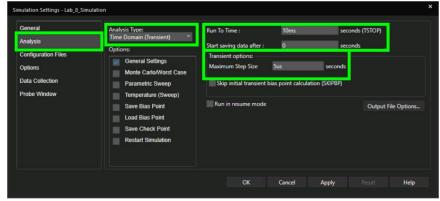
b. With the mouse pointer, click before and after the resistor. Press "Esc" to exit voltage marker mode.



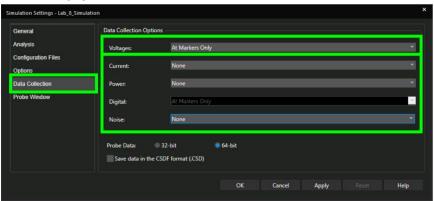
- 9. In the PSpice menu select new simulation
 - a. Name your simulation. Then click create.



- b. You will see the Simulation Settings window.
 - i. In the Analysis tab, change Analysis Type to "Time Domain (Transient)".
 - ii. Change "Run to Time" to "10ms".
 - iii. Leave "Start saving data after" value as zero.
 - iv. In the "Transient Options" section, change "Maximum Step Size" to "5us"

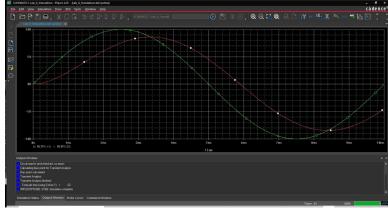


- v. Next click on the "Data Collection" Tab.
- vi. Change Voltages to "At Markers Only". Change Current, Power and Noise to "None"



vii. Next click Apply then OK.

10. In the PSpice menu, find and click on run. This will open the simulation window. As seen below.



11. Fill up the tables below for the first cycle and third cycle (increase the simulation time if needed) of v_m , $v_{sm} \varphi$ and plot V_m and φ in terms of R. For a given R, are v_m , $v_{sm} \varphi$ same in the different cycles? Do not need to explain.

Table 1 Capacitive responses in the RC circuit (the First Cycle)

R (Ω)	50	100	200	500	1000	1500	2000	2500	3000	4000
V_m										
V_{sm}										
φ										

Table 2 Capacitive responses in the RC circuit (the Third Cycle)

$R(\Omega)$	50	100	200	500	1000	1 <i>5</i> 00	2000	2500	3000	4000
V_m										
V_{sm}										
φ										

Questions and conclusions

- Is the frequency of the response (output) different from that of the source (input)?
- Do the peak values of the source and the response change when you change the resistance of the resistor R? If yes, how do they change?
- Does the phase φ of the response change as you change the resistance of the resistor R? If yes, how does the phase φ change?
- Summarize your findings of this lab.