1) 6.5

Assuming that it does right to left exponentiation and that expt can take multiple arguments, explicit use of parenthesis is necessary to get the expected outcome.

2) 6.12

if (a!=0 && a.next == true)

Rewritten to work without short-circuiting:

else

//code after "if" code

- 3) a/b > 0 && b/a < 0
  - a. evaluation with a == 0:

the first part of the expression will evaluate to 0 which means the boolean will short circuit and the rest of the conditional statement.

statement = 0

evaluation with b==0:

Making b == 0 is going to cause an error because it will attempt to divide by 0

- b. It would make sense, because it would allow the programmer to not worry about accidentally dividing by zero
- 4) The argument isn't convincing because it gives a programmer too much brevity to make code that cannot be optimized. I C, the best way to do what he is talking about would be to use the

continue statement. Many other languages have similar control structures

- 5) MIPS for do-while
  - a. The Language uses short-circuit evaluation

DOWHILE:

#loop body

li \$t0, -20

lw \$t1,-8(sp)

ble \$t1,\$t0, WHILE\_OUT

li \$t0,-10

blt \$t1,\$t0, WHILE

li \$t0,20

bgt \$t,\$t0,while

WHILE\_OUT:

b. The language doesn't use short-circuit evaluation

DOWHILE:

#loop body

li \$t0,-20

lw \$t1,-8(sp)

sgt \$t2,\$t1,\$t0

li \$t0,-10

slt \$t3,\$t1,\$t0

li \$t0,20

sgt \$t4,\$t1,\$t0

or \$t3,\$t3,\$t4

and \$t2,\$t2,\$t3

bnez \$t2,DOWHILE

#after loop

```
6) MIPS for C switch
   Т
                         0
             .word
             .word
                         .L1
             .word
                         .L2
             .word
                         .L4
                         .L3
             .word
             .word
                         .L3
                         $t0,-8(sp)
            lw
                         $t0,$t0,199
            subi
            blez
                         $t0 .L4
                         $t1,5
            li
                         $t0,$t1,.L4
            bgt
            la
                         $t1,T
                         $t0,$t0,4
            mul
            add
                         $t1,$t2,$t0
                         $t2,($t1)
            lw
            jr
                         $t2
.L1
      #code for foo1();
.L2
      #code for foo2();
      j
            .L5
.L3
      #code for foo3();
      j
            .L5
.L4
      #code for foo4();
.L5
            nop
```