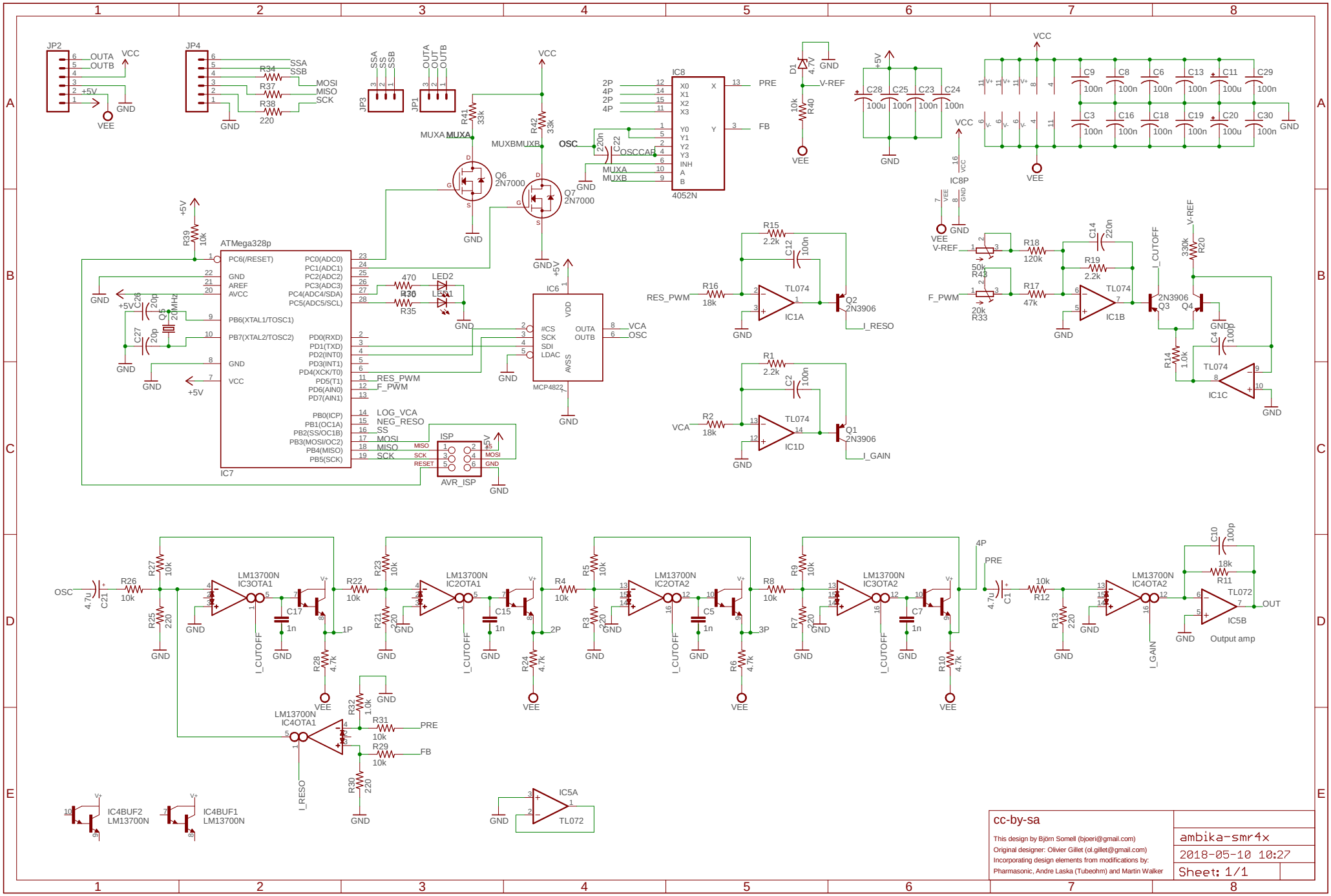
JP2
OUTA
OUTB
VCC
+5V
GND
VEE
JP4
SSA
SSB
MOSI
MISO
SCK
R34
R37
R38
220
JP3
JP1
OUTA
OUT
OUTB
R41
33k
R42
33k
MUXA
MUXB
OSC
Q6
2N7000
Q7
2N7000
IC8
4052N
PRE
FB
D1
4.7V
V-REF
R40
10k
C28
C25
C23
C24
100u
100n
C9
C8
C6
C13
C11
C29
C3
C16
C18
C19
C20
C30
IC8P
R39
10k
ATMega328p
PC6(/RESET)
GND
AREF
AVCC
PB6(XTAL1/TOSC1)
PB7(XTAL2/TOSC2)
PC0(ADC0)
PC1(ADC1)
PC2(ADC2)
PC3(ADC3)
PC4(ADC4/SDA)
PC5(ADC5/SCL)
PD0(RXD)
PD1(TXD)
PD2(INT0)
PD3(INT1)
PD4(XCK/T0)
PD5(T1)
PD6(AIN0)
PD7(AIN1)
PB0(ICP)
PB1(OC1A)
PB2(SS/OC1B)
PB3(MOSI/OC2)
PB4(MISO)
PB5(SCK)
IC7
Q5
20MHz
C26
20p
C27
20p
470
R36
R35
LED2
LED1
RES_PWM
F_PWM
LOG_VCA
NEG_RESO
SS
MOSI
MISO
SCK
ISP
AVR_ISP
RESET
IC6
MCP4822
#CS
SCK
SDI
LDAC
VDD
VSS
OUTA
OUTB
VCA
OSC
R15
2.2k
C12
100n
R16
18k
TL074
IC1A
Q2
2N3906
I_RESO
R1
2.2k
C2
100n
R2
18k
IC1D
Q1
2N3906
I_GAIN
R18
120k
R19
2.2k
C14
220n
R17
47k
IC1B
50k
R43
20k
R33
I_CUTOFF
330k
R20
2N3906
Q3
Q4
R14
1.0k
C4
100p
IC1C
LM13700N
IC3OTA1
IC2OTA1
IC2OTA2
IC3OTA2
IC4OTA1
IC4OTA2
R26
10k
R27
10k
R25
220
C21
4.7u
C17
1n
C15
1n
C5
1n
C7
1n
R28
4.7k
R24
4.7k
R6
4.7k
R10
4.7k
R22
10k
R23
10k
R21
220
R4
10k
R5
10k
R3
220
R8
10k
R9
10k
R7
220
1P
2P
3P
4P
C1
4.7u
R12
10k
R13
220
C10
100p
R11
18k
TL072
IC5B
Output amp
OUT
R32
1.0k
R31
10k
R29
10k
R30
220
IC4BUF2
LM13700N
IC4BUF1
LM13700N
IC5A
TL072
cc-by-sa
This design by Björn Somell (bjoeri@gmail.com)
Original designer: Olivier Gillet (ol.gillet@gmail.com)
Incorporating design elements from modifications by:
Pharmasonic, Andre Laska (Tubeohm) and Martin Walker
ambika-smr4x
2018-05-10 10:27
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