

YIG DRIVER VOLTAGE

GATE DRIVE VOLTAGE. MAKE SURE TO KEEP THIS VPP+VG OF YIG TRANSISTORS.

Sheet: /power/
File: power.sch

Title:

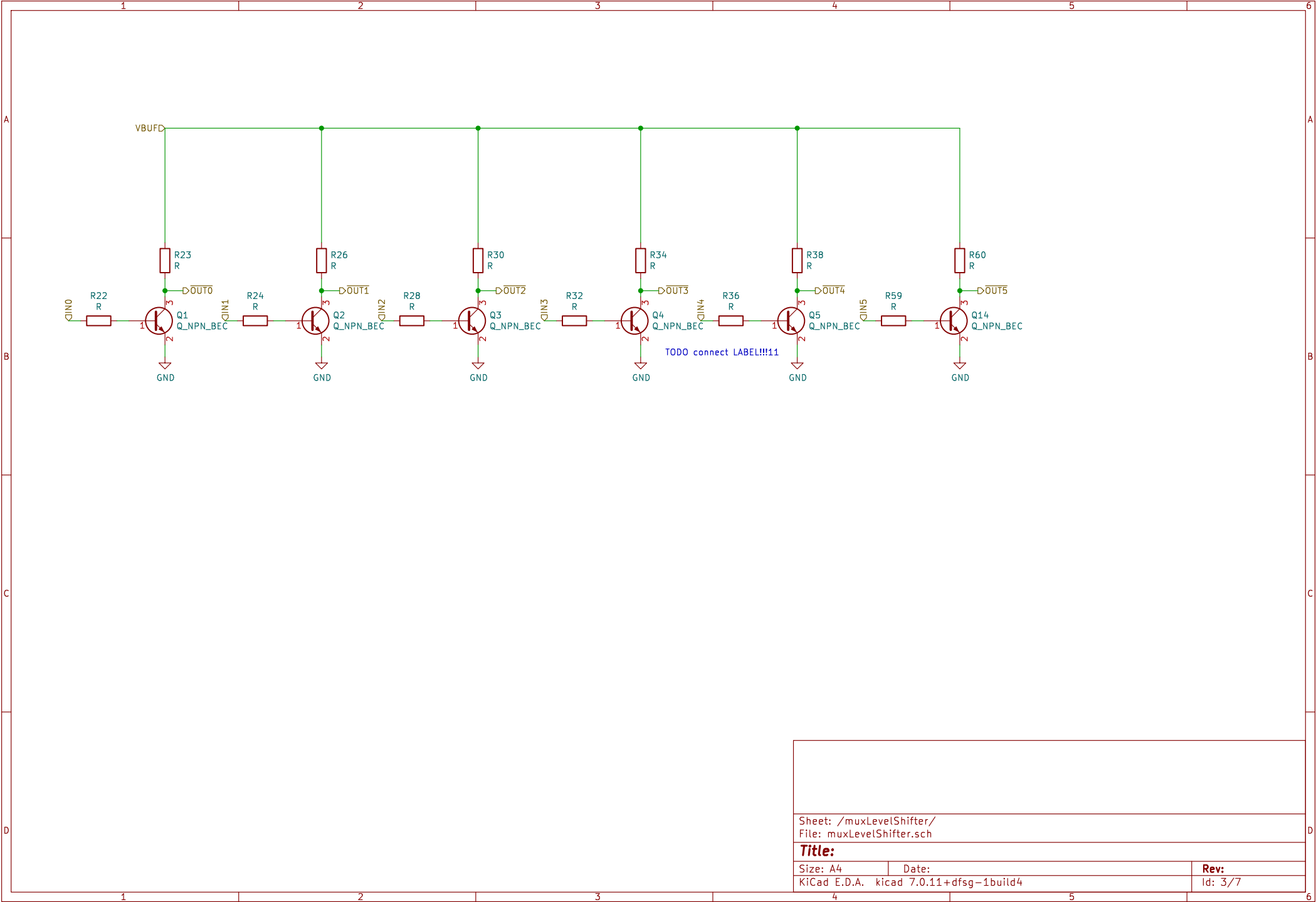
Size: A4

Date:

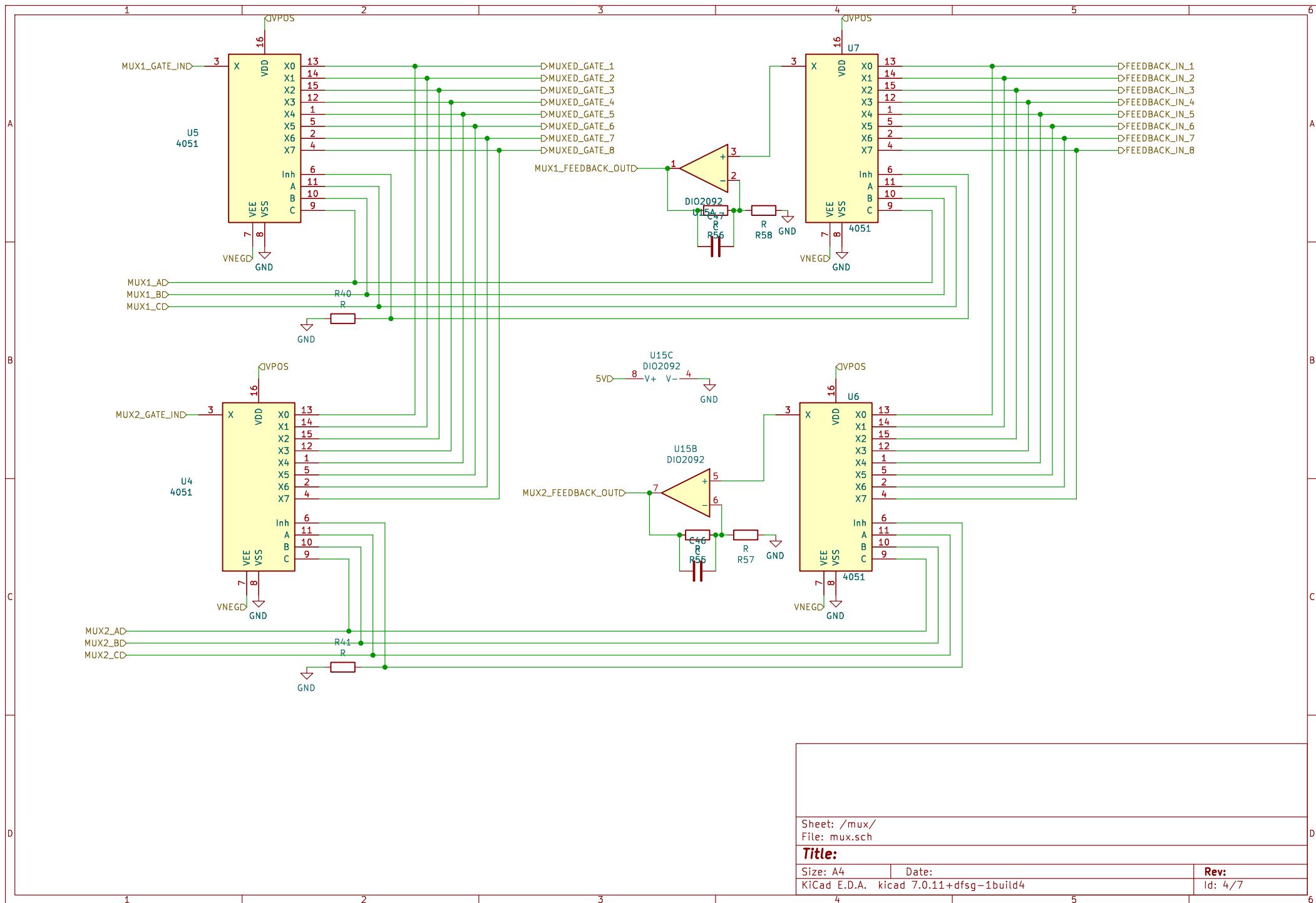
KiCad E.D.A. kicad 7.0.11+dfsg-1build4

Rev:

Id: 2/7



Sheet: /muxLevelShifter/ File: muxLevelShifter.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 7.0.11+dfsg-1build4		Id: 3/7



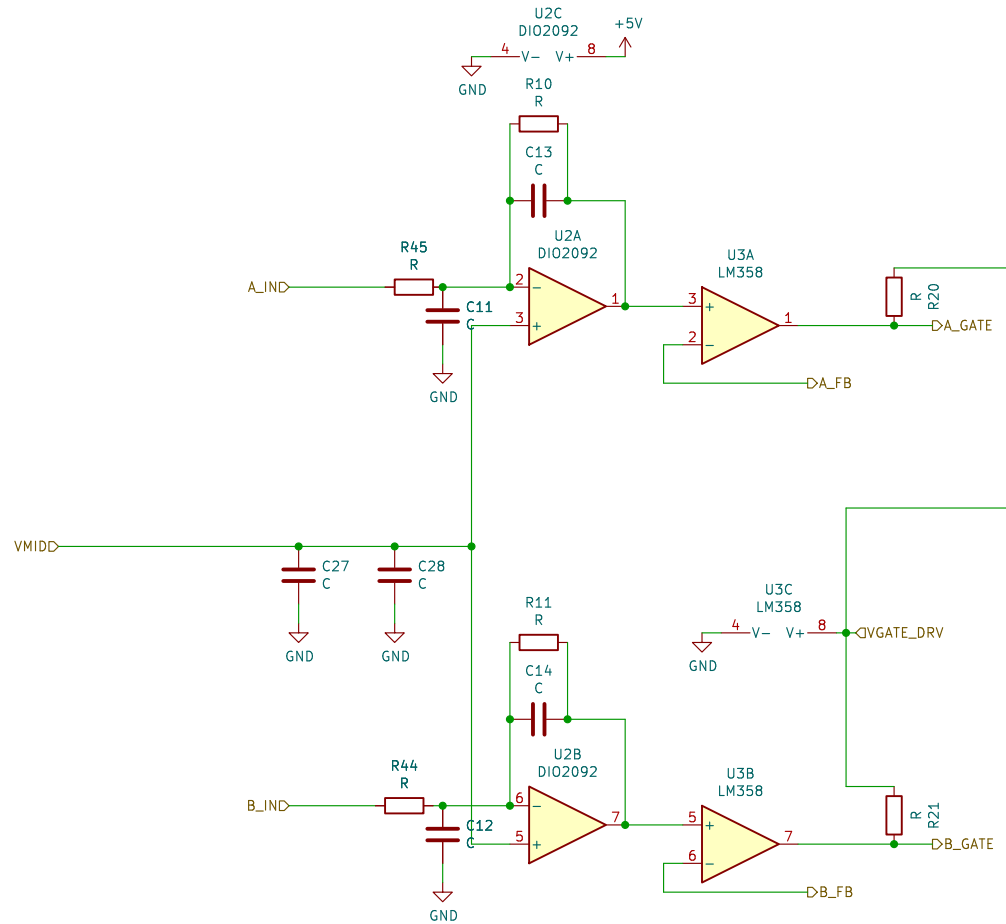
Sheet: /mux/
File: mux.sch

Title:

Size: A4
KiCad E.D.A. kicad 7.0.11+dfsg-1build4

Date:

Rev:
Id: 4/7

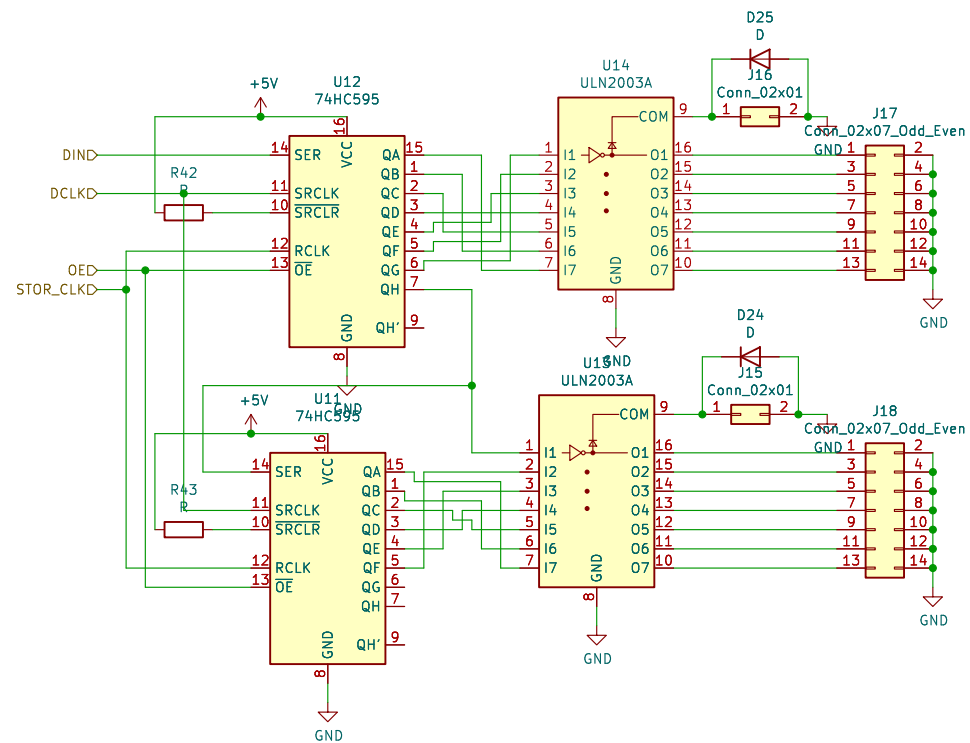


Sheet: /yigBuffer/
File: yigBuffer.sch

Title:

Size: A4 Date:
KiCad E.D.A. kicad 7.0.11+dfsg-1build4

Rev:
Id: 5/7



Sheet: /relay_control/
File: relay_control.sch

Title:

Size: A4

Date:

KiCad E.D.A. kicad 7.0.11+dfsg-1build4

Rev:

Id: 6/7

