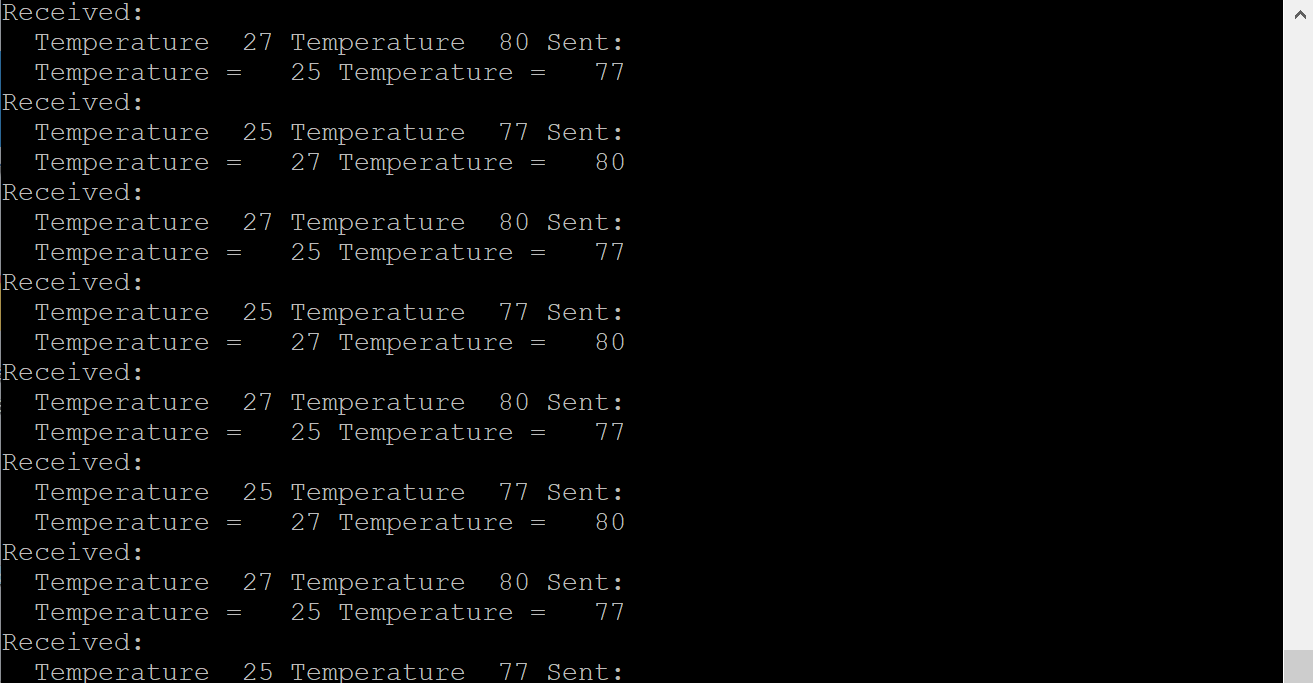
**Date Submitted: 10/17/2018 11:38 PM**

**Task 01:** Modify the supplied code to transmit and receive the Internal Temperature and verify the results.

**Youtube Link:** [**https://www.youtube.com/watch?v=9ROVdqjZEPs**](https://www.youtube.com/watch?v=9ROVdqjZEPs)



**Modified Code:**

/\*#include <stdbool.h>

#include <stdint.h>

#include "inc/hw\_memmap.h"

#include "driverlib/gpio.h"

#include "driverlib/pin\_map.h"

#include "driverlib/ssi.h"

#include "driverlib/sysctl.h"

#include "driverlib/uart.h"

#include "utils/uartstdio.h"

#include "utils/uartstdio.c"

\*/

**#define** PART\_TM4C123GH6PM

**#include** <stdint.h>

**#include** <stdbool.h>

**#include** "stdlib.h"

**#include** "inc/hw\_ints.h"

**#include** "inc/hw\_memmap.h"

**#include** "inc/hw\_uart.h"

**#include** "inc/hw\_gpio.h"

**#include** "inc/hw\_pwm.h"

**#include** "inc/hw\_types.h"

**#include** "driverlib/adc.h"

**#include** "driverlib/timer.h"

**#include** "driverlib/gpio.h"

**#include** "driverlib/interrupt.h"

**#include** "driverlib/pin\_map.h"

**#include** "driverlib/rom.h"

**#include** "driverlib/rom\_map.h"

**#include** "driverlib/sysctl.h"

**#include** "driverlib/uart.h"

**#include** "driverlib/udma.h"

**#include** "driverlib/pwm.h"

**#include** "driverlib/ssi.h"

**#include** "driverlib/systick.h"

**#include** "driverlib/adc.h"

**#include** "utils/uartstdio.h"

**#include** "utils/uartstdio.c"

**#include** <string.h>

**#include** "inc/tm4c123gh6pm.h"

//

// Number of bytes to send and receive.

//

//\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

**#define** NUM\_SSI\_DATA 2

//\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

//

// This function sets up UART0 to be used for a console to display information

// as the example is running.

//

//\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

**void**

**InitConsole**(**void**)

{

//

// Enable GPIO port A which is used for UART0 pins.

// **TODO**: change this to whichever GPIO port you are using.

//

**SysCtlPeripheralEnable**(SYSCTL\_PERIPH\_GPIOA);

//

// Configure the pin muxing for UART0 functions on port A0 and A1.

// This step is not necessary if your part does not support pin muxing.

// **TODO**: change this to select the port/pin you are using.

//

**GPIOPinConfigure**(GPIO\_PA0\_U0RX);

**GPIOPinConfigure**(GPIO\_PA1\_U0TX);

//

// Enable UART0 so that we can configure the clock.

//

**SysCtlPeripheralEnable**(SYSCTL\_PERIPH\_UART0);

//

// Use the internal 16MHz oscillator as the UART clock source.

//

**UARTClockSourceSet**(UART0\_BASE, UART\_CLOCK\_PIOSC);

//

// Select the alternate (UART) function for these pins.

// **TODO**: change this to select the port/pin you are using.

//

**GPIOPinTypeUART**(GPIO\_PORTA\_BASE, GPIO\_PIN\_0 | GPIO\_PIN\_1);

//

// Initialize the UART for console I/O.

//

**UARTStdioConfig**(0, 115200, 16000000);

}

//\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

//

// Configure SSI0 in master Freescale (SPI) mode. This example will send out

// 3 bytes of data, then wait for 3 bytes of data to come in. This will all be

// done using the polling method.

//

//\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

**int**

**main**(**void**)

{

**#if** defined(TARGET\_IS\_TM4C129\_RA0) || \

defined(TARGET\_IS\_TM4C129\_RA1) || \

defined(TARGET\_IS\_TM4C129\_RA2)

uint32\_t ui32SysClock;

**#endif**

uint32\_t pui32DataTx[NUM\_SSI\_DATA];

uint32\_t pui32DataRx[NUM\_SSI\_DATA];

uint32\_t ui32Index;

//

// Set the clocking to run directly from the external crystal/oscillator.

// **TODO**: The SYSCTL\_XTAL\_ value must be changed to match the value of the

// crystal on your board.

//

**#if** defined(TARGET\_IS\_TM4C129\_RA0) || \

defined(TARGET\_IS\_TM4C129\_RA1) || \

defined(TARGET\_IS\_TM4C129\_RA2)

ui32SysClock = SysCtlClockFreqSet((SYSCTL\_XTAL\_25MHZ |

SYSCTL\_OSC\_MAIN |

SYSCTL\_USE\_OSC), 25000000);

**#else**

//SysCtlClockSet(SYSCTL\_SYSDIV\_1 | SYSCTL\_USE\_OSC | SYSCTL\_OSC\_MAIN |

// SYSCTL\_XTAL\_16MHZ);

**SysCtlClockSet**(SYSCTL\_SYSDIV\_2\_5|SYSCTL\_USE\_PLL|SYSCTL\_OSC\_MAIN|SYSCTL\_XTAL\_16MHZ);

**#endif**

//

// Set up the serial console to use for displaying messages. This is

// just for this example program and is not needed for SSI operation.

//

InitConsole();

//

// Display the setup on the console.

//

**UARTprintf**("SSI ->\n");

**UARTprintf**(" Mode: SPI\n");

**UARTprintf**(" Data: 8-bit\n\n");

//

// The SSI0 peripheral must be enabled for use.

//

**SysCtlPeripheralEnable**(SYSCTL\_PERIPH\_SSI0);

//

// For this example SSI0 is used with PortA[5:2]. The actual port and pins

// used may be different on your part, consult the data sheet for more

// information. GPIO port A needs to be enabled so these pins can be used.

// **TODO**: change this to whichever GPIO port you are using.

//

**SysCtlPeripheralEnable**(SYSCTL\_PERIPH\_GPIOA);

//

**GPIOPinConfigure**(GPIO\_PA2\_SSI0CLK);

**GPIOPinConfigure**(GPIO\_PA3\_SSI0FSS);

**GPIOPinConfigure**(GPIO\_PA4\_SSI0RX);

**GPIOPinConfigure**(GPIO\_PA5\_SSI0TX);

**GPIOPinTypeSSI**(GPIO\_PORTA\_BASE, GPIO\_PIN\_5 | GPIO\_PIN\_4 | GPIO\_PIN\_3 |

GPIO\_PIN\_2);

//

// Configure and enable the SSI port for SPI master mode. Use SSI0,

// system clock supply, idle clock level low and active low clock in

// freescale SPI mode, master mode, 1MHz SSI frequency, and 8-bit data.

// For SPI mode, you can set the polarity of the SSI clock when the SSI

// unit is idle. You can also configure what clock edge you want to

// capture data on. Please reference the datasheet for more information on

// the different SPI modes.

//

**#if** defined(TARGET\_IS\_TM4C129\_RA0) || \

defined(TARGET\_IS\_TM4C129\_RA1) || \

defined(TARGET\_IS\_TM4C129\_RA2)

SSIConfigSetExpClk(SSI0\_BASE, ui32SysClock, SSI\_FRF\_MOTO\_MODE\_0,

SSI\_MODE\_MASTER, 1000000, 8);

**#else**

**SSIConfigSetExpClk**(SSI0\_BASE, **SysCtlClockGet**(), SSI\_FRF\_MOTO\_MODE\_0,

SSI\_MODE\_MASTER, 1000000, 8);

**#endif**

//

// Enable the SSI0 module.

//

**SSIEnable**(SSI0\_BASE);

uint32\_t ADCValues[1];

uint32\_t TempValueC ;

uint32\_t TempValueF ;

**SysCtlPeripheralEnable**(SYSCTL\_PERIPH\_ADC0);

**SysCtlDelay**(3);

// UARTprintf("HERE\n");

**ADCSequenceConfigure**(ADC0\_BASE, 3, ADC\_TRIGGER\_PROCESSOR, 0);

//UARTprintf("HERE\n");

**ADCSequenceStepConfigure**(ADC0\_BASE, 3, 0, ADC\_CTL\_TS | ADC\_CTL\_IE |

ADC\_CTL\_END);

**ADCSequenceEnable**(ADC0\_BASE, 3);

**ADCIntClear**(ADC0\_BASE, 3);

**while**(1){

**ADCProcessorTrigger**(ADC0\_BASE, 3);

**while**(!**ADCIntStatus**(ADC0\_BASE, 3, false))

{

}

**ADCIntClear**(ADC0\_BASE, 3);

**ADCSequenceDataGet**(ADC0\_BASE, 3, ADCValues);

TempValueC = (uint32\_t)(147.5 - ((75.0\*3.3 \*(**float**)ADCValues[0])) / 4096.0);

TempValueF = ((TempValueC \* 9) + 160) / 5;

\

**while**(**SSIDataGetNonBlocking**(SSI0\_BASE, &pui32DataRx[0]))

{

}

//

// Initialize the data to send.

//

pui32DataTx[0] = TempValueC;

pui32DataTx[1] = TempValueF;

// pui32DataTx[2] = 'i';

//

// Display indication that the SSI is transmitting data.

//

**UARTprintf**("Sent:\n ");

//

// Send 3 bytes of data.

//

**for**(ui32Index = 0; ui32Index < NUM\_SSI\_DATA; ui32Index++)

{

//

// Display the data that SSI is transferring.

//

**UARTprintf**("Temperature = %3d ", pui32DataTx[ui32Index]);

//

// Send the data using the "blocking" put function. This function

// will wait until there is room in the send FIFO before returning.

// This allows you to assure that all the data you send makes it into

// the send FIFO.

//

**SSIDataPut**(SSI0\_BASE, pui32DataTx[ui32Index]);

}

//

// Wait until SSI0 is done transferring all the data in the transmit FIFO.

//

**while**(**SSIBusy**(SSI0\_BASE))

{

}

//

// Display indication that the SSI is receiving data.

//

**UARTprintf**("\nReceived:\n ");

//

// Receive 3 bytes of data.

//

**for**(ui32Index = 0; ui32Index < NUM\_SSI\_DATA; ui32Index++)

{

//

// Receive the data using the "blocking" Get function. This function

// will wait until there is data in the receive FIFO before returning.

//

**SSIDataGet**(SSI0\_BASE, pui32DataRx[ui32Index]);

//

// Since we are using 8-bit data, mask off the MSB.

//

// pui32DataRx[ui32Index] &= 0x00FF;

//

// Display the data that SSI0 received.

//

**UARTprintf**("Temperature %3d ", pui32DataRx[ui32Index]);

}

}

//

// Return no errors

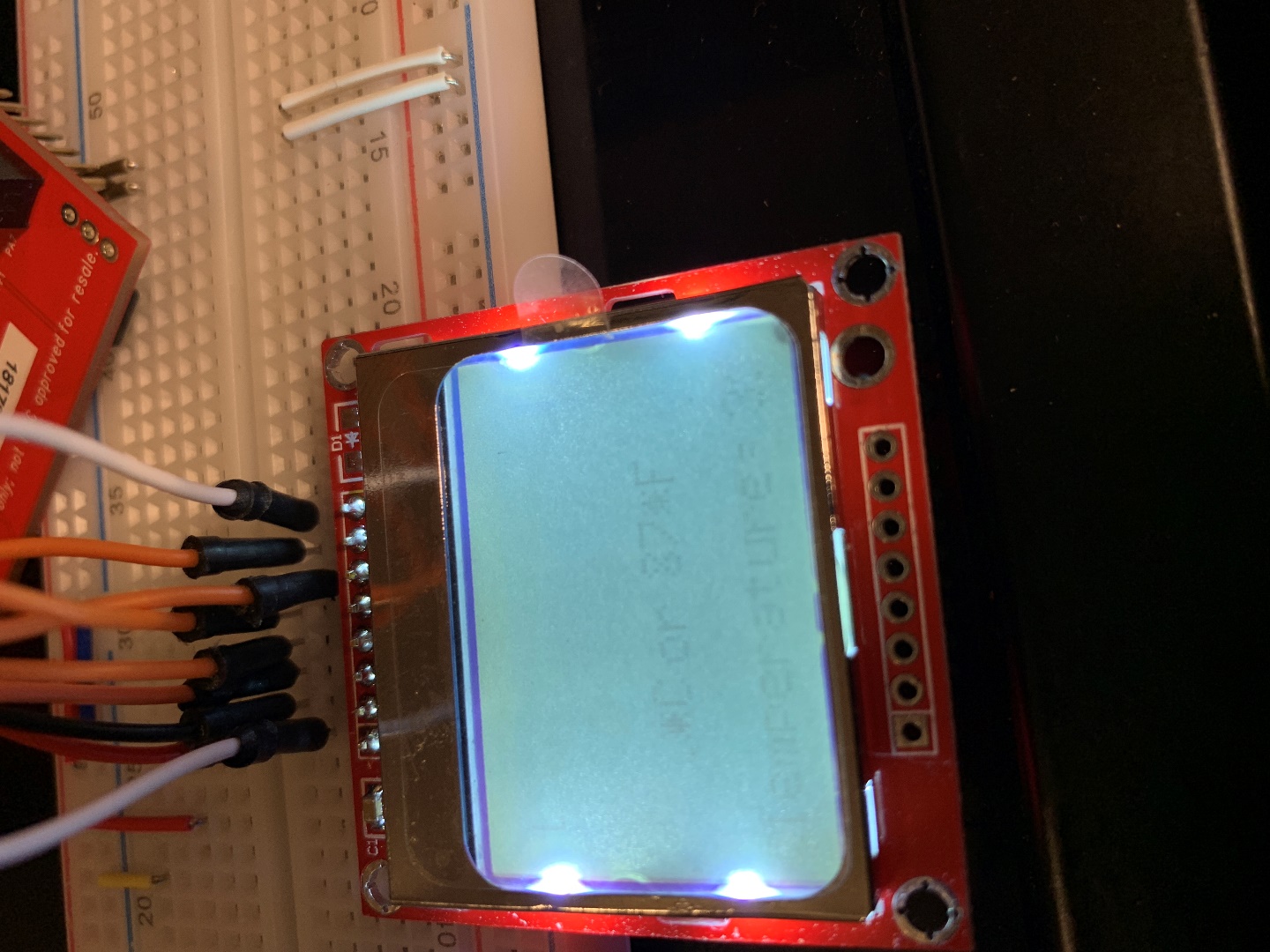
//

**return**(0);

}

**------------------------------------------------------------------------------------**

**Task 02:** Display the z-axis results in Nokia5110 GLCD. If task is not working, display the Lab 5 – Temperature on the LCD as: “Temperature: 72.92 F, 20.34 F”. Update every sec. using the timer.

****

Youtube Link: <https://www.youtube.com/watch?v=ZFxclQuzh1Q>

**Modified Code:**

**#define** PART\_TM4C123GH6PM

**#include** <stdint.h>

**#include** <stdbool.h>

**#include** "stdlib.h"

**#include** "inc/hw\_ints.h"

**#include** "inc/hw\_memmap.h"

**#include** "inc/hw\_uart.h"

**#include** "inc/hw\_gpio.h"

**#include** "inc/hw\_pwm.h"

**#include** "inc/hw\_types.h"

**#include** "driverlib/adc.h"

**#include** "driverlib/timer.h"

**#include** "driverlib/gpio.h"

**#include** "driverlib/interrupt.h"

**#include** "driverlib/pin\_map.h"

**#include** "driverlib/rom.h"

**#include** "driverlib/rom\_map.h"

**#include** "driverlib/sysctl.h"

**#include** "driverlib/uart.h"

**#include** "driverlib/udma.h"

**#include** "driverlib/pwm.h"

**#include** "driverlib/ssi.h"

**#include** "driverlib/systick.h"

**#include** "driverlib/adc.h"

**#include** "utils/uartstdio.h"

**#include** "utils/uartstdio.c"

**#include** <string.h>

**#include** "inc/tm4c123gh6pm.h"

**#include** "utils/Nokia5110.h"

**#include** "utils/Nokia5110.c"

**void**

**InitConsole**(**void**)

{

//

// Enable GPIO port A which is used for UART0 pins.

**SysCtlPeripheralEnable**(SYSCTL\_PERIPH\_GPIOA);

//

// Configure the pin muxing for UART0 functions on port A0 and A1.

// This step is not necessary if your part does not support pin muxing.

**GPIOPinConfigure**(GPIO\_PA0\_U0RX);

**GPIOPinConfigure**(GPIO\_PA1\_U0TX);

//

// Enable UART0 so that we can configure the clock.

//

**SysCtlPeripheralEnable**(SYSCTL\_PERIPH\_UART0);

//

// Use the internal 16MHz oscillator as the UART clock source.

//

**UARTClockSourceSet**(UART0\_BASE, UART\_CLOCK\_PIOSC);

//

// Select the alternate (UART) function for these pins.

**GPIOPinTypeUART**(GPIO\_PORTA\_BASE, GPIO\_PIN\_0 | GPIO\_PIN\_1);

//

// Initialize the UART for console I/O.

//

**UARTStdioConfig**(0, 115200, 16000000);

}

**int** **main**(**void**)

{

**SysCtlClockSet**(SYSCTL\_SYSDIV\_2\_5|SYSCTL\_USE\_PLL|SYSCTL\_OSC\_MAIN|SYSCTL\_XTAL\_16MHZ);

SysTick\_Init();

InitConsole();

**startSSI0**();

**initialize\_screen**(BACKLIGHT\_ON,SSI0);

**int** i;

**char** buf[11];

**int** max=11,current\_pos=0;

set\_buttons\_up\_down();

**unsigned** **char** menu\_elements[12][25];

menu\_elements[0][0]='1';

menu\_elements[0][1]=0x00;

menu\_elements[1][0]='2';

menu\_elements[1][1]=0x00;

menu\_elements[2][0]='3';

menu\_elements[2][1]=0x00;

menu\_elements[3][0]='4';

menu\_elements[3][1]=0x00;

menu\_elements[4][0]='5';

menu\_elements[4][1]=0x00;

menu\_elements[5][0]='6';

menu\_elements[5][1]=0x00;

menu\_elements[6][0]='7';

menu\_elements[6][1]=0x00;

menu\_elements[7][0]='8';

menu\_elements[7][1]=0x00;

menu\_elements[8][0]='9';

menu\_elements[8][1]=0x00;

menu\_elements[9][0]='1';

menu\_elements[9][1]='0';

menu\_elements[9][2]=0x00;

menu\_elements[10][0]='1';

menu\_elements[10][1]='1';

menu\_elements[10][2]=0x00;

menu\_elements[11][0]='1';

menu\_elements[11][1]='2';

menu\_elements[11][2]=0x00;

set\_menu(menu\_elements);

**clear\_screen**(SSI0);

**screen\_write**("CPE\n403!",ALIGN\_CENTRE\_CENTRE,SSI0);

SysTick\_Wait50ms(100);

uint32\_t ADCValues[1];

uint32\_t TempValueC ;

uint32\_t TempValueF ;

**SysCtlPeripheralEnable**(SYSCTL\_PERIPH\_ADC0);

**SysCtlDelay**(3);

**ADCSequenceConfigure**(ADC0\_BASE, 3, ADC\_TRIGGER\_PROCESSOR, 0);

**ADCSequenceStepConfigure**(ADC0\_BASE, 3, 0, ADC\_CTL\_TS | ADC\_CTL\_IE |

ADC\_CTL\_END);

**ADCSequenceEnable**(ADC0\_BASE, 3);

**ADCIntClear**(ADC0\_BASE, 3);

**while**(1)

{

**clear\_screen**(SSI0);

**ADCProcessorTrigger**(ADC0\_BASE, 3);

**while**(!**ADCIntStatus**(ADC0\_BASE, 3, false))

{

}

**ADCIntClear**(ADC0\_BASE, 3);

**ADCSequenceDataGet**(ADC0\_BASE, 3, ADCValues);

TempValueC = (uint32\_t)(147.5 - ((75.0\*3.3 \*(**float**)ADCValues[0])) / 4096.0);

TempValueF = ((TempValueC \* 9) + 160) / 5;

sprintf(buf, "Temperature = %3d\*C or %3d\*F\n", TempValueC, TempValueF);

**screen\_write**(buf,ALIGN\_CENTRE\_CENTRE,SSI0);

SysTick\_Wait50ms(100);

}

**return** 0;

}

// The delay parameter is in units of the 16 MHz core clock.

**void** **SysTick\_Wait**(**unsigned** **long** delay){

NVIC\_ST\_RELOAD\_R = delay-1; // number of counts to wait

NVIC\_ST\_CURRENT\_R = 0; // any value written to CURRENT clears

**while**((NVIC\_ST\_CTRL\_R&0x00010000)==0){ // wait for count flag

}

}

**void** **SysTick\_Wait50ms**(**unsigned** **long** delay){

**unsigned** **long** i;

**for**(i=0; i<delay; i++){

SysTick\_Wait(800000); // wait 50ms

}

}

**void** **SysTick\_Init**(**void**){

NVIC\_ST\_CTRL\_R = 0; // disable SysTick during setup

NVIC\_ST\_CTRL\_R = 0x00000005; // enable SysTick with core clock

}