

Web & Git

bkits.github.io
github.com/bkits
dst.phys.uoa.gr

Vasileios Kitsakis

Computer Engineer

Programming

C/C++
Java
XML
SQLite
Android
Bash
HTML/CSS
HLS
VHDL

Platforms

Android Studio
Matlab
Vivado

Systems

Debian
Windows

Experience

- 2017 - Now **FPGA Developer** NTUA-ICCS
Implementation of DSP Functions, 5G-PHOS
- 2016 - 2017 **FPGA Developer** LN2 DB
Parallel Sorting Architecture for Decoders
Implementation of Mathematical Operators and Functions for FEC Decoders

Education

- 2017 - now **PHD candidate** NKUA
Efficient Design of Digital Baseband Tranceivers for Fifth Generation Mobile Communication Networks
- 2017 **Master's Degree in Computer Engineering** NKUA
IPPS Control and Computing
- 2015 **Bachelor's Degree in Physics** NKUA
Direction "Electronics-Computers-Telecommunications-Control" of the Undergraduate Program of the Faculty of Physics

Publications

Kitsakis, V & A. Papatheofanous, E & Reisis, D & Lentaris, G & Soudris, Dimitrios
Parity Based In-Place FFT Architecture for Continuous Flow Applications
97-100. 10.1109/ICECS.2018.8617990

V. Kitsakis, K. Nakos, D. Reisis, N. Vlassopoulos
Parallel Memory Accessing for FFT Architectures
Journal of Signal Processing Systems. November 2018, Volume 90, Issue 11, pp 1593–1607

Ioannis Patronas, Vasileios Kitsakis, Nikolaos Gkatzios, Dionysios Reisis, Konstantinos Christodouloupoulos, Emmanouel Varvarigos
Scheduler Accelerator for TDMA Data Centers
26th Euromicro International Conference on Parallel, Distributed and Network-based Processing, PDP 2018, Cambridge United Kingdom, March 2018

Angelos Kyriakos, Ioannis Patronas, Georgios Tzimas, Vasileios Kitsakis, Dionysios Reisis
Realizing virtual output queues in high throughput data center nodes
2017 Panhellenic Conference on Electronics and Telecommunications (PACET), Xanthi, Greece, Nov. 2017

Certifications

- 2017 **Android Basics Nanodegree by Google** Udacity Co-Created with Google
Nanodegree Program

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