

# BRETT SAIKI

Industrial PhD Student ~ U.W. and Intel

 bsaiki.com

 bksaiki

 bksaiki@gmail.com

 bretttsaiki

## SUMMARY

Graduate student doing research in programming languages, computer number systems, and term rewriting; obsessed with all things floating-point in both software and hardware.

**Languages:** C, C++, Racket, Rust, Java, Python

**Interests:** Programming Languages, Floating-point, Numerics, Verification

## EDUCATION

**University of Washington** | *Paul G. Allen School of Computer Science and Engineering* Sep. 2024 — Present  
PhD Computer Science and Engineering  
Seattle, WA

**University of Washington** | *Paul G. Allen School of Computer Science and Engineering* Sep. 2023 — Present  
M.S. Computer Science and Engineering  
Seattle, WA

**University of Washington** | *Paul G. Allen School of Computer Science and Engineering* Aug. 2019 — Jun. 2023  
B.S. Computer Engineering, B.A. Mathematics  
Seattle, WA

## EXPERIENCE

**Intel Corporation** | Seattle, WA (Remote) Sept 2024 — Present  
GPU Logic Design Engineer

- researching computer numerics, programming languages, and rewriting engines (at the University of Washington)
- developing libraries for simulating and formally verifying numerical hardware in GPUs and other accelerators

**University of Washington** | Seattle, WA Sep. 2023 — Sep. 2024  
Research Assistant

- developed tools and libraries for floating-point accuracy optimization and term rewriting
- collaborated with undergraduate students, graduate students, and professors

**Intel Corporation** | Folsom, CA Jun. 2023 — Sep. 2023  
Mathematical Hardware Intern Jun. 2022 — Sep. 2022

- developed compilers for translating numerical specifications, libraries for formally verifying hardware designs, and visualization tools for simulating numerical algorithms
- improved high-level graphics hardware algorithms

**University of Washington** | Seattle, WA Sep. 2022 — Jun. 2023  
Undergraduate Research Assistant Dec. 2019 — Jun. 2022

- developed tools and libraries for floating-point accuracy optimization and term rewriting
- collaborated with graduate students, professors, and industrial groups

## SELECTED PUBLICATIONS

### Target-Aware Implementation of Real Expressions (Conditional)

*Architectural Support for Programming Languages and Operating Systems (ASPLOS) 2025*

Brett Saiki, Jackson Brough, Jonas Regehr, Jesús Ponce, Varun Pradeep, Aditya Akhileshwaran, Zachary Tatlock, Pavel Panchekha

### Equality Saturation Theory Exploration à la Carte

*Object-Oriented Programming, Systems, Languages and Applications (OOPSLA) 2023*

Anjali Pal, Brett Saiki, Ryan Tjoa, Cynthia Richey, Amy Zhu, Oliver Flatt, Max Willsey, Zachary Tatlock, Chandrakana Nandi

### Rewrite Rule Inference Using Equality Saturation | *Distinguished Paper Award*

*Object-Oriented Programming, Systems, Languages and Applications (OOPSLA) 2021*

Chandrakana Nandi, Max Willsey, Amy Zhu, Brett Saiki, Yisu Wang, Adam Anderson, Adriana Schulz, Dan Grossman, Zachary Tatlock

### Combining Precision Tuning and Rewriting

*IEEE International Symposium on Computer Arithmetic (ARITH) 2021*

Brett Saiki, Oliver Flatt, Chandrakana Nandi, Pavel Panchekha, Zachary Tatlock

## RESEARCH

**FPBench** | Project  
FPCore tools, compilers, benchmarks  
NSV 2016

**Herbie** | Project  
Floating-point accuracy improver  
PLDI 2015, ARITH 2021, UIST 2023

**Ruler** | Project  
Rewrite rule synthesis for EqSat  
OOPSLA 2021, OOPSLA 2023

## PROJECTS

**Minim** | Project  
Scheme interpreter written in C

**mpmfnun** | Project | Docs  
Number systems library in Rust

**generic-flonum** | Project | Docs  
Alternate MPFR interface in Racket with subnormalization and exponent bounds