

# Induction

**Mathematical induction** (side condition:  $n$  not free in  $\Gamma$ ):

$$\frac{\underline{\Gamma} \vdash \underline{P}(0) \quad \underline{\Gamma}, \underline{P}(n) \vdash \underline{P}(n+1)}{\underline{\Gamma} \vdash \forall n \cdot \underline{P}(n)}$$

$$\frac{\underline{\Gamma}, (\forall m < n \cdot \underline{P}(m)) \vdash \underline{P}(n)}{\underline{\Gamma} \vdash \forall n \cdot \underline{P}(n)}$$

The two rules above represent weak and strong induction, respectively.

**Structural induction** (side condition:  $xs$ ,  $x$  not free in  $\Gamma$ ):

$$\frac{\underline{\Gamma} \vdash \underline{P}([]) \quad \underline{\Gamma}, \underline{P}(xs) \vdash \underline{P}(x : xs)}{\underline{\Gamma} \vdash \forall xs \cdot \underline{P}(xs)}$$

$$\frac{\underline{\Gamma}, (\forall xs' \sqsubset xs \cdot \underline{P}(xs')) \vdash \underline{P}(xs)}{\underline{\Gamma} \vdash \forall xs \cdot \underline{P}(xs)}$$

The two rules above represent weak and strong structural induction, respectively.

The subterm relation for lists, denoted by  $\sqsubset$ , is defined as follows:

$$\forall x, xs \cdot xs \sqsubset x : xs$$

$$\forall xs, ys, zs \cdot xs \sqsubset ys \wedge ys \sqsubset zs \Rightarrow xs \sqsubset zs$$

## -Strong structural induction

- We assume that I.H. holds for all subterms
- 1. We prove  $\forall xs \cdot P(xs)$  by strong structural induction on  $xs$
- 2. We assume, the induction hypothesis holds for all proper subterms of  $xs$
- 3. Let  $---$  be arbitrary
- 4. We proceed by a case analysis on  $xs$ 
  - ↪ Für jeden Typ, den  $xs$  annehmen kann, müssen wir nun die Gleichheit zeigen
- 5. Nun, Terme so umformen, dass wir möglichst viel I.H. für subterms einsetzen können.
- Subterms can be:
  - Subformulas
  - Subtrees

# IMP

$n$	for numerals (Numeral)
$x, y, z$	for variables (Var)
$e, e', e_1, e_2$	for arithmetic expressions (Aexp)
$b, b_1, b_2$	for boolean expressions (Bexp)
$s, s', s_1, s_2$	for statements (Stm)

- Numeral  $\rightarrow$  Value  
 - Variable  $\rightarrow$  Numeral  
 $\sigma(x)$

- States:
  - Associates a value with each variable
  - $\sigma(x) \rightarrow$  Value

- Arithmetic Expression:
  - $A[[x]]\sigma = \sigma(x)$
  - $A[[n]]\sigma = N[[n]]$
  - $A[[e_1 \text{ op } e_2]]\sigma$   
 $= A[[e_1]]\sigma \text{ op } A[[e_2]]\sigma$

- Boolean Expression:

- $B[[e_1 \text{ op } e_2]]\sigma = \text{tt if } A[[e_1]]\sigma \text{ op } A[[e_2]]\sigma$
- $B[[b_1 \oplus b_2]] \quad (\oplus \in \text{and/or})$
- $B[[\text{not } b]]\sigma = \text{tt if } B[[b]]\sigma = \text{ff}$

## Free variables

### Arithmetic expressions

$FV(e_1 \text{ op } e_2)$	$= FV(e_1) \cup FV(e_2)$
$FV(n)$	$= \emptyset$
$FV(x)$	$= \{x\}$

### Boolean expressions

$FV(e_1 \text{ op } e_2)$	$= FV(e_1) \cup FV(e_2)$
$FV(\text{not } b)$	$= FV(b)$
$FV(b_1 \text{ or } b_2)$	$= FV(b_1) \cup FV(b_2)$
$FV(b_1 \text{ and } b_2)$	$= FV(b_1) \cup FV(b_2)$

### Statements

$FV(\text{skip})$	$= \emptyset$
$FV(x := e)$	$= \{x\} \cup FV(e)$
$FV(s_1 ; s_2)$	$= FV(s_1) \cup FV(s_2)$
$FV(\text{if } b \text{ then } s_1 \text{ else } s_2 \text{ end})$	$= FV(b) \cup FV(s_1) \cup FV(s_2)$
$FV(\text{while } b \text{ do } s \text{ end})$	$= FV(b) \cup FV(s)$

## - Substitution

• Replaces each free occurrence of variable  $x$  with  $e$

$$\cdot (e_1 \text{ op } e_2)[x \rightarrow e] \equiv (e_1[x \rightarrow e] \text{ op } e_2[x \rightarrow e])$$

$$\cdot n[x \rightarrow e] \equiv n$$

$$\cdot y[x \rightarrow e] \equiv \begin{cases} e & \text{if } x = y \\ y & \text{otherwise} \end{cases}$$

$$\cdot (\text{not } b)[x \rightarrow e] \equiv \text{not}(b[x \rightarrow e])$$

$$\cdot (b_1 \oplus b_2)[x \rightarrow e] \equiv (b_1[x \rightarrow e] \oplus b_2[x \rightarrow e])$$

$$\cdot \underline{\text{Lemma}}: \beta \underbrace{[b[x \rightarrow e]]}_{\text{first substitute}} \sigma \equiv \beta \underbrace{[b] \sigma}_{\text{first evaluate}} \underbrace{[x \rightarrow \alpha([e]) \sigma]}_{\text{then substitute directly with new evaluations}}$$

$\hookrightarrow x$  must be free:  $x \in FV(e)$

## Operational semantics

- Describes how the state is modified during the execution of a statement

### Big-step semantics

- Transition system  $(\Gamma, \mathcal{T}, \rightarrow)$
- Big-step transitions are of the form  $\langle s, \sigma \rangle \rightarrow \sigma'$
- The transition system permits a transition  $\langle s, \sigma \rangle \rightarrow \sigma'$ , written as  $\vdash \langle s, \sigma \rangle \rightarrow \sigma'$ , iff there exists a finite derivation tree ending in  $\text{root}(\tau) \equiv \langle s, \sigma \rangle \rightarrow \sigma'$
- Beweis zwei Transitions sind gleich

1. Transition tree für erste Transition bauen

2. Andere Transition tree so bauen, dass er sich mit Erstem überschneidet

- The big-step semantics of IMP is deterministic
- Non-determinism:  $x := 1 \sqcap (x := 2; x := x + 2)$ 
  - Big-step semantics cannot describe non-terminating computations, if only one non-deterministic branch terminates successfully, we will only see that result
- Parallelism cannot be modeled

## Small-step semantics

- Focuses attention on the individual steps of an execution

-  $k$ -step execution:  $\gamma \xrightarrow{^k} \gamma'$

↳ There is an execution from  $\gamma$  to  $\gamma'$  in exactly  $k$  steps

-  $\gamma \xrightarrow{^1} \gamma' \triangleq \gamma \rightarrow \gamma'$

- Derivation sequence:  $\gamma_0, \gamma_1, \dots$

· If the derivation sequence is finite, then last configuration is either a terminal or a stuck configuration

## Difference

- Operational Semantics:
  - Prove it for specific values
  - Big-step  $\stackrel{?}{=}$  Natural semantics
  - Small-step  $\stackrel{?}{=}$  Structural Sem.
- Axiomatic Semantics:
  - Prove it for arbitrary values
  - Hoare triple

## Program Correctness

- Partial correctness: if a program terminates then there will be a certain relationship between initial and final state

- Total correctness: partial correctness + termination

## Hoare Logic

-  $\{P\} s \{Q\}$  ( $P$ =Precondition/ $Q$ =Postcondition)

$\hookrightarrow$  1. If  $P$  evaluates to true in an initial state or

2. If the execution of  $s$  from  $\sigma$  terminates in the state  $\sigma'$

3. Then  $Q$  will evaluate to true in  $\sigma'$

- Logic variables: only occur in Pre-/Postcondition

• can be used to save values

in the initial state, so they can be referred later in Postcondition

•  $\{x = N\}$

↑ Logical variable (capital letter)

- Derivation tree:  $\vdash \{P\} s \{Q\}$

• There exists a finite derivation tree

• During proofs, we often need to perform semantic reasoning e.g. applying mathematical properties of arithmetic rules

-  $\{P\} \xleftarrow{\text{original}} \{P'\}$  Hier können wir Precondition strengthen

$\{Q'\} \xleftarrow{\text{stärkere Postcondition}} \{Q\}$

$F \{Q\} \xleftarrow{\text{original}}$

- while-loop:  $\frac{\{b \neq 1\} P \} S \{ P \}}{\{P\} \text{ while } b \neq 1 \text{ do } S \text{ end } \{ \neg b = 1 \} P}$

$\{P\}$  while  $b \neq 1$  do  $S$  end  $\{ \neg b = 1 \} P$

$\{P\}$  is the loop invariant, and

because it also holds, when  
the loop terminates

• finding loop invariant

- do some loop-executions,  
write down the states of  
each execution and try to  
figure out a pattern

- prove termination: • write as precondition  $\{ \text{true} \}$

• write as postcondition  $\{ \text{false} \}$

$\hookrightarrow$  if proof goes through then  
statement is non-terminating

- Total correctness: •  $\{ P \} S \{ \neg Q \}$

• we only need to change the  
while-loop: each iteration must  
decrease the value  
of the loop-iterator

### Proof outline

$\{ P \}$   
if  $b$  then  $\frac{\{ b \neq 1\} P \} S_1 \{ Q \}}{\{ P \}}$  if  $b$  then  $S_1$  else  $S_2$  and  $\{ Q \}$  (if  $Ax$ )  
 $\{ \neg b = 1 \} P$

$S_1$   
 $\{ Q \}$   
else  
 $\{ \neg b = 1 \} P$   
 $S_2$   
 $\{ Q \}$   
end  
 $\{ Q \}$

### Assignment:

$$\begin{array}{l|l|l} \{ z+1 \leq x \} & \{ z-1 \leq x \} & \{ z \leq x \} \\ z = z+1 & z = z-1 & z = z \end{array}$$

$$\begin{array}{l|l|l} \{ \dots \} & \{ \dots \} & \{ \dots \} \\ \{ \dots \} & \{ \dots \} & \{ \dots \} \\ \{ \dots \} & \{ \dots \} & \{ \dots \} \end{array}$$

$$t = \{ \dots \}$$

## Finding invariance

- while  $i < k \Rightarrow i \leq k$

## Total correctness

while not  $x = 1$  do

$$\{ \dots \mid x = z \}$$

$$\leftarrow \{ \dots \mid x - 1 < z \}$$

$$x := x - 1$$

$$\{ \dots \mid x < z \}$$

what that we prove, that  
the iterator variable  
is decreased every  
iteration  
 $\rightarrow$  loop will terminate

## Promela

- input language of spin-model-checker
- constant: #define N 5
- structure: typedef vector<int x; int y>
- global channel: chan buf = [2] of {int}
- global variable: byte counter;
- process: proc-type myProc(int p) { ... }
- active processes: active [N] proc-type myProc (...) { ... }
- variable declarations: int d[3]
- channel declarations: chan c1 = [2] of {int}
  - can store 2 messages
- chan c2 = [] of {int}
  - rendez-vous
- number of states: #program locations \*  $\prod$  variable # possible values

- assert ( $\Xi$ ) : aborts execution if expression evaluates to 0 (=false)

- if

$:: x < \max x \rightarrow x = x + 1;$  } looks for all true

$:: x > \min x \rightarrow x = x - 1;$  } options and chooses

fi one non-deterministically

- do

$:: x < \max x \rightarrow x = x + 1;$  } chooses repeatedly

$:: x > \min x \rightarrow x = x - 1;$  } an option non-deterministic,

ad until break/goto is executed

- atomic  $\{\}$  executes atomically

```
#define N 5
bit Account_locks[N];
inline lock(n) {
    atomic {
        Account_locks[n] == 0;
        Account_locks[n] = 1;
    }
}
```

Blocking, until it is true

- inline lock(n) can be used everywhere and is later just replaced with the actual code

- Send and Receive channel

- chan ch =  $\Sigma S$  of {unitype, int}

- Buffered: ch ! req, ? (sends message)

- ch ? req, n (receives message, if req matches)

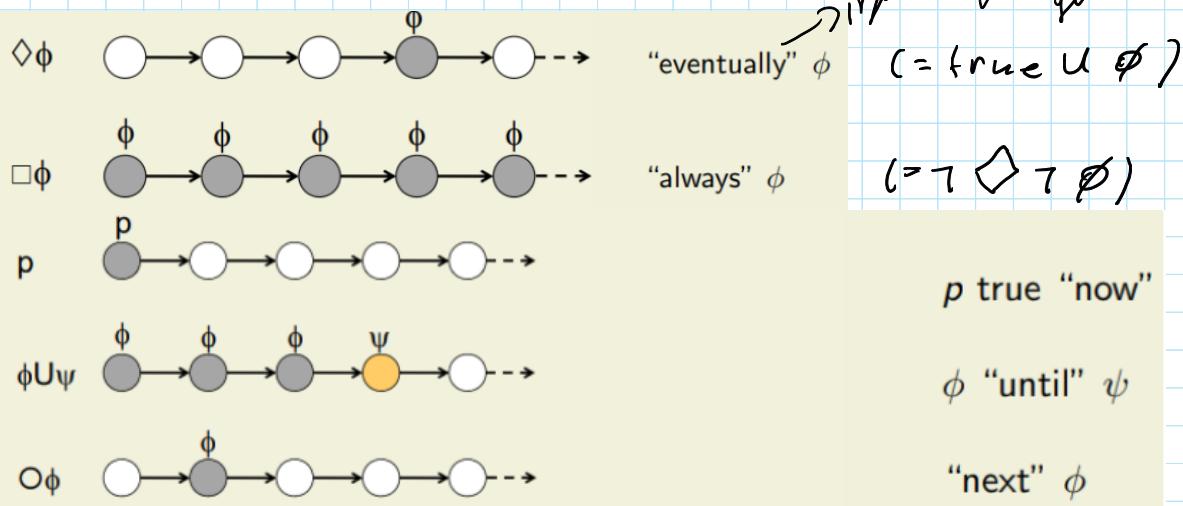
- Unbuffered: · Send is only executable, if there is a receive operation that can

be executed simultaneously

↳ Model synchronous communication

- main: init{...}
- printf("...");
- Over type: int type = {req, ab}
- Start threads: active [w] proc type myproc (...) { ... }

## Linear temporal logic



- A file is always open or closed:  $\Box (\text{open} \vee \text{closed})$
- Once  $\psi$  is true, then  $\psi$  is always true:  $\Box (\psi \Rightarrow \Box \psi)$
- Eventually  $\psi$  will always hold:  $\Diamond \Box \psi$
- Every time  $\psi$  holds,  $\phi$  will eventually hold:  $\Box (\psi \Rightarrow \Diamond \phi)$

## Safety property

- Wenn man einen finite trace (= bad trace) finden kann, der dafür sorgt, dass deine property nie mehr gilt handelt es sich um eine safety property

## Liveness property

- Ein infinite trace, die das property erfüllt kann hinteren an ein finite trace angehängt werden, und dann erfüllt der neue trace die property