

2. Katz and Bosiello 3.18

Implement Hazard free Circuits for each boolean function

$$a). F(A, B, C) = \overline{BC} + \overline{AC}$$

K-Map with Examples

A Karnaugh map for two inputs, AB, with four states labeled 00, 01, 11, and 10. The state 00 is marked with a 1. The state 11 is marked with a 1 and has a circled '1' above it, indicating a hazard.

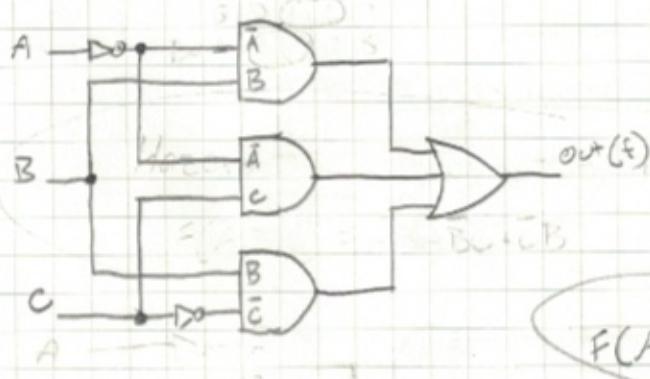
$$F(A, B, C) = \bar{B}\bar{C} + \bar{A}\bar{C} + \bar{A}\bar{B}$$

ScutH table

A	B	C	D	out
0	0	0	0	0
0	0	1	0	1
0	1	0	1	1
0	1	1	0	1
1	0	0	0	0
1	0	1	0	0
1	1	0	1	1
1	1	1	0	0

Static hazard eliminated

static 0-Hazard cannot exist in POS form

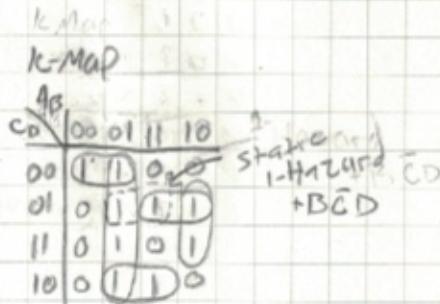


$$F(A, B, C) = \overline{B} \overline{C} + \overline{A} C + \overline{A} \overline{B}$$

2 Continued, Katz and Boariello 3.18

$$B) F(A, B, C, D) = \Sigma m(0, 4, 5, 6, 7, 9, 11, 13, 14)$$

truth table

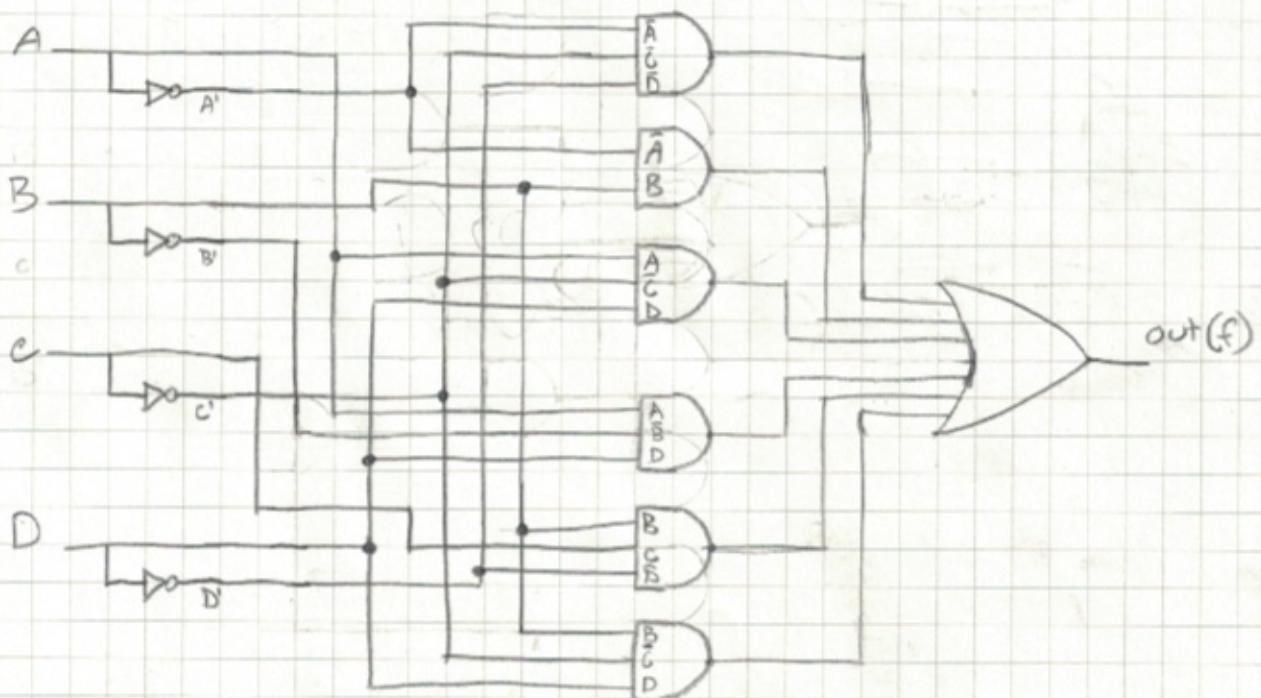


XOR	A	B	C	D	out
0	0	0	0	0	1
1	0	0	0	1	0
2	0	0	1	0	0
3	0	0	1	1	0
4	0	1	0	0	1
5	0	1	0	1	1
6	0	1	1	0	1
7	0	1	1	1	1
8	1	0	0	0	0
9	1	0	0	1	1
10	1	0	1	0	0
11	1	0	1	1	1
12	1	1	0	0	0
13	1	1	0	1	1
14	1	1	1	0	1
15	1	1	1	1	0

$$F(A, B, C, D) = \bar{A}\bar{C}\bar{D} + \bar{A}B + A\bar{C}D + A\bar{B}D + BCD$$

$$F(A, B, C, D) = \bar{A}\bar{C}\bar{D} + \bar{A}B + A\bar{C}D + A\bar{B}D + BC\bar{D} + B\bar{C}D$$

state 1 hazard eliminated
 $+BCD$



a. Continued, Katz and Bartello 3.18

$$c) F(A, B, C) = (A+B)(\bar{B}+C)$$

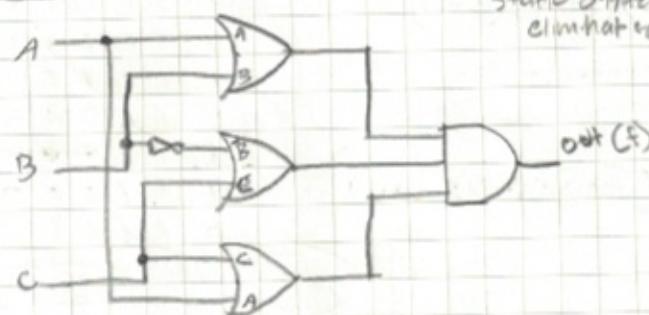
K-map

A	B	00	01	11	10
0	0	0	0	1	1
1	0	0	0	1	1

static 0-Hazard

$$F(A, B, C) = (A+B)(C+\bar{B})(C+A)$$

truth table			
A	B	C	OUT
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	1

static 0-Hazard
eliminated

2. Continued, Icate and Bozello 3.18

$$D). F(A, B, C, D) = \prod m(0, 1, 3, 5, 7, 8, 9, 13, 15)$$

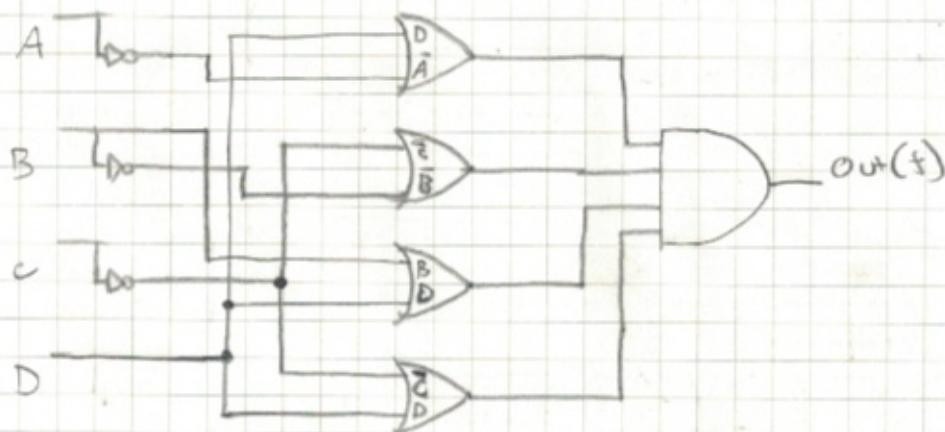
K-map

	A	B	C	D	AB	CD	0000	0001	0011	0110
m0	0	0	0	0	00	00	0	0	0	0
m1	0	0	0	1	00	01	0	0	0	1
m2	0	0	1	0	01	00	0	0	1	0
m3	0	0	1	1	01	10	0	0	1	0
m4	0	1	0	0	01	00	1	0	0	1
m5	0	1	0	1	01	11	1	0	0	0
m6	0	1	1	0	01	00	1	1	0	1
m7	0	1	1	1	01	11	0	1	1	0
m8	1	0	0	0	10	00	0	0	0	0
m9	1	0	0	1	10	01	0	0	0	1
m10	1	0	1	0	10	10	1	0	1	0
m11	1	0	1	1	10	11	1	1	1	1
m12	1	1	0	0	11	00	0	1	0	1
m13	1	1	0	1	11	01	0	1	0	0
m14	1	1	1	0	11	10	1	1	1	0
m15	1	1	1	1	11	11	0	1	1	0

truth table

	A	B	C	D	Out
m0	0	0	0	0	0
m1	0	0	0	1	0
m2	0	0	1	0	1
m3	0	0	1	1	0
m4	0	1	0	0	1
m5	0	1	0	1	0
m6	0	1	1	0	1
m7	0	1	1	1	0
m8	1	0	0	0	0
m9	1	0	0	1	0
m10	1	0	1	0	1
m11	1	0	1	1	1
m12	1	1	0	0	1
m13	1	1	0	1	0
m14	1	1	1	0	1
m15	1	1	1	1	0

$$F(A, B, C, D) = (\bar{B} + \bar{C})(\bar{A} + D)(B + D)(\bar{C} + D)$$

static G-hazard
eliminated

2. Continued, Katz and Bartello 3.18.

$$E) F(A,B,C,D,E) = \sum m(0,1,3,4,7,11,12,15,16,17,20,28)$$

layer 1
A=0

D	B	C	0	0	1	1	0
0	0	0	1	0	0	0	0
0	1	0	0	0	0	0	0
1	1	1	1	1	0	0	0
1	0	0	0	0	0	0	0

Hazards

layer 2
A=1

D	B	C	0	0	1	1	0
0	0	0	1	1	1	0	0
0	1	0	0	0	0	0	0
1	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0

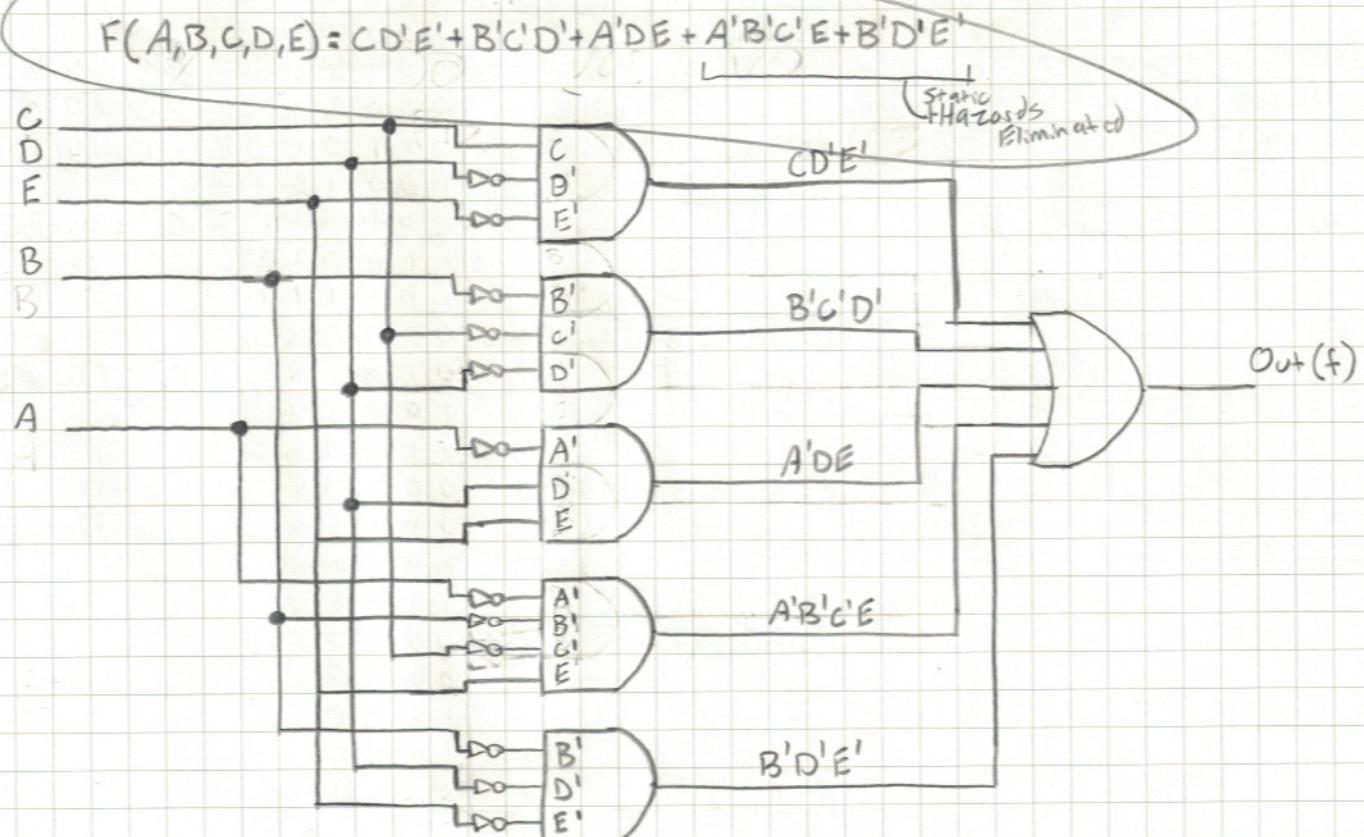
Hazard Fixes

A'B'C'E
B'D'E'

truth table

m	A	B	C	D	E	out
0	0	0	0	0	0	1
1	0	0	0	0	1	1
2	0	0	0	1	0	0
3	0	0	0	1	1	1
4	0	0	1	0	0	1
5	0	0	1	0	1	0
6	0	0	1	1	0	0
7	0	0	1	1	1	1
8	0	1	0	0	0	0
9	0	1	0	0	1	0
10	0	1	0	1	0	0
11	0	1	0	1	1	1
12	0	1	1	0	0	1
13	0	1	1	0	1	0
14	0	1	1	1	0	0
15	0	1	1	1	1	1
16	1	0	0	0	0	1
17	1	0	0	0	1	1
18	1	0	0	1	0	0
19	1	0	0	1	1	1
20	1	0	1	0	0	1
21	1	0	1	0	1	0
22	1	0	1	1	0	0
23	1	0	1	1	1	0
24	1	1	0	0	0	0
25	1	1	0	0	1	0
26	1	1	0	1	0	0
27	1	1	0	1	1	0
28	1	1	1	0	0	1
29	1	1	1	0	1	0
30	1	1	1	1	0	0
31	1	1	1	1	1	0

$$F(A,B,C,D,E) = \overline{CD} + \overline{C}\overline{D}\overline{E} + \overline{C}\overline{D}E + \overline{C}DE + A'DE + A'B'C'E + B'D'E'$$



3) Karnaugh and Boole's 5.12

Implement the ALU in question 5.12

Fout IC Maps

$$\begin{aligned} x \otimes y &= x'y' + x'y \\ (x \otimes y)' &= xy + x'y' \end{aligned}$$

A	B	C	F ₁
0	0	0	0
0	1	0	1
1	0	0	0

$$\begin{aligned} F_1 &= AB'C' + A'BC' + A'B'C + ABC \\ \text{xor} & C'(AB' + A'B) + C(A'B' + AB) \\ \text{xor}' & C'(A \oplus B) + C(AB' + AB) \\ \text{xor} & C'(A \oplus B) + C(A \oplus B)' \\ & C \oplus A \oplus B \end{aligned}$$

A	B	C	F ₁
0	0	0	0
0	1	0	1
1	0	0	0

* Same as B minus A

A	B	C	F ₁
0	0	0	0
0	1	0	1
1	0	0	0

* Same as B minus A

$$F_1 = A \oplus B$$

$$F_1 = A + B$$

$$F_1 = AB$$

Truth Table

ALU Operation	S ₂	S ₁	S ₀	A	B	C _{in}	F	C _{out}
F ₁ = 0 = 0 + 0	0	0	0	X	X	X	0	X
F ₁ = B minus A	0	0	1	0	0	0	0	0
A + B							0	1
A \oplus B \oplus C							0	1
AB' + AC + BC'							0	0

F ₁ = 0 = 0 + 0	S ₂	S ₁	S ₀	A	B	C _{in}	F	C _{out}
F ₁ = B minus A	0	0	1	0	0	0	0	0
A + B							0	1
A \oplus B \oplus C							0	1
AB' + AC + BC'							0	0

F ₁ = A minus B	S ₂	S ₁	S ₀	A	B	C _{in}	F	C _{out}
F ₁ = A \oplus B	0	1	0	0	0	0	0	0
A \oplus B \oplus C							0	1
AB' + A'C + BC							0	0

F ₁ = A plus B	S ₂	S ₁	S ₀	A	B	C _{in}	F	C _{out}
F ₁ = A \oplus B	0	1	1	0	0	0	0	0
A \oplus B \oplus C							0	1
AB' + BC + AC							0	0

F ₁ = A XOR B	S ₂	S ₁	S ₀	A	B	C _{in}	F	C _{out}
A \oplus B	1	0	0	X	X	X	0	X
							0	1
							0	0

F ₁ = A OR B	S ₂	S ₁	S ₀	A	B	C _{in}	F	C _{out}
A + B	1	0	1	0	0	0	0	0
							0	1
							0	0

F ₁ = A AND B	S ₂	S ₁	S ₀	A	B	C _{in}	F	C _{out}
AB	1	1	0	0	0	0	0	0
							0	1
							0	0

F ₁ = 1	S ₂	S ₁	S ₀	A	B	C _{in}	F	C _{out}
	1	1	1	X	X	X	1	X
							0	1
							0	0

Cout ICmaps

B

A	B	C	F ₁
0	0	0	0
0	1	0	1
1	0	0	0

A	B	C	F ₁
0	0	0	0
0	1	0	1
1	1	0	0

Cout = AB + A'C + BC

A	B	C	F ₁
0	0	0	0
0	1	0	1
1	0	0	0

Cout = AB + BC + AC

A	B	C	F ₁
0	0	0	0
0	1	0	1
1	0	0	0

Cout = Don't Care

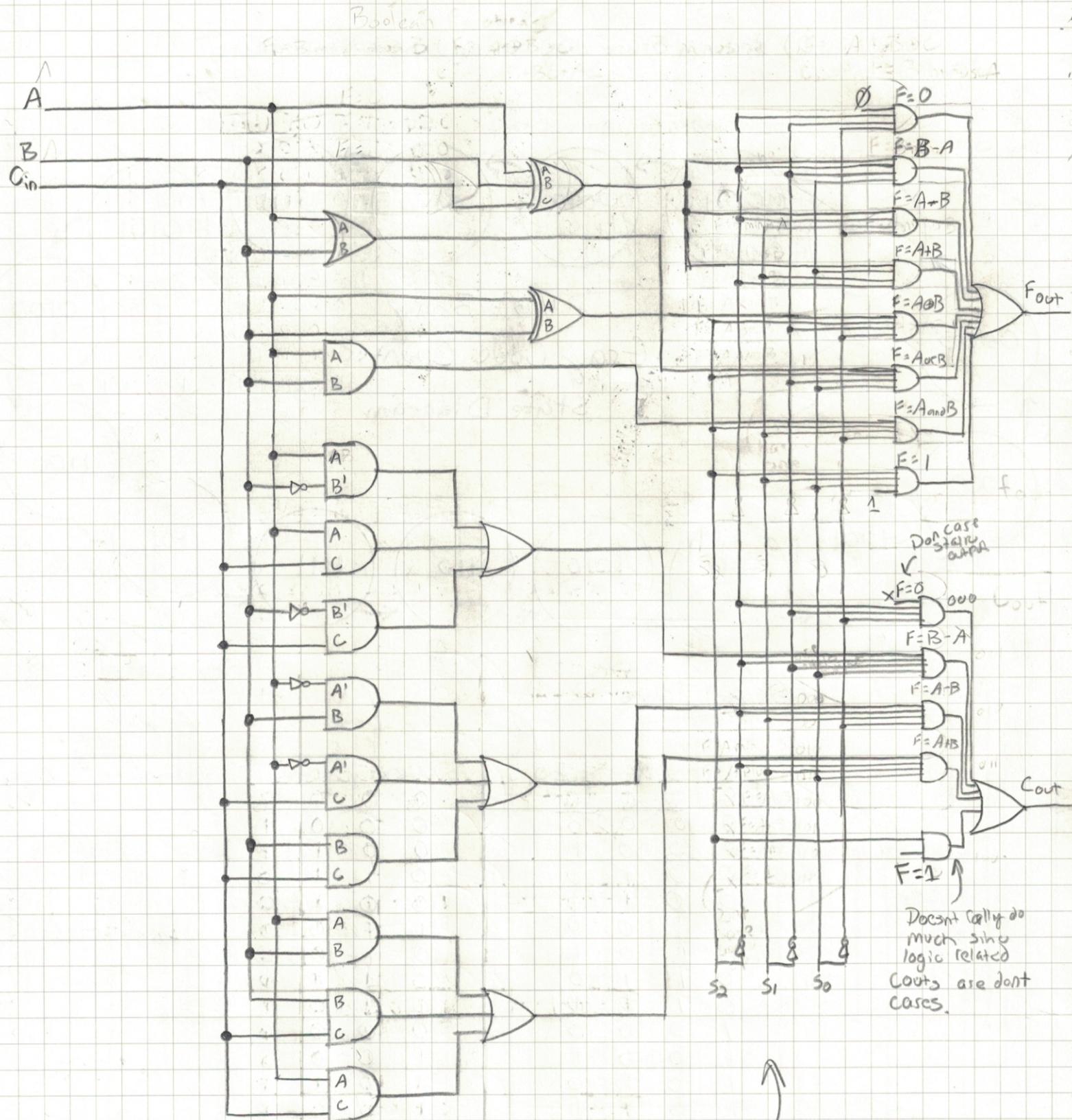
A	B	C	F ₁
0	0	0	0
0	1	0	1
1	0	0	0

Cout = Don't Care

A	B	C	F ₁
0	0	0	0
0	1	0	1
1	0	0	0

Cout = Don't Care

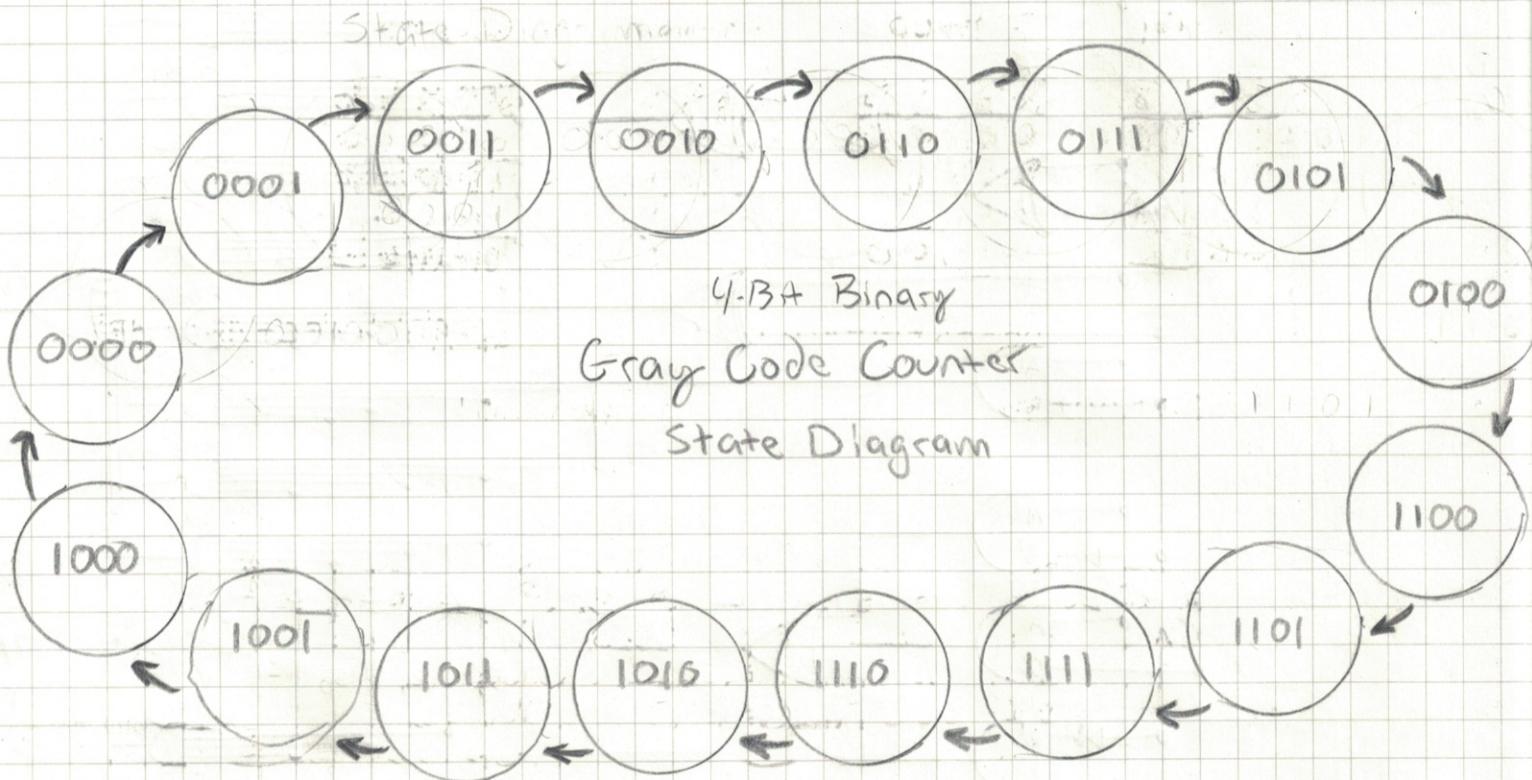
3) Continued, Katz and Boedtlo 5.12



Sorry if the Mux Gates
are messy, I saw last minute
that our muxs had to be
drawn out at the gate level

4) Katz and Boštík 7.4

a) draw a state diagram and next state table

State Table

Present State	Next State			
	Q_3	Q_2	Q_1	Q_0
0 0 0 0	0	0	0	1
0 0 0 1	0	0	1	1
0 0 1 0	0	1	1	0
0 0 1 1	0	0	1	0
0 1 0 0	1	1	0	0
0 1 0 1	0	1	0	0
0 1 1 0	0	1	1	1
0 1 1 1	0	1	0	1
1 0 0 0	0	0	0	0
1 0 0 1	1	0	0	0
1 0 1 0	1	0	1	1
1 0 1 1	1	0	0	1
1 1 0 0	1	1	0	1
1 1 0 1	1	1	1	1
1 1 1 0	1	0	1	0
1 1 1 1	1	1	1	0

4) Continued Icate and Basileto 7.4

B) Implement the Counter Using D-Flip flops

Q₃ K-Map

A	B	C	D	Q ₃
0	0	0	1	0
0	0	1	1	1
0	1	0	1	1
1	0	0	1	1
1	0	1	0	0
0	0	0	0	0

Q₂ K-Map

A	B	C	D	Q ₂
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
1	0	0	0	0
1	0	1	0	1
0	1	0	0	0
0	0	1	0	0

Q₁ K-Map

A	B	C	D	Q ₁
0	0	0	0	0
0	0	0	1	0
0	1	0	0	1
1	0	0	0	0
1	0	1	0	1
0	1	0	0	0
0	0	1	0	0

Q₀ K-Map

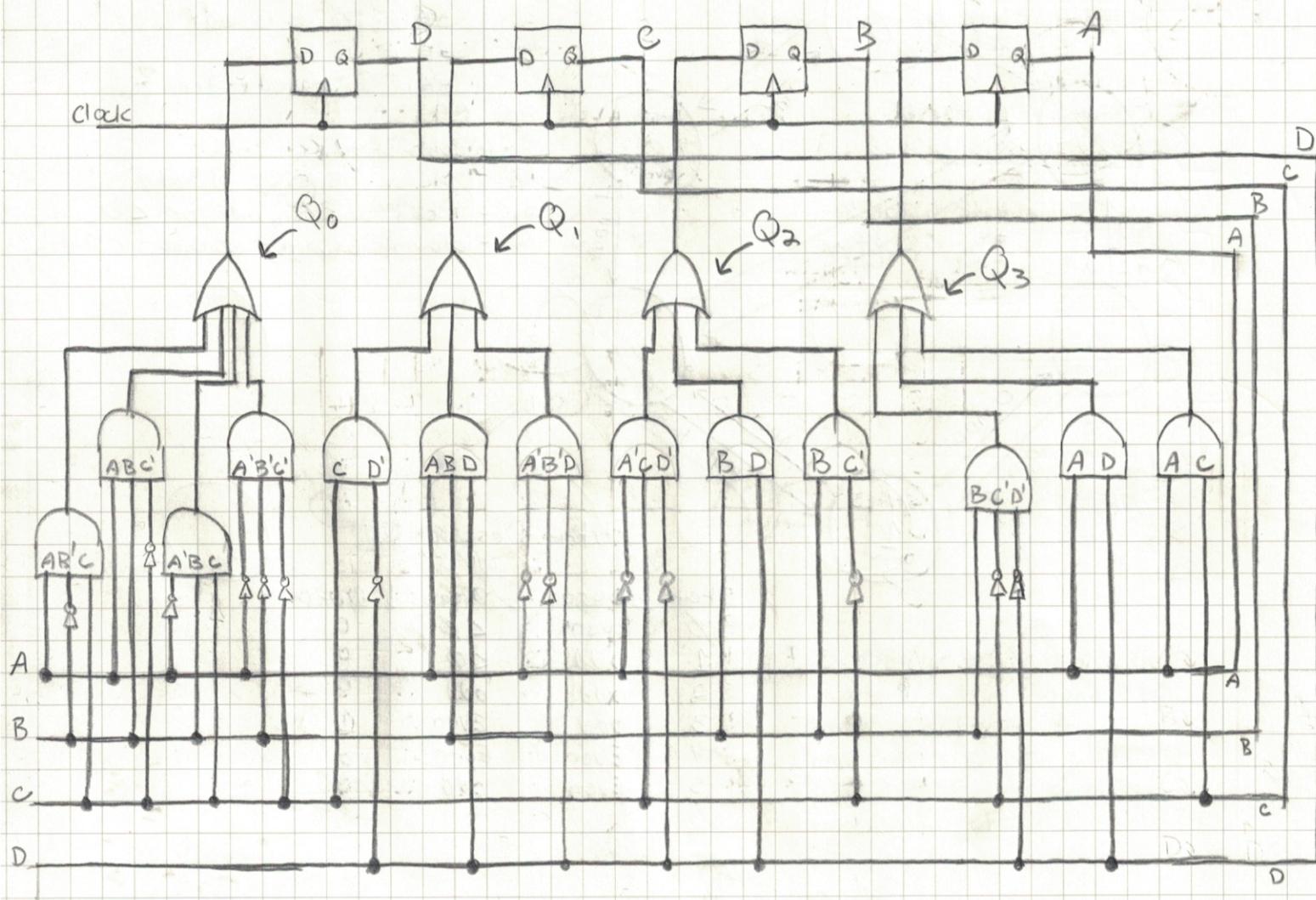
A	B	C	D	Q ₀
0	0	0	0	0
0	1	0	1	0
1	0	1	0	0
1	0	1	1	1
0	0	1	0	0
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1

$$Q_2 = BC' + BD + A'CD'$$

$$Q_3 = BCD' + AD + AC$$

$$Q_1 = A'BD + ABD + CD'$$

$$Q_0 = A'B'C' + A'BC + ABC' + AB'C$$

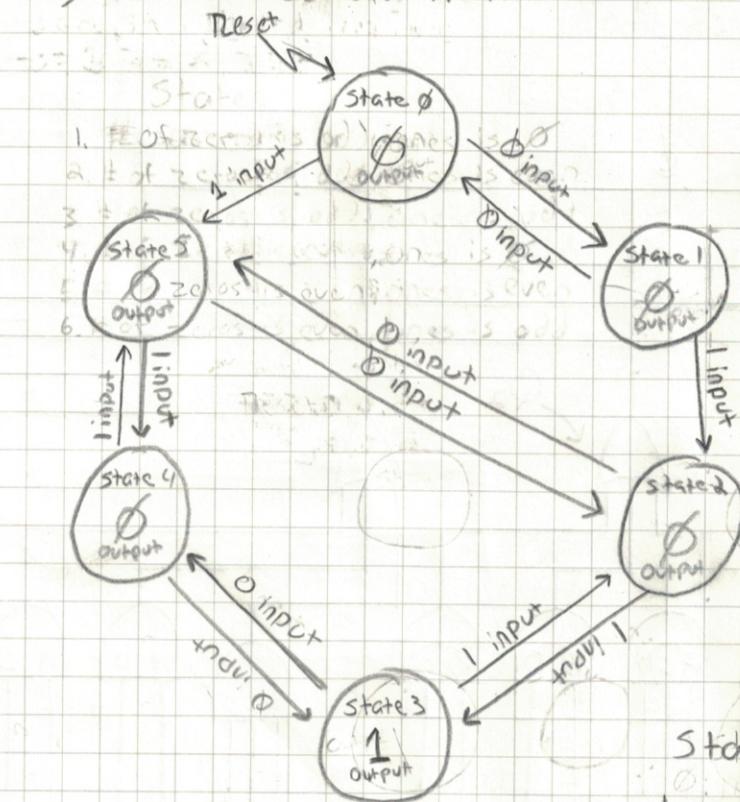


4) Continued Icate and Bosello 7.4

c) Do you have to worry about self starting?
why or why not?

A.) Self starting shouldn't be a worry with this counter since it is always already self starting. We know this simply because all possible states are accounted for in the sequence. Also, it's fortunate that all possible states are actually in the count sequence, so it will take 0 additional transitions to be sure the counter is in the count sequence.

5) Icate and Bosello 7.27



Notes

- one input
- one output
- should be 1 if...
 - # of Zeros is odd
 - # of 1s is even
 - # of 1s >= greater than zeros
- in each state...
 - 0 can be even or odd
 - 1 can be even, odd, or < 0
 - allowing 6 unique combos for states

State Descriptions

State	Zeros	Ones	Output
0	Even	<0	0
1	Odd	<0	0
2	Odd	odd	0
3	Odd	Even	1
4	Even	Even	0
5	Even	odd	0

b) Katz and Bozella 8.2 State Reduction

State table

Present state	Next state		Output	Reduced state
	X=0	X=1		
A	B	C	0	A
B	A	C	0	A G
C	D	C	0	D
D	D	E	1	D
E	A	F	0	A
F	B	G	0	A B
G	A	E	0	A E

State Assignment

A: 000 E: 100

B: 001 F: 101

C: 010 G: 110

D: 011

B	AB				
C	BD	AD			
D	X	X	X		
E	A,B	AB	AB	AB	
F	B,B	AB	AB	AB	AB
G	B,A	BA	BA	BA	BA
	A				
	B				
	C				
	D				
	E				
	F				
	G				
	A	B	C	D	E

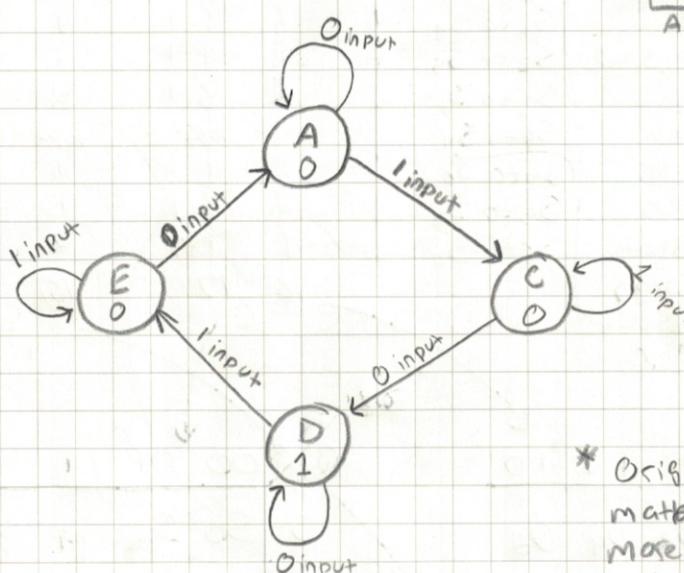
1st pass: A ≠ D, B ≠ D, C ≠ D
 and pass: A ≠ C, B ≠ C, C ≠ E, C ≠ F, C ≠ G
 D ≠ E, D ≠ F, D ≠ G
 3rd Pass: A ≠ E, A ≠ F, A ≠ G
 B ≠ E, B ≠ F, B ≠ G

Equivalent state list

A, B,	<u>AB</u>
F, G,	
G, E,	FG, GF, EF
F, E,	<u>FGF</u>

All Reduced States

AB, C, D,	<u>EFG</u>
A, C, D,	<u>E</u>



* Originally tried to use row matching but found it more confusing than implication chart although probably faster, so chose implication chart

7) Katz and Boivello 8.7 (State Assignment)

A) Minimum bit change heuristic

State Map

$Q_1 Q_0$	00	01	10	11	10
Q_2	0	$S_0 S_1 S_3 S_5$			
1	$S_2 S_4 S_6$	X			

State Assignment Table

State Name	Assignment		
	Q_0	Q_1	Q_2
S_0	0	0	0
S_1	0	0	1
S_2	1	0	0
S_3	0	1	1
S_4	1	0	1
S_5	0	1	0
S_6	1	1	1

Since single bit changes are adjacent due to the nature of LC-maps, and Gray code, and the state diagram followed a sort of two path flow, I chose to start by guessing and checking the two main state transition flows ($S_0, 2, 4, 6$ & $S_0, 1, 3, 5$) with each state adjacent to the next in rows similar to the diagram paths. This created many single bit changes but state changes out of "order" (e.g. $S_1 \rightarrow S_3$) proved difficult, for which I ended up guessing and checking 4 variations below and choosing the one with fewest bits changed in state transitions overall. S_0 always started at 000.

Transition	Assignment Bit Changes			
$S_0 \rightarrow S_1$	0	1	1	1
$S_0 \rightarrow S_2$	1	1	2	1
$S_1 \rightarrow S_3$	1	1	1	1
$S_1 \rightarrow S_4$	2	3	2	1
$S_2 \rightarrow S_3$	2	1	2	3
$S_2 \rightarrow S_4$	1	1	1	1
$S_3 \rightarrow S_5$	1	1	1	1
$S_3 \rightarrow S_6$	2	3	2	1
$S_4 \rightarrow S_5$	N/A	N/A	N/A	N/A
$S_4 \rightarrow S_6$	1	1	1	1
$S_5 \rightarrow S_0$	2	3	1	1
$S_5 \rightarrow S_6$	1	1	1	1
$S_6 \rightarrow S_0$	1	1	2	3
	16	17	15	16
				(15)

1st

$Q_1 Q_0$	00	01	11	10
Q_2	0	$S_0 S_2 S_4 S_6$		
1	X $S_1 S_3 S_5$			

and

$Q_1 Q_0$	00	01	11	10
Q_2	0	$S_0 S_2 S_4 S_6$		
1	$S_1 S_3 S_5$	X		

3rd

$Q_1 Q_0$	00	01	11	10
Q_2	0	$S_0 S_1 S_3 S_5$		
1	X $S_2 S_4 S_6$			

4th

$Q_1 Q_0$	00	01	11	10
Q_2	0	$S_0 S_1 S_3 S_5$		
1	$S_2 S_4 S_6$	X		

7) Katz and Bozzello 8.7 (State Assignment)

B) State Assignment Guidelines

State Assignments

$S_0 = 000$
$X = 001$
$S_6 = 010$
$S_5 = 011$
$S_3 = 110$
$S_4 = 111$
$S_1 = 100$
$S_2 = 101$

Highest Priority: $(S_1, S_2) (S_5, S_2, S_4, S_5, S_1) \times 2$
 $(S_3, S_4) (S_5, S_6) \times 2$

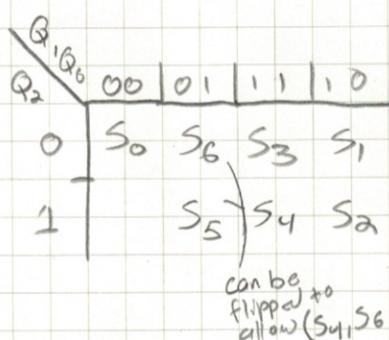
Medium Priority: $(S_1, S_2) (S_3, S_4) \times 2$
 $(S_4, S_6) S_5, S_6$

Lowest Priority: $S_0, S_1, S_4, S_3, S_2 = 1/0$
 $S_0, S_4, S_5, S_6 = 0/0$
 $S_1, S_2, S_3 = 0/1$
 $S_3, S_6 = 1/1$

Explanation

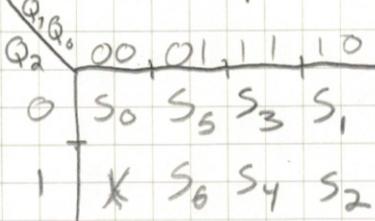
For the first iteration of the state map I was able to easily accomplish the top 3 adjacencies occurring in the high and mid priority 1-Bits. $(S_1, S_2 - S_3, S_4 - S_5, S_6)$. I was unsure of exactly where they should go but I knew they should stay together. I noticed the S_5, S_6 adjacency could be flipped to also support the mid priority S_4, S_6, S_0 so I did that. Of course, S_0 started at 000 and S_0 had no high or mid priorities so \emptyset was left empty.

$S_0 = 000$
$X = 001$
$S_5 = 010$
$S_6 = 011$
$S_3 = 110$
$S_4 = 111$
$S_1 = 100$
$S_2 = 101$



can be flipped to allow (S_4, S_6) adjacency

State Map



State Assignment Table

State Name	Assignment	Q_0	Q_1	Q_2
S_0		0	0	0
S_1		1	0	0
S_2		1	0	1
S_3		1	1	0
S_4		1	1	1
S_5		0	1	0
S_6		0	1	1